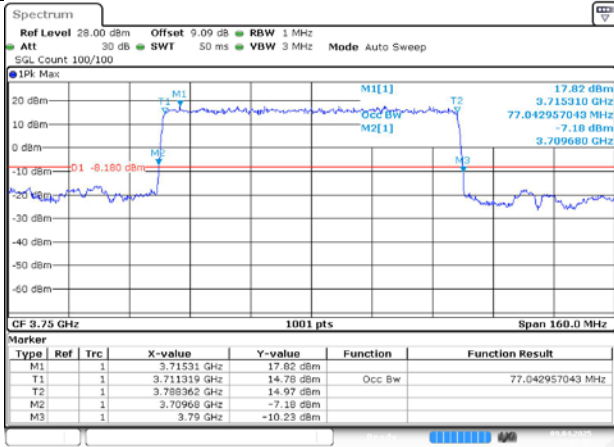


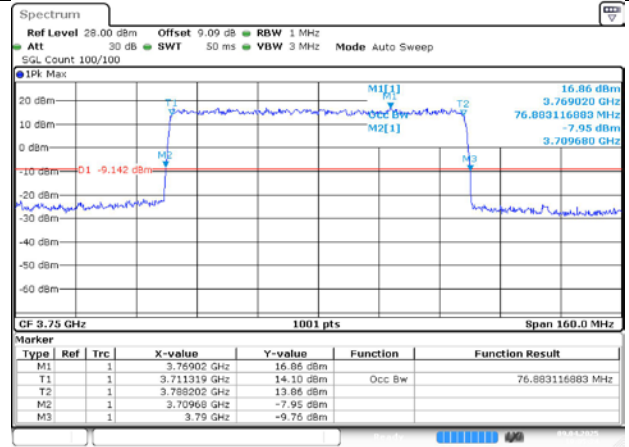
n78A / 30KHz / 80MHz

MCH / DFT-Pi2BPSK / Outer_Full



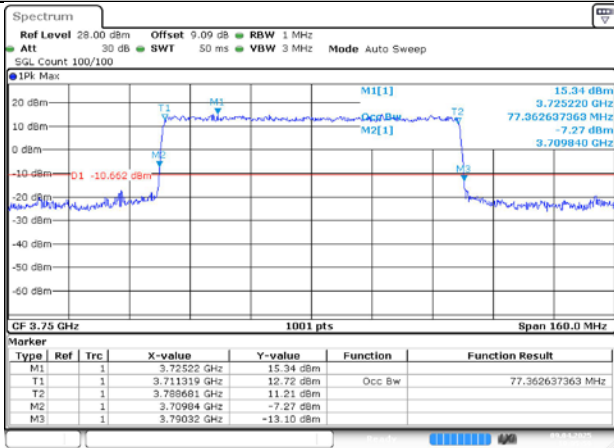
Date: 9 APR 2025 10:05:05

MCH / DFT-QPSK / Outer_Full



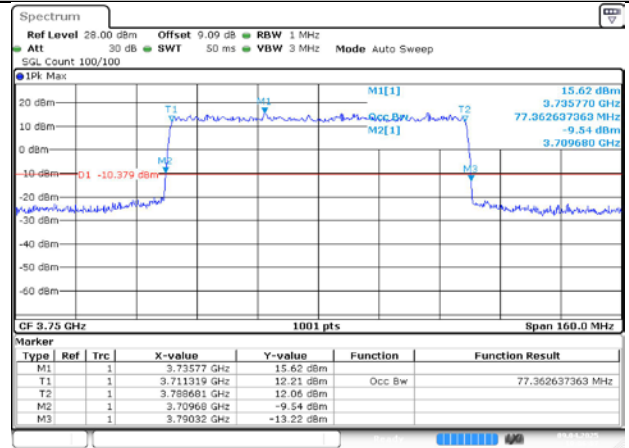
Date: 9 APR 2025 10:05:23

MCH / CP-QPSK / Outer_Full



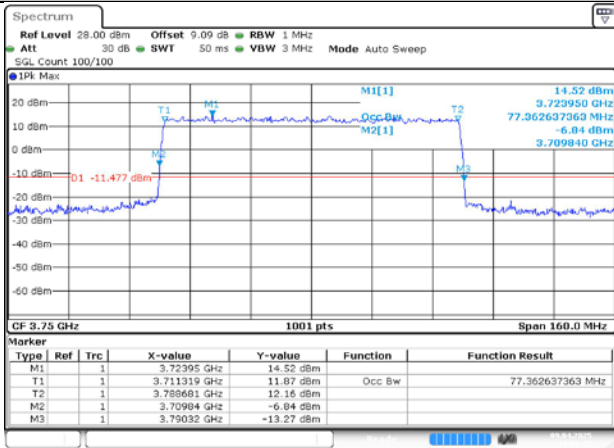
Date: 9 APR 2025 10:06:00

MCH / CP-16QAM / Outer_Full



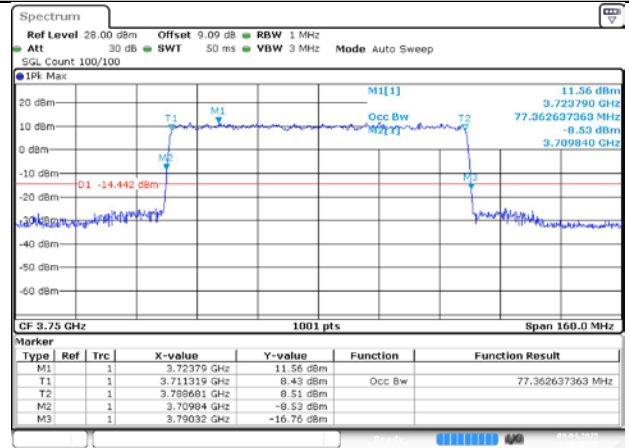
Date: 9 APR 2025 10:06:07

MCH / CP-64QAM / Outer_Full



Date: 9 APR 2025 10:06:15

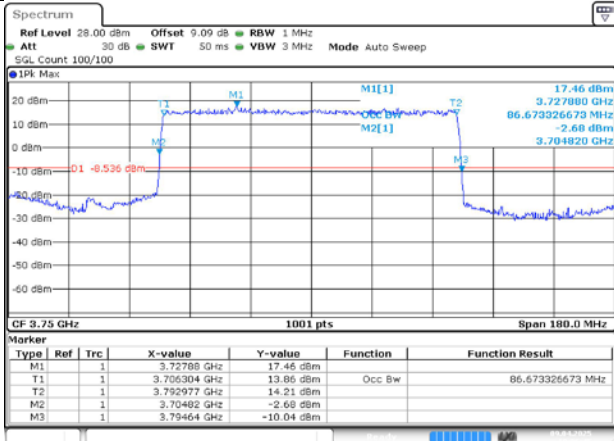
MCH / CP-256QAM / Outer_Full



Date: 9 APR 2025 10:06:25

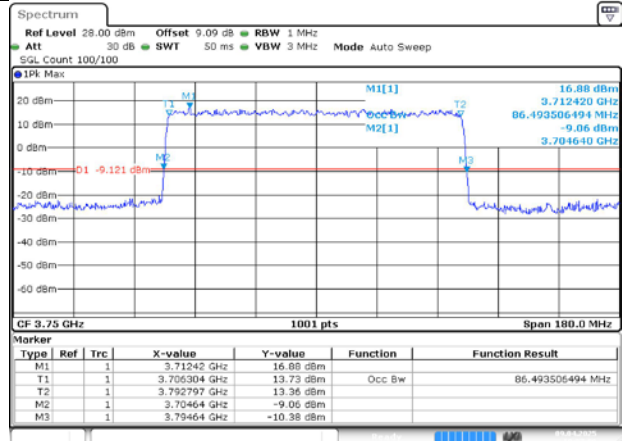
n78A / 30KHz / 90MHz

MCH / DFT-Pi2BPSK / Outer_Full



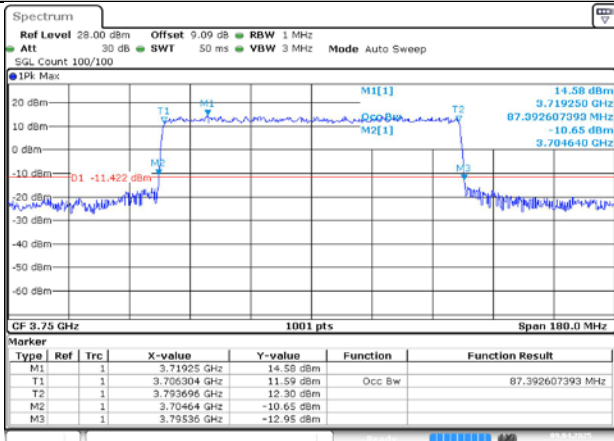
Date: 9 APR 2025 10:09:45

MCH / DFT-QPSK / Outer_Full



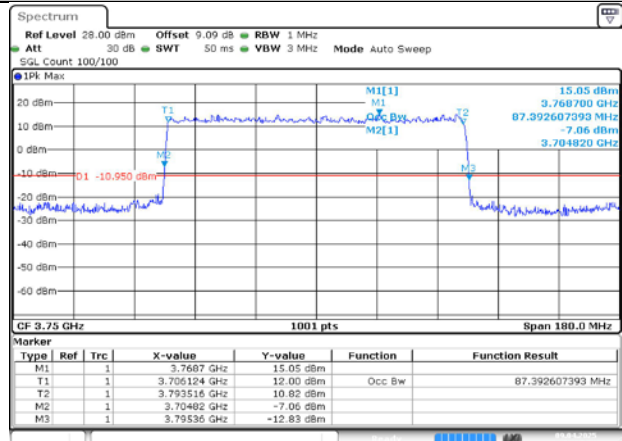
Date: 9 APR 2025 10:07:09

MCH / CP-QPSK / Outer_Full



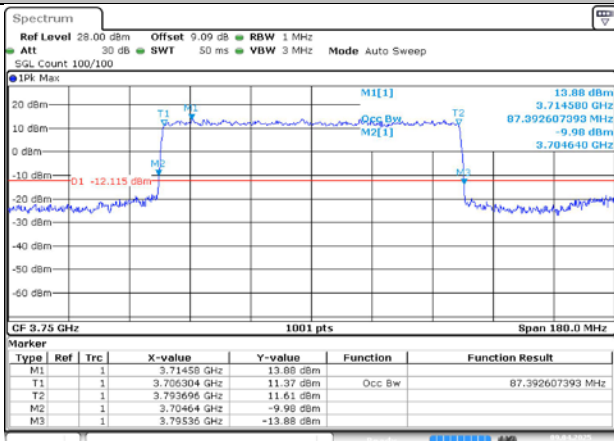
Date: 9 APR 2025 10:07:45

MCH / CP-16QAM / Outer_Full



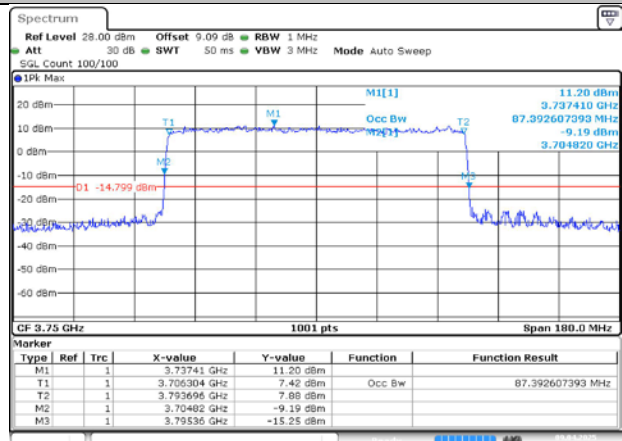
Date: 9 APR 2025 10:07:53

MCH / CP-64QAM / Outer_Full



Date: 9 APR 2025 10:08:01

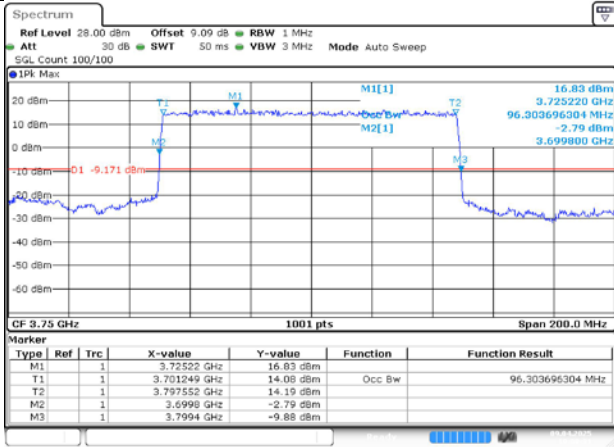
MCH / CP-256QAM / Outer_Full



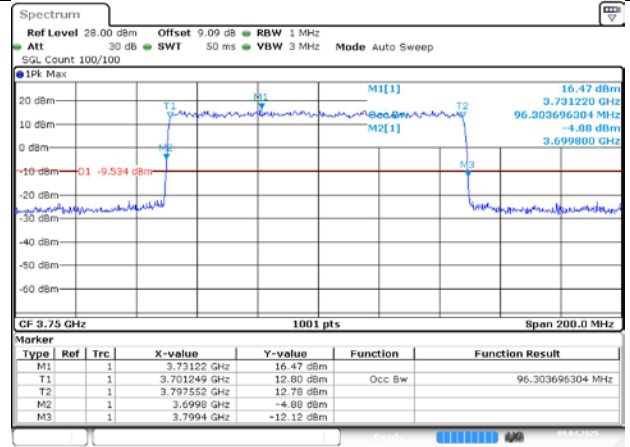
Date: 9 APR 2025 10:08:11

n78A / 30KHz / 100MHz

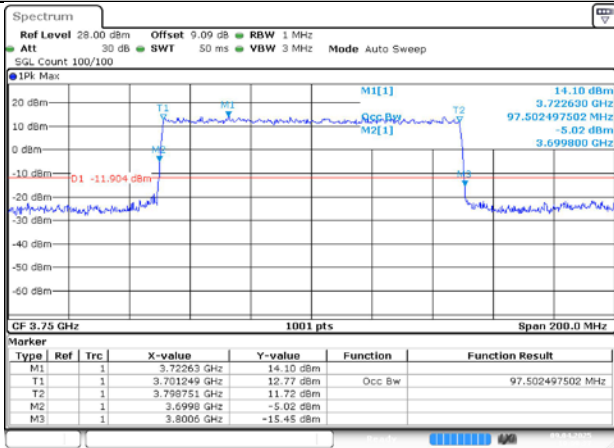
MCH / DFT-Pi2BPSK / Outer_Full



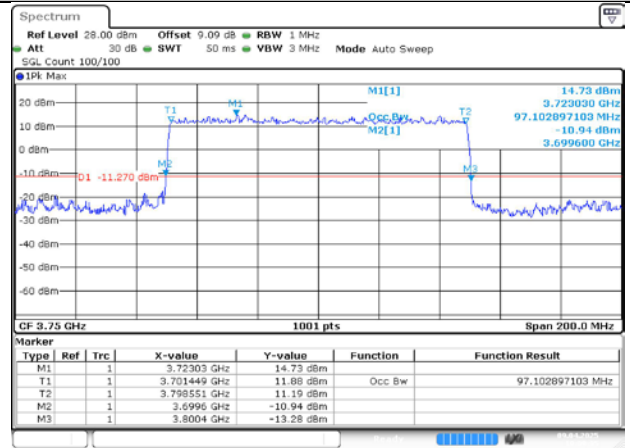
MCH / DFT-QPSK / Outer_Full



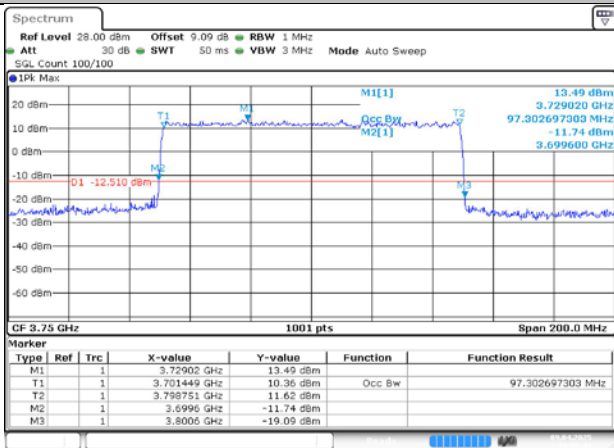
MCH / CP-QPSK / Outer_Full



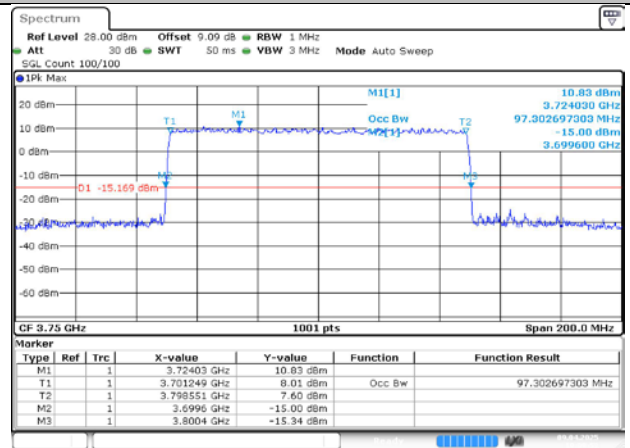
MCH / CP-16QAM / Outer_Full



MCH / CP-64QAM / Outer_Full

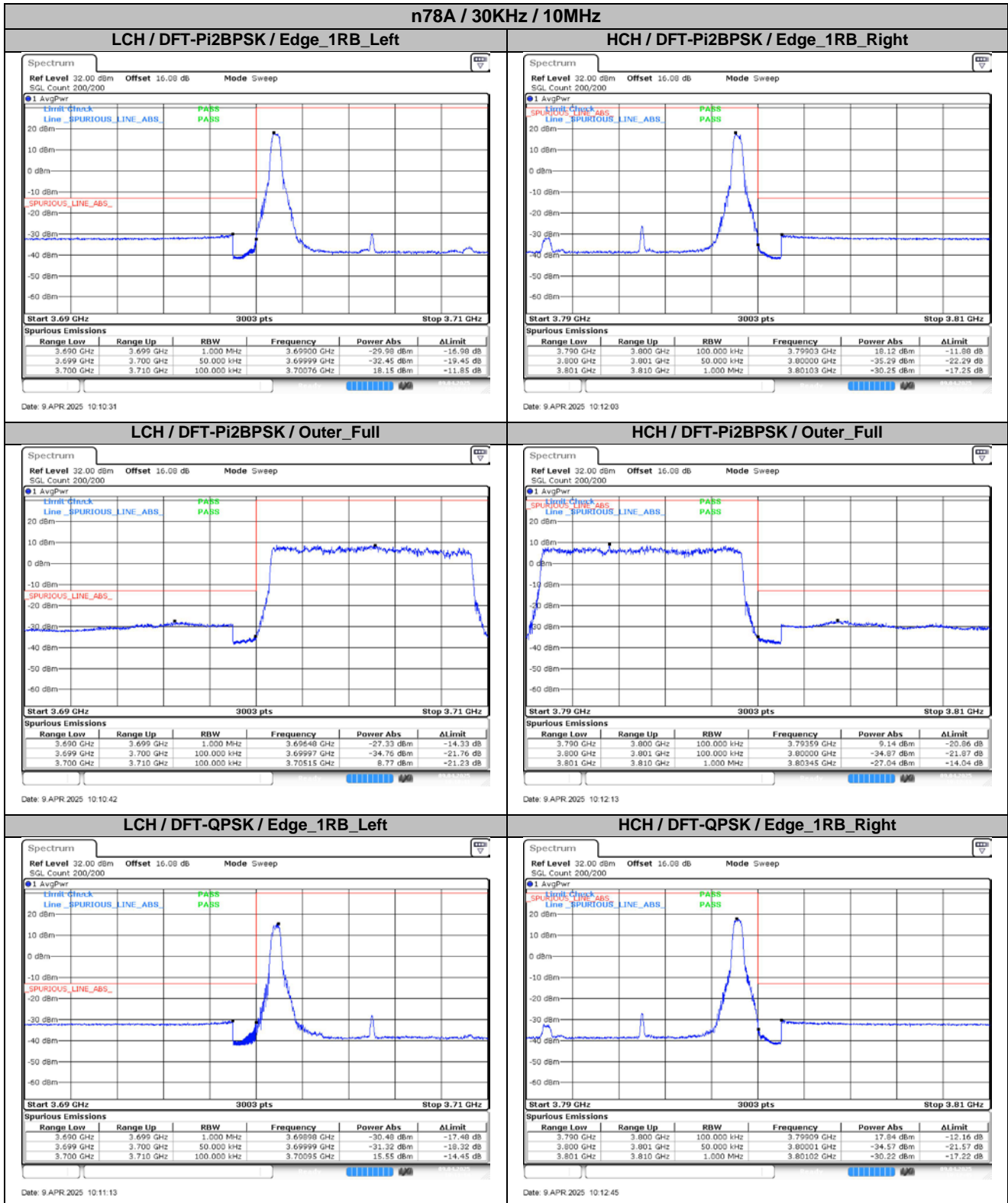


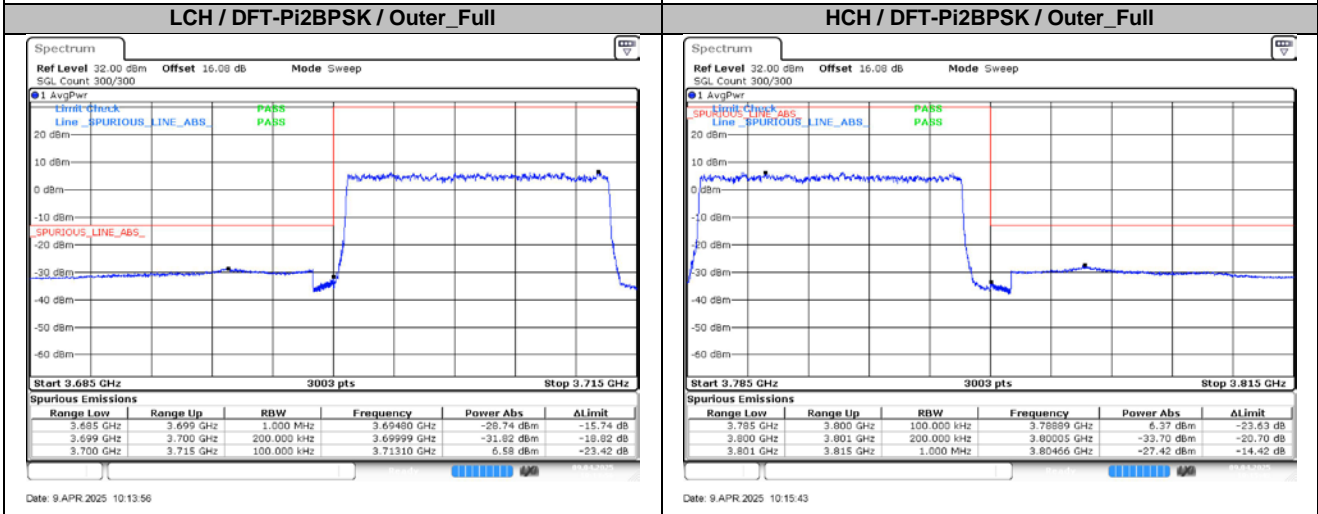
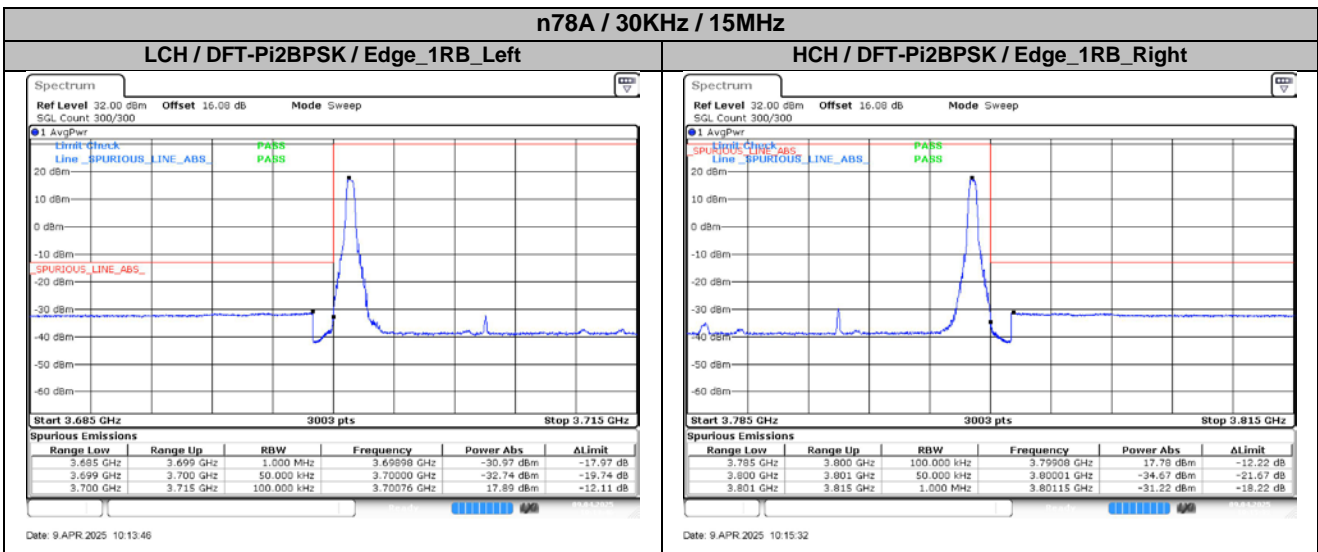
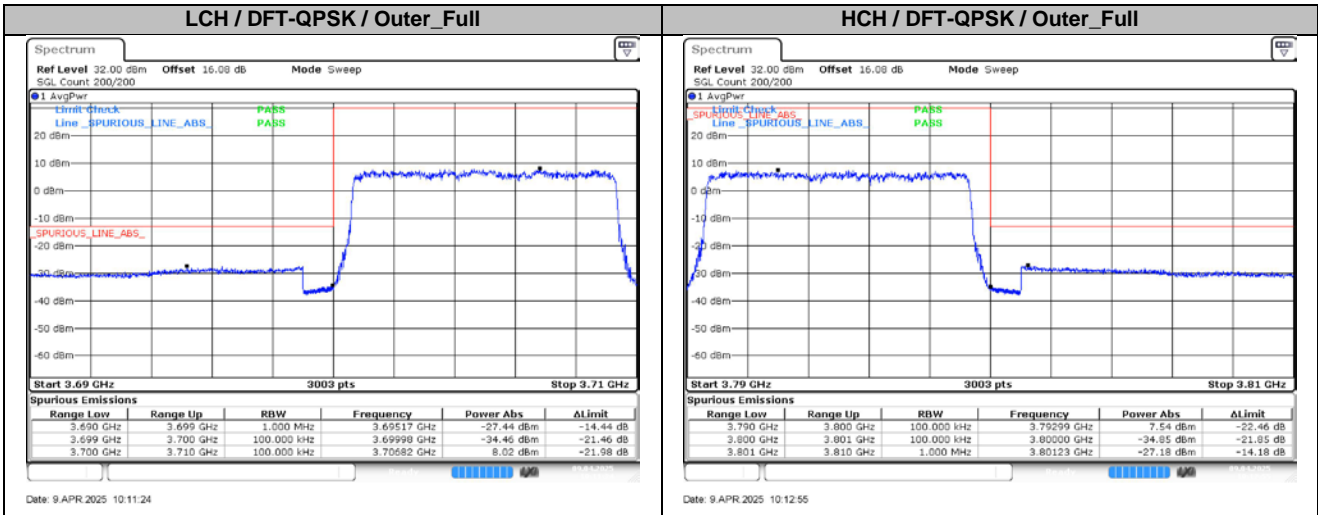
MCH / CP-256QAM / Outer_Full

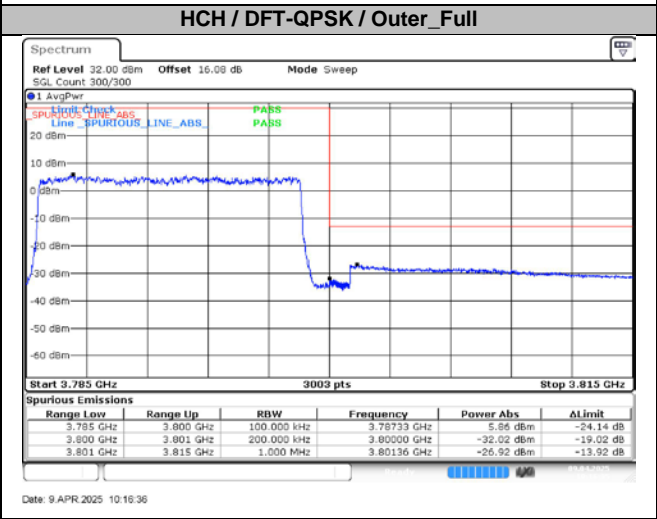
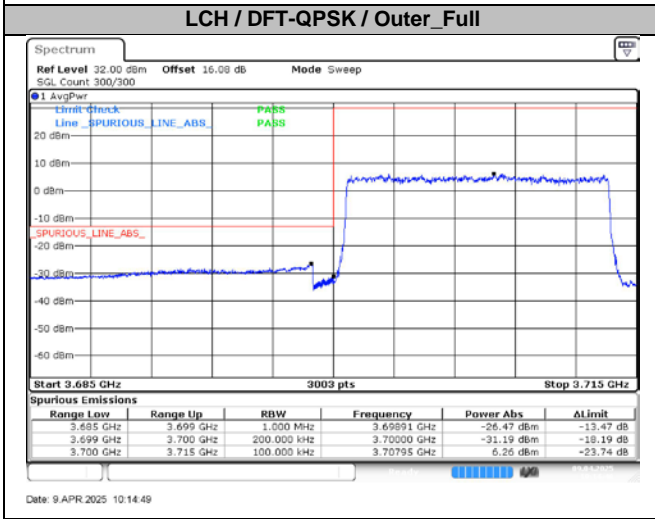
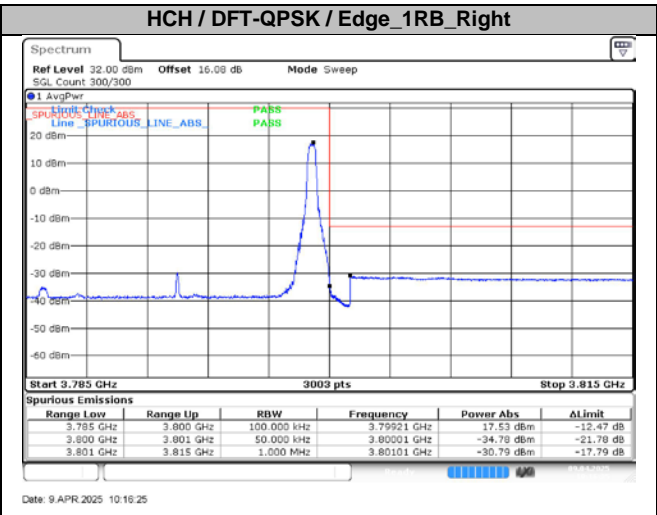
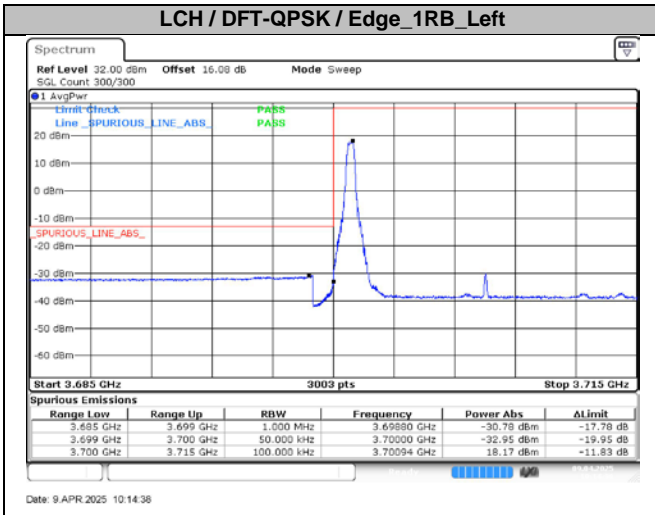


5. Conducted Band Edges

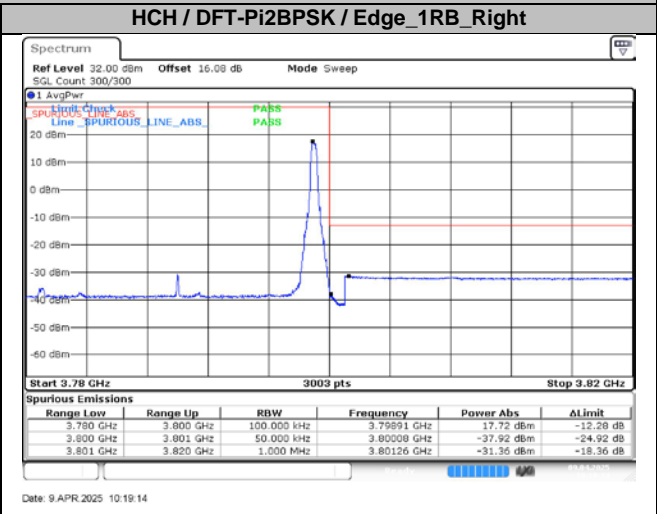
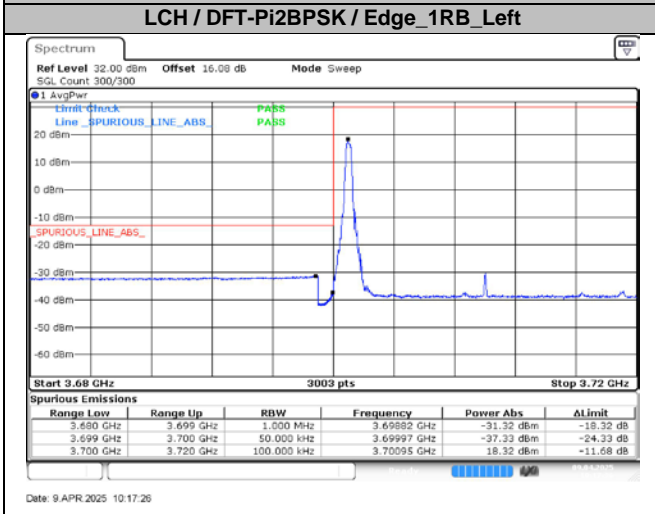
5.1. Test Plots for SCS=30KHz

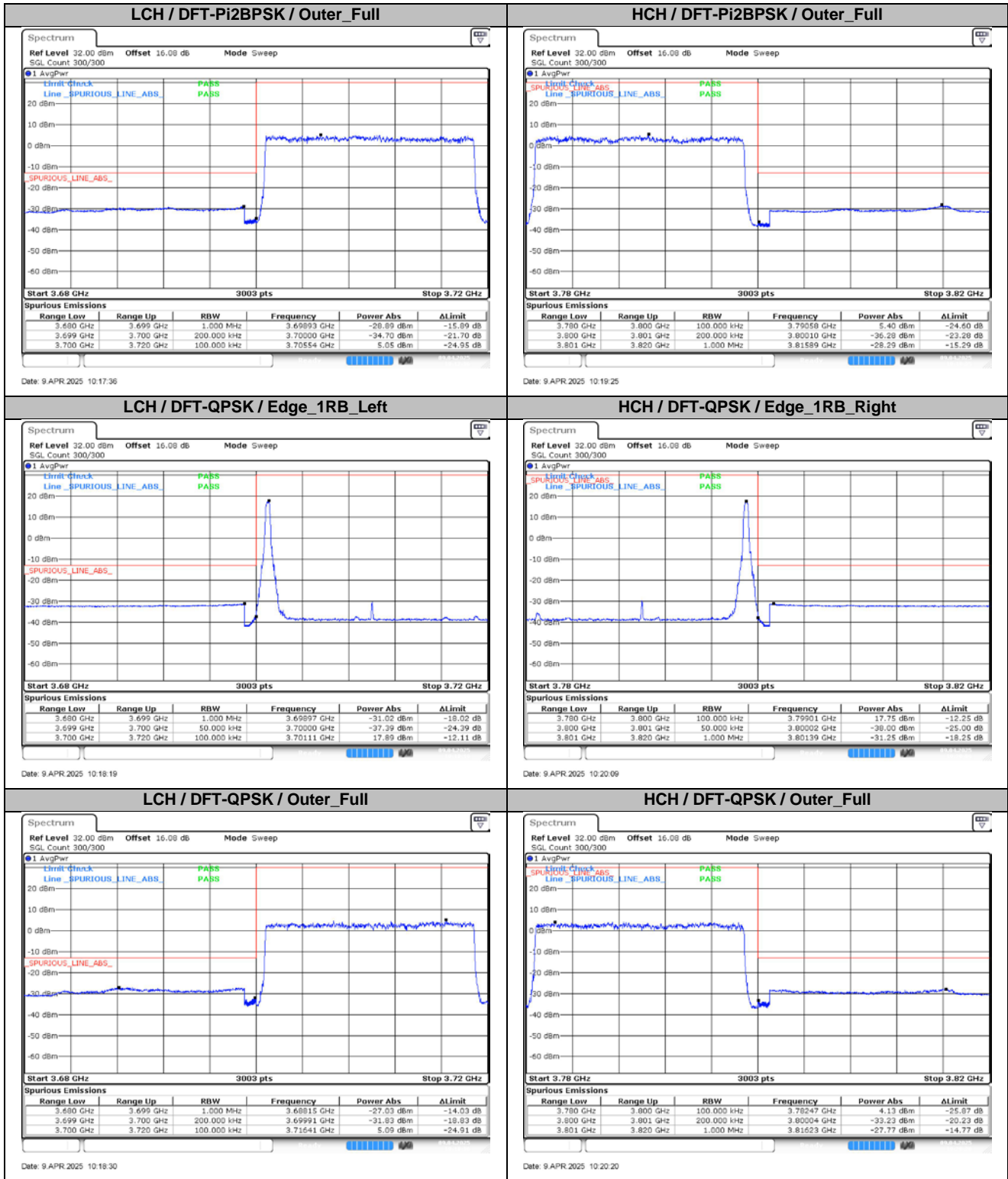






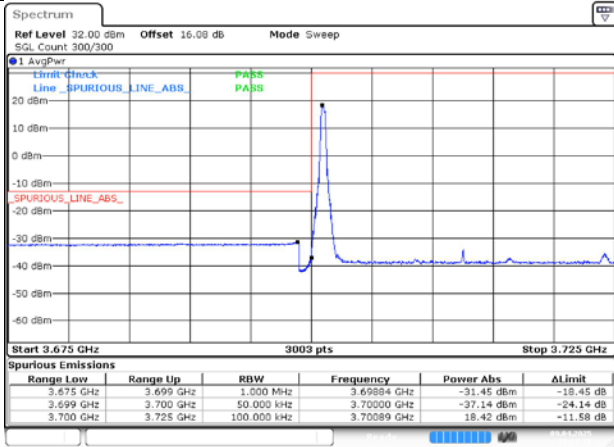
n78A / 30KHz / 20MHz





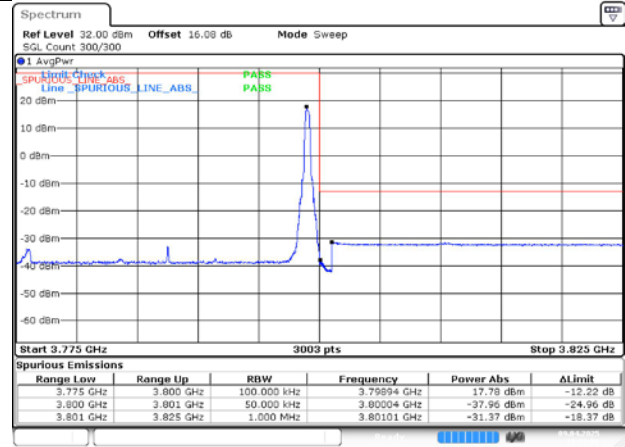
n78A / 30KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



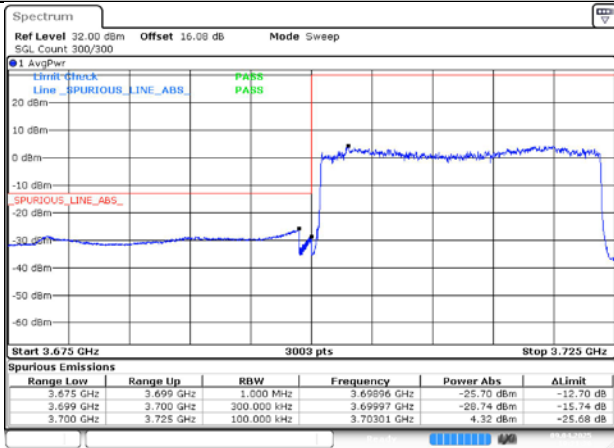
Date: 9 APR 2025 10:21:10

HCH / DFT-Pi2BPSK / Edge_1RB_Right



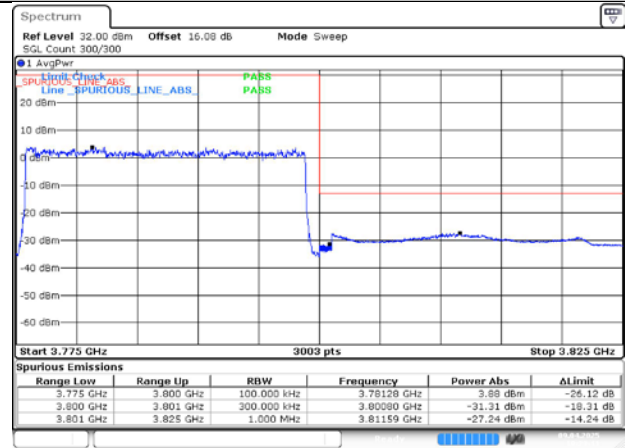
Date: 9 APR 2025 10:23:00

LCH / DFT-Pi2BPSK / Outer_Full



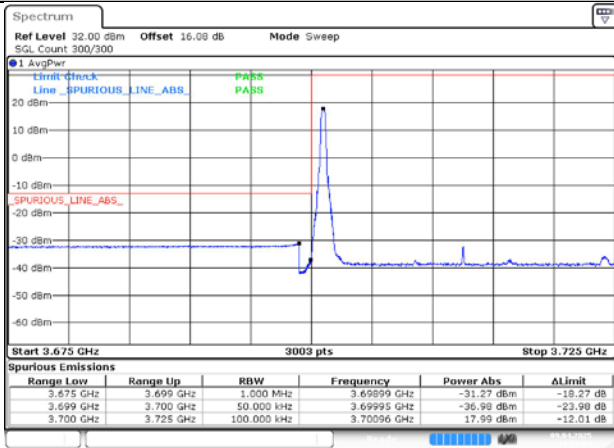
Date: 9 APR 2025 10:21:20

HCH / DFT-Pi2BPSK / Outer_Full



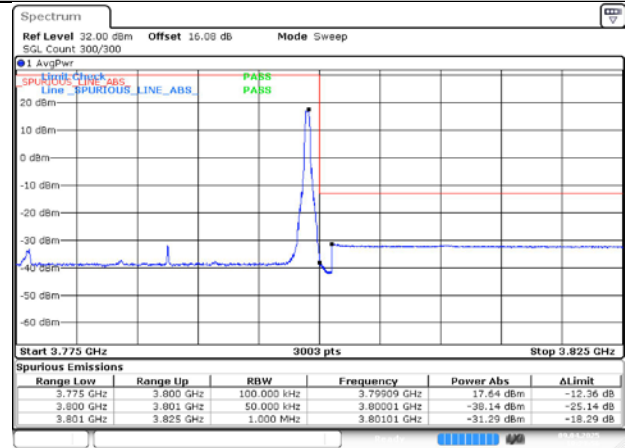
Date: 9 APR 2025 10:23:12

LCH / DFT-QPSK / Edge_1RB_Left

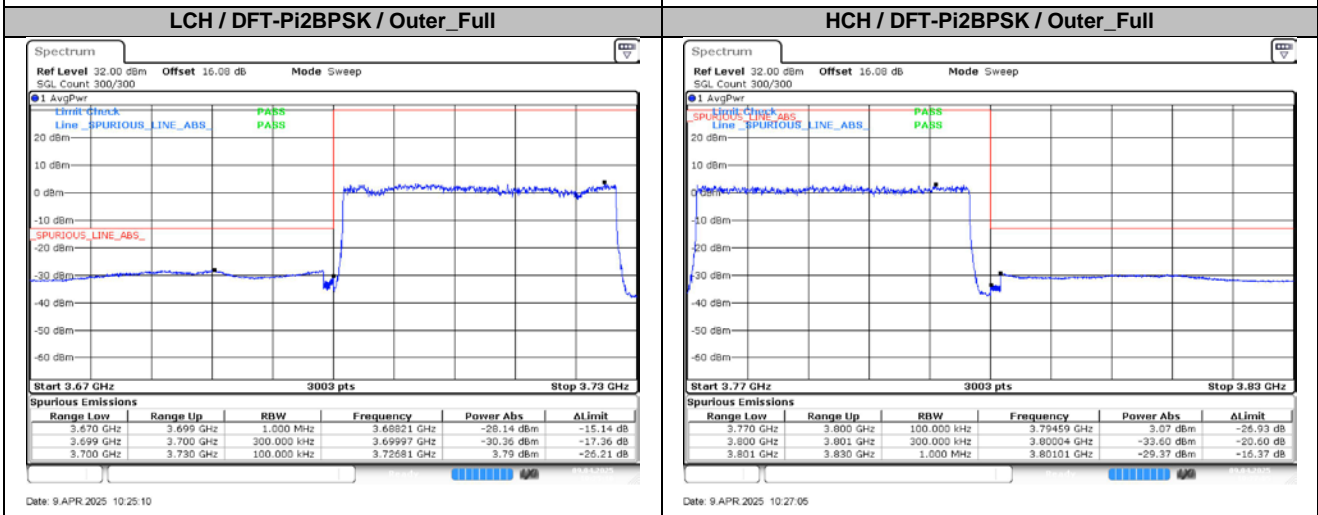
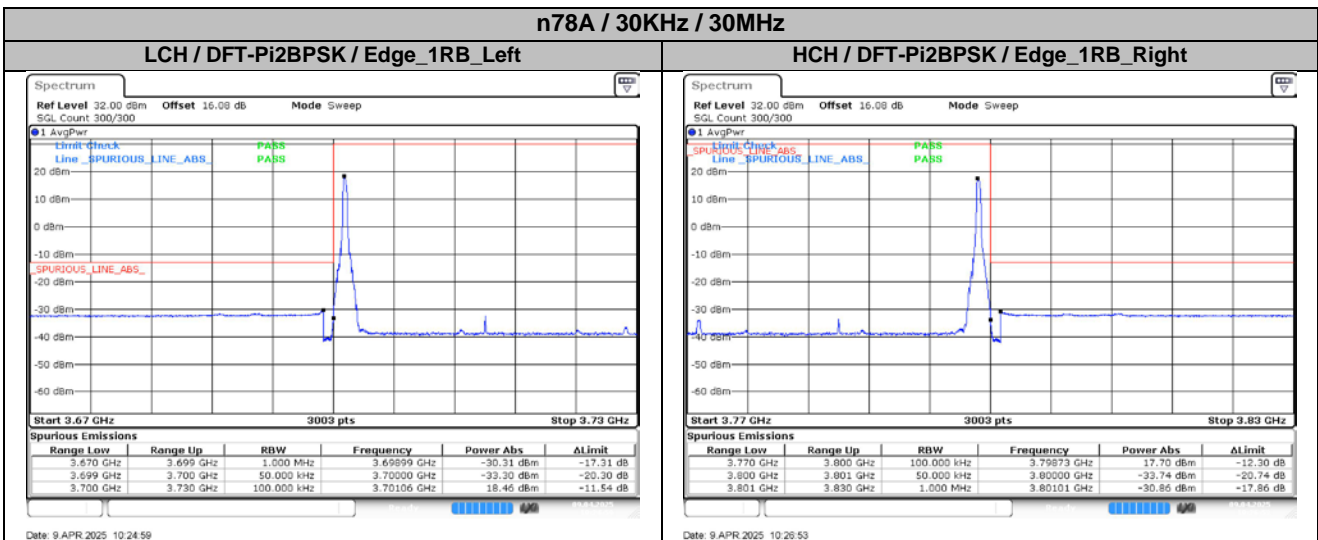
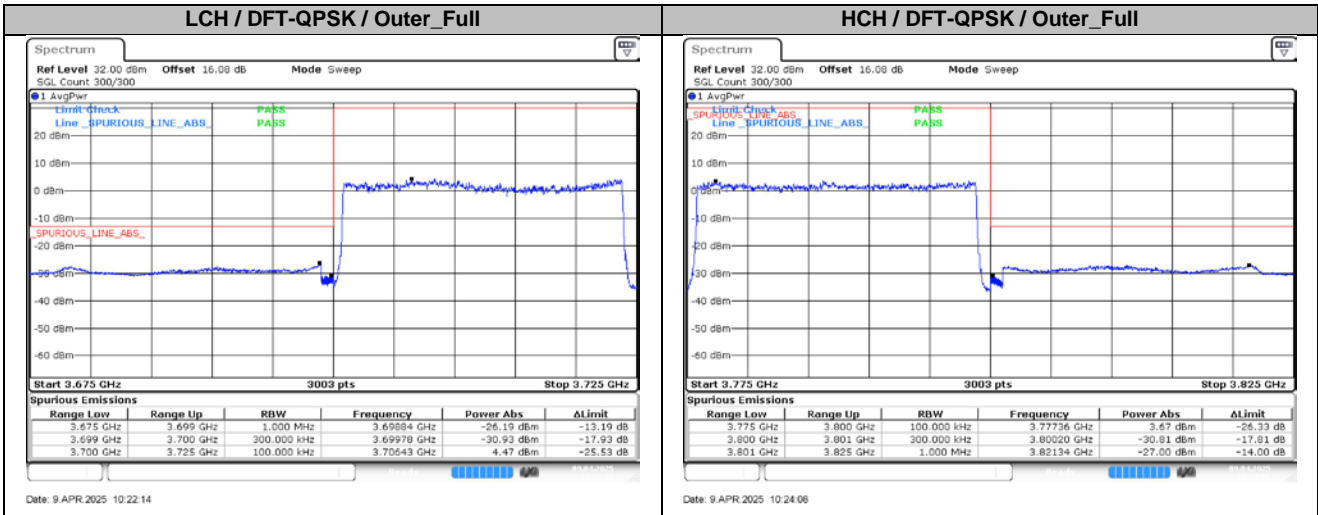


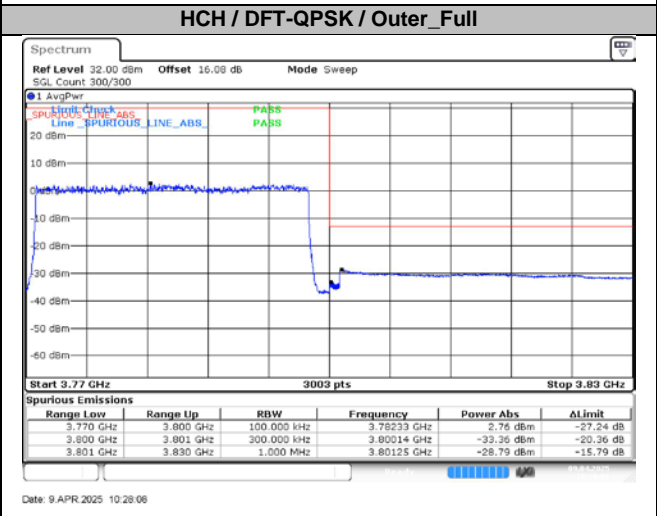
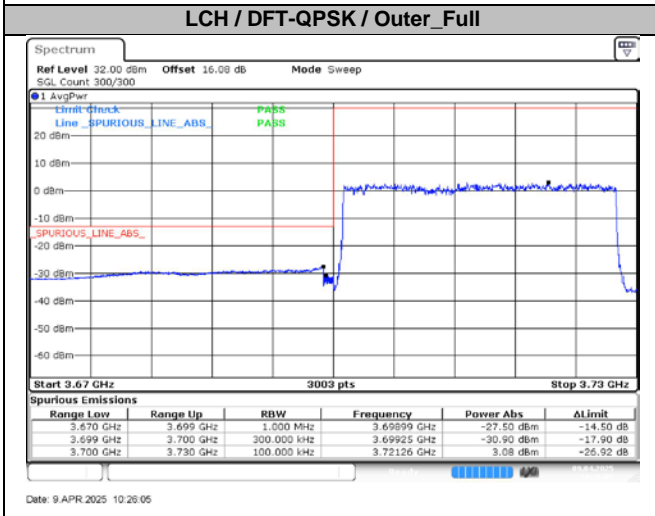
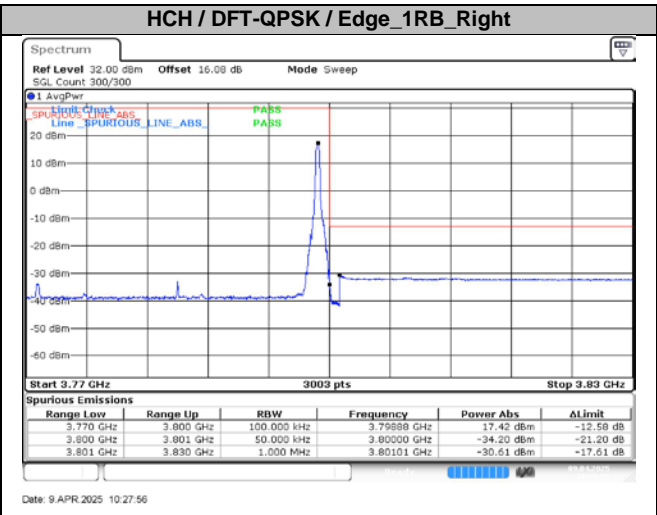
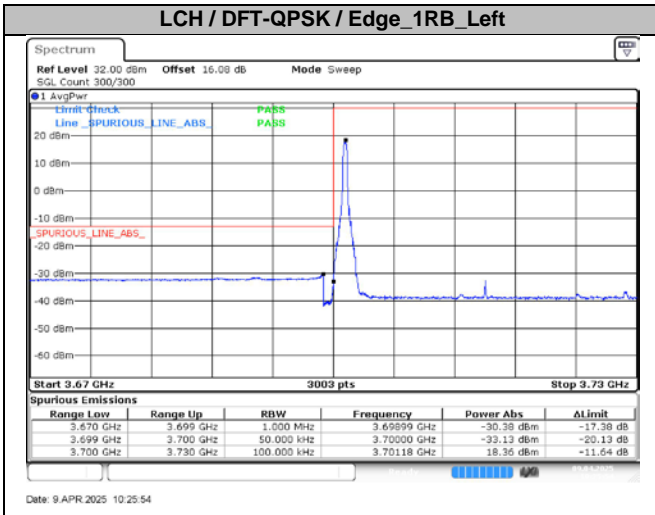
Date: 9 APR 2025 10:22:04

HCH / DFT-QPSK / Edge_1RB_Right

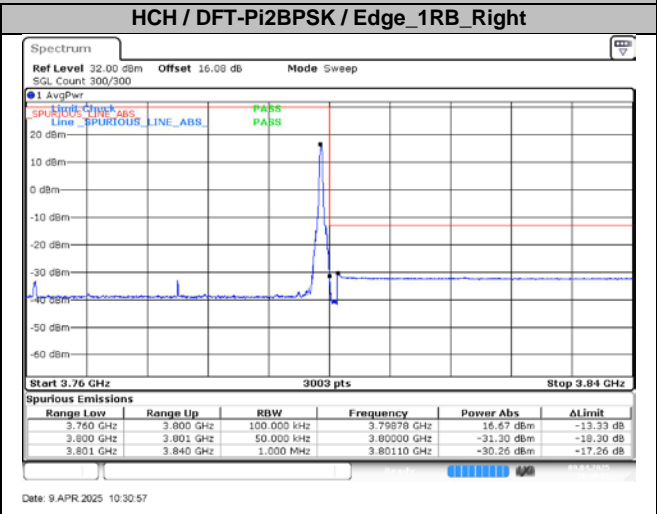
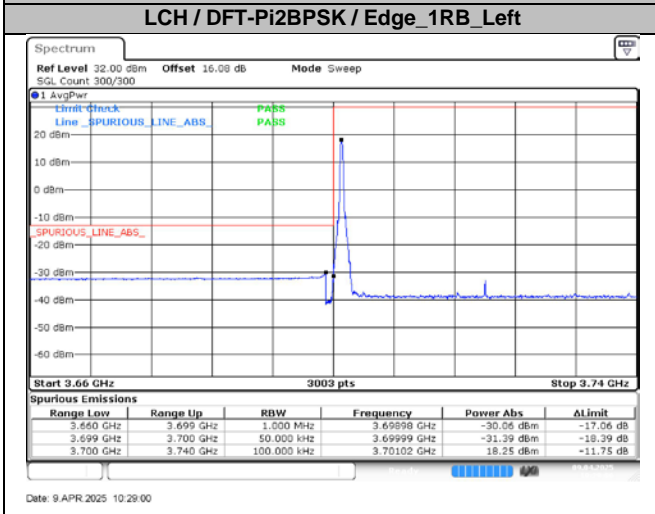


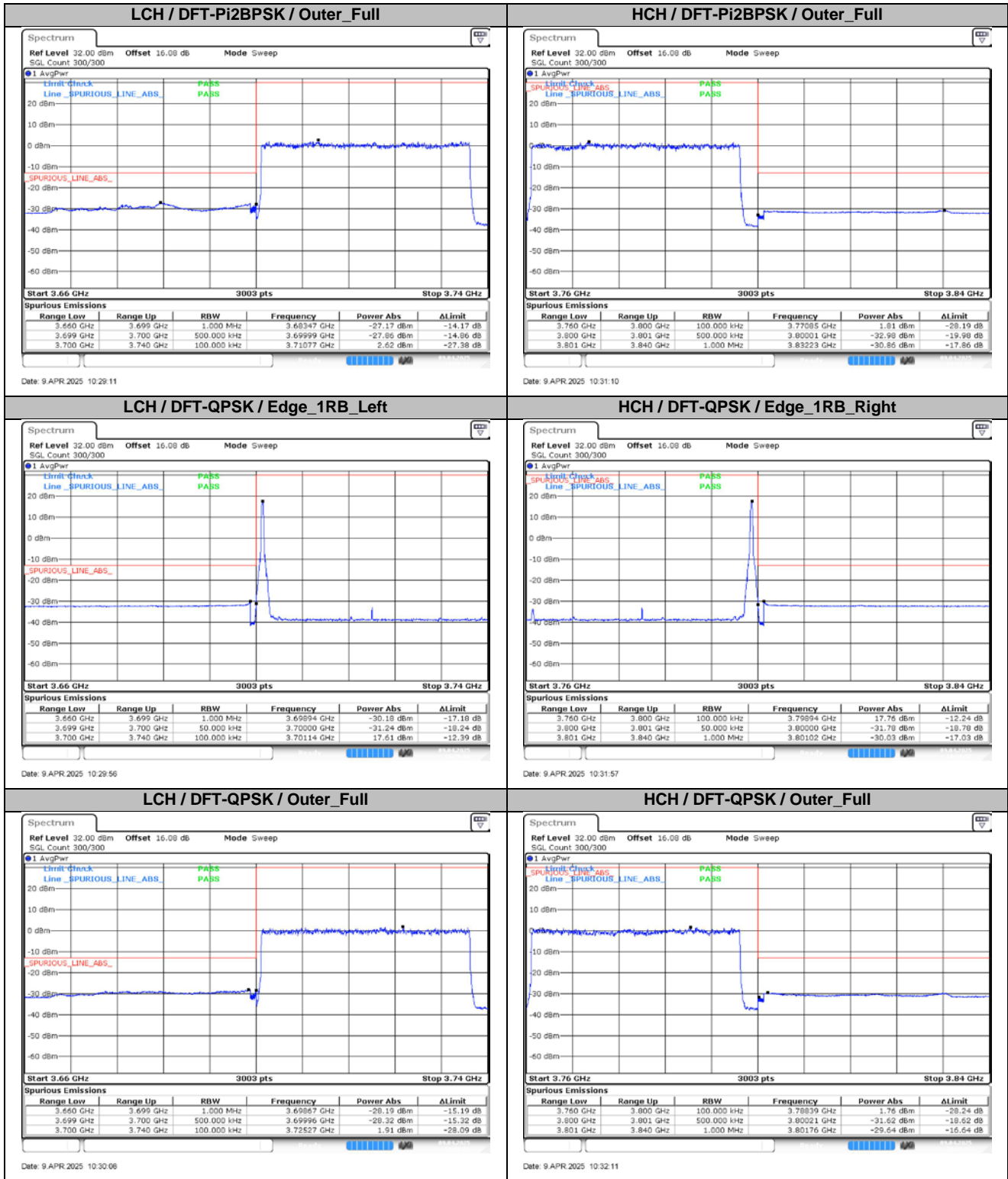
Date: 9 APR 2025 10:23:56





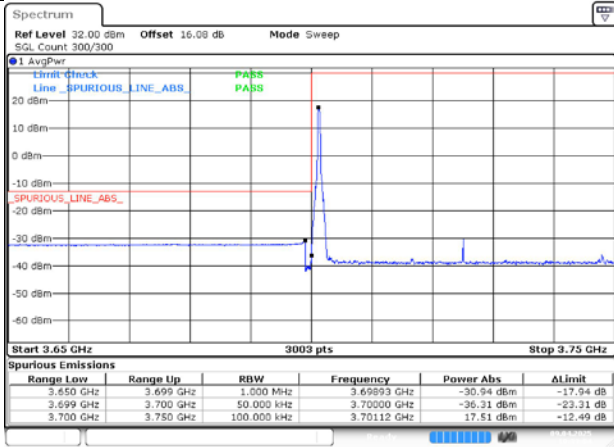
n78A / 30KHz / 40MHz





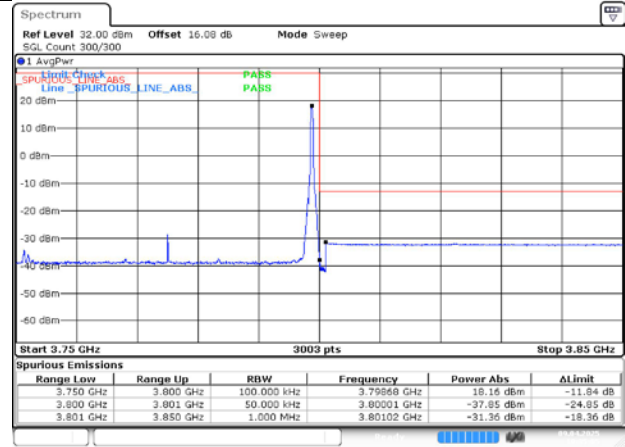
n78A / 30kHz / 50MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



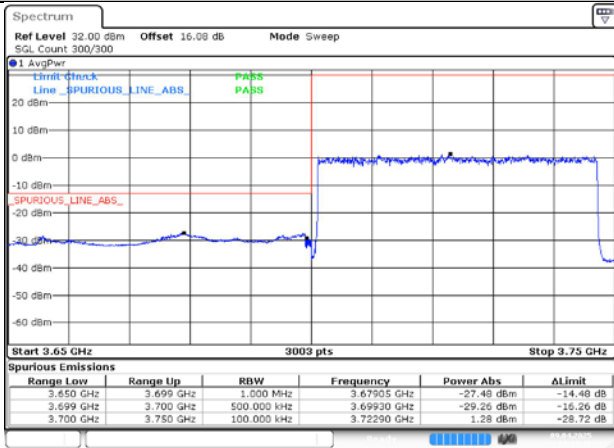
Date: 9 APR 2025 10:33:04

HCH / DFT-Pi2BPSK / Edge_1RB_Right



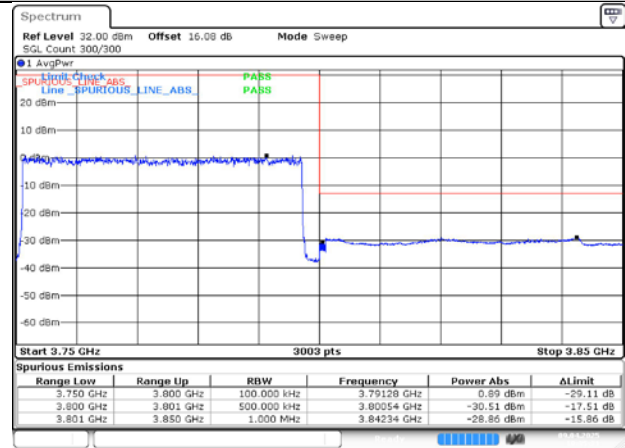
Date: 9 APR 2025 10:35:05

LCH / DFT-Pi2BPSK / Outer_Full



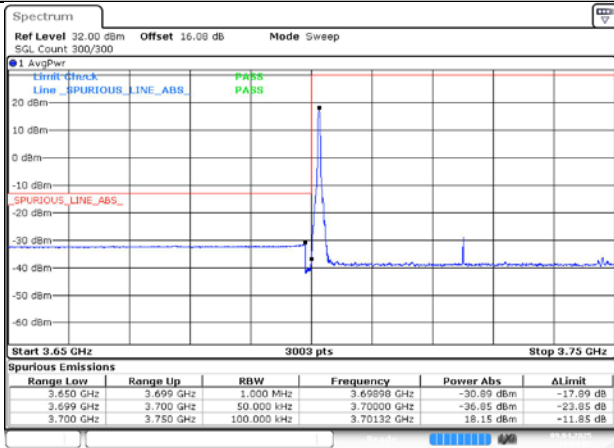
Date: 9 APR 2025 10:33:16

HCH / DFT-Pi2BPSK / Outer_Full



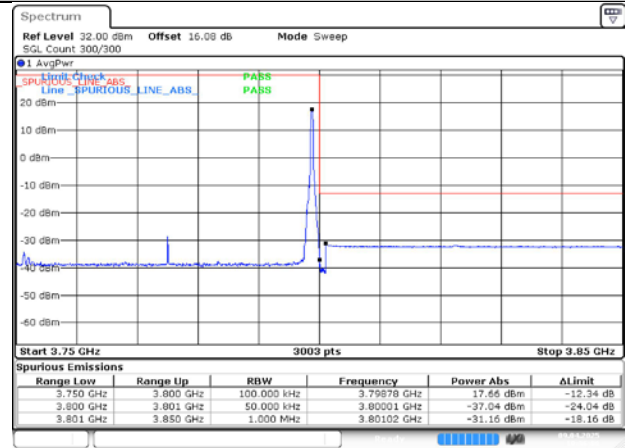
Date: 9 APR 2025 10:35:20

LCH / DFT-QPSK / Edge_1RB_Left



Date: 9 APR 2025 10:34:02

HCH / DFT-QPSK / Edge_1RB_Right



Date: 9 APR 2025 10:36:09