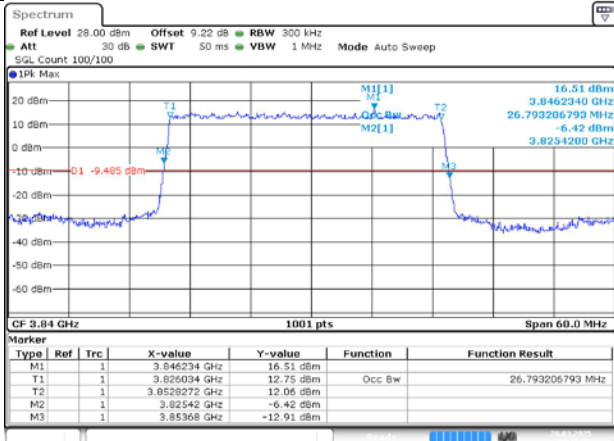


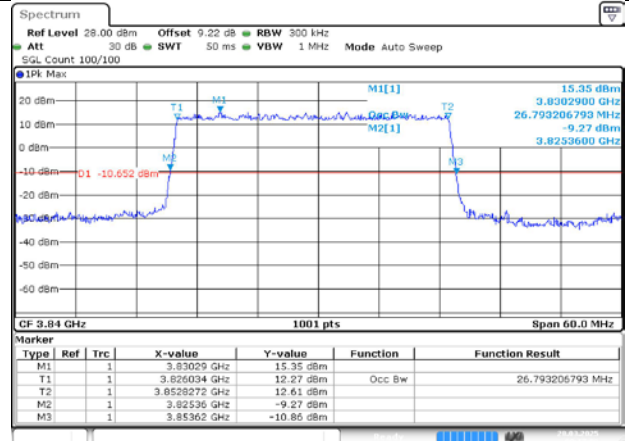
n77A / 30KHz / 30MHz

MCH / DFT-Pi2BPSK / Outer\_Full



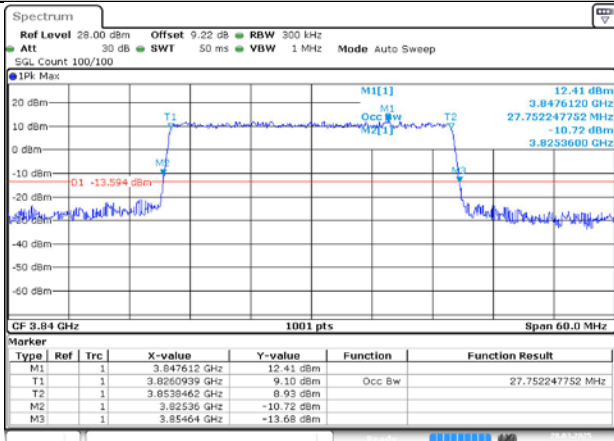
Date: 28.MAR.2025 00:16:43

MCH / DFT-QPSK / Outer\_Full



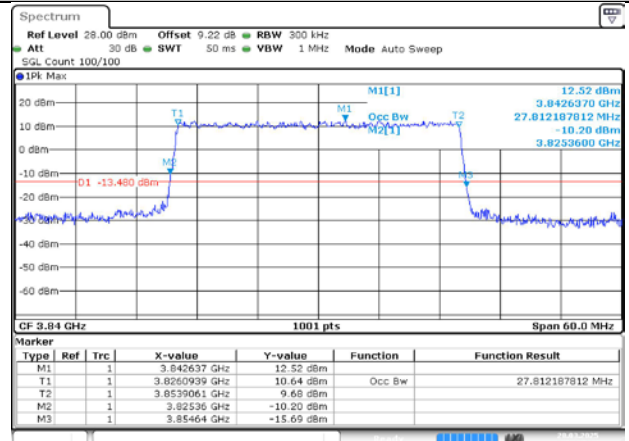
Date: 28.MAR.2025 00:17:02

MCH / CP-QPSK / Outer\_Full



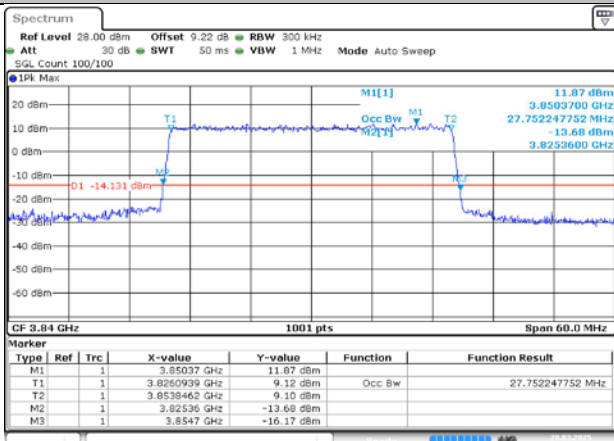
Date: 28.MAR.2025 00:17:39

MCH / CP-16QAM / Outer\_Full



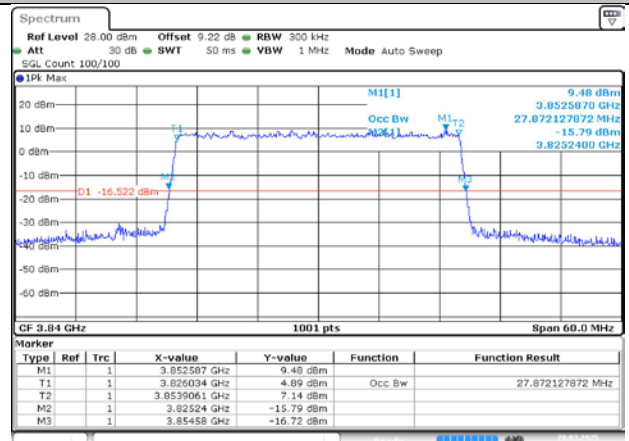
Date: 28.MAR.2025 00:17:47

MCH / CP-64QAM / Outer\_Full

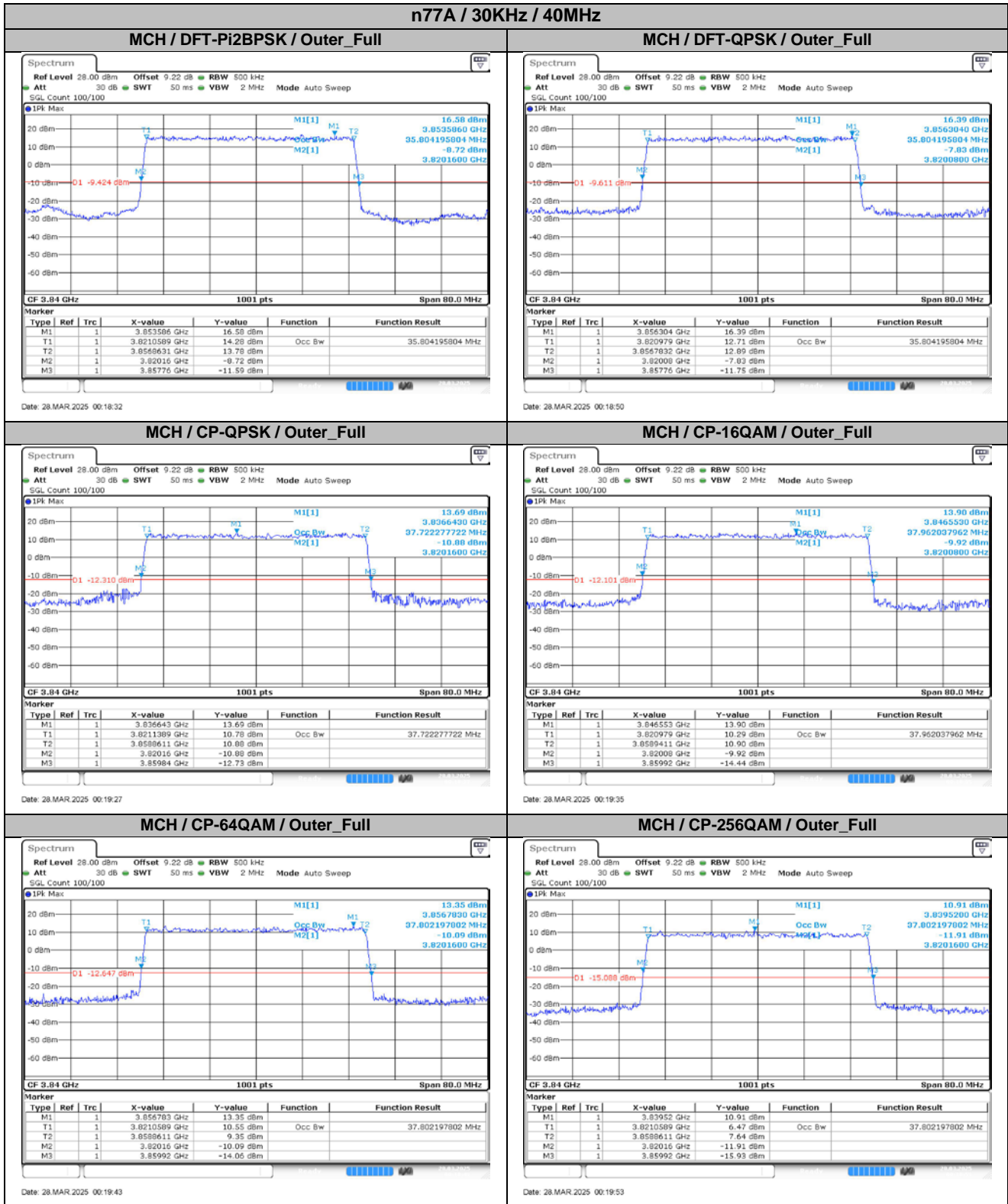


Date: 28.MAR.2025 00:17:54

MCH / CP-256QAM / Outer\_Full

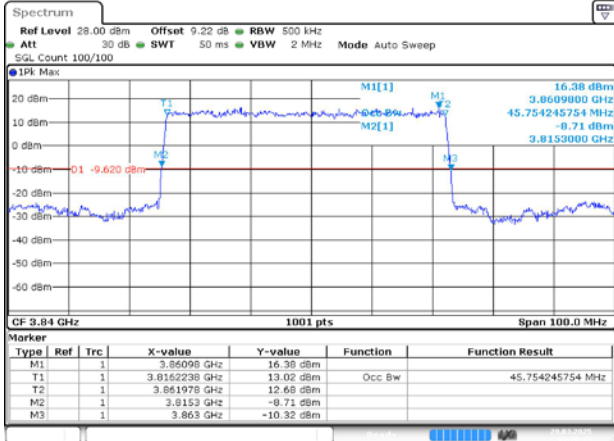


Date: 28.MAR.2025 00:18:04



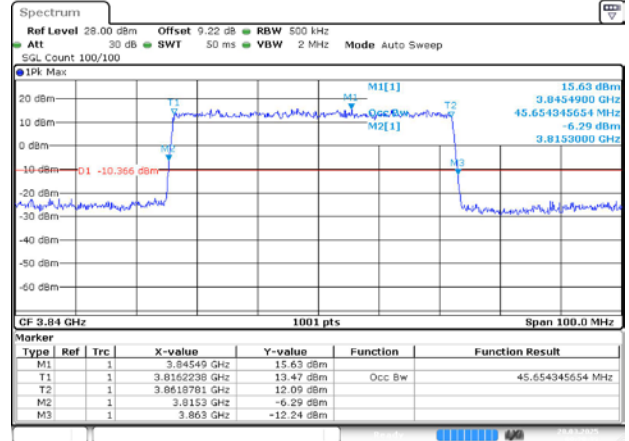
n77A / 30kHz / 50MHz

MCH / DFT-Pi2BPSK / Outer\_Full



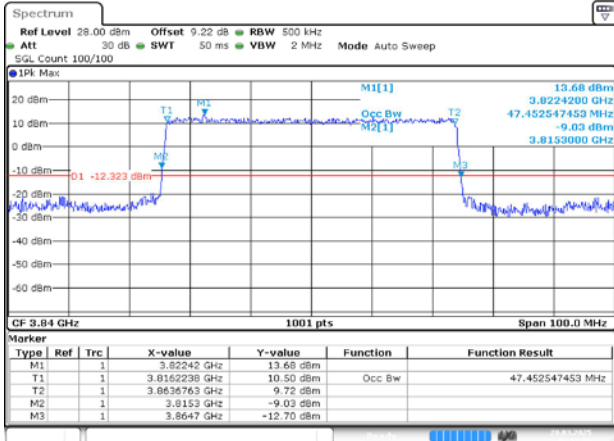
Date: 28.MAR.2025 00:20:15

MCH / DFT-QPSK / Outer\_Full



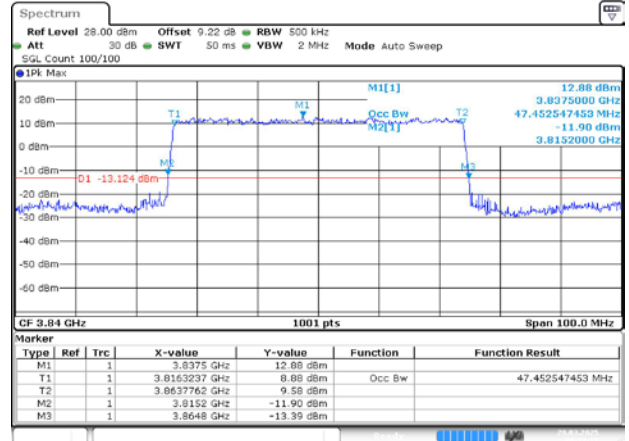
Date: 28.MAR.2025 00:20:33

MCH / CP-QPSK / Outer\_Full



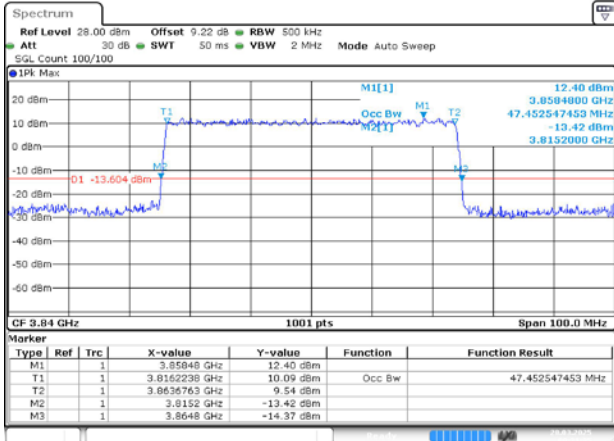
Date: 28.MAR.2025 00:21:09

MCH / CP-16QAM / Outer\_Full



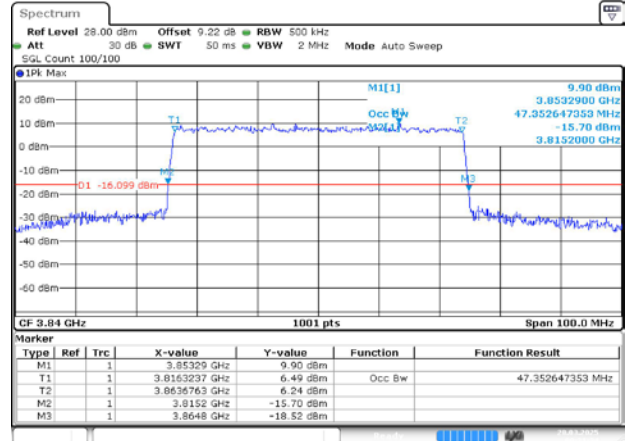
Date: 28.MAR.2025 00:21:17

MCH / CP-64QAM / Outer\_Full

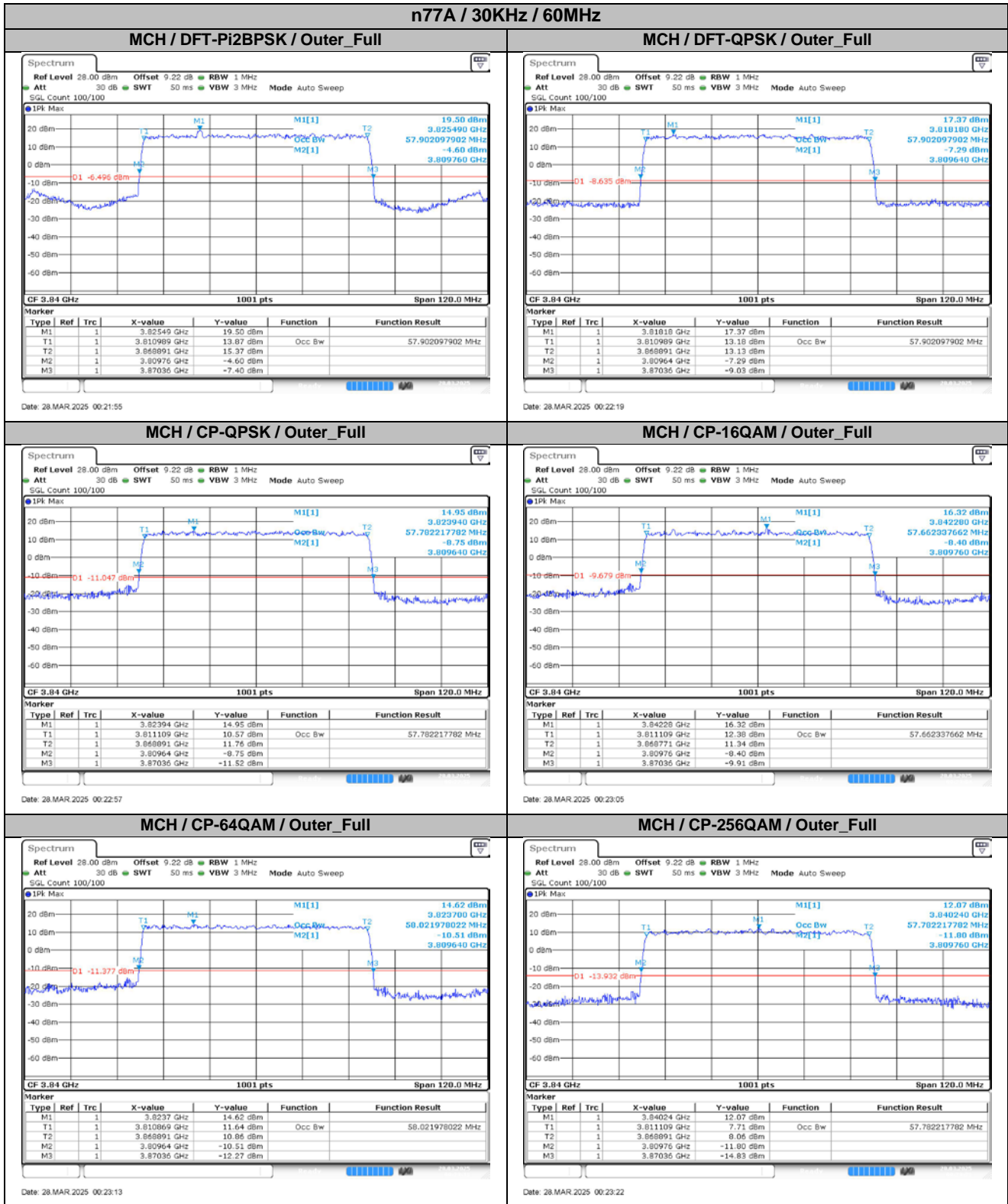


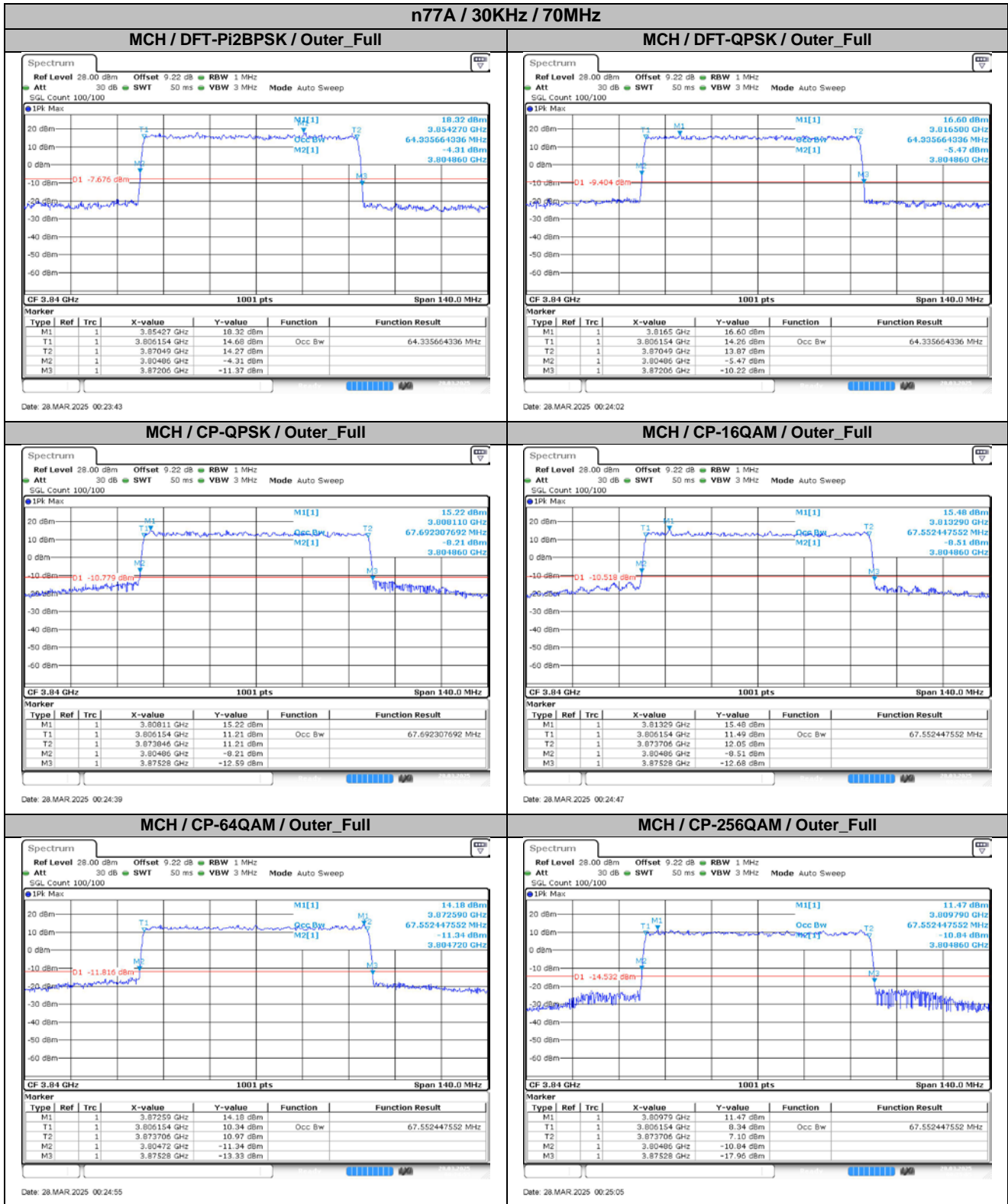
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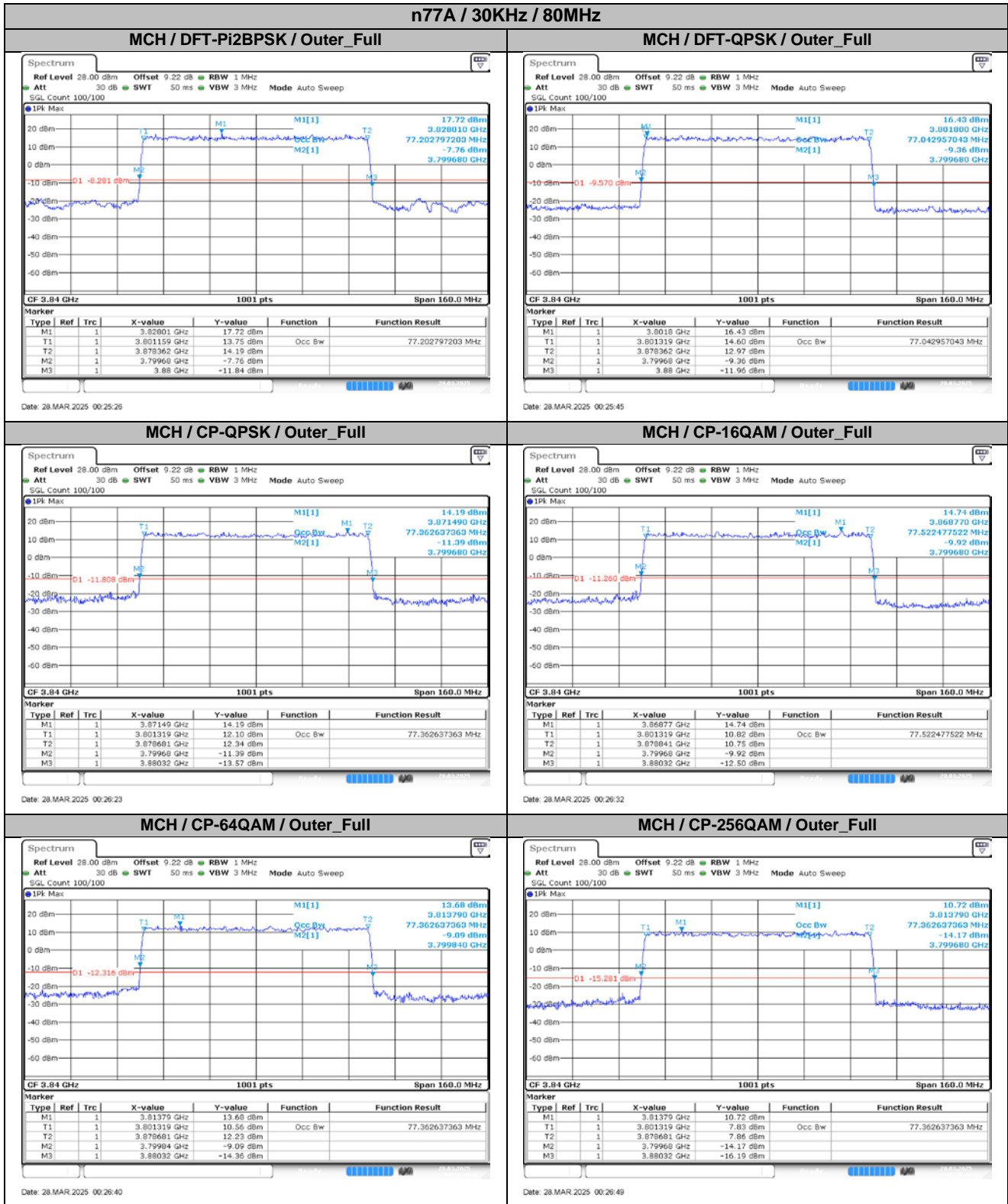
MCH / CP-256QAM / Outer\_Full



Date: 28.MAR.2025 00:21:34

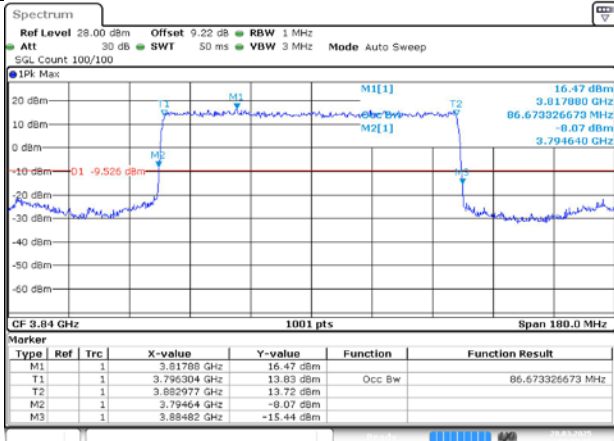






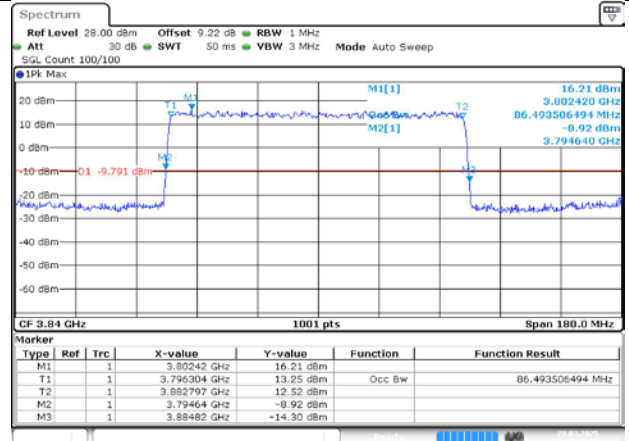
n77A / 30KHz / 90MHz

MCH / DFT-Pi2BPSK / Outer\_Full



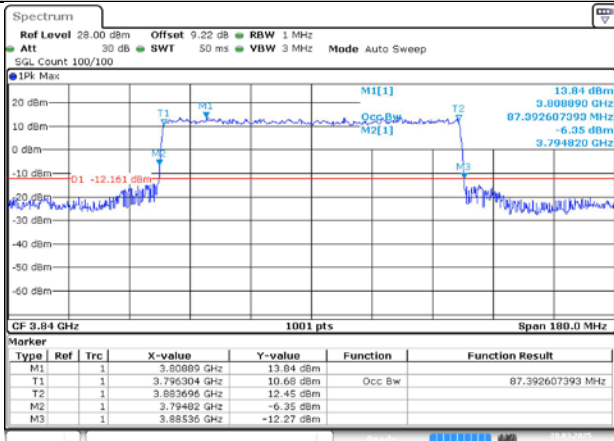
Date: 28.MAR.2025 00:27:16

MCH / DFT-QPSK / Outer\_Full



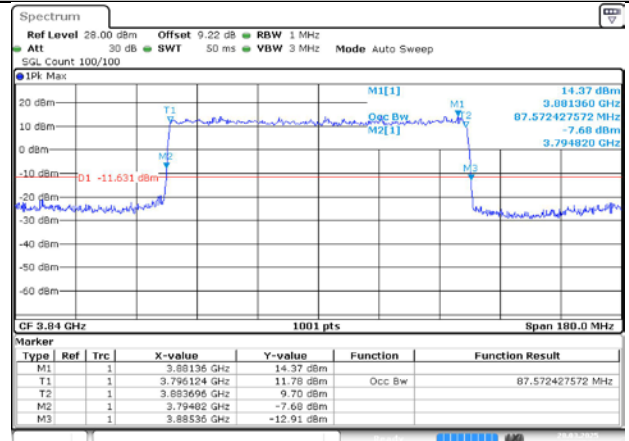
Date: 28.MAR.2025 00:27:34

MCH / CP-QPSK / Outer\_Full



Date: 28.MAR.2025 00:28:11

MCH / CP-16QAM / Outer\_Full



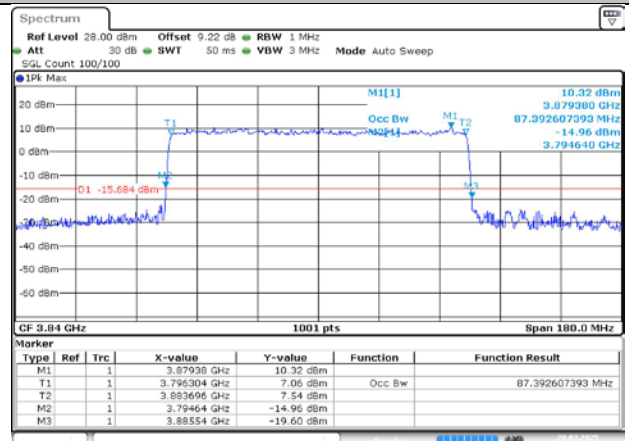
Date: 28.MAR.2025 00:28:20

MCH / CP-64QAM / Outer\_Full



Date: 28.MAR.2025 00:28:29

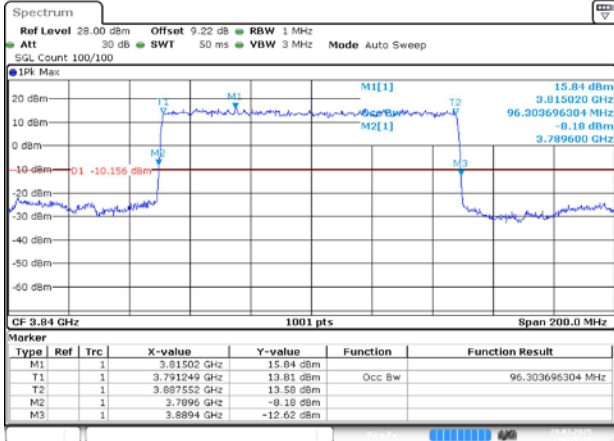
MCH / CP-256QAM / Outer\_Full



Date: 28.MAR.2025 00:28:38

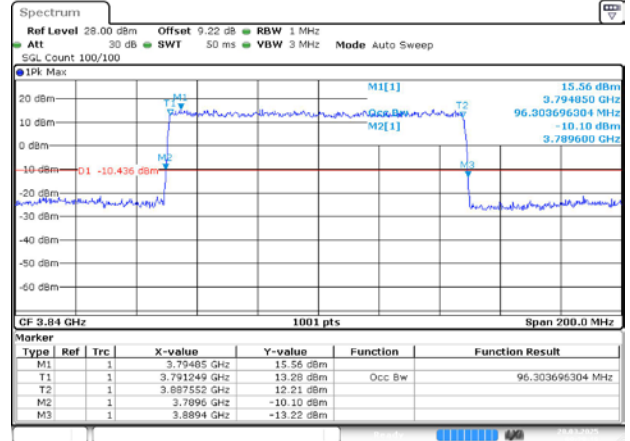
n77A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer\_Full



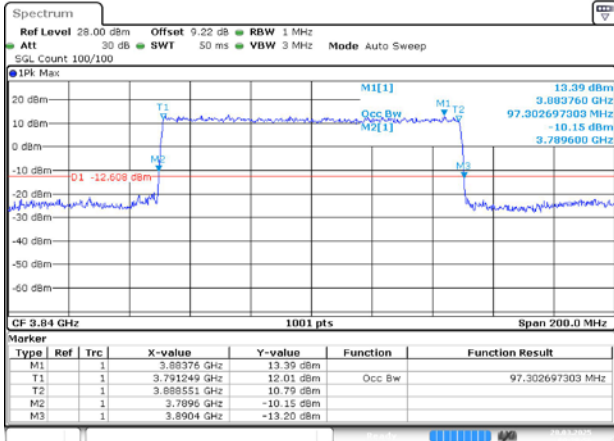
Date: 28.MAR.2025 00:29:00

MCH / DFT-QPSK / Outer\_Full



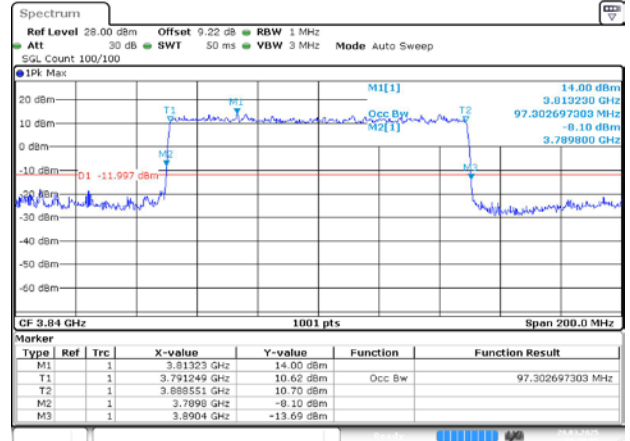
Date: 28.MAR.2025 00:29:18

MCH / CP-QPSK / Outer\_Full



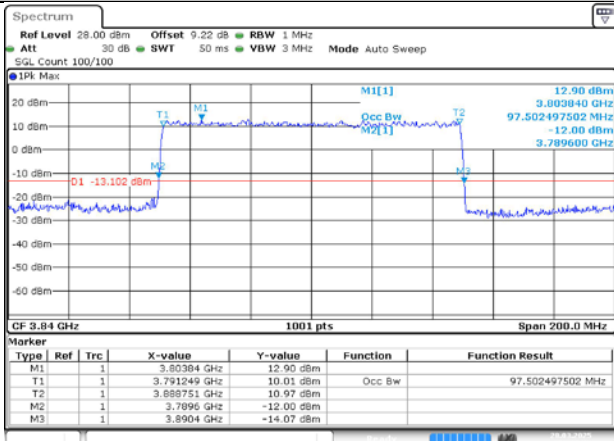
Date: 28.MAR.2025 00:29:56

MCH / CP-16QAM / Outer\_Full



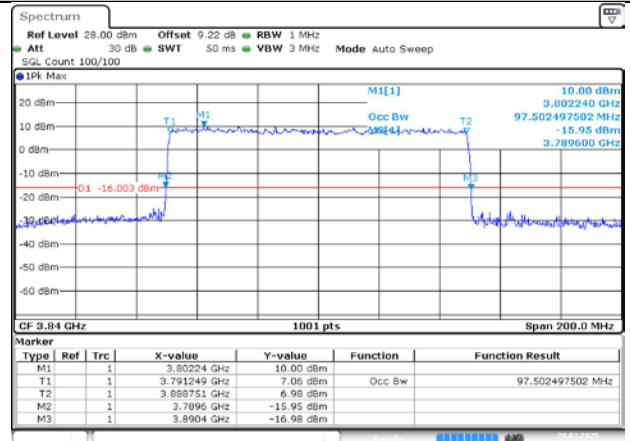
Date: 28.MAR.2025 00:30:05

MCH / CP-64QAM / Outer\_Full



Date: 28.MAR.2025 00:30:13

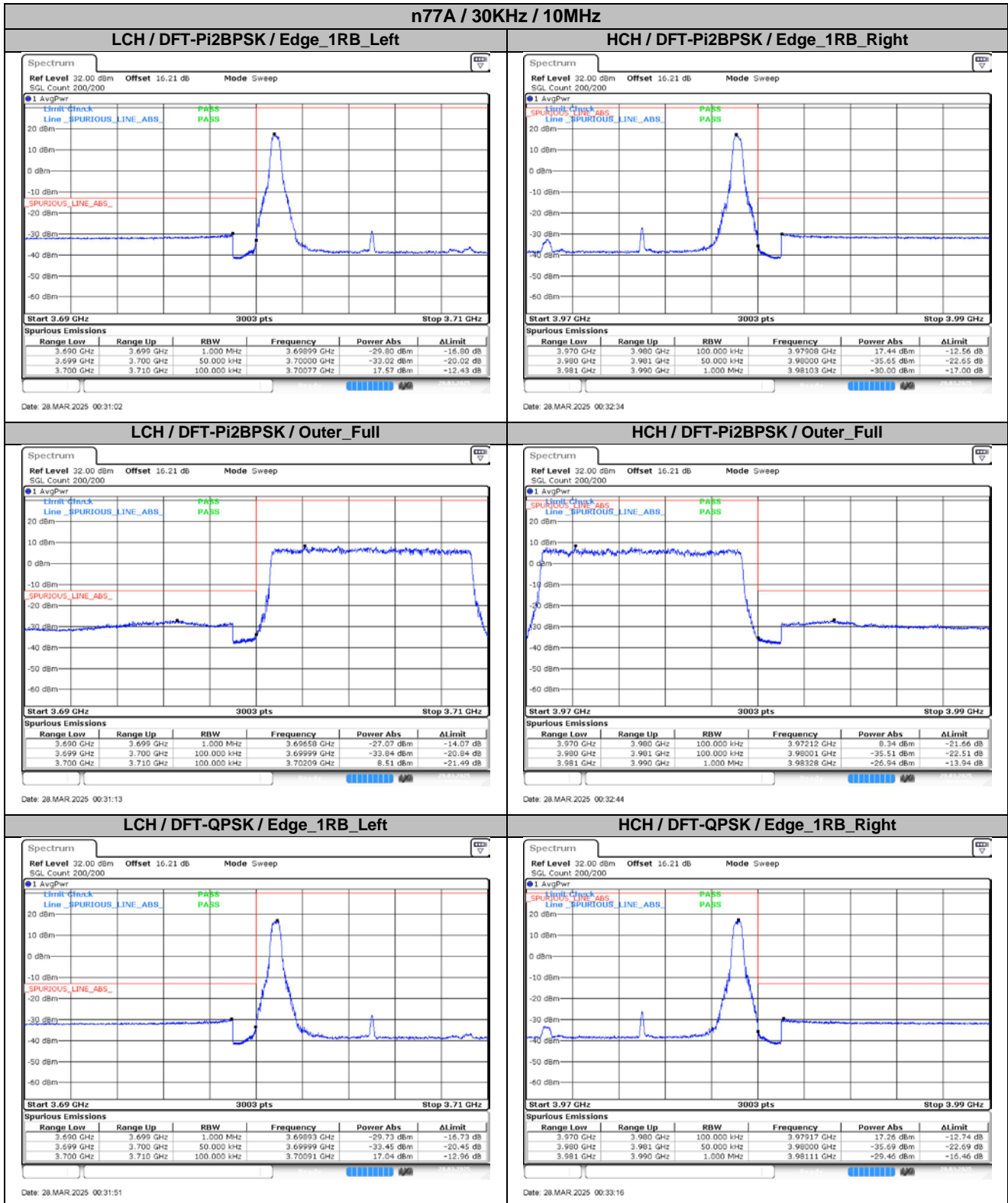
MCH / CP-256QAM / Outer\_Full

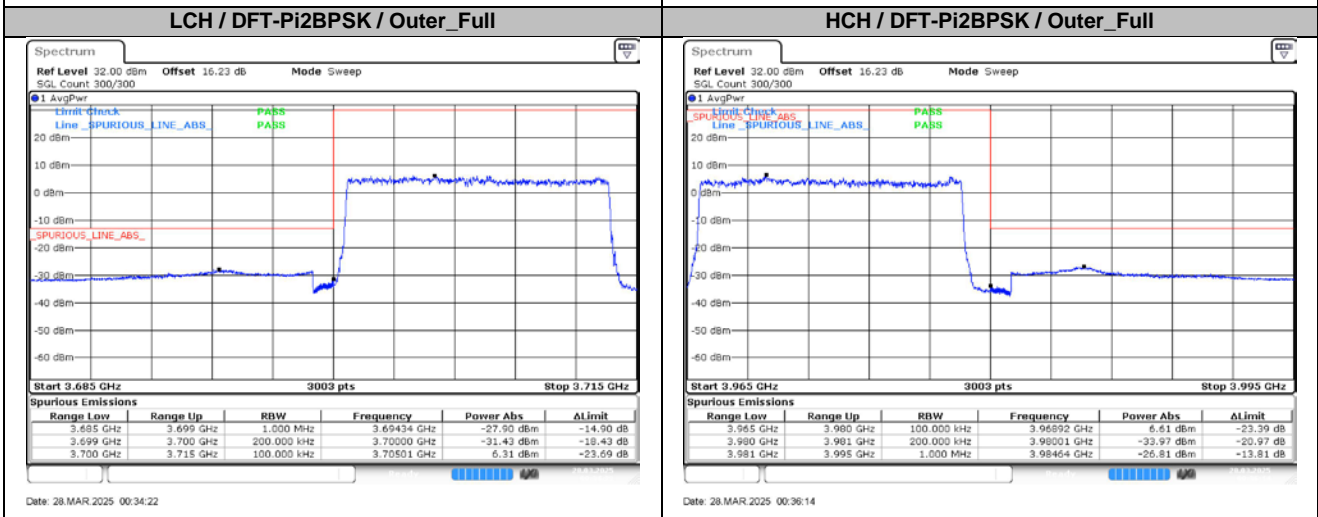
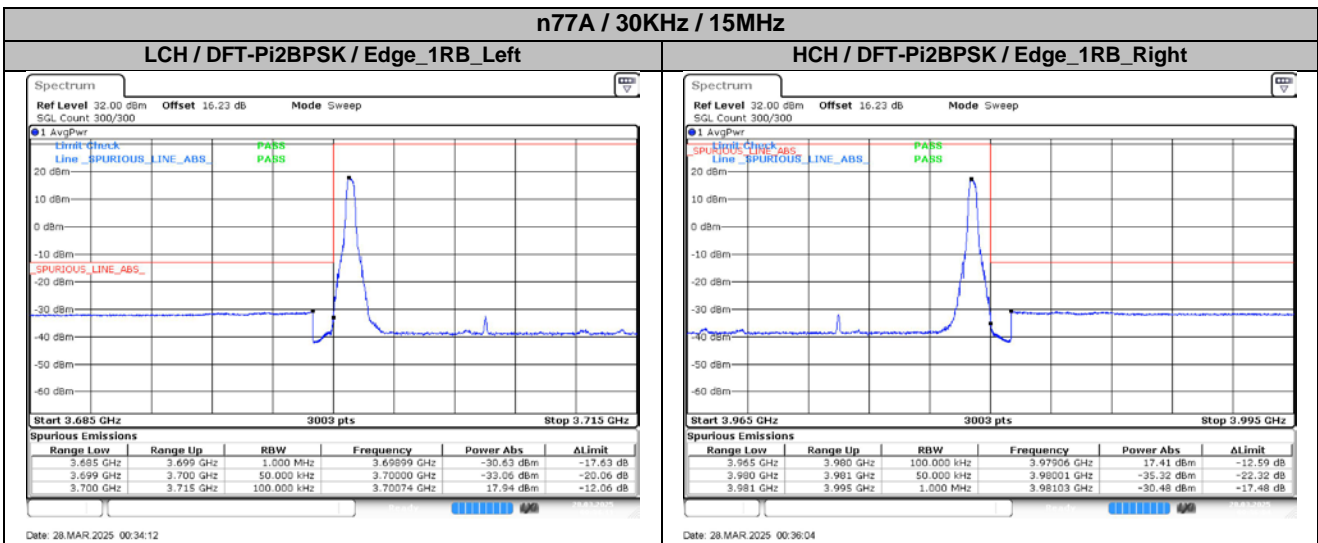
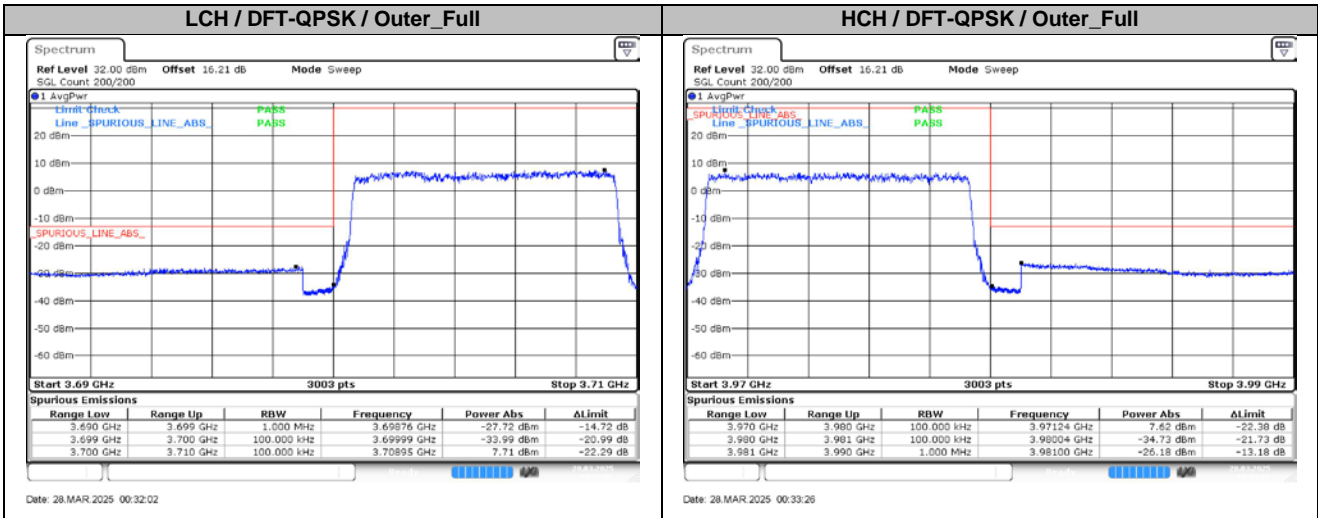


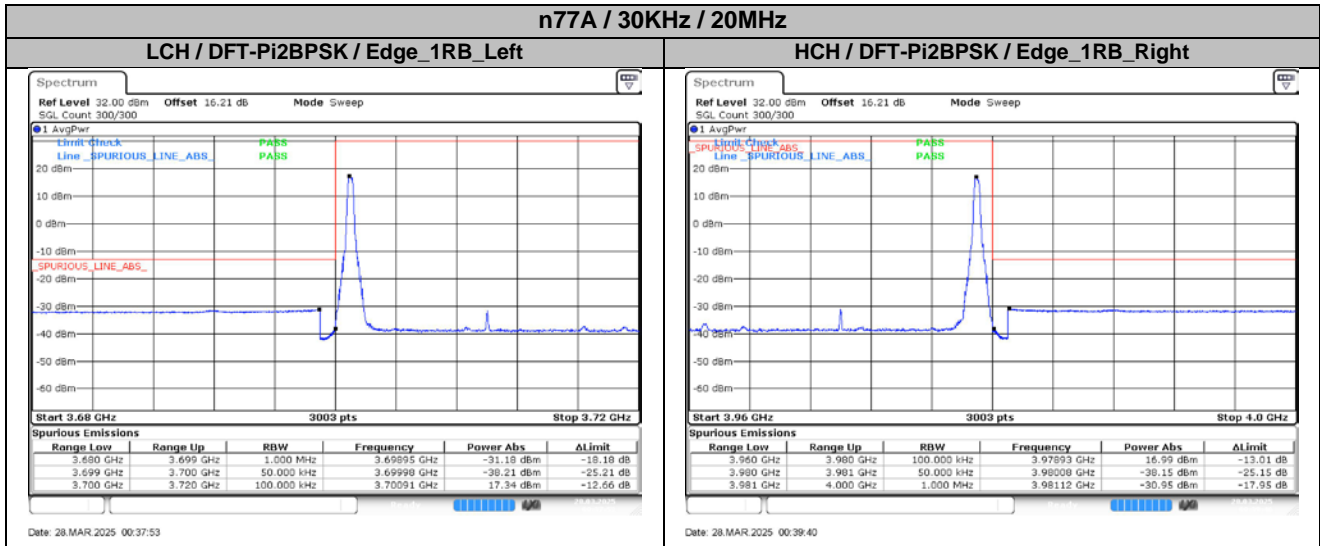
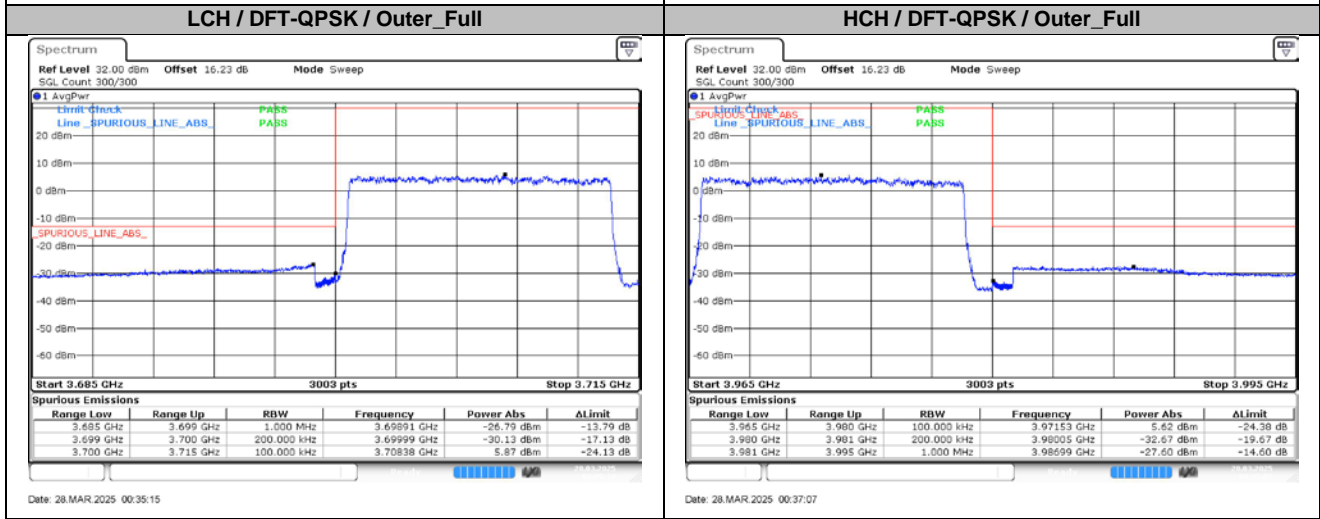
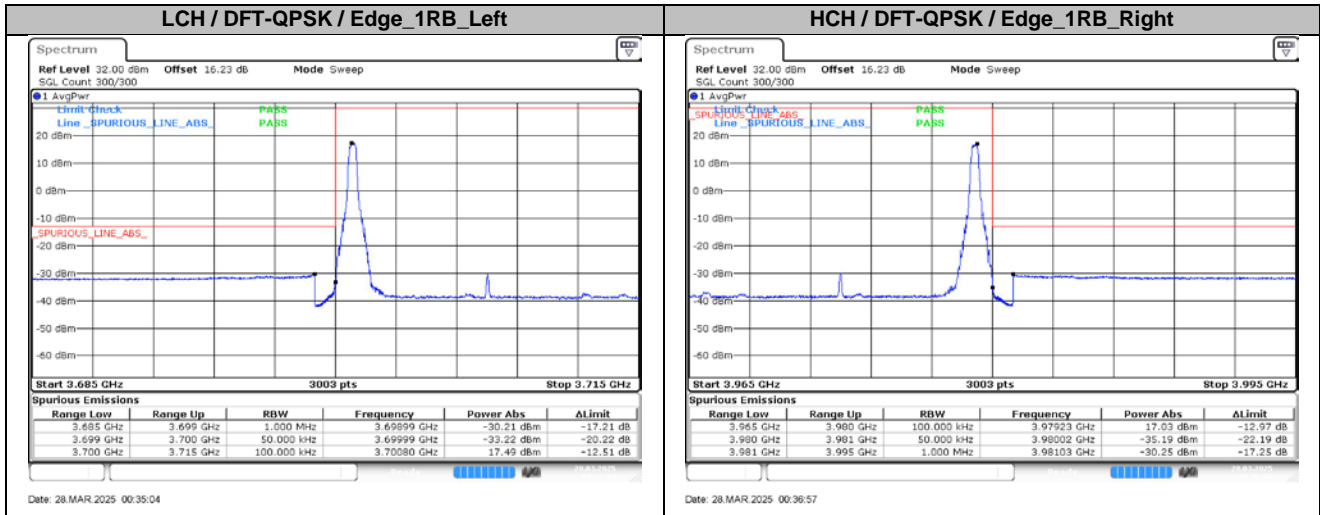
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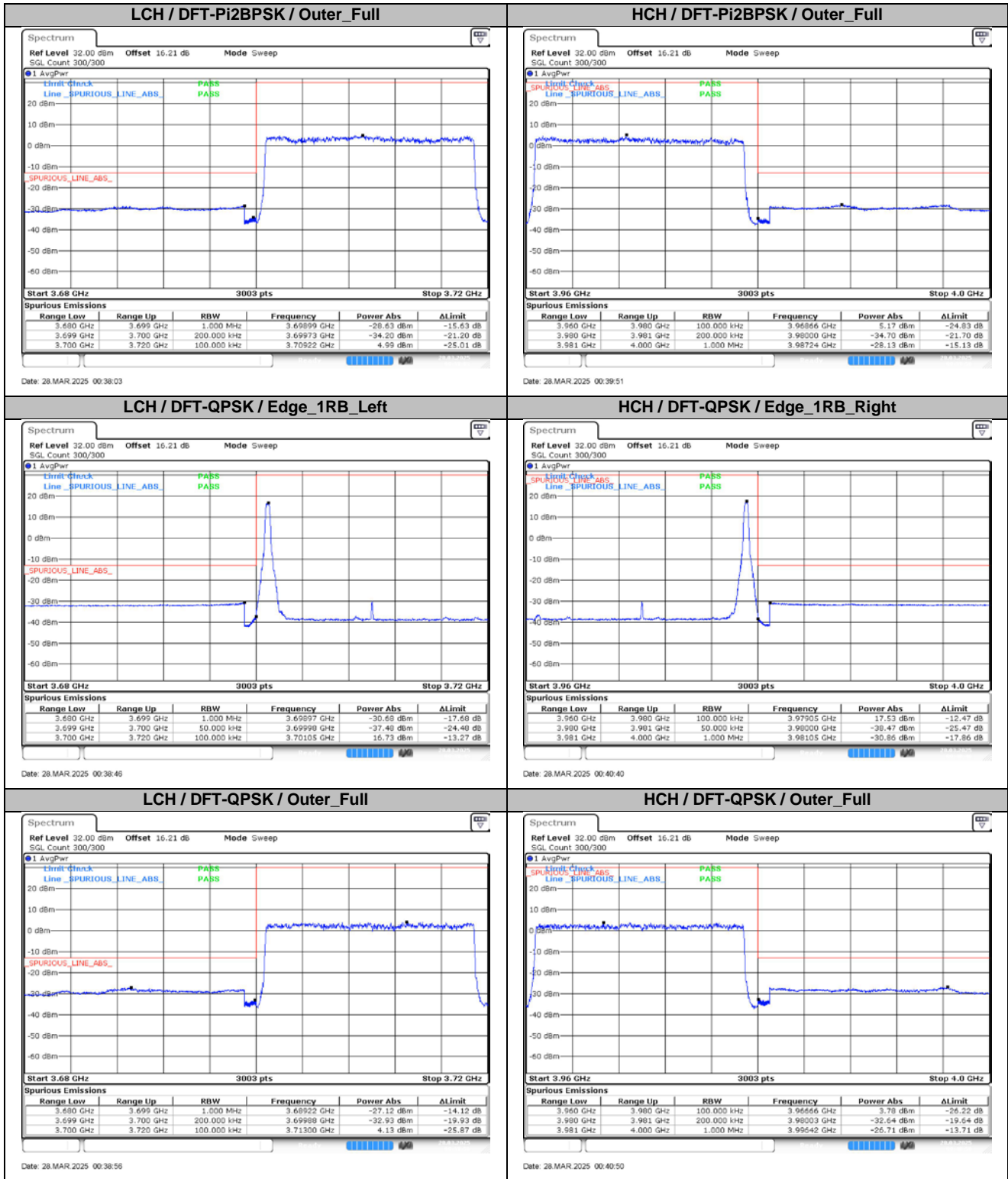
### 5. Conducted Band Edges

#### 5.1. Test Plots for SCS=30KHz



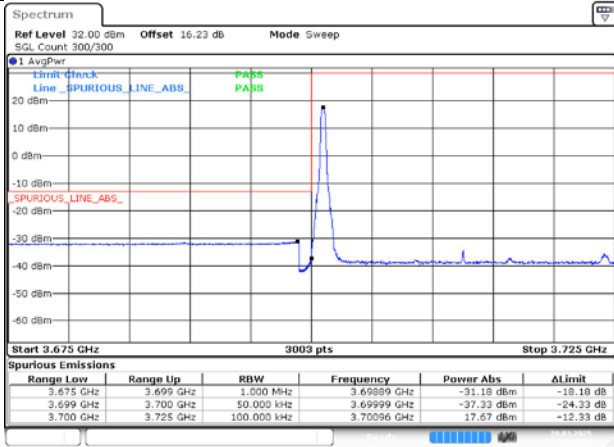






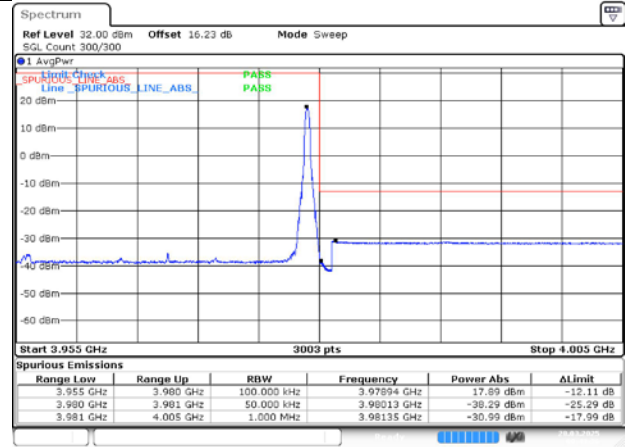
n77A / 30KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



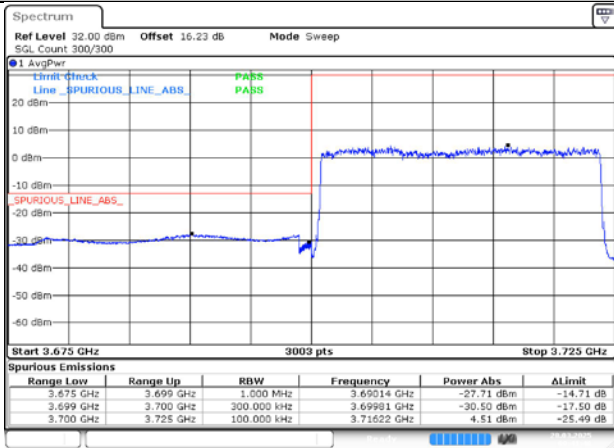
Date: 28.MAR.2025 00:41:37

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



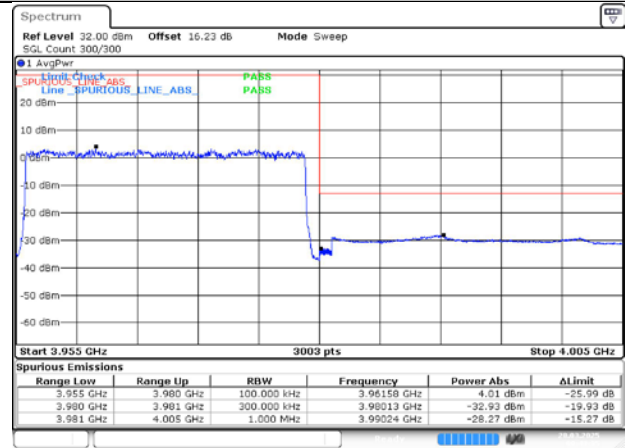
Date: 28.MAR.2025 00:43:26

LCH / DFT-Pi2BPSK / Outer\_Full



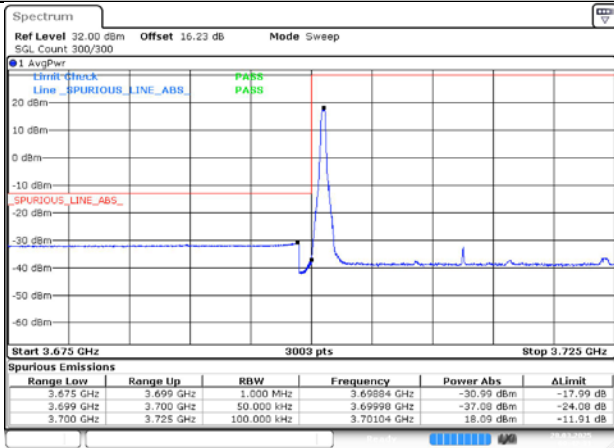
Date: 28.MAR.2025 00:41:47

HCH / DFT-Pi2BPSK / Outer\_Full



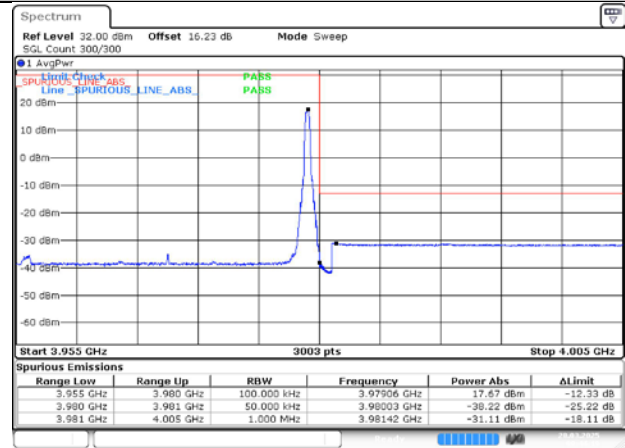
Date: 28.MAR.2025 00:43:36

LCH / DFT-QPSK / Edge\_1RB\_Left



Date: 28.MAR.2025 00:42:31

HCH / DFT-QPSK / Edge\_1RB\_Right



Date: 28.MAR.2025 00:44:19

