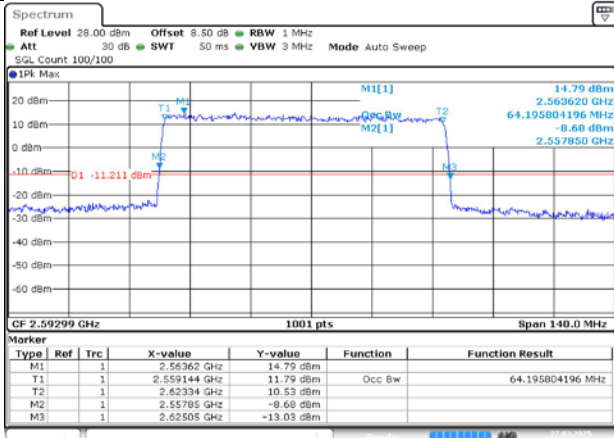


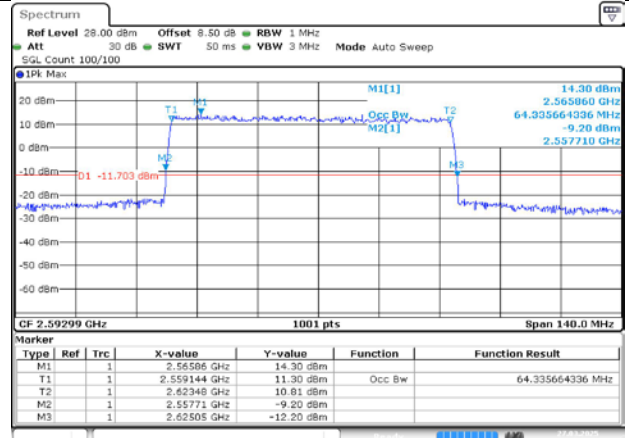
n41A / 30KHz / 70MHz

MCH / DFT-Pi2BPSK / Outer_Full



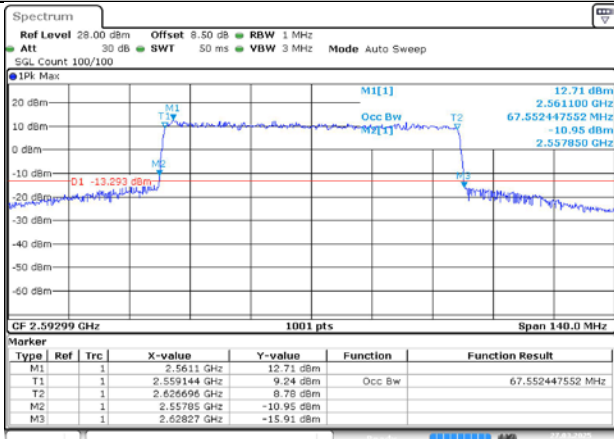
Date: 27.MAR.2025 21:48:19

MCH / DFT-QPSK / Outer_Full



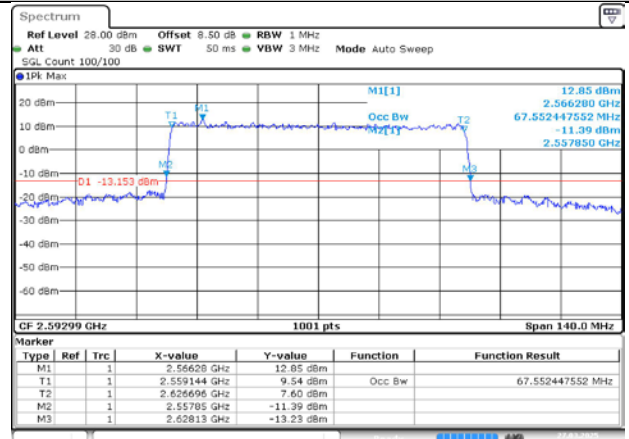
Date: 27.MAR.2025 21:48:47

MCH / CP-QPSK / Outer_Full



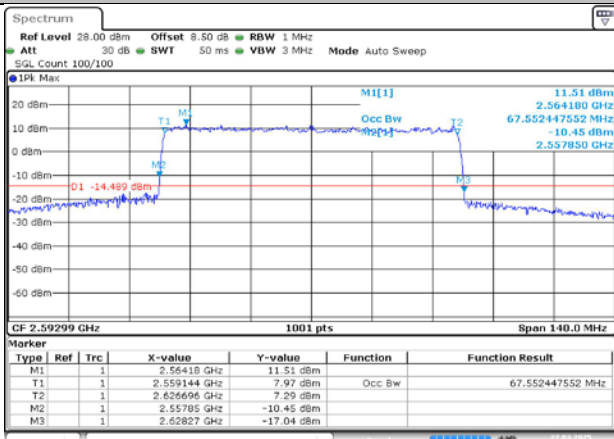
Date: 27.MAR.2025 21:49:24

MCH / CP-16QAM / Outer_Full



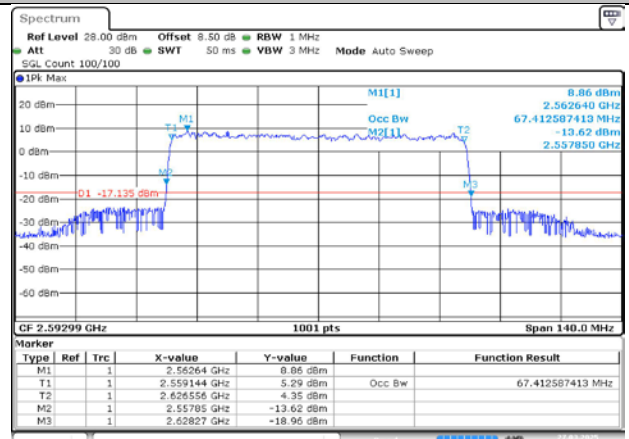
Date: 27.MAR.2025 21:49:32

MCH / CP-64QAM / Outer_Full

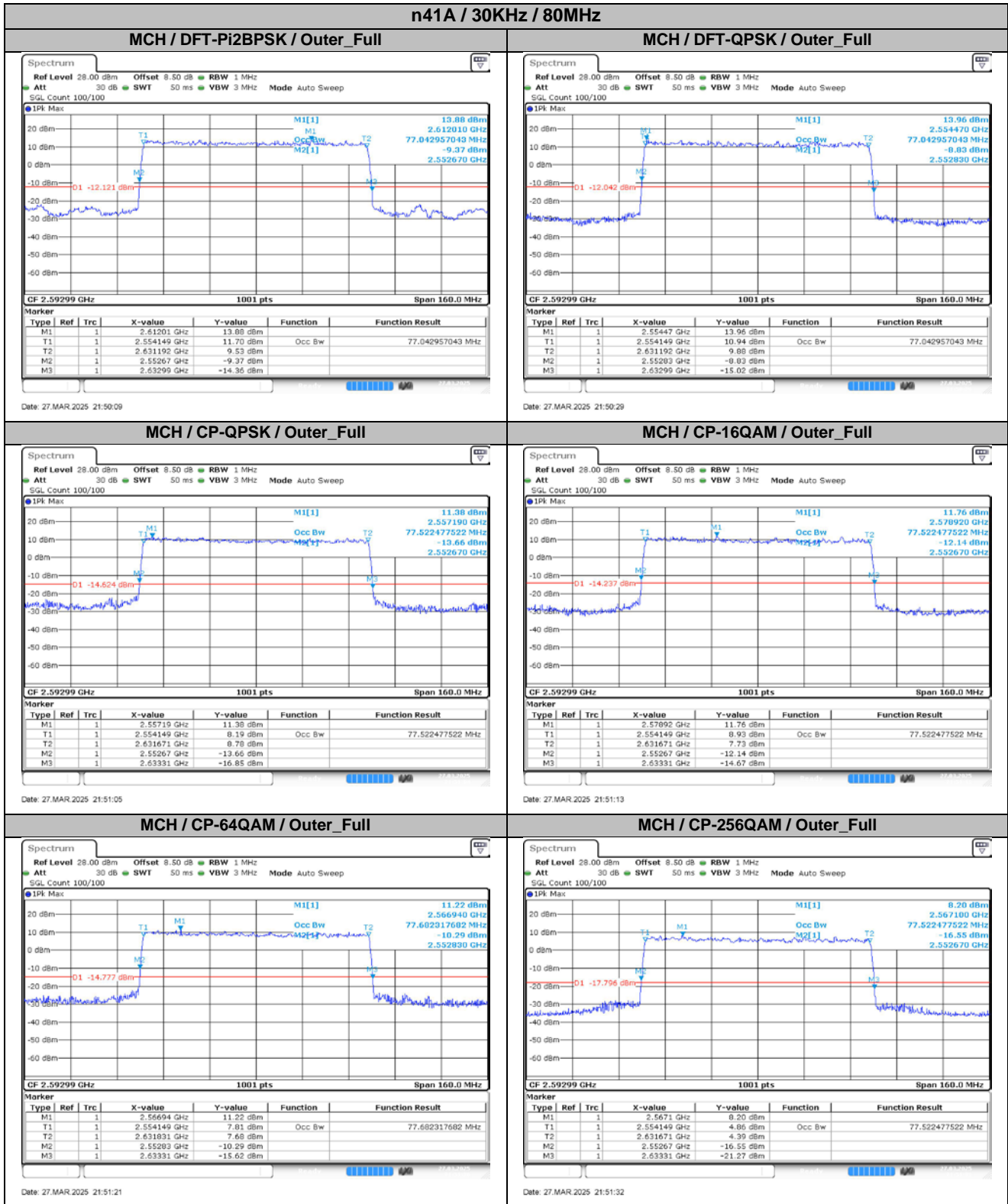


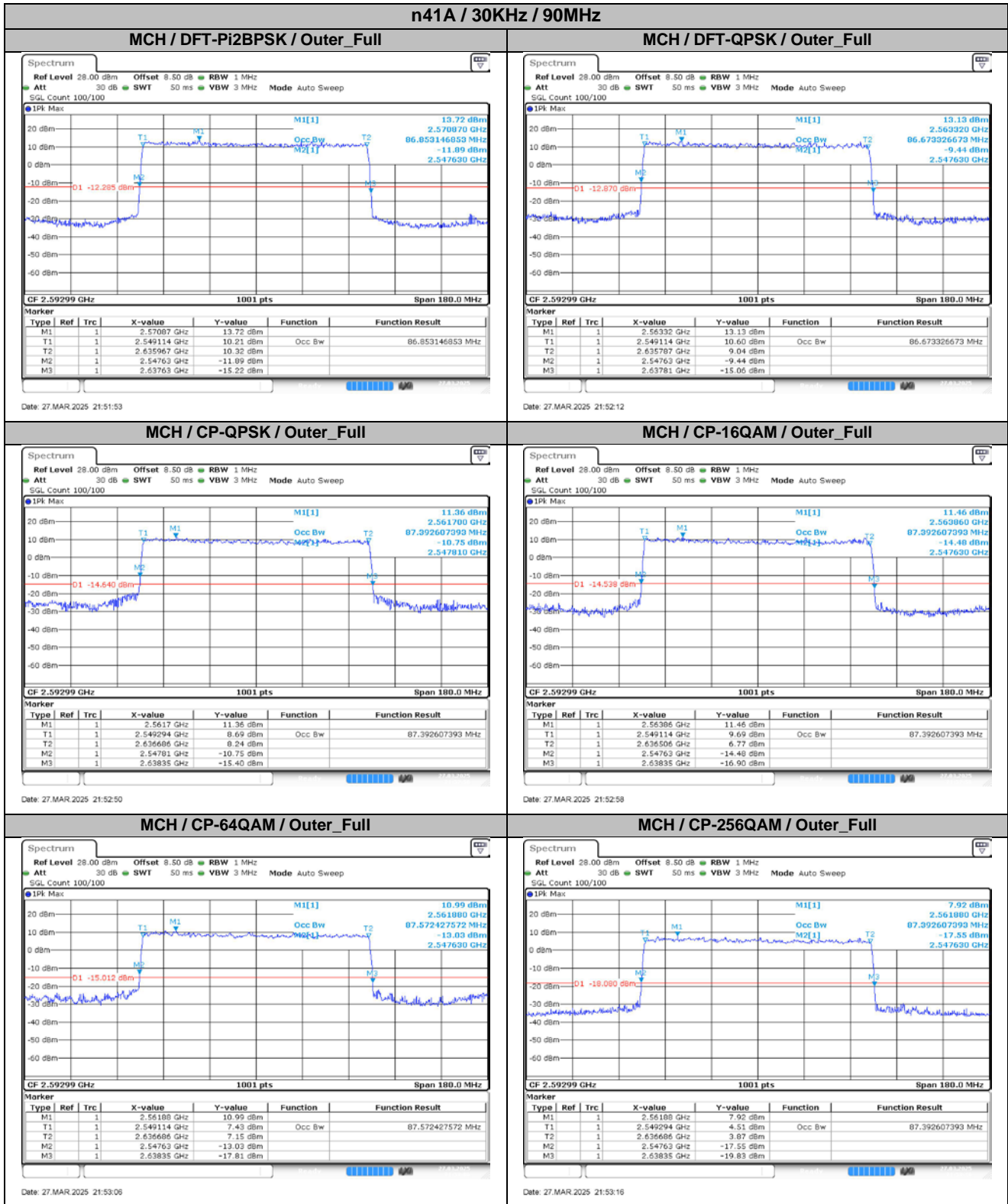
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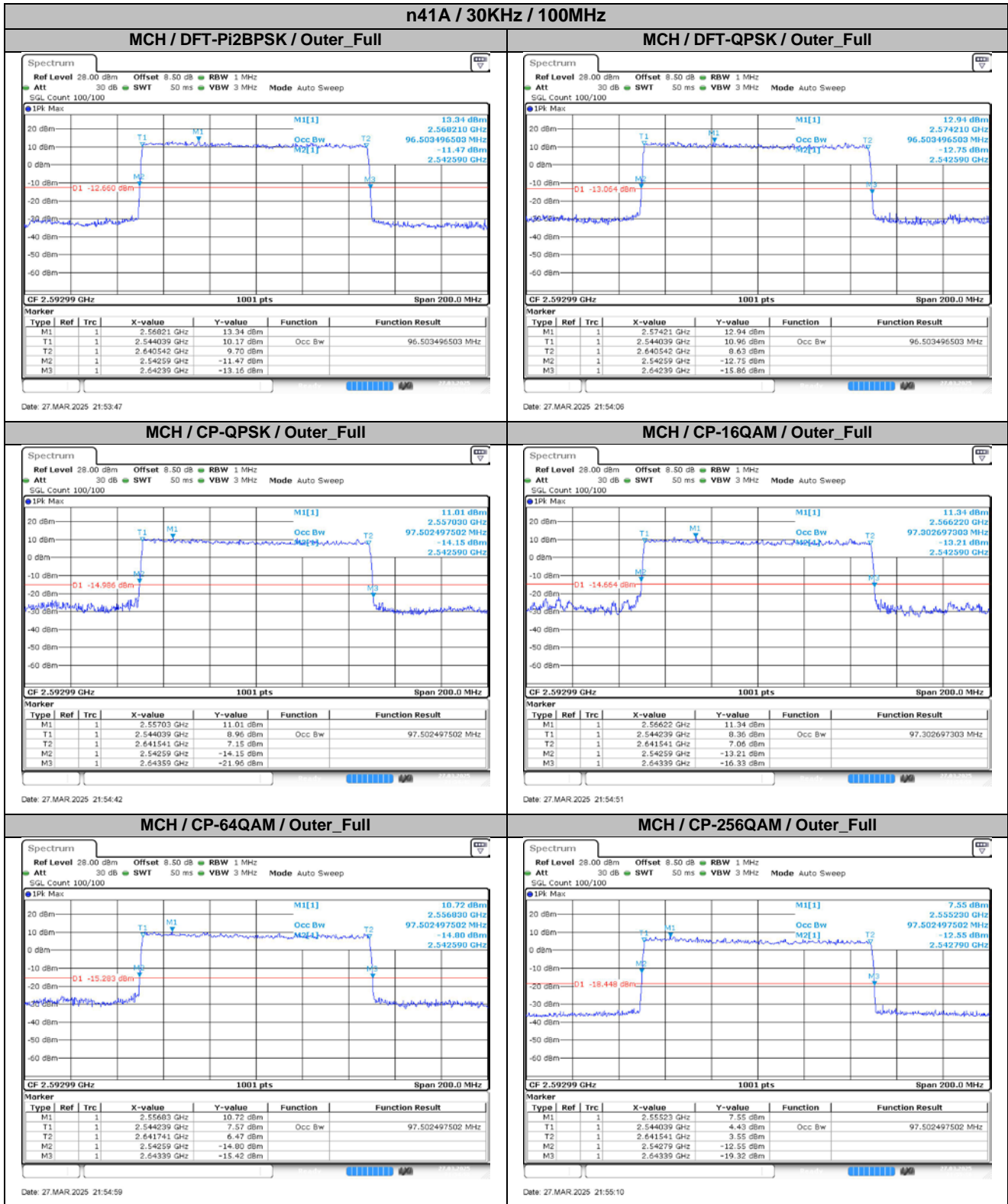
MCH / CP-256QAM / Outer_Full



Date: 27.MAR.2025 21:49:48

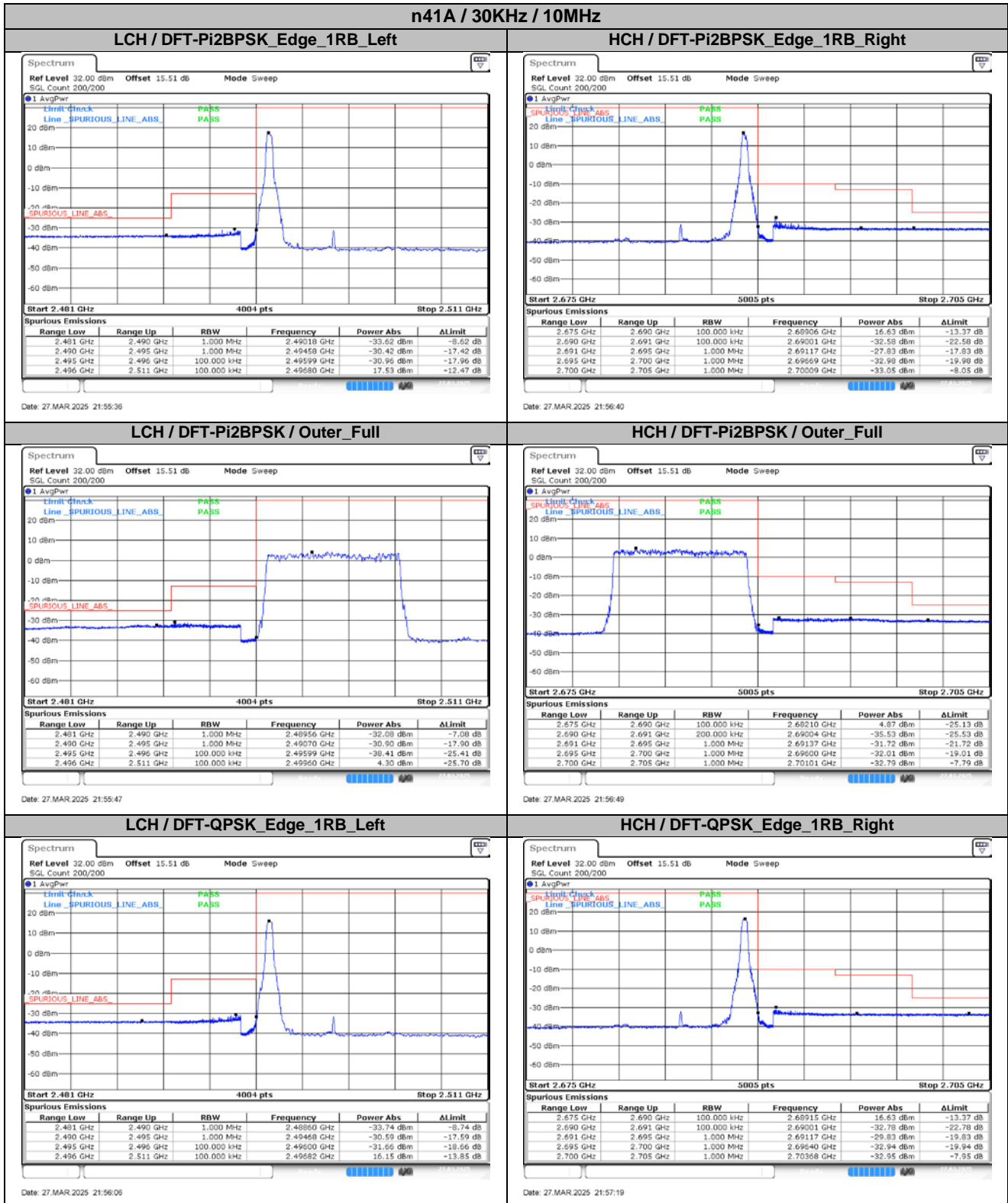


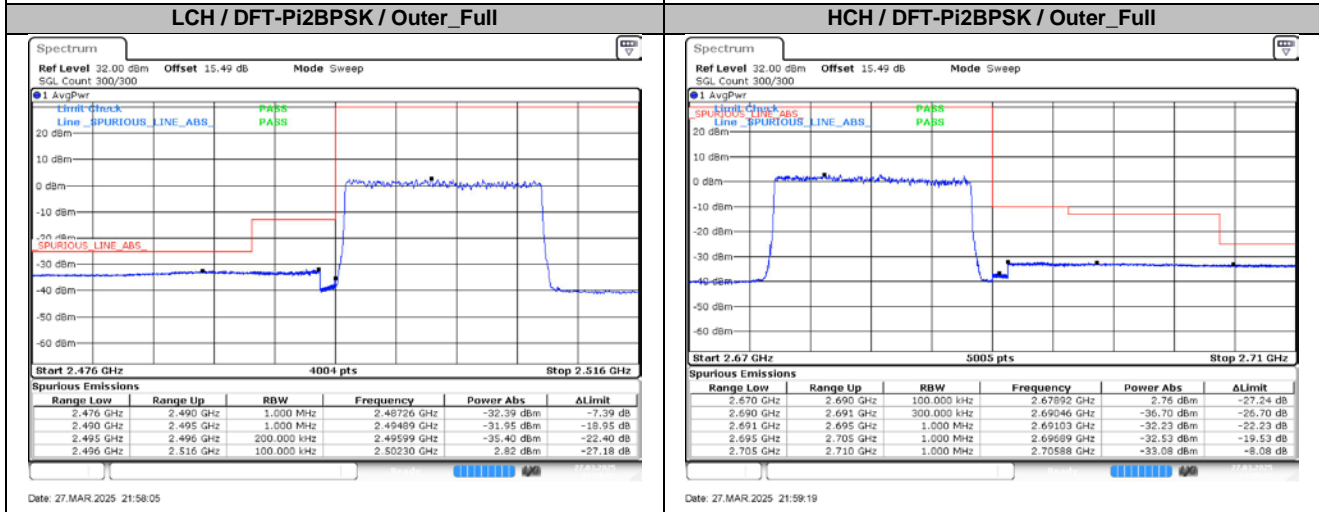
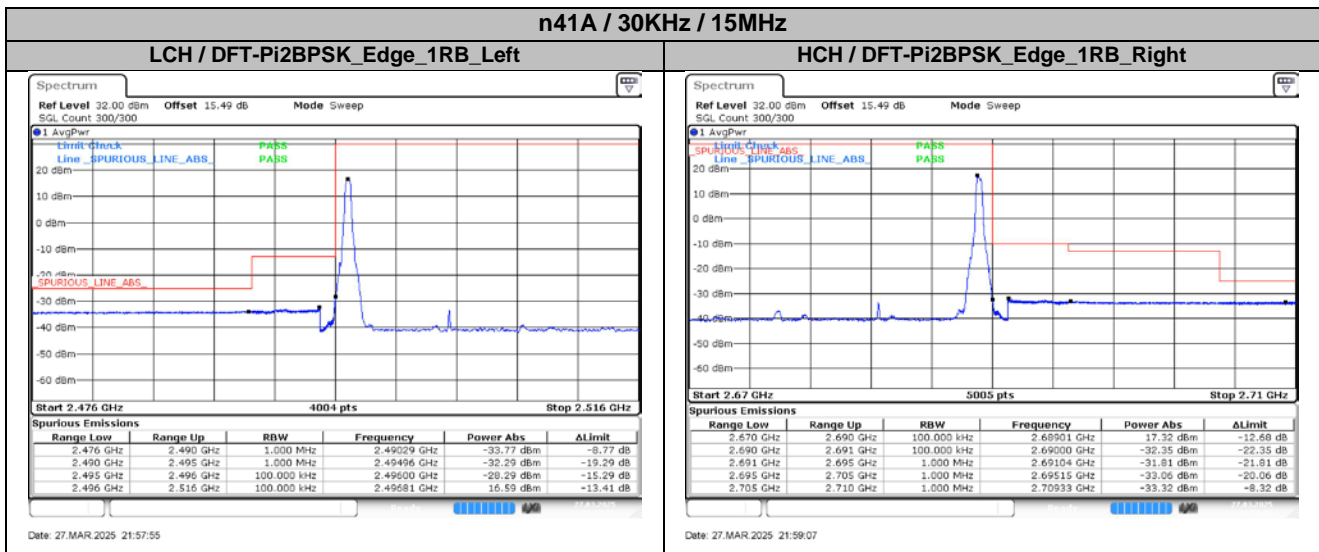
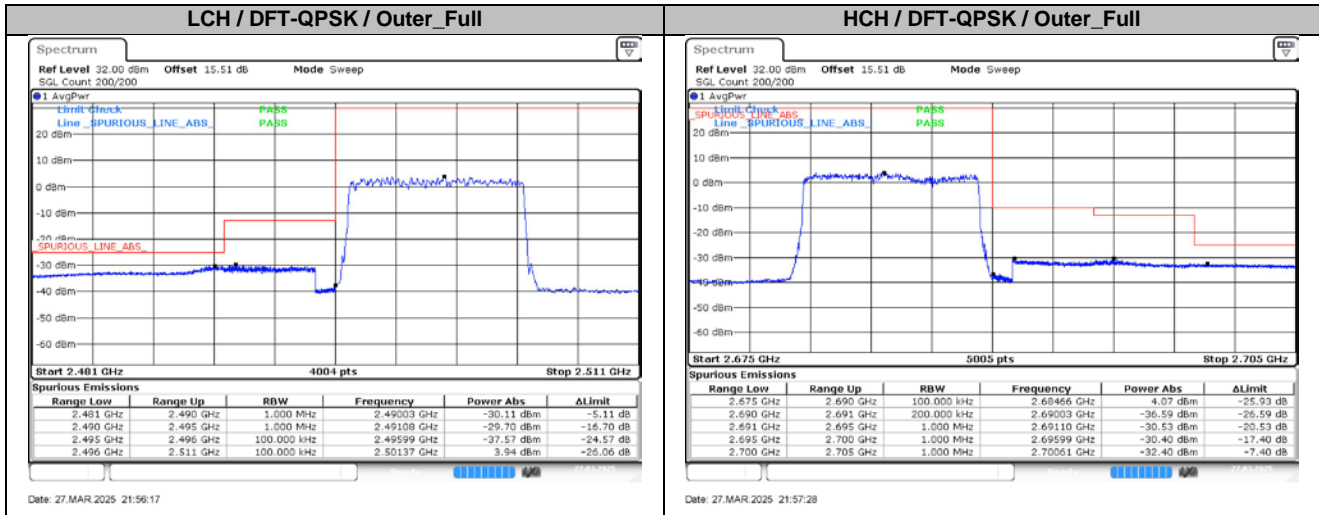


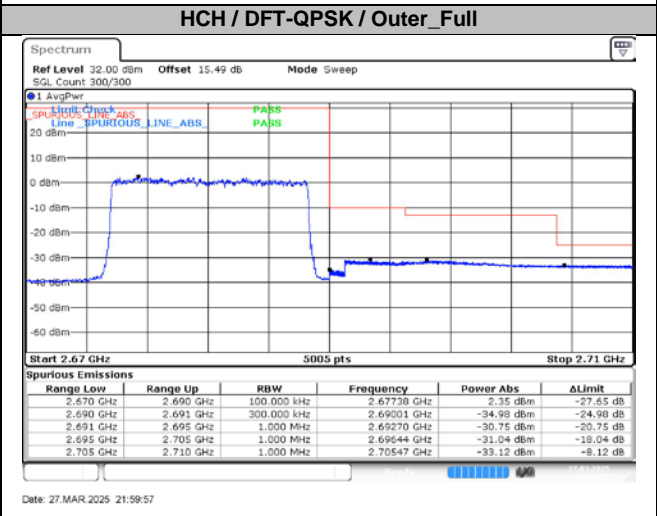
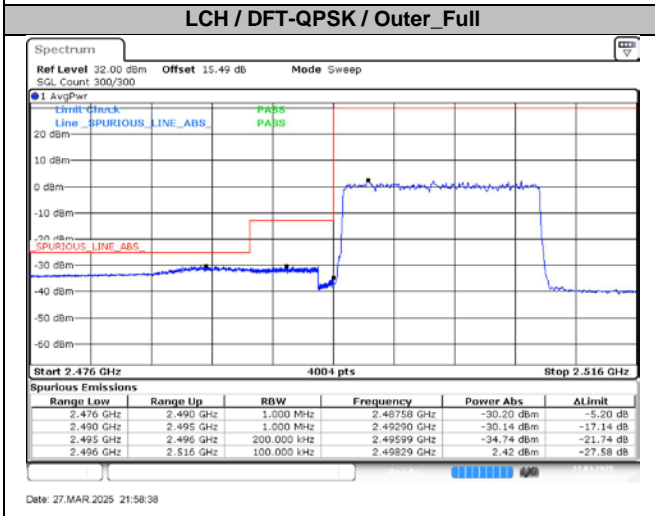
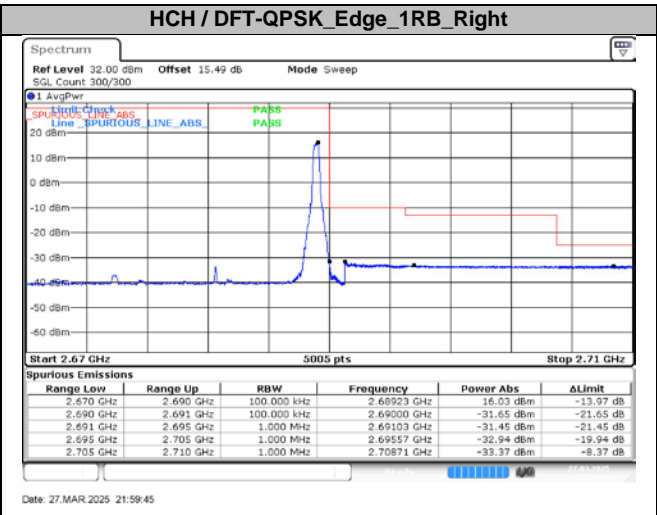
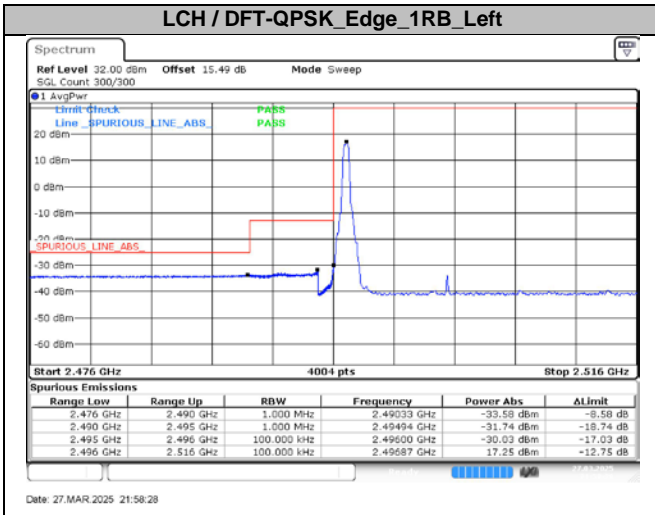


5. Conducted Band Edges

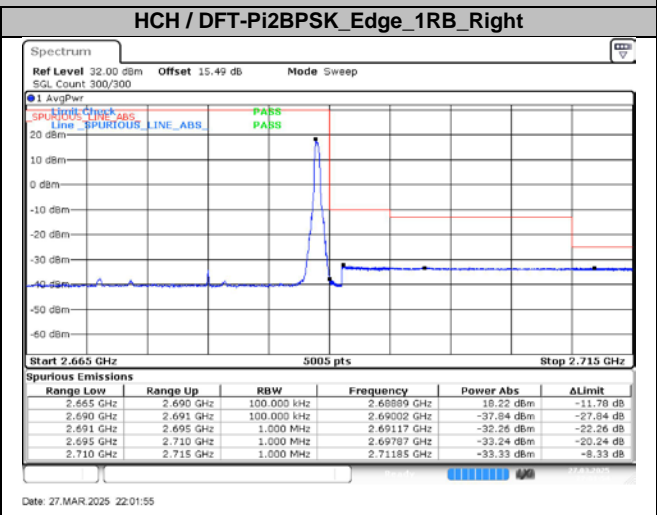
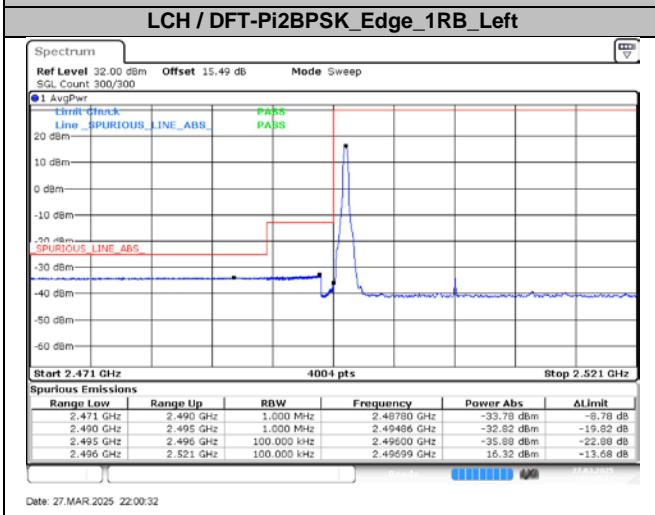
5.1. Test Plots for SCS=30KHz

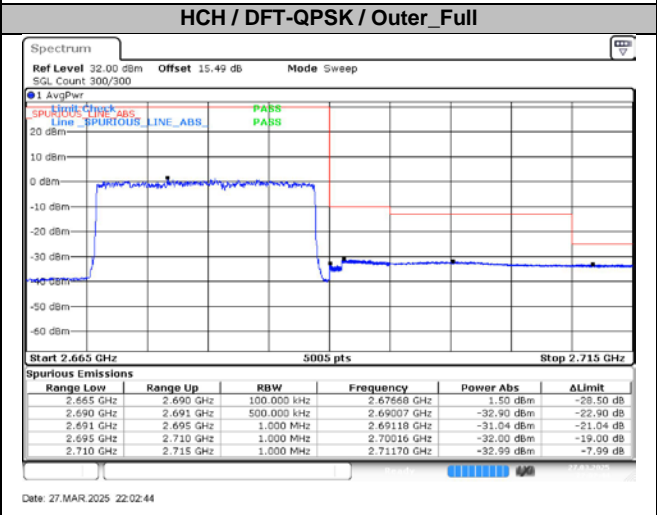
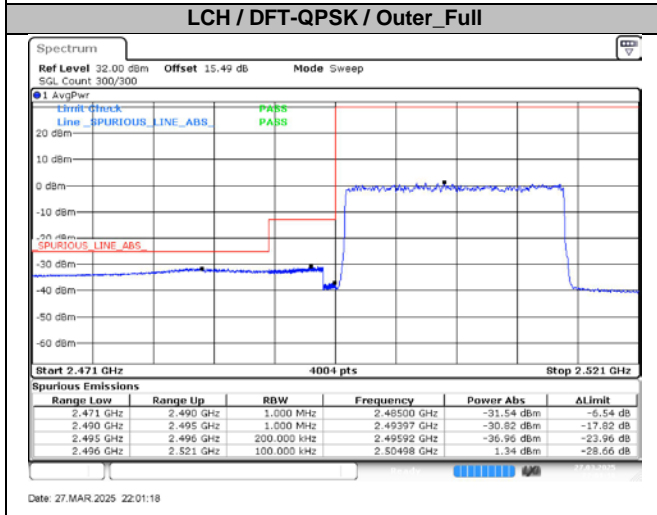
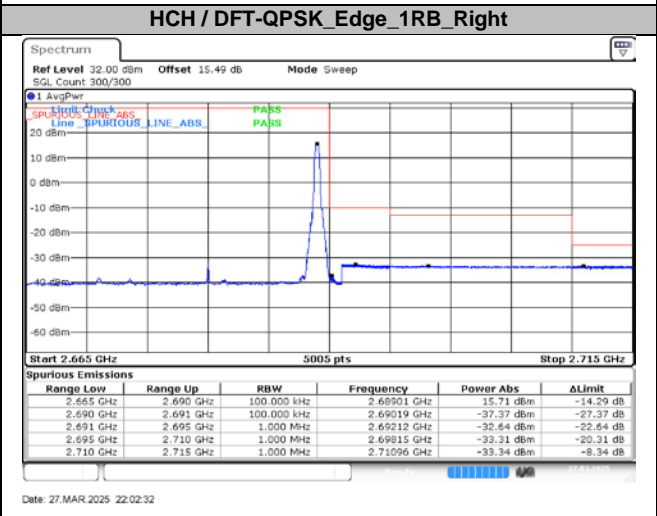
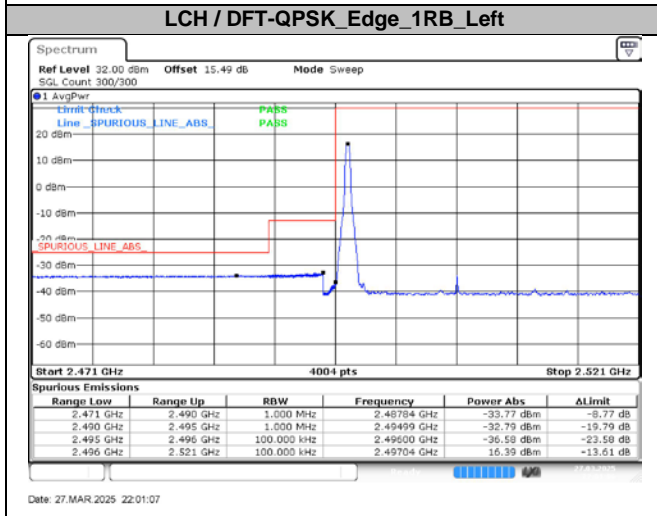
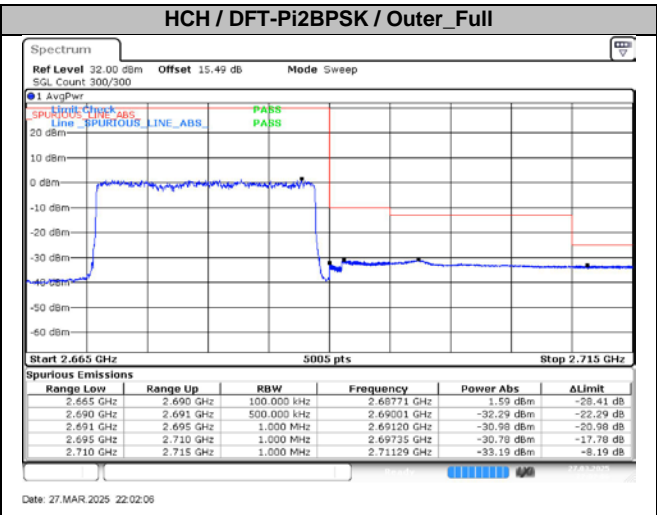
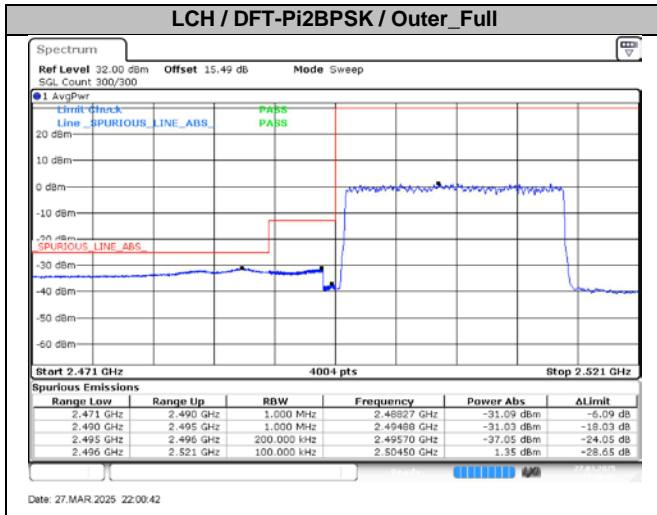






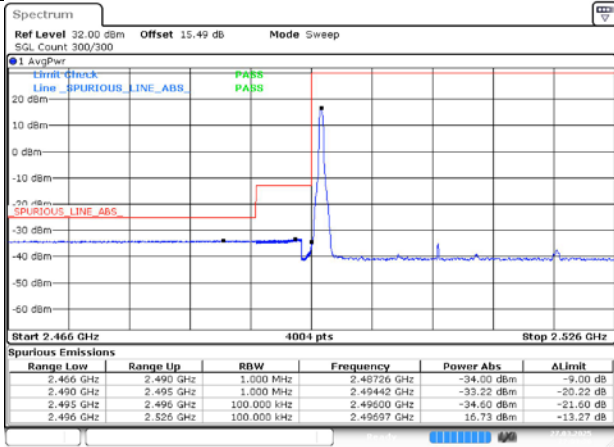
n41A / 30kHz / 20MHz





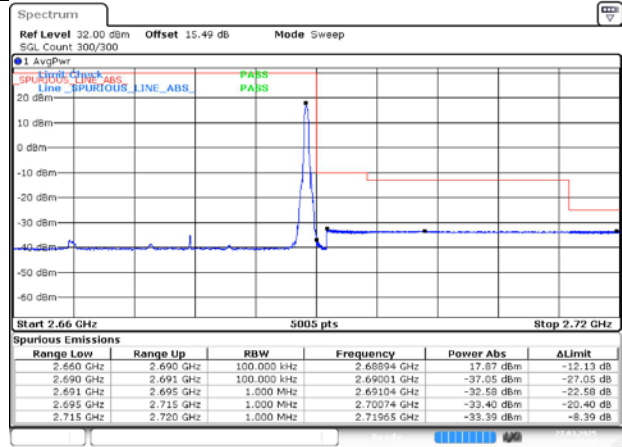
n41A / 30kHz / 25MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



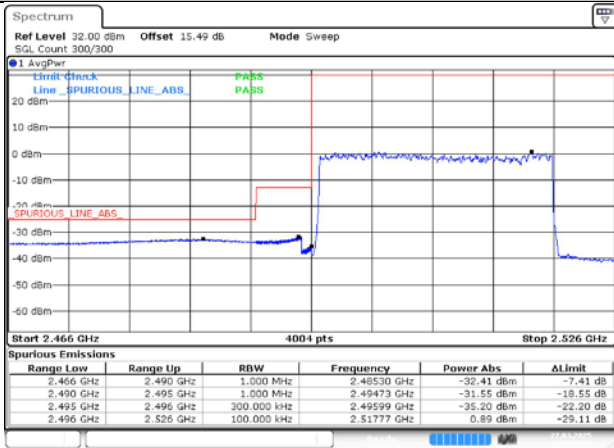
Date: 27.MAR.2025 22:03:21

HCH / DFT-Pi2BPSK_Edge_1RB_Right



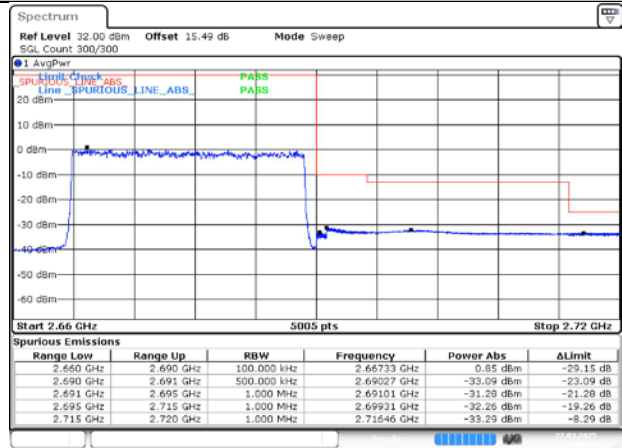
Date: 27.MAR.2025 22:04:34

LCH / DFT-Pi2BPSK / Outer_Full



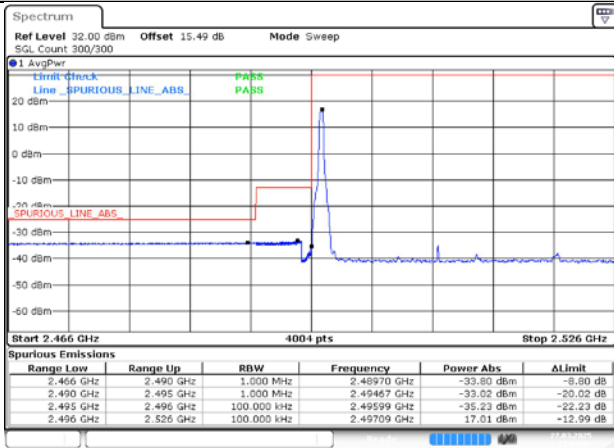
Date: 27.MAR.2025 22:03:31

HCH / DFT-Pi2BPSK / Outer_Full



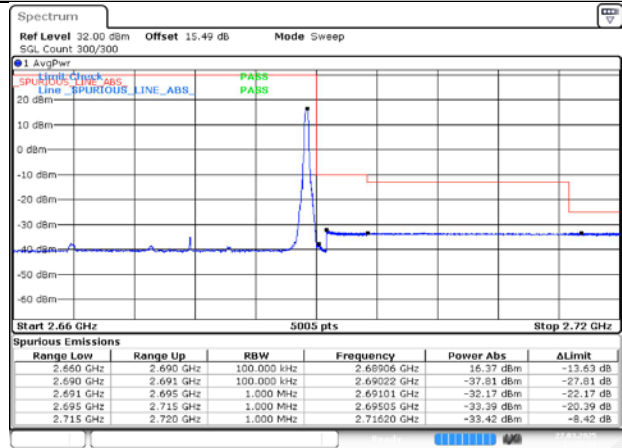
Date: 27.MAR.2025 22:04:46

LCH / DFT-QPSK_Edge_1RB_Left

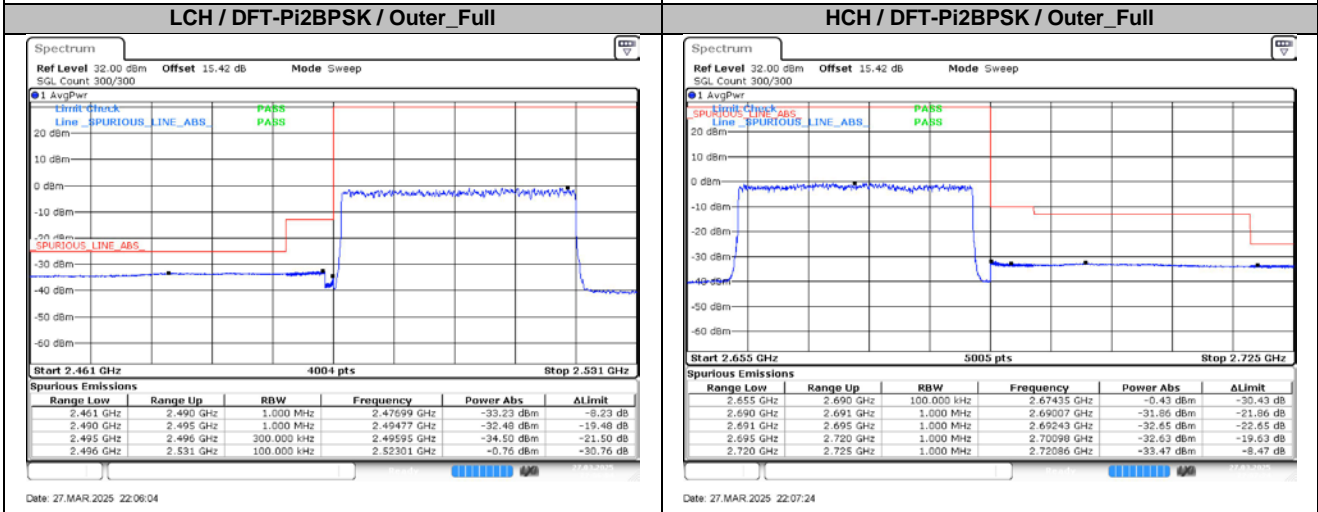
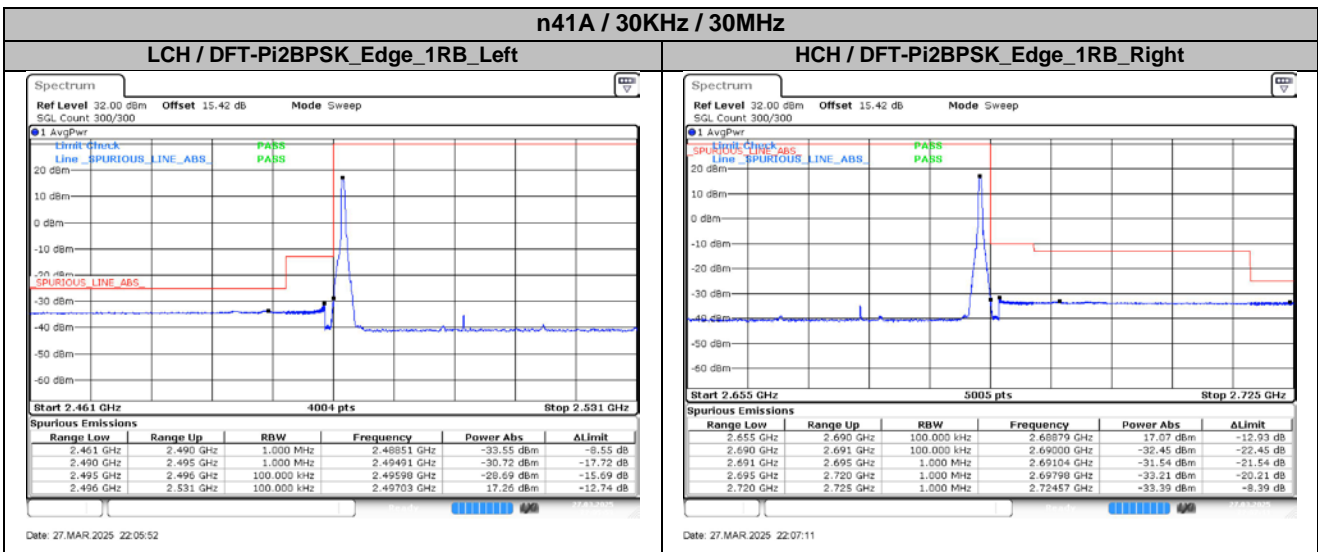
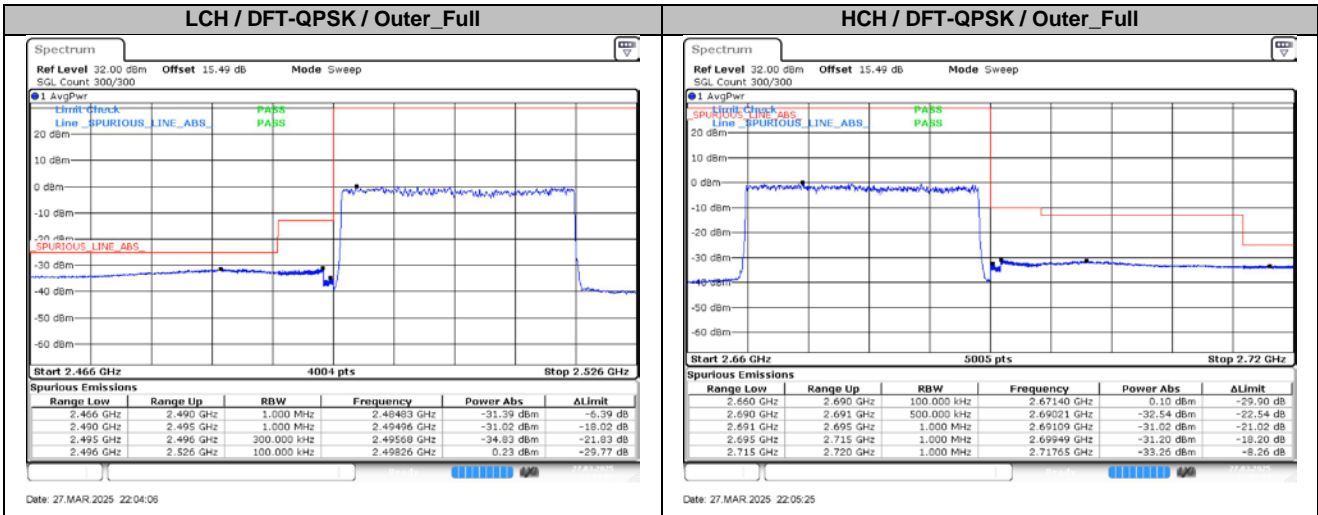


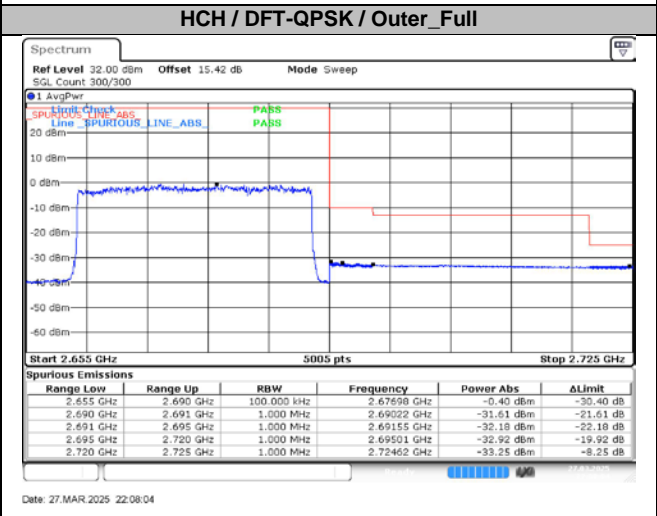
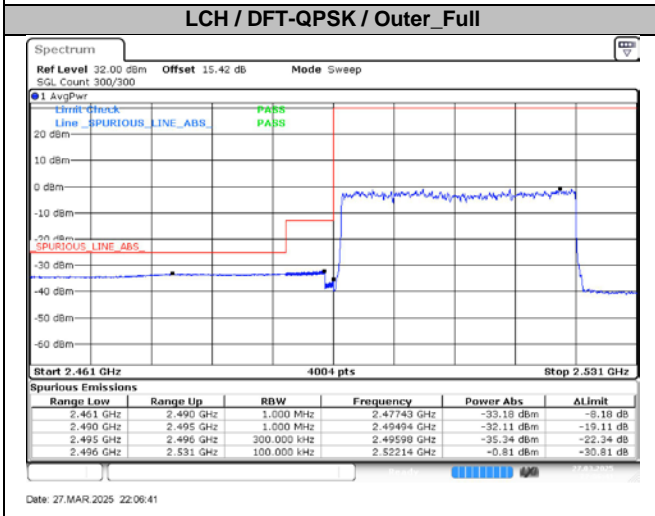
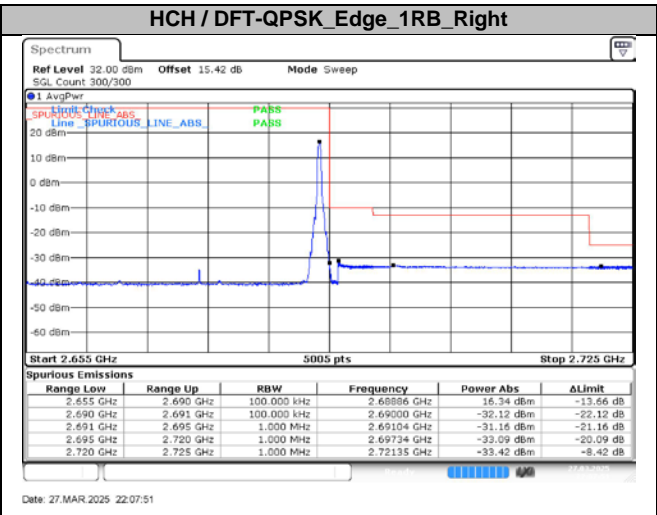
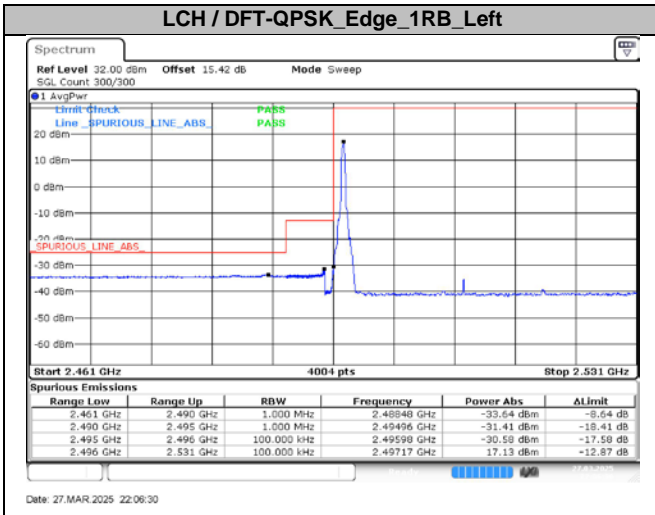
Date: 27.MAR.2025 22:03:56

HCH / DFT-QPSK_Edge_1RB_Right

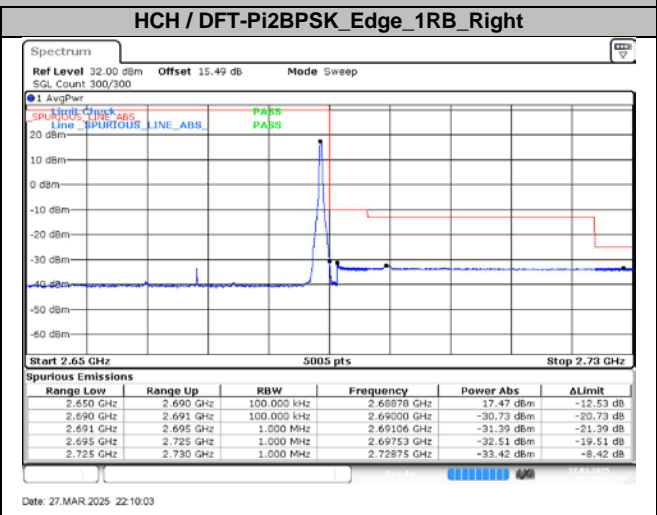
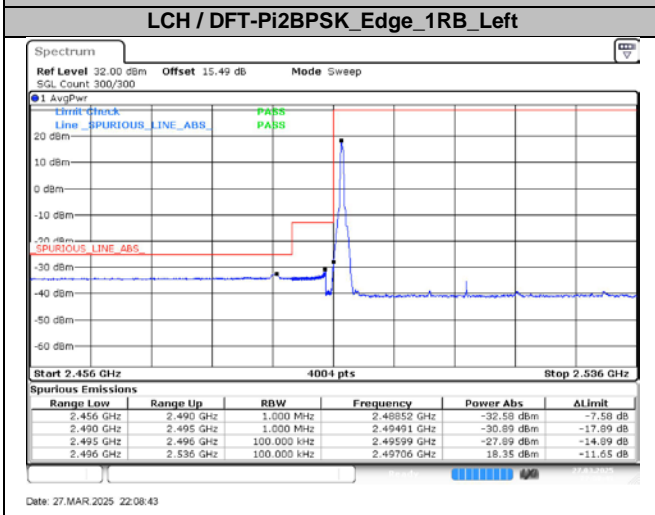


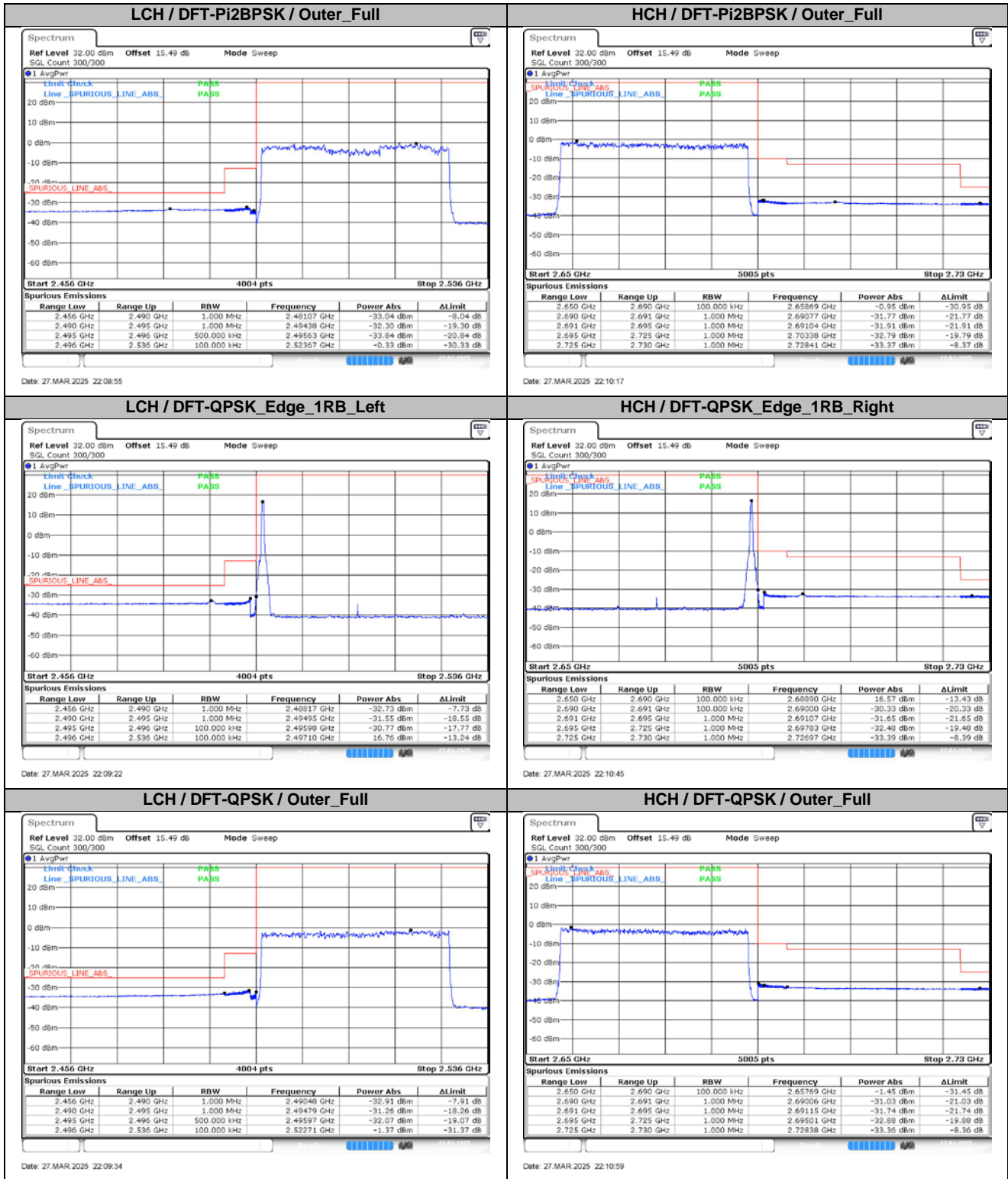
Date: 27.MAR.2025 22:05:13





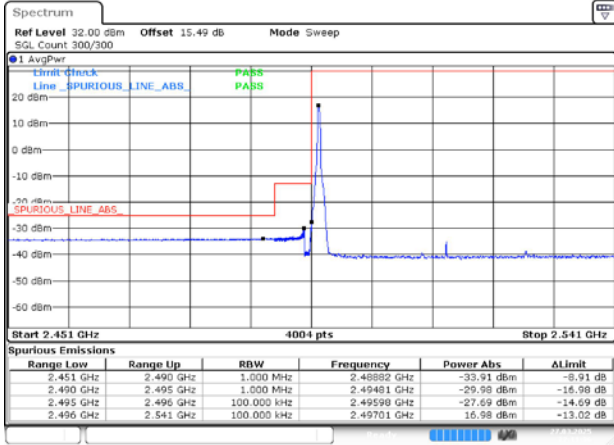
n41A / 30kHz / 35MHz



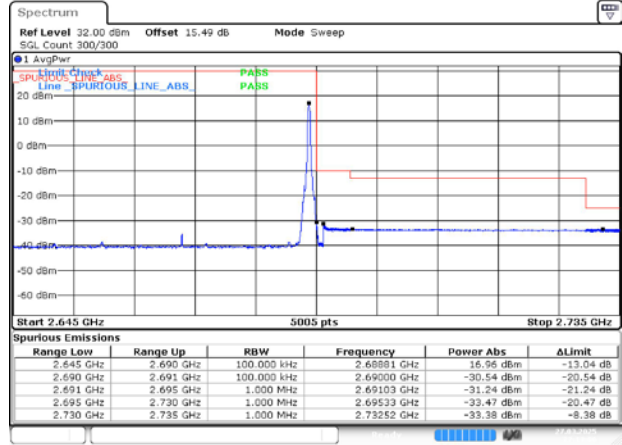


n41A / 30KHz / 40MHz

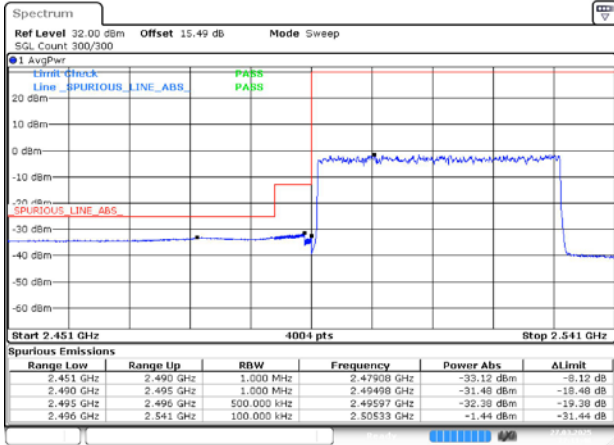
LCH / DFT-Pi2BPSK_Edge_1RB_Left



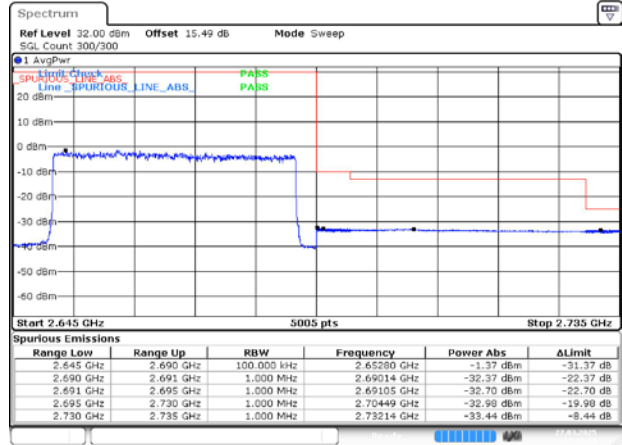
HCH / DFT-Pi2BPSK_Edge_1RB_Right



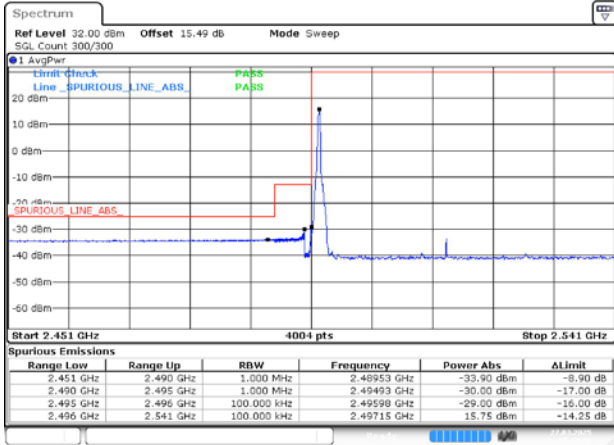
LCH / DFT-Pi2BPSK / Outer_Full



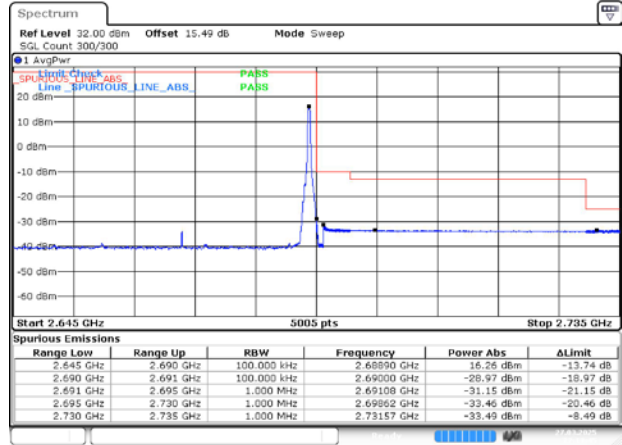
HCH / DFT-Pi2BPSK / Outer_Full

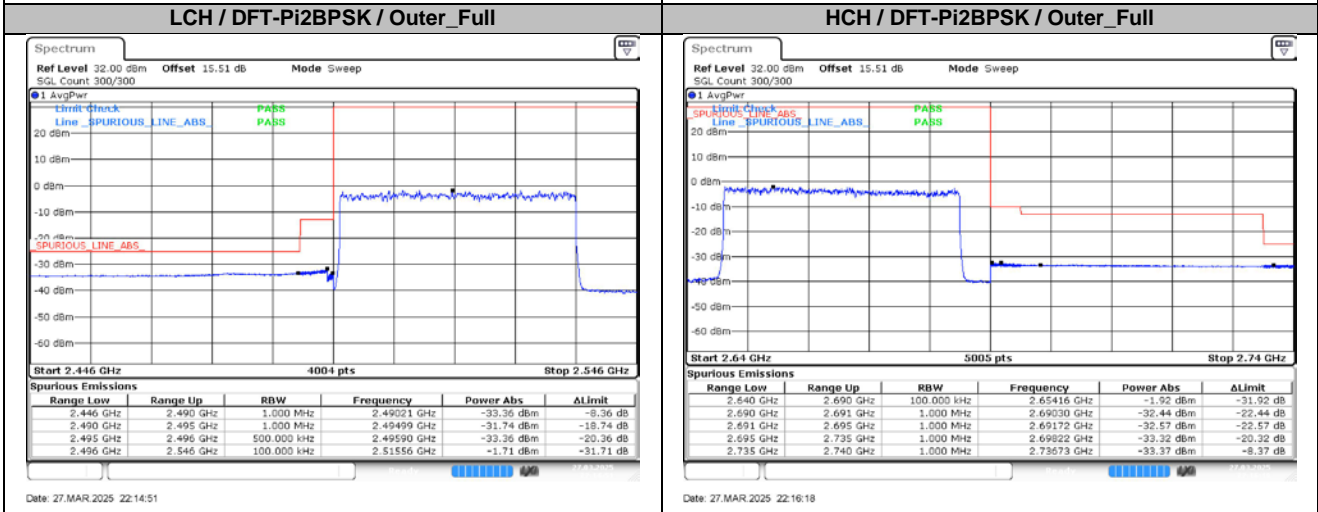
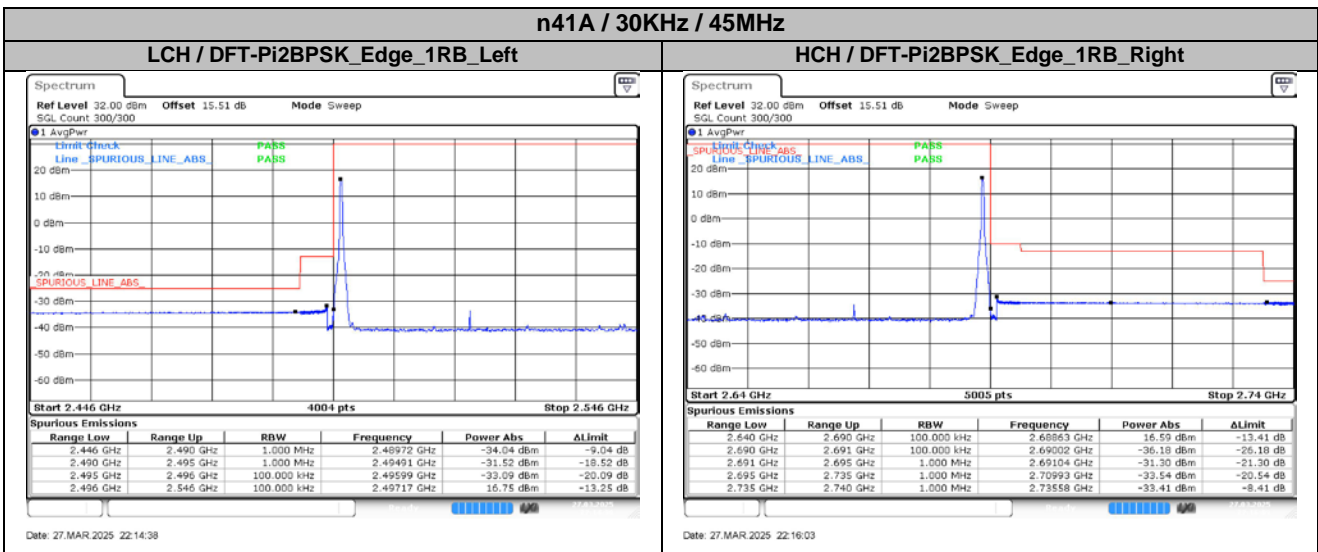
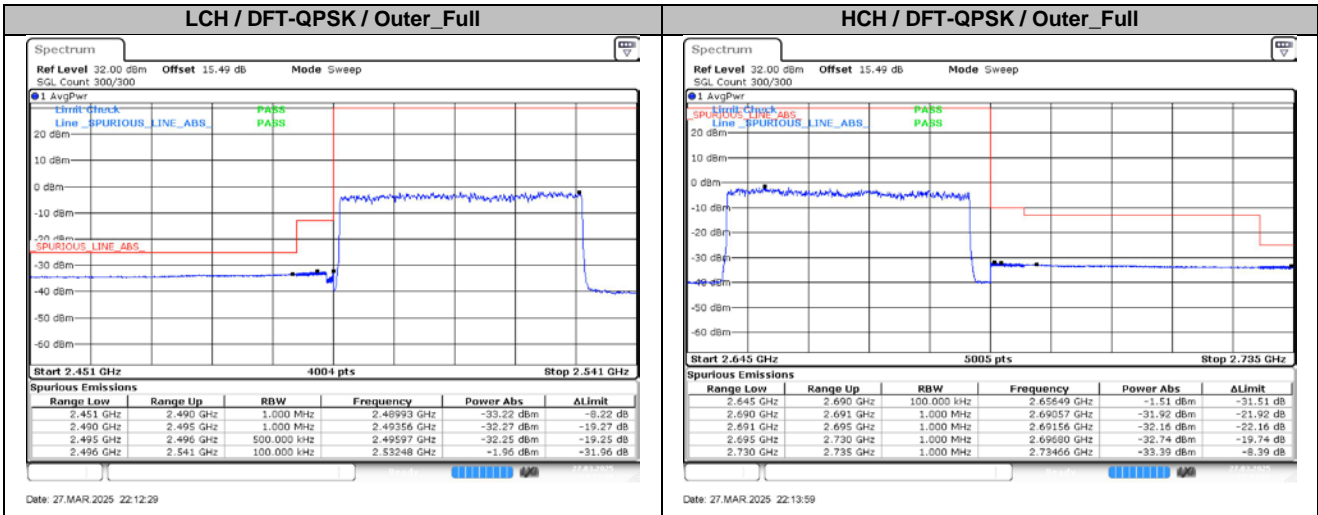


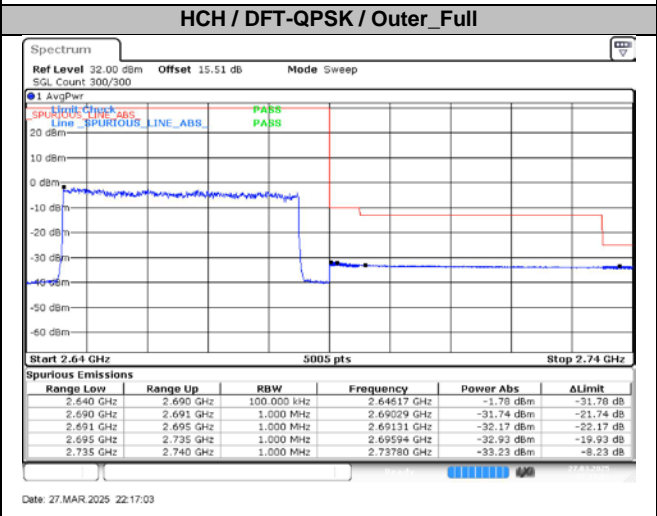
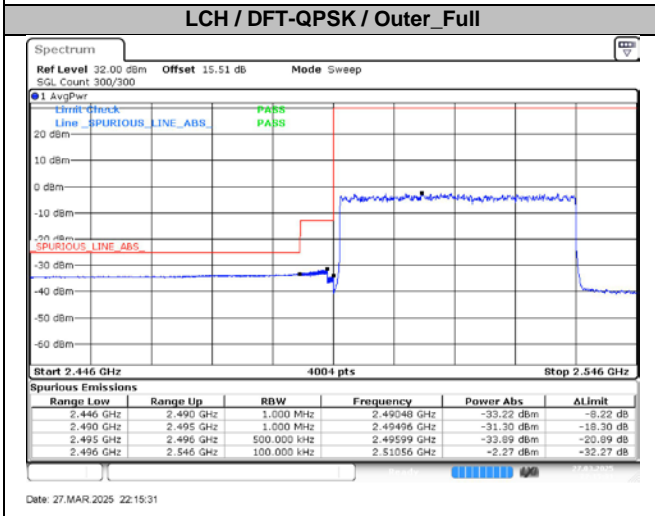
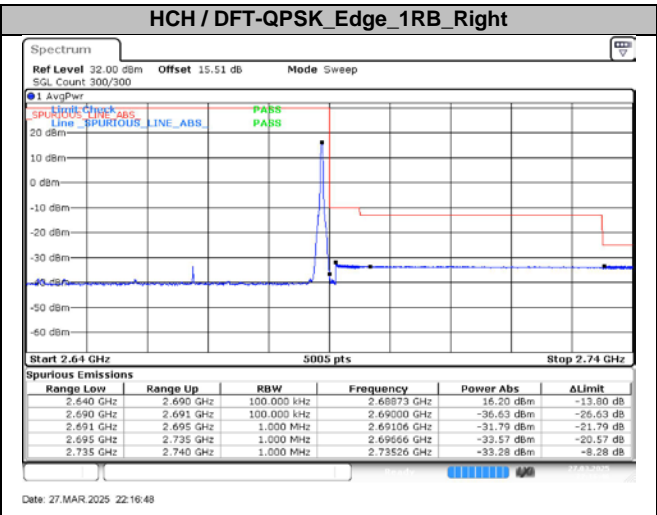
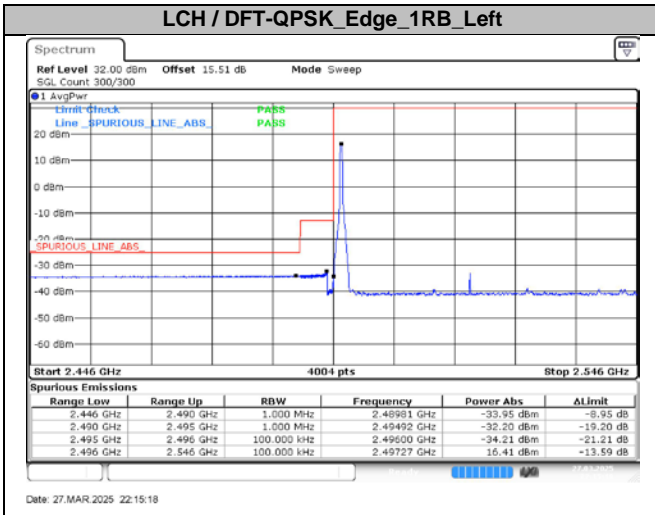
LCH / DFT-QPSK_Edge_1RB_Left



HCH / DFT-QPSK_Edge_1RB_Right







n41A / 30kHz / 50MHz

