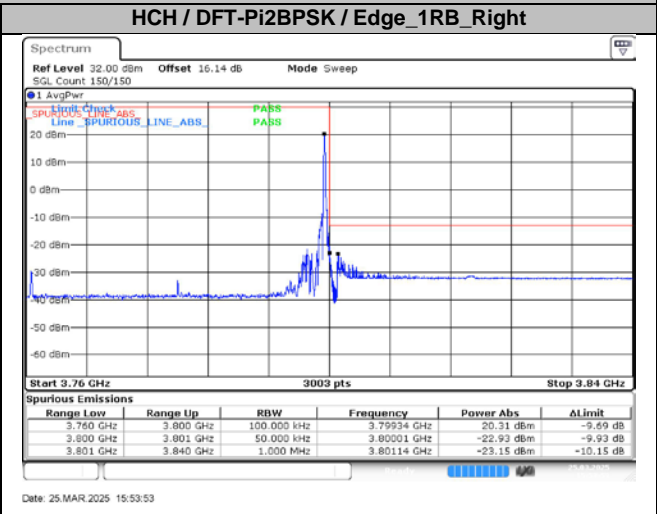
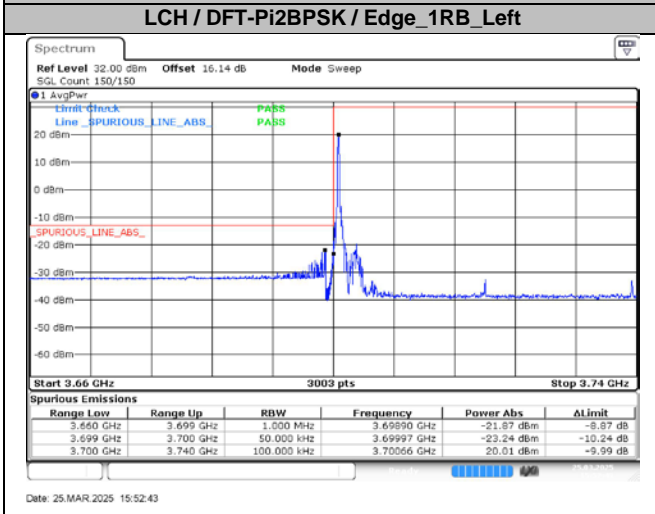
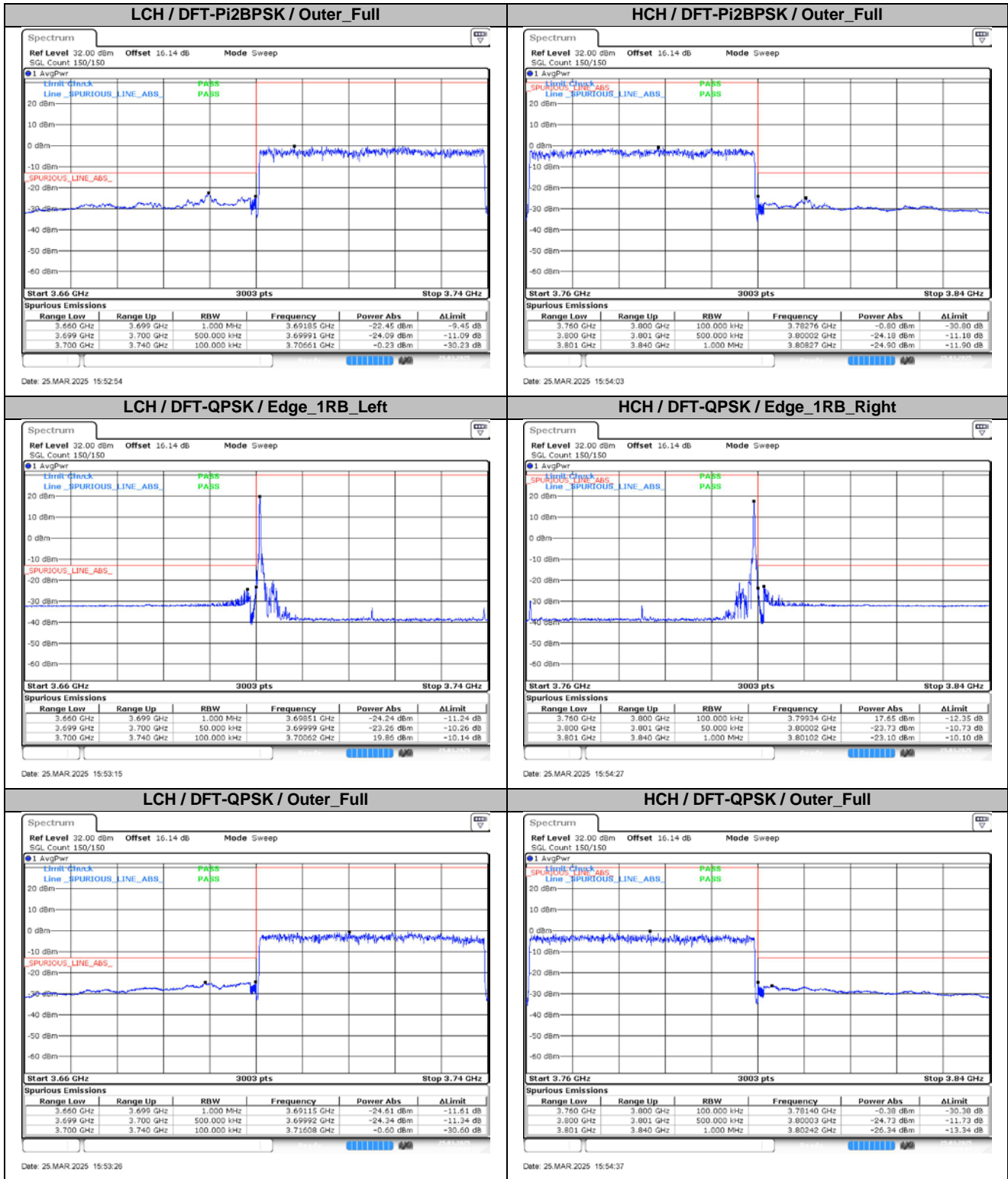


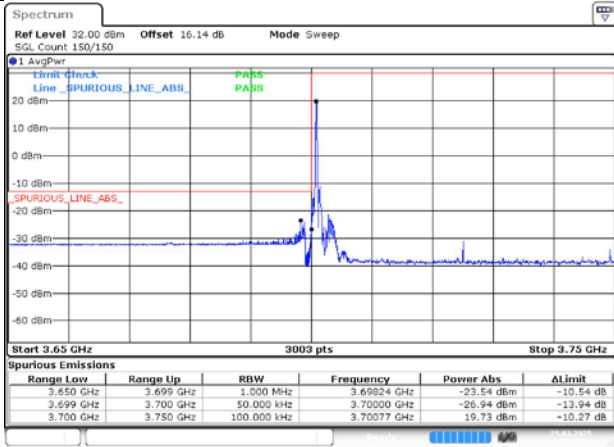
n78A / 15KHz / 40MHz





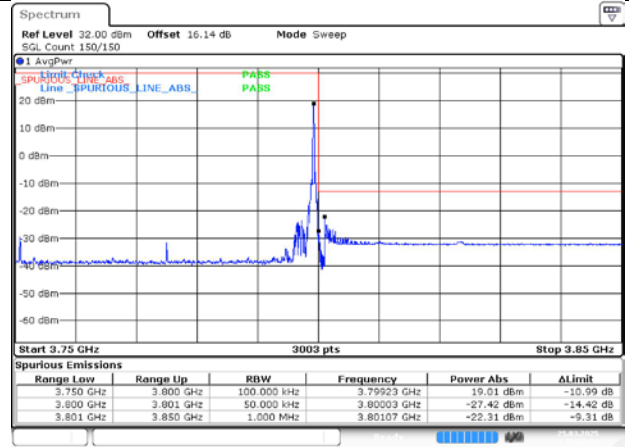
n78A / 15KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



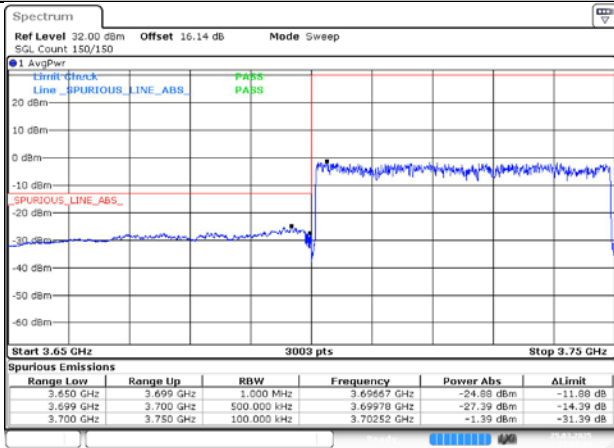
Date: 25.MAR.2025 15:55:02

HCH / DFT-Pi2BPSK / Edge_1RB_Right



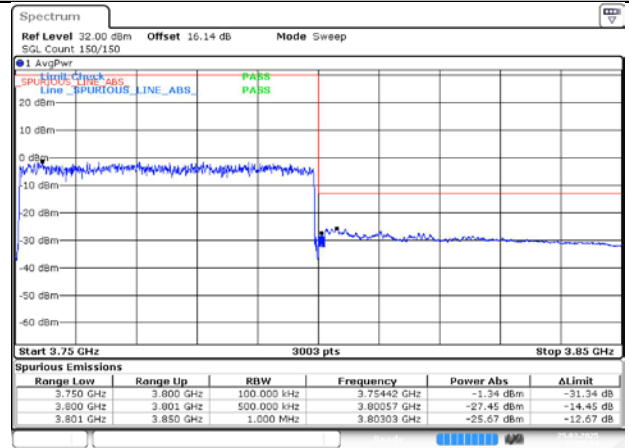
Date: 25.MAR.2025 15:57:24

LCH / DFT-Pi2BPSK / Outer_Full



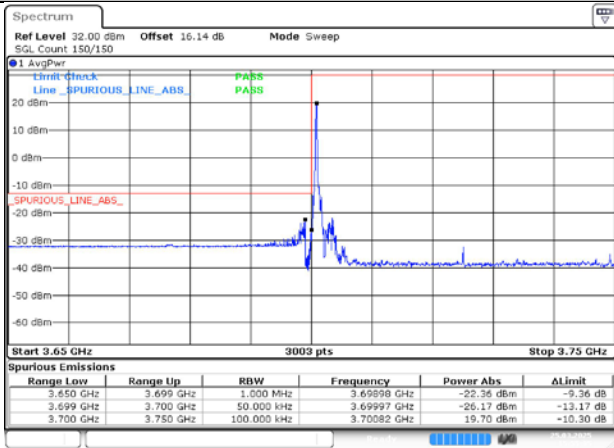
Date: 25.MAR.2025 15:55:14

HCH / DFT-Pi2BPSK / Outer_Full



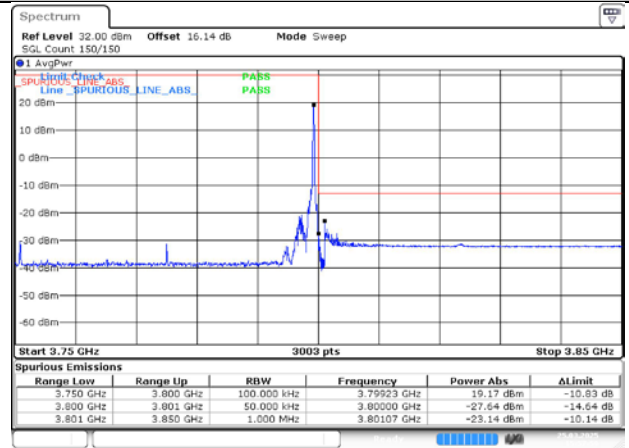
Date: 25.MAR.2025 15:57:34

LCH / DFT-QPSK / Edge_1RB_Left

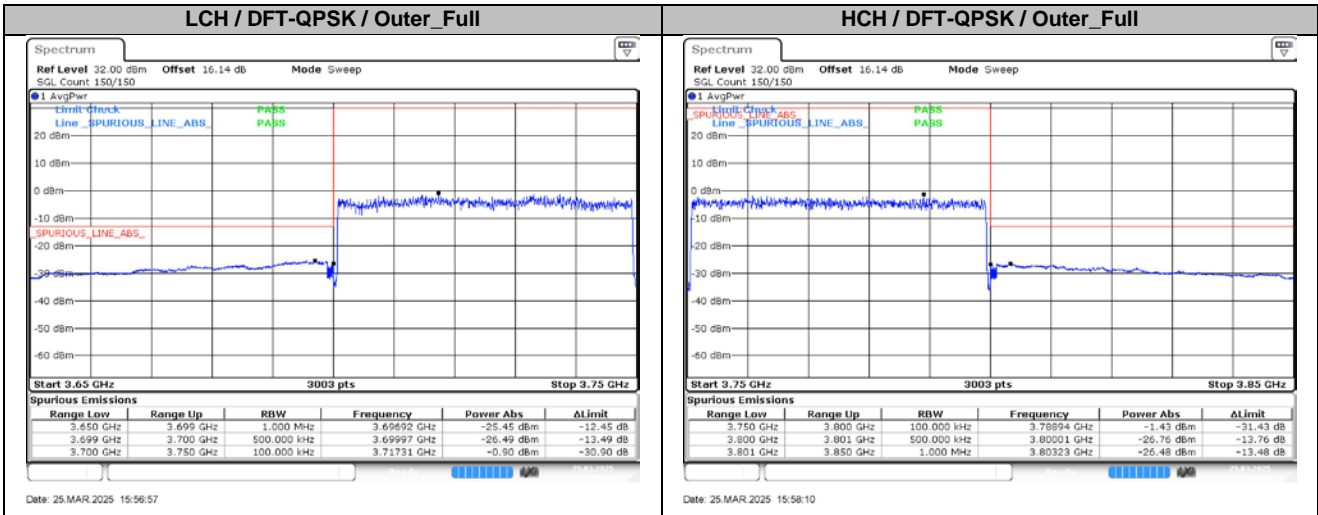


Date: 25.MAR.2025 15:55:36

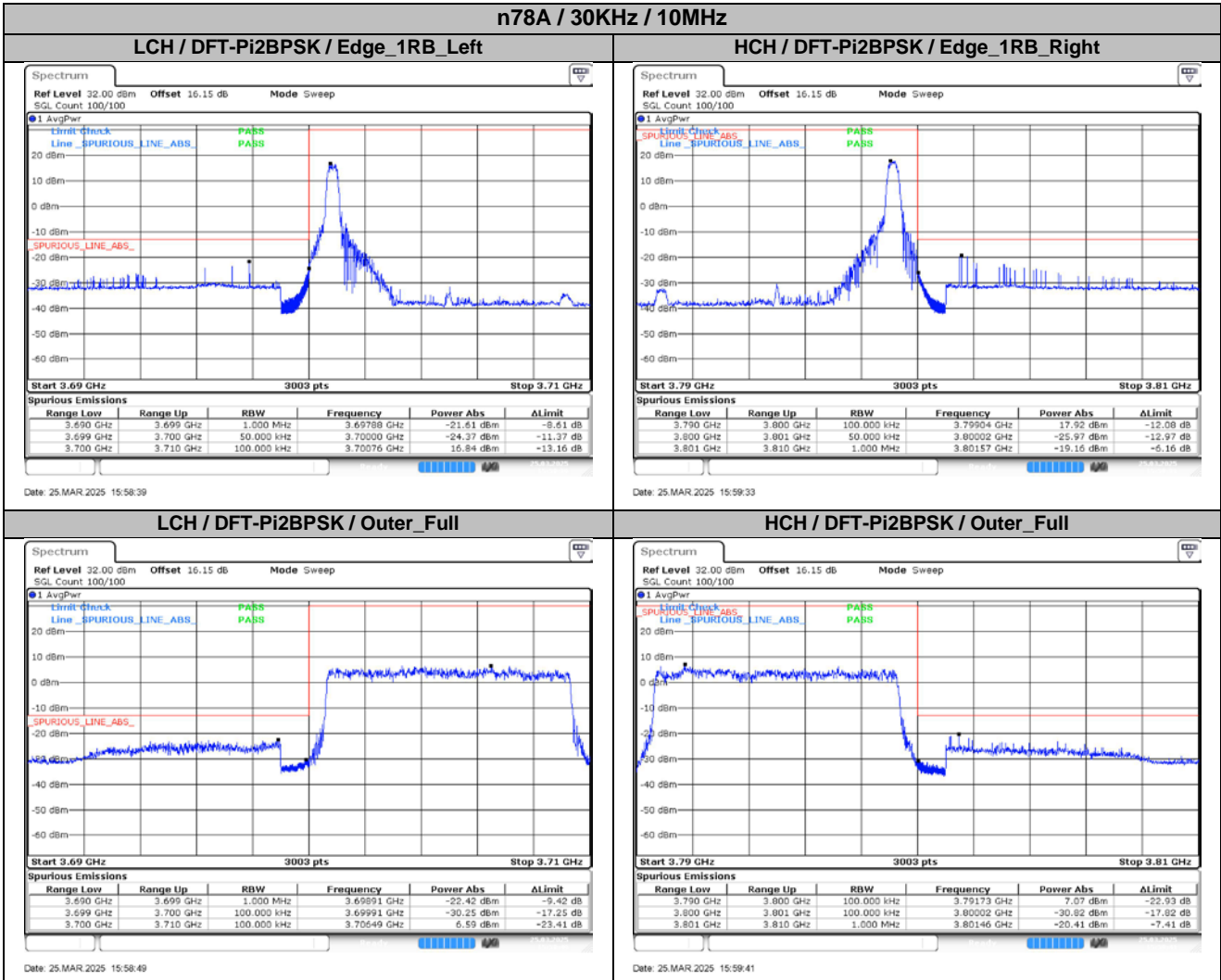
HCH / DFT-QPSK / Edge_1RB_Right

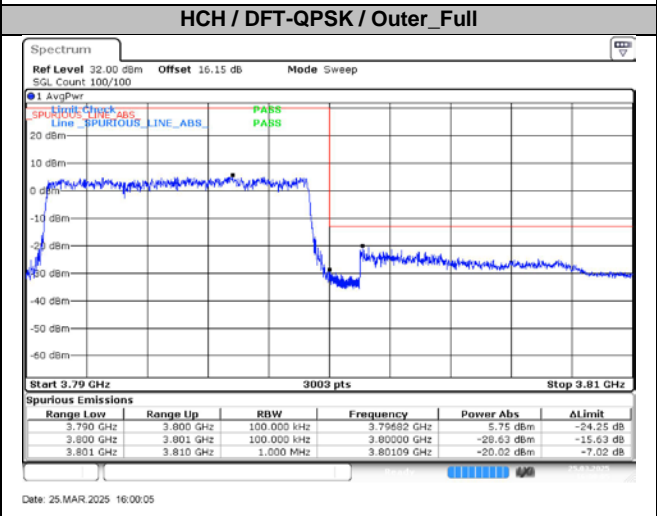
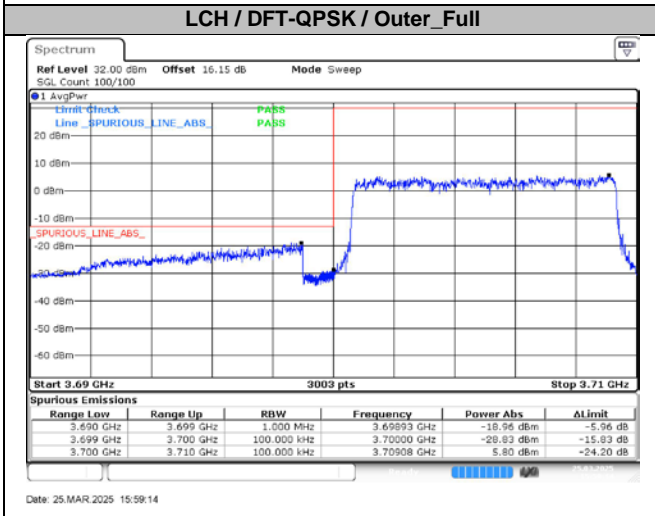
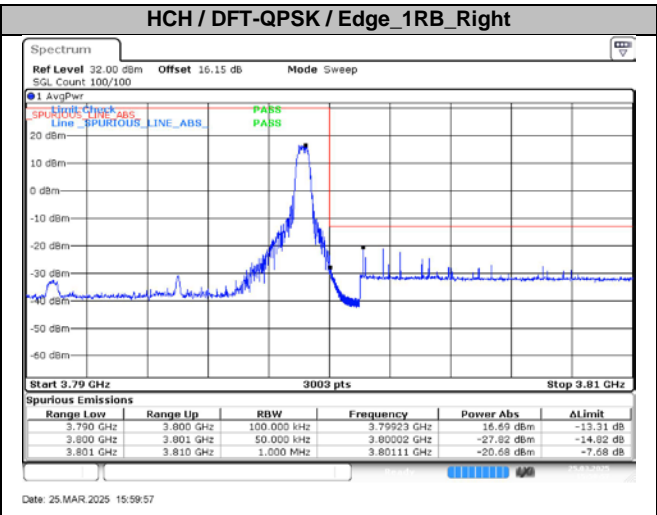
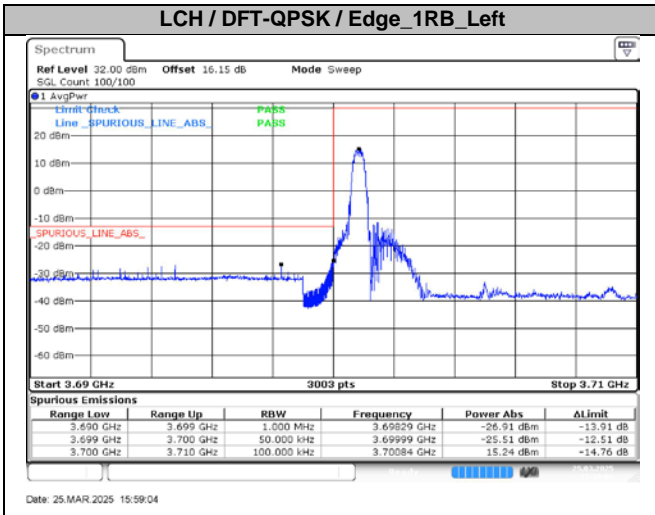


Date: 25.MAR.2025 15:57:59

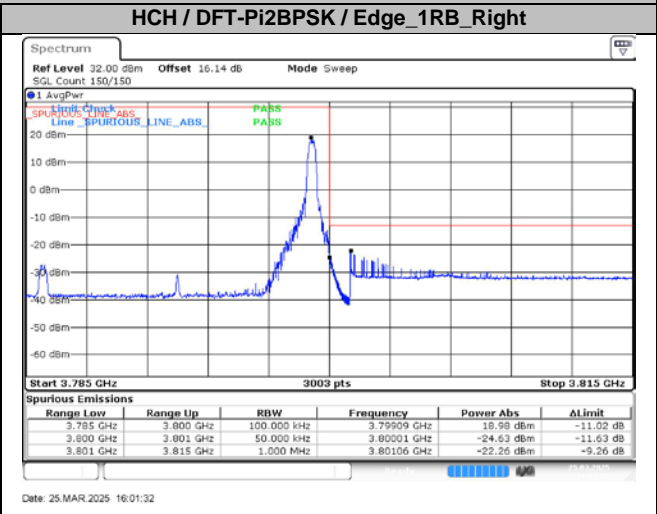
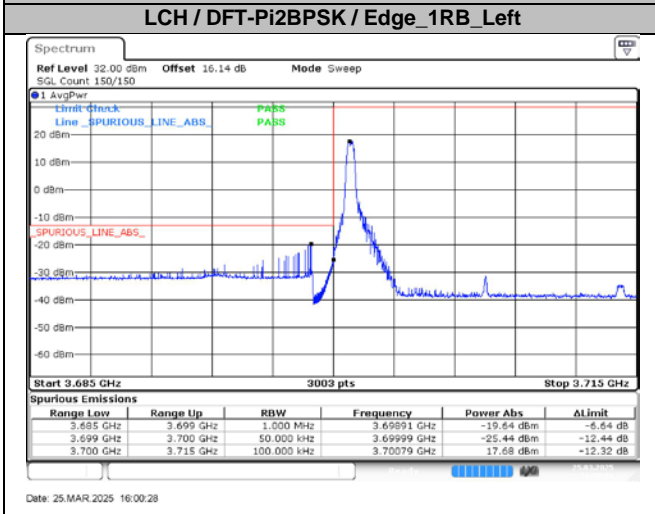


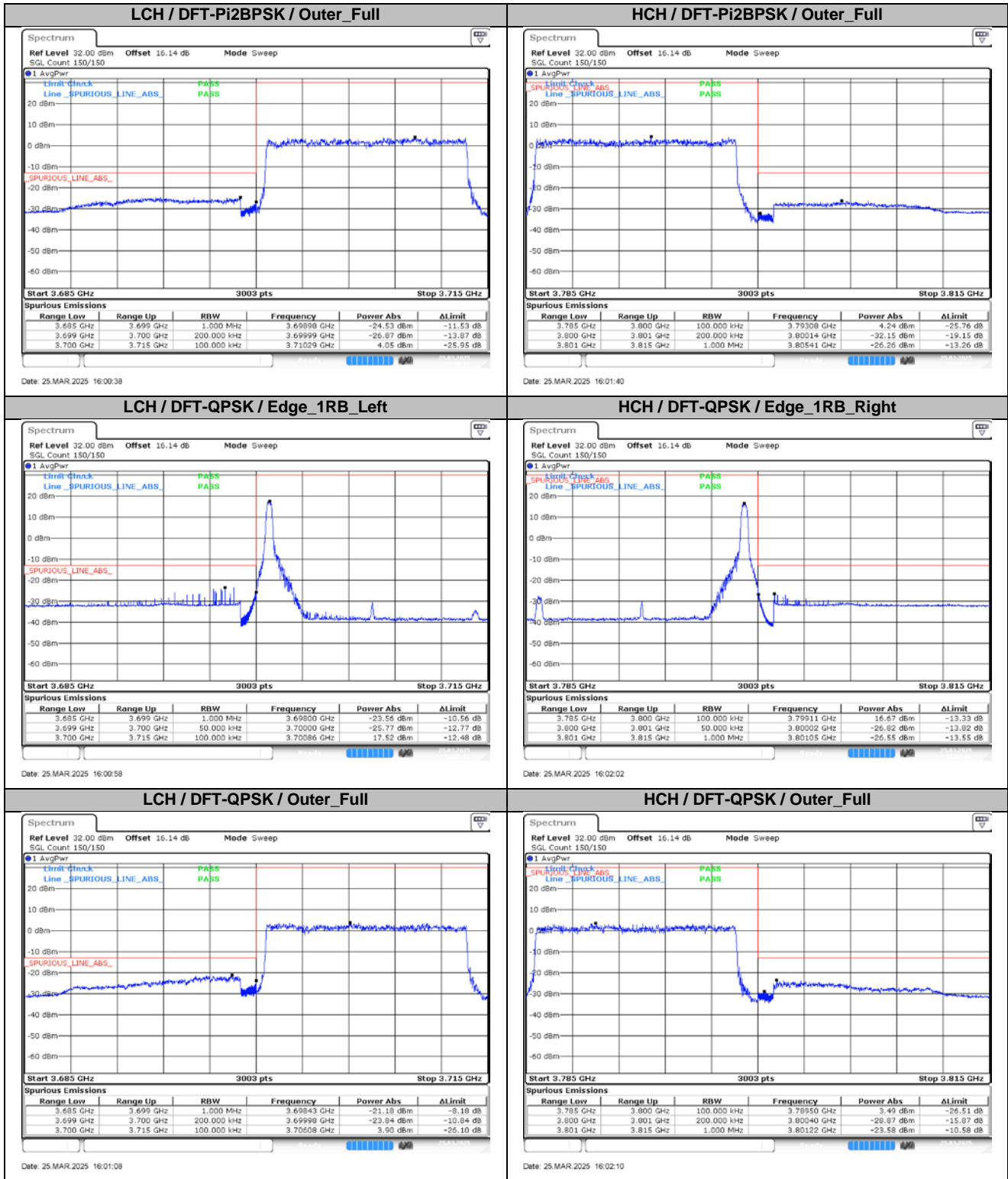
5.2. Test Plots for SCS=30KHz





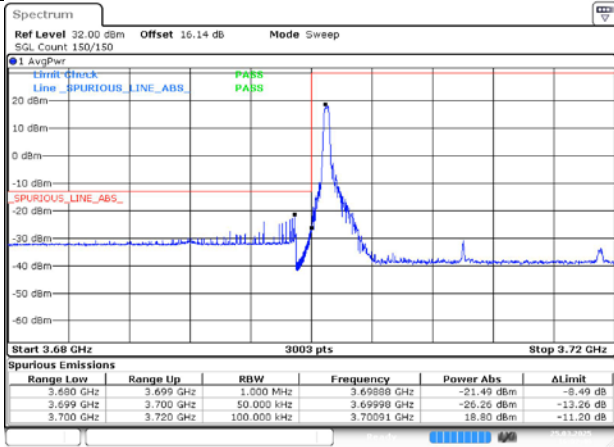
n78A / 30KHz / 15MHz





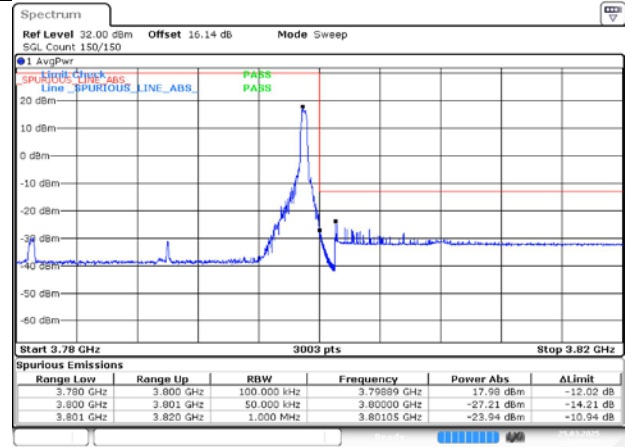
n78A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



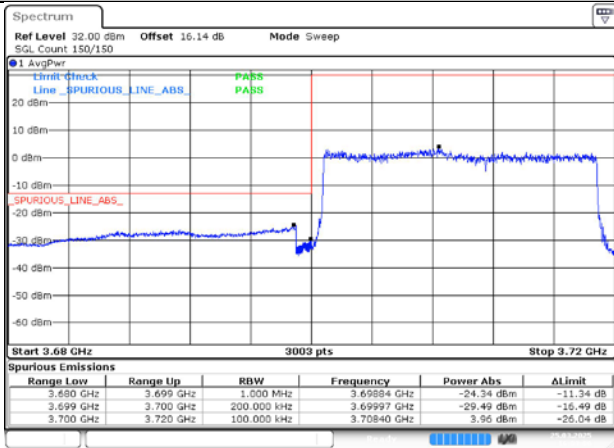
Date: 25.MAR.2025 16:02:50

HCH / DFT-Pi2BPSK / Edge_1RB_Right



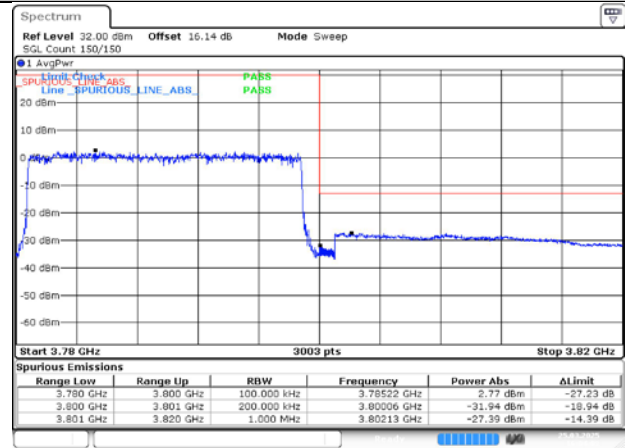
Date: 25.MAR.2025 16:03:54

LCH / DFT-Pi2BPSK / Outer_Full



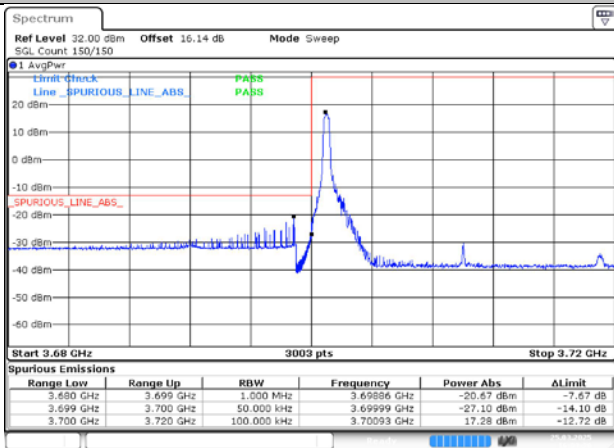
Date: 25.MAR.2025 16:03:00

HCH / DFT-Pi2BPSK / Outer_Full



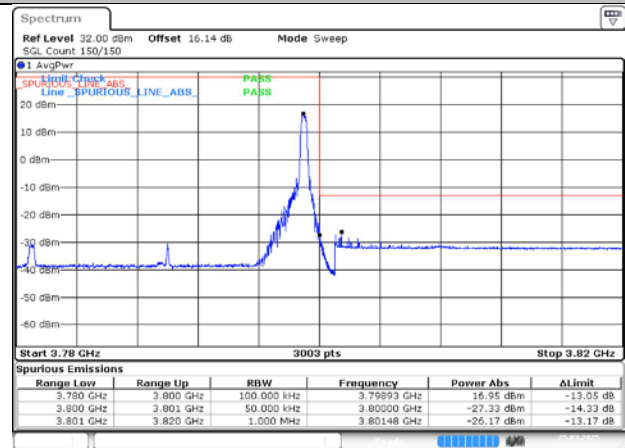
Date: 25.MAR.2025 16:04:03

LCH / DFT-QPSK / Edge_1RB_Left

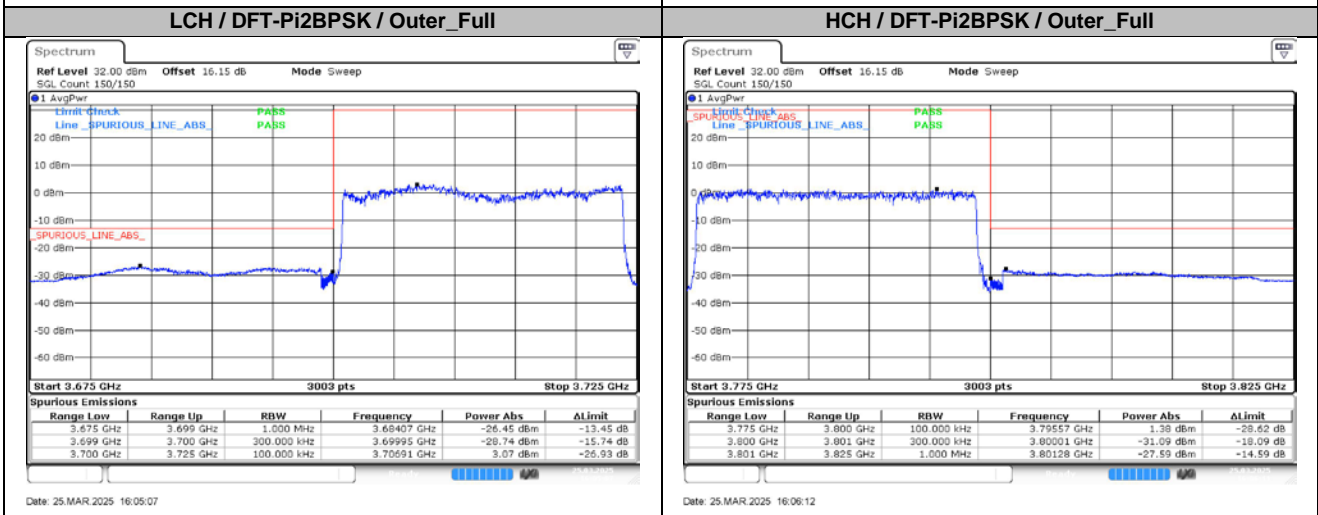
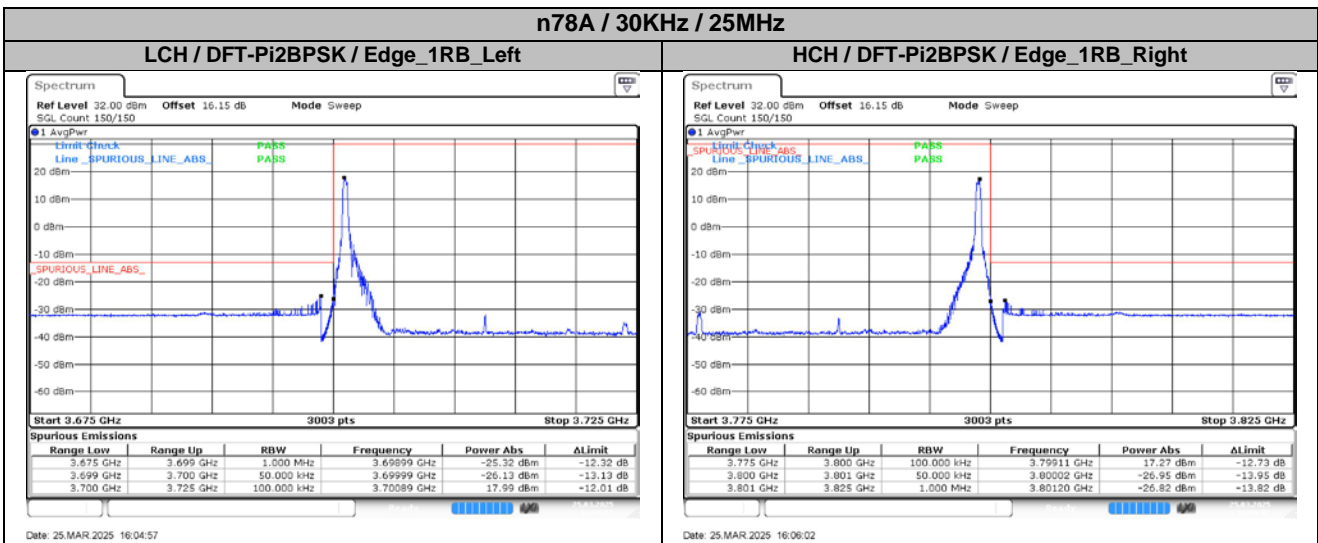
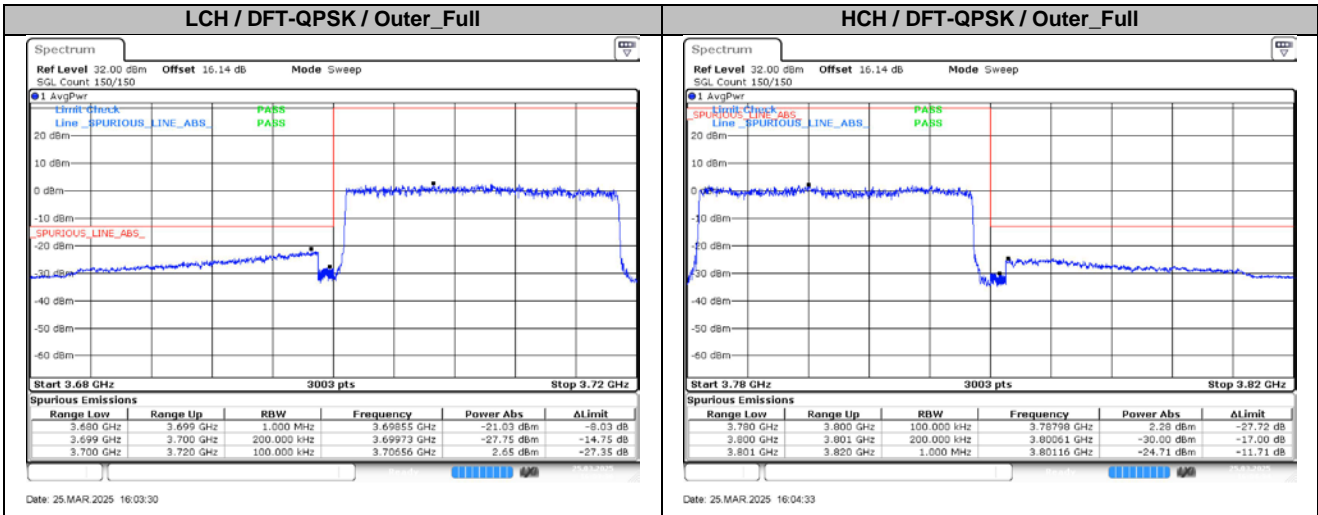


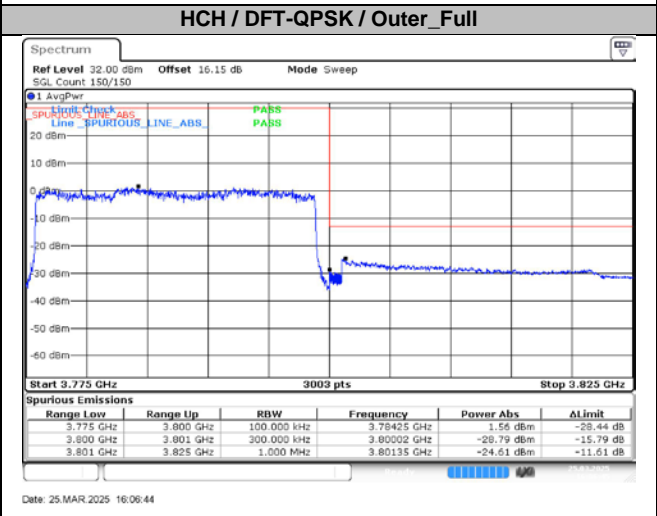
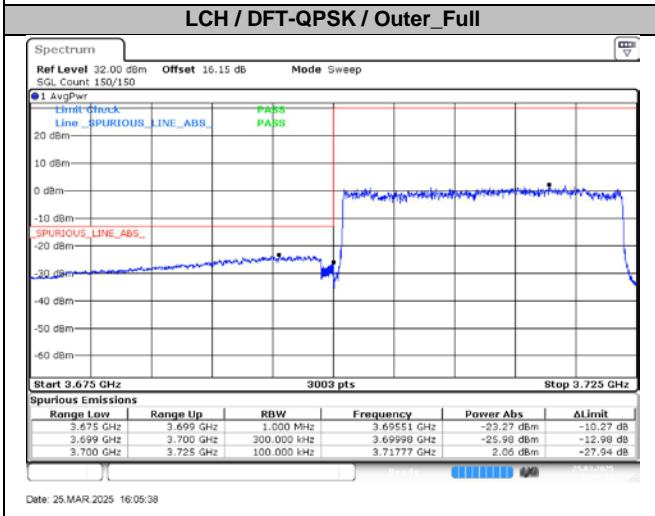
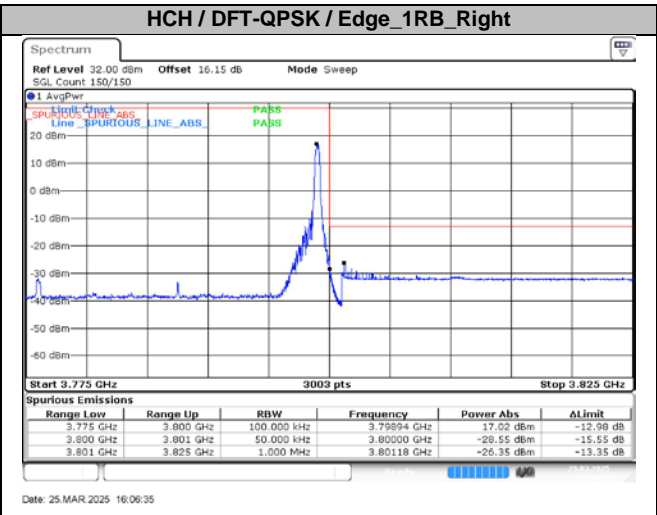
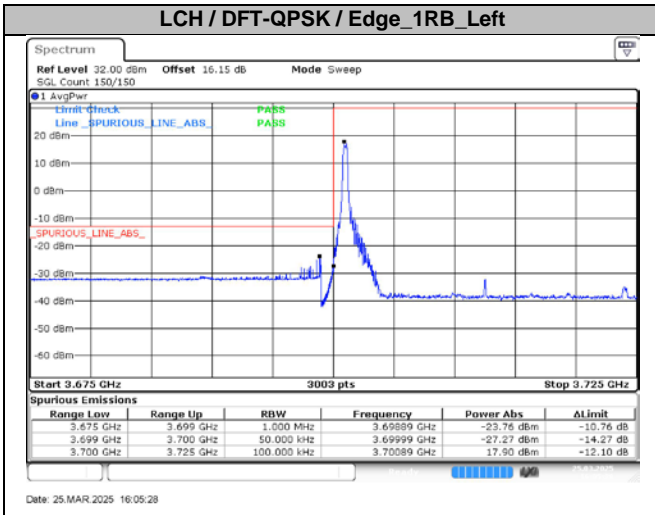
Date: 25.MAR.2025 16:03:20

HCH / DFT-QPSK / Edge_1RB_Right

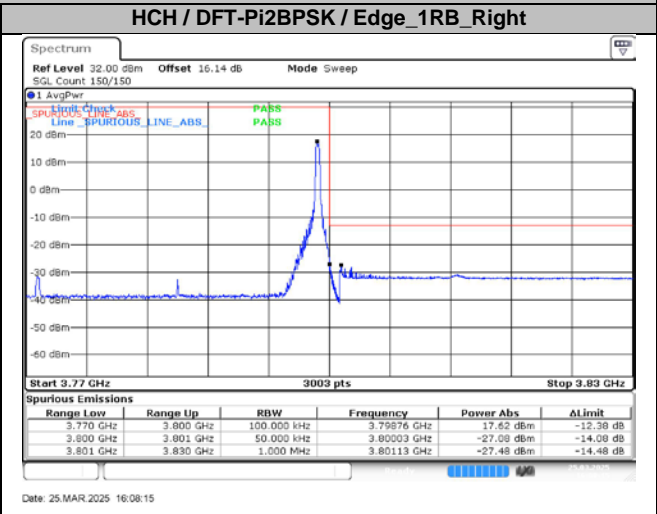
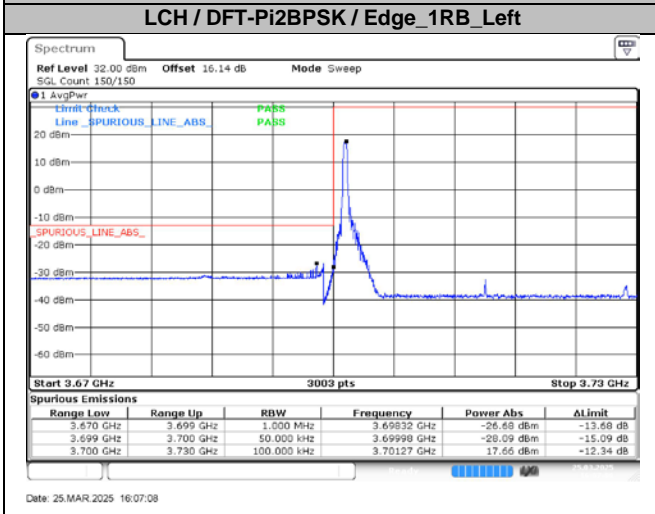


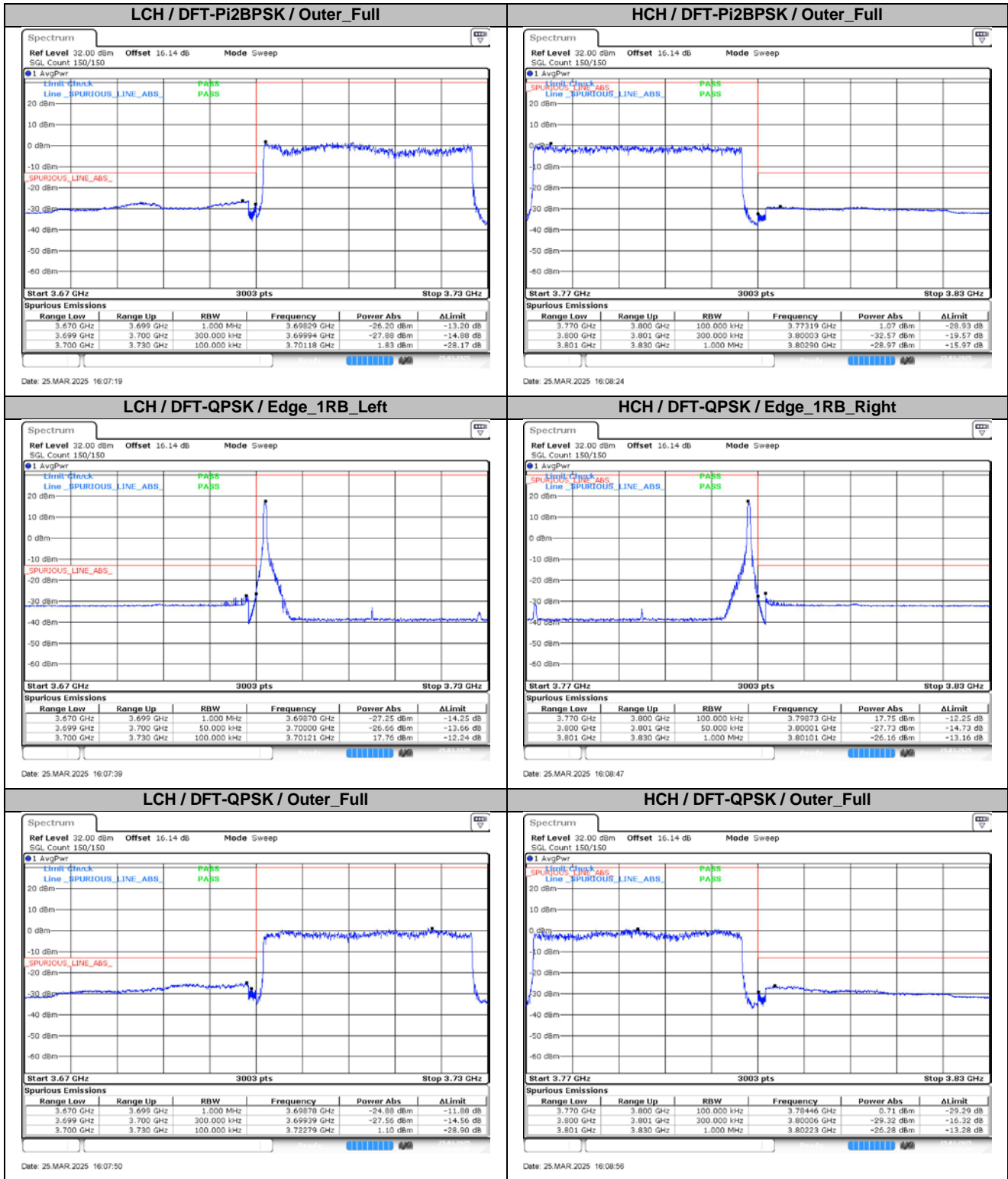
Date: 25.MAR.2025 16:04:25





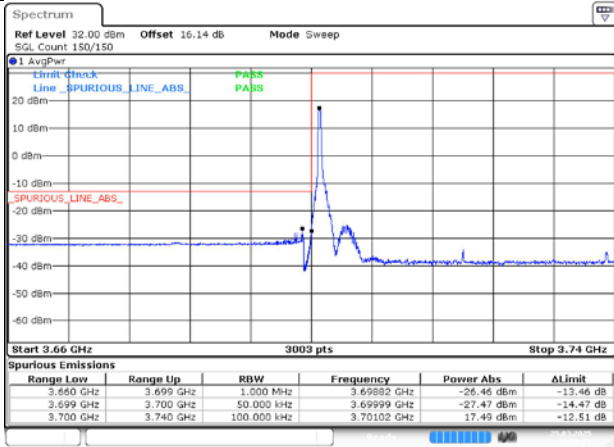
n78A / 30KHz / 30MHz





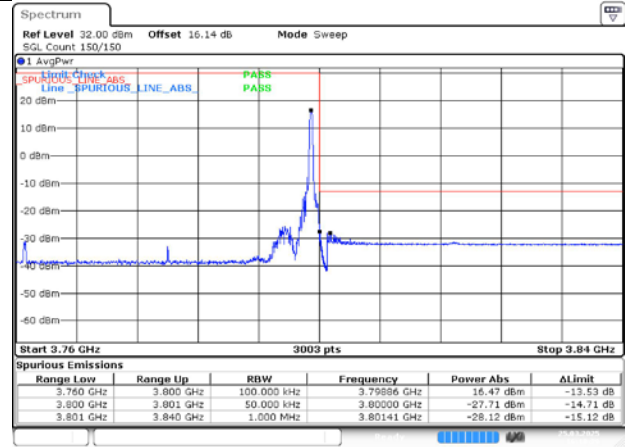
n78A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



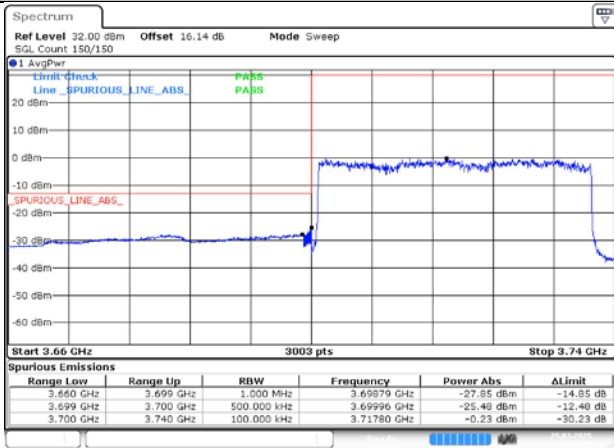
Date: 25.MAR.2025 16:09:20

HCH / DFT-Pi2BPSK / Edge_1RB_Right



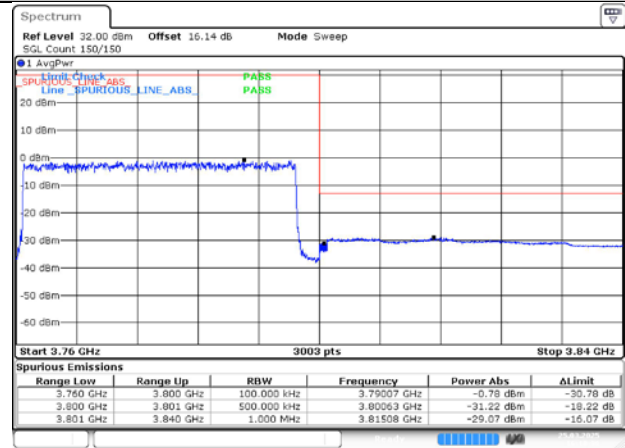
Date: 25.MAR.2025 16:10:28

LCH / DFT-Pi2BPSK / Outer_Full



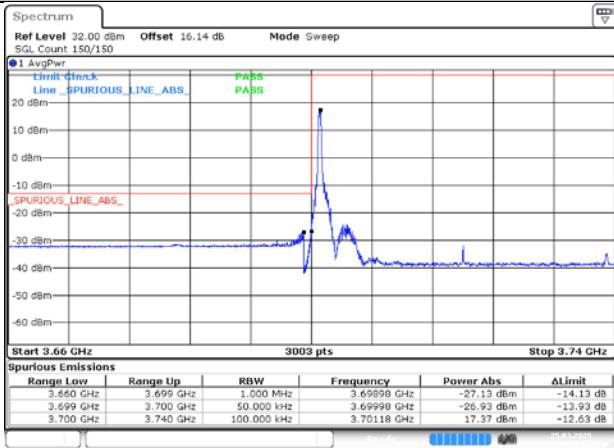
Date: 25.MAR.2025 16:09:31

HCH / DFT-Pi2BPSK / Outer_Full



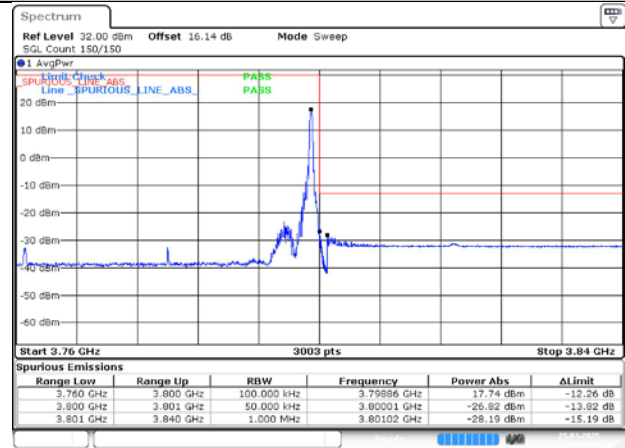
Date: 25.MAR.2025 16:10:38

LCH / DFT-QPSK / Edge_1RB_Left

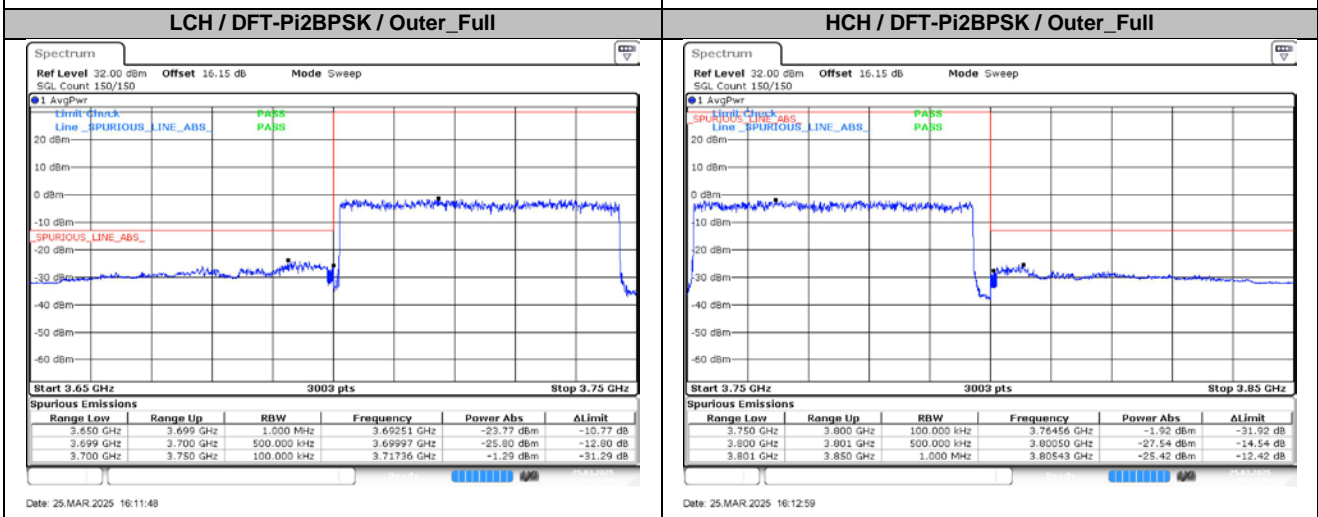
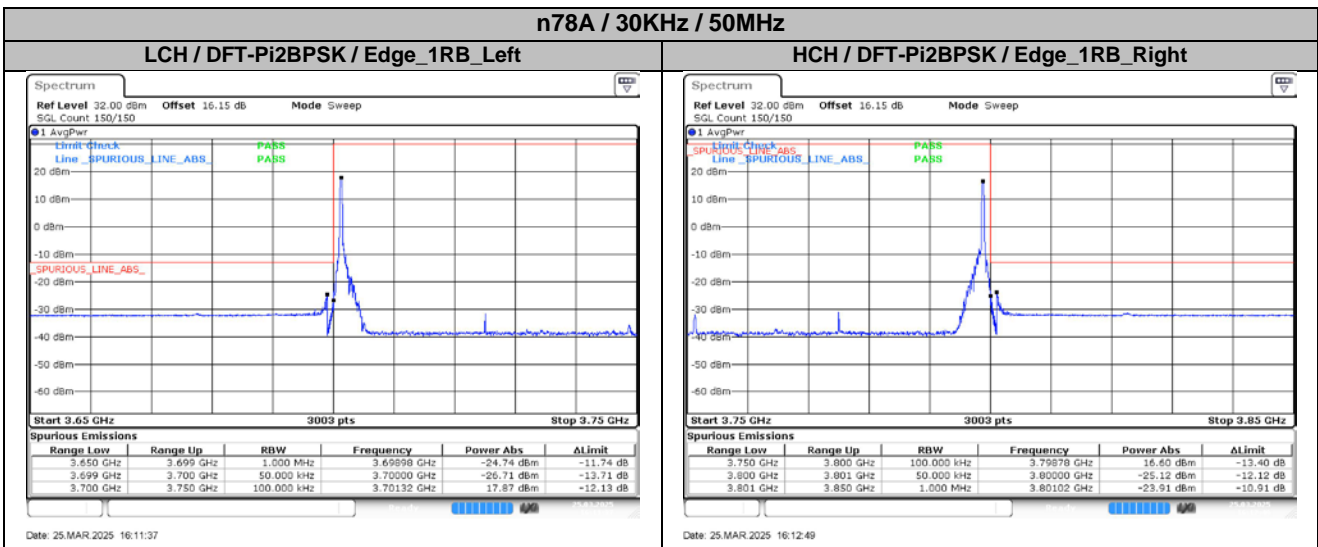
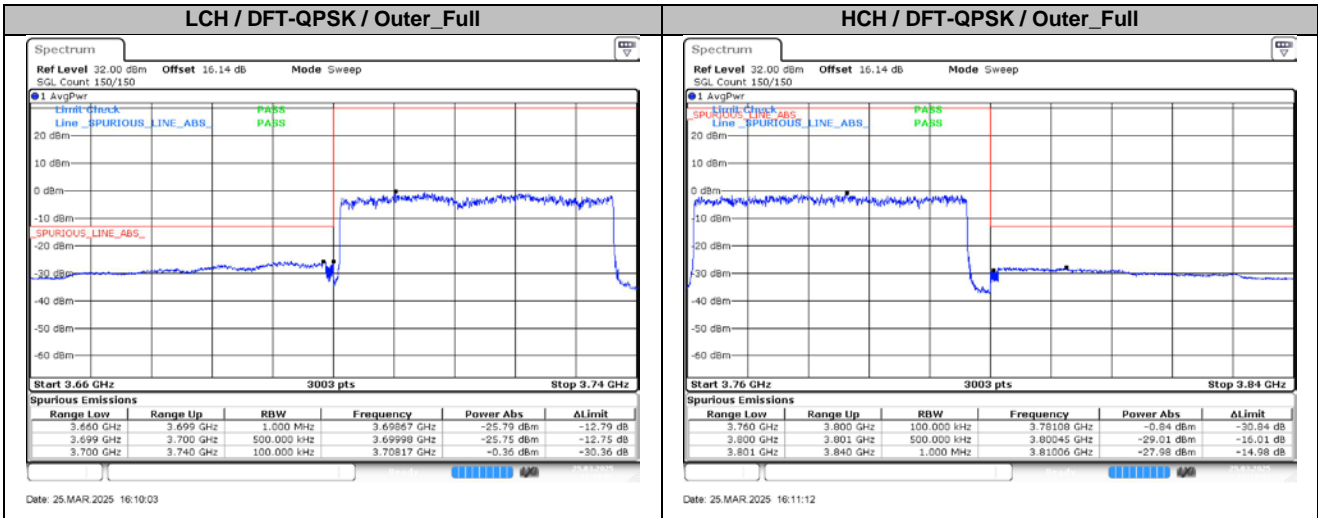


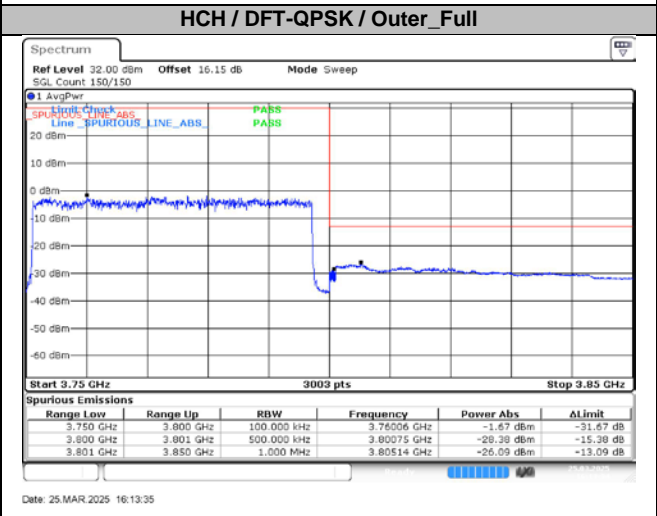
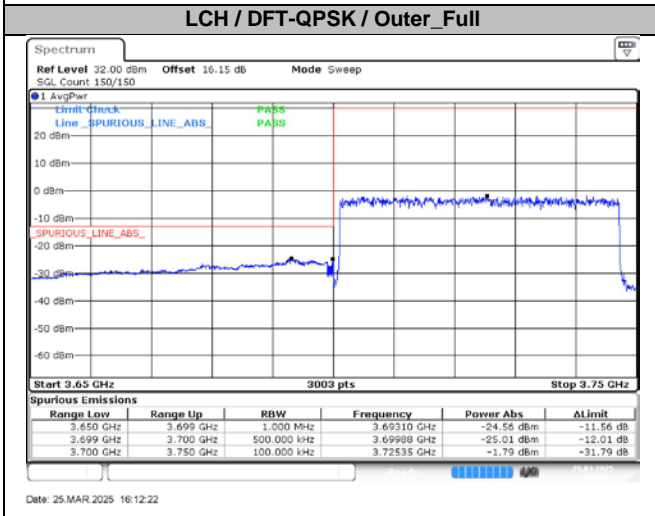
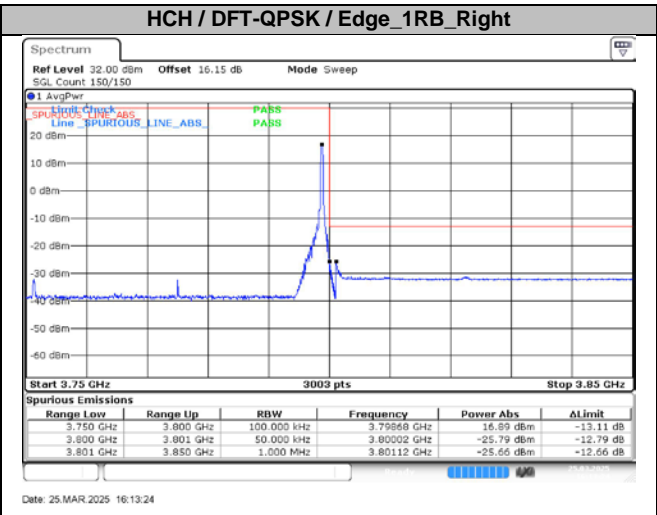
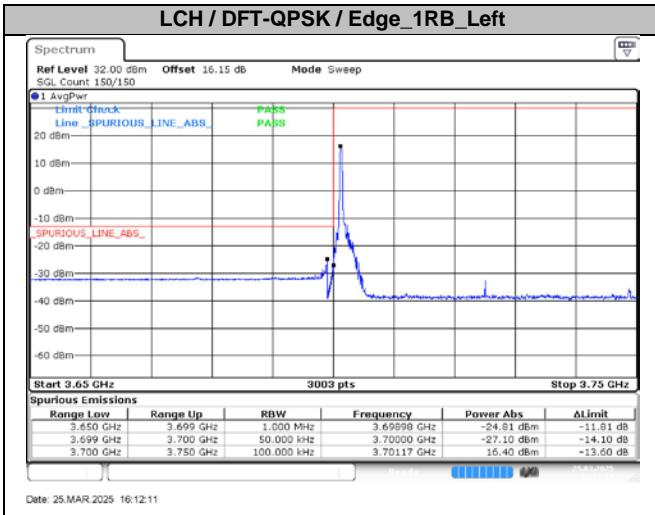
Date: 25.MAR.2025 16:09:52

HCH / DFT-QPSK / Edge_1RB_Right

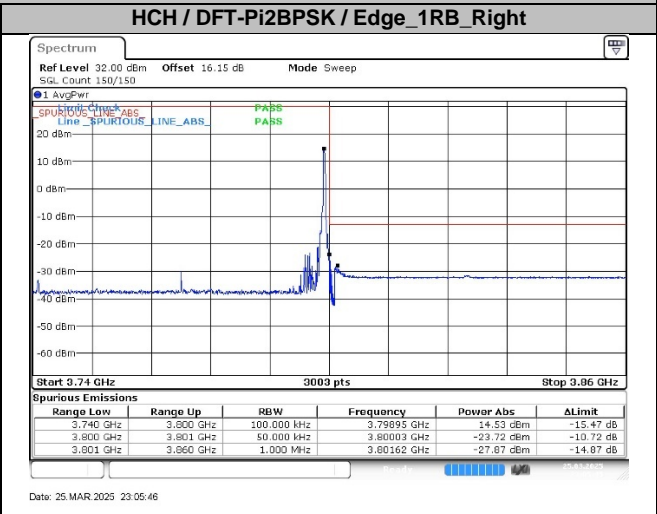
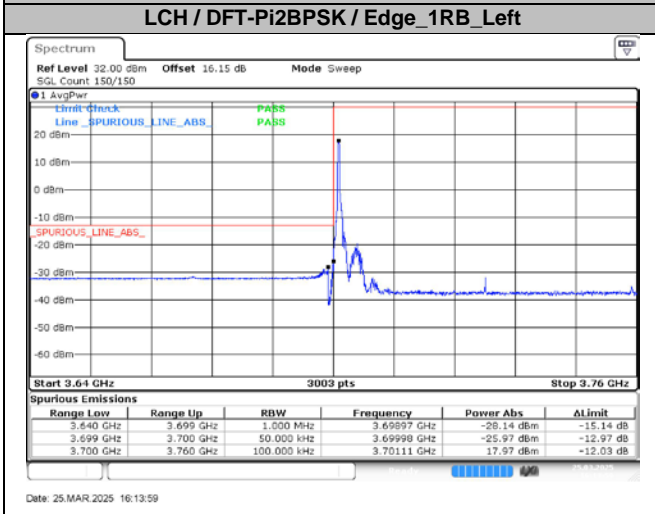


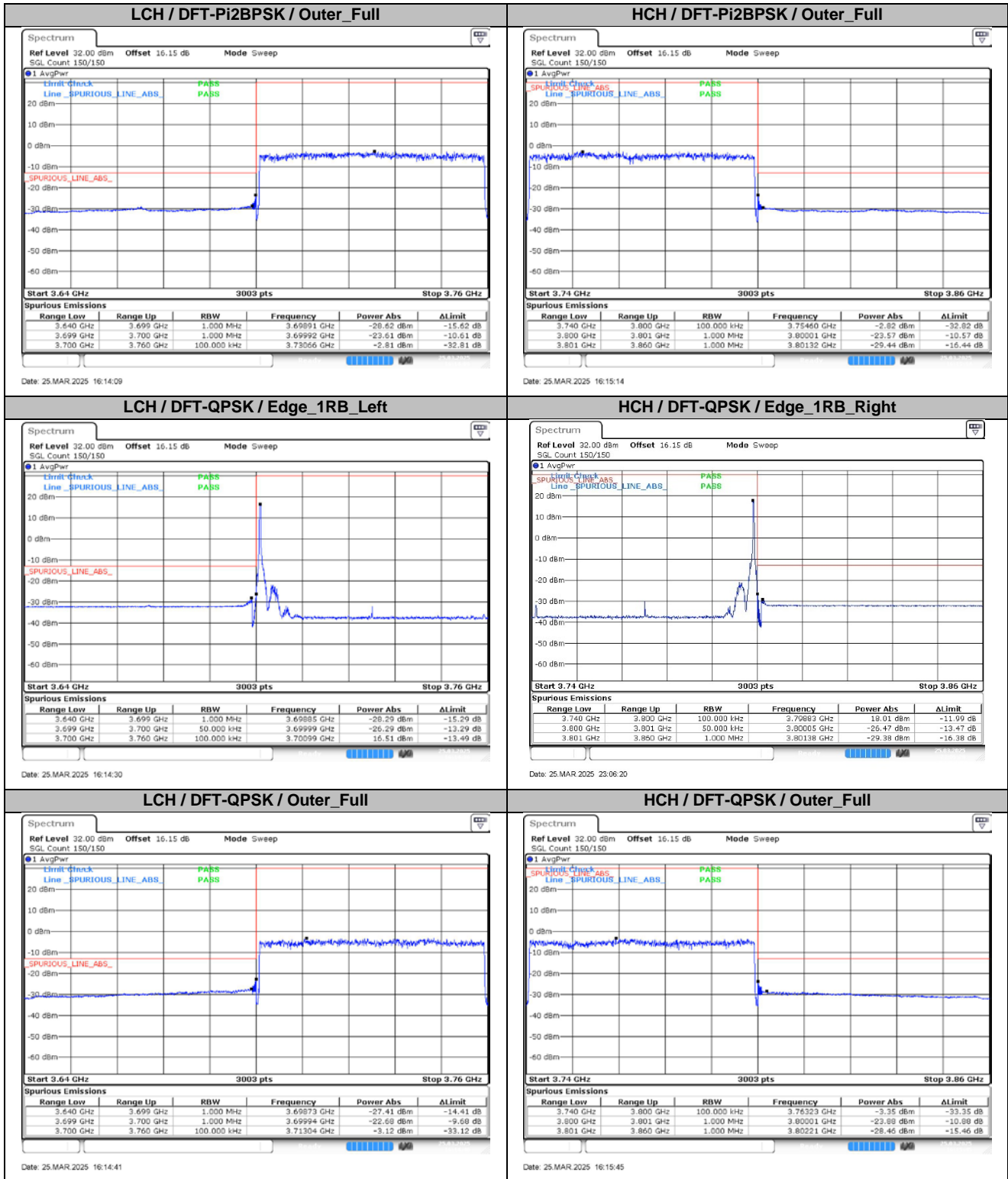
Date: 25.MAR.2025 16:11:02





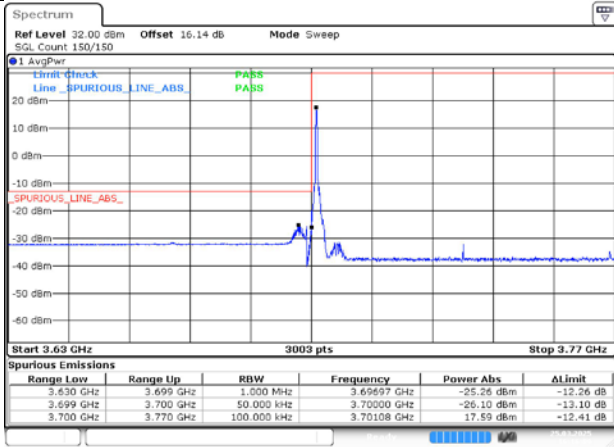
n78A / 30KHz / 60MHz





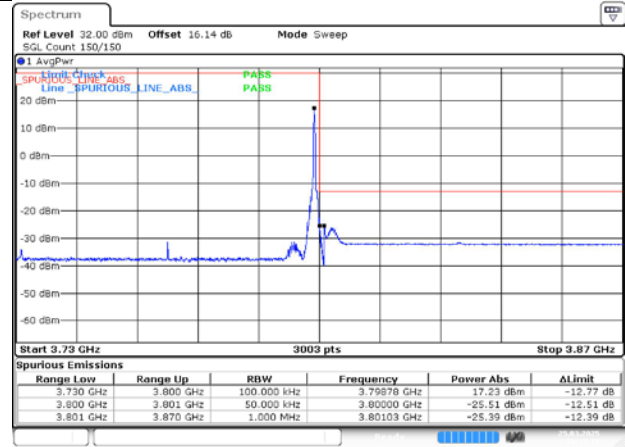
n78A / 30KHz / 70MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



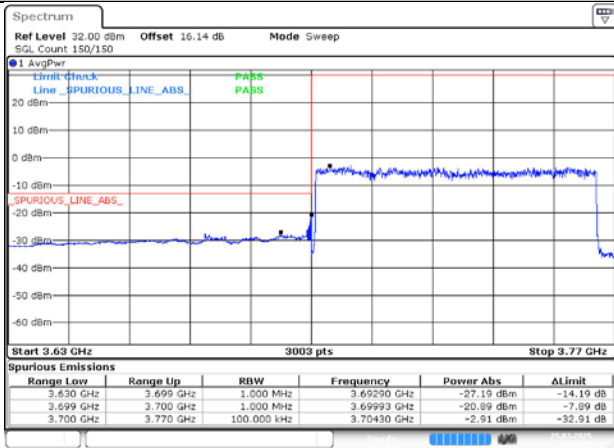
Date: 25.MAR.2025 16:16:10

HCH / DFT-Pi2BPSK / Edge_1RB_Right



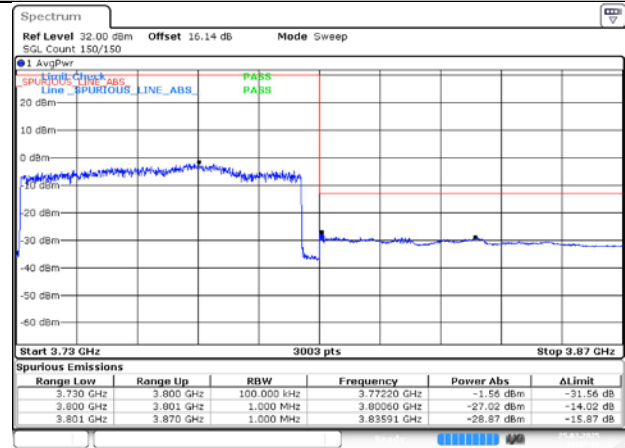
Date: 25.MAR.2025 16:17:17

LCH / DFT-Pi2BPSK / Outer_Full



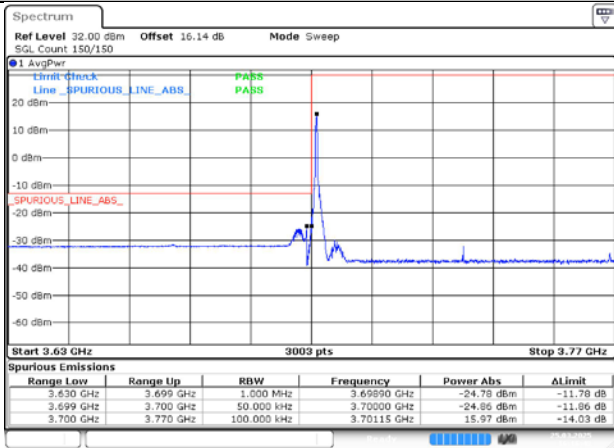
Date: 25.MAR.2025 16:16:21

HCH / DFT-Pi2BPSK / Outer_Full



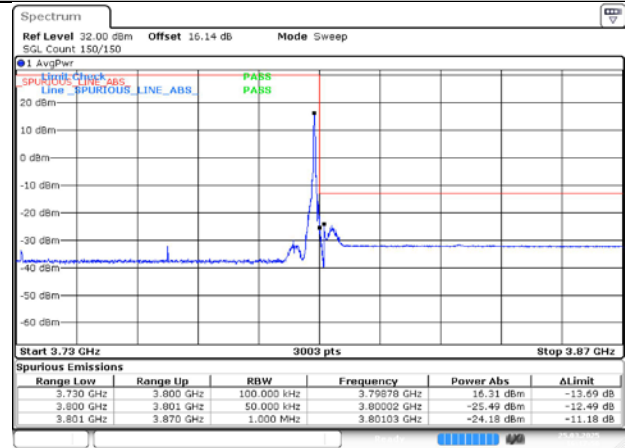
Date: 25.MAR.2025 16:17:26

LCH / DFT-QPSK / Edge_1RB_Left



Date: 25.MAR.2025 16:16:42

HCH / DFT-QPSK / Edge_1RB_Right



Date: 25.MAR.2025 16:17:50