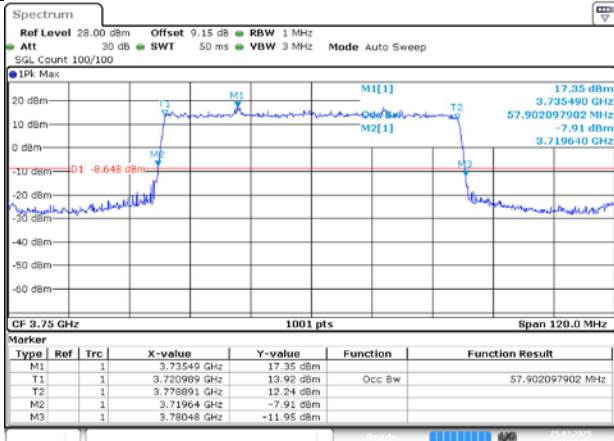


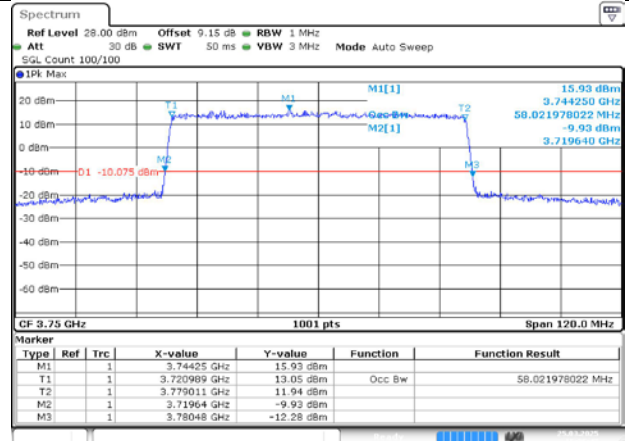
n78A / 30KHz / 60MHz

MCH / DFT-Pi2BPSK / Outer_Full



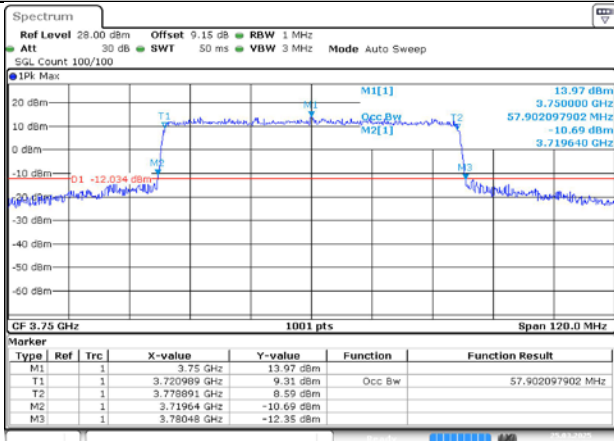
Date: 25.MAR.2025 15:36:38

MCH / DFT-QPSK / Outer_Full



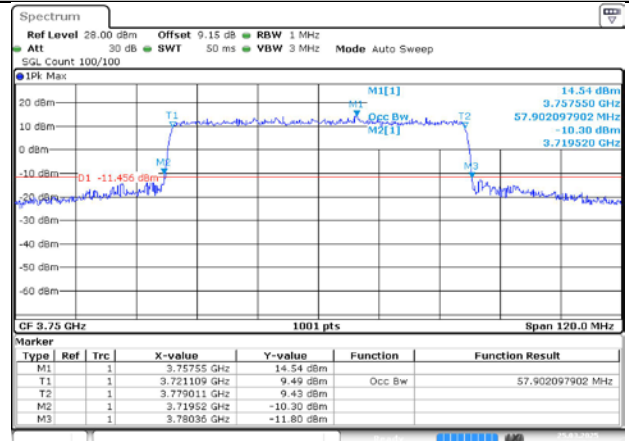
Date: 25.MAR.2025 15:36:47

MCH / CP-QPSK / Outer_Full



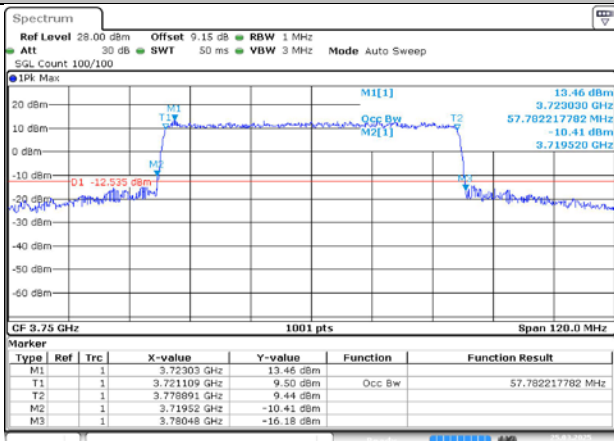
Date: 25.MAR.2025 15:36:57

MCH / CP-16QAM / Outer_Full



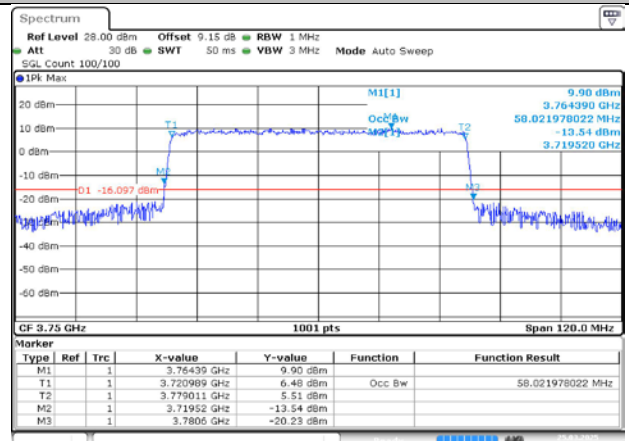
Date: 25.MAR.2025 15:37:06

MCH / CP-64QAM / Outer_Full

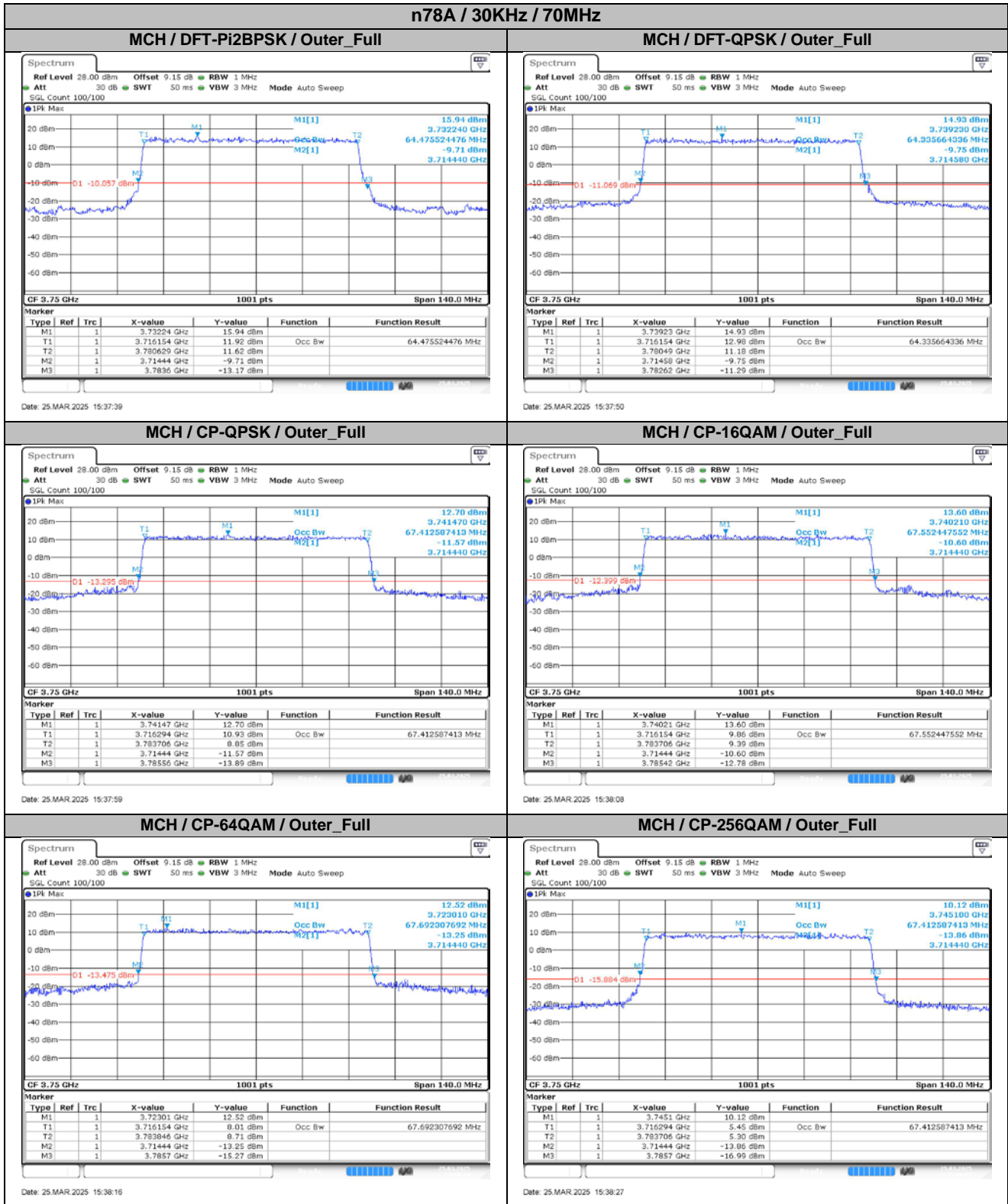


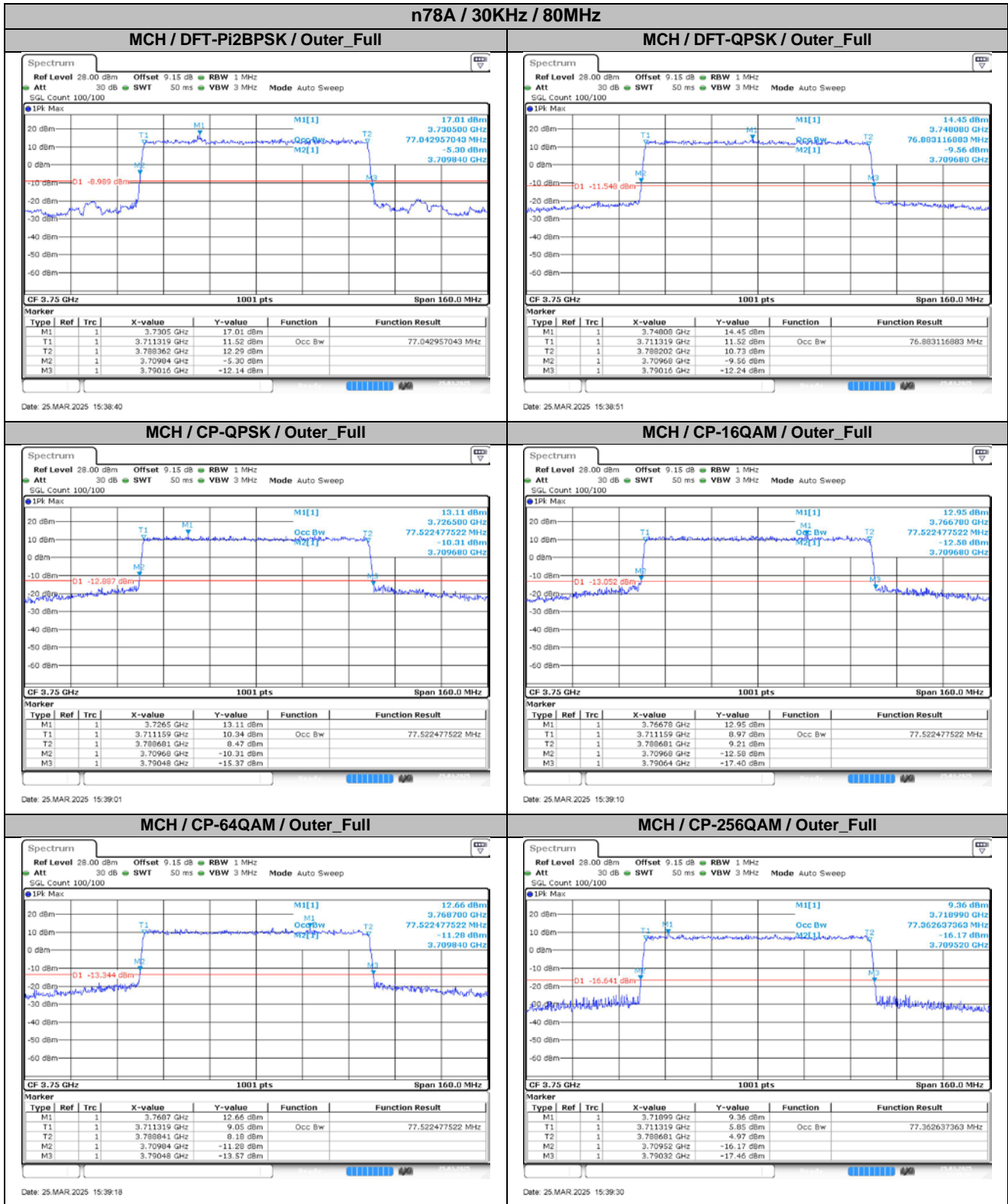
Date: 25.MAR.2025 15:37:14

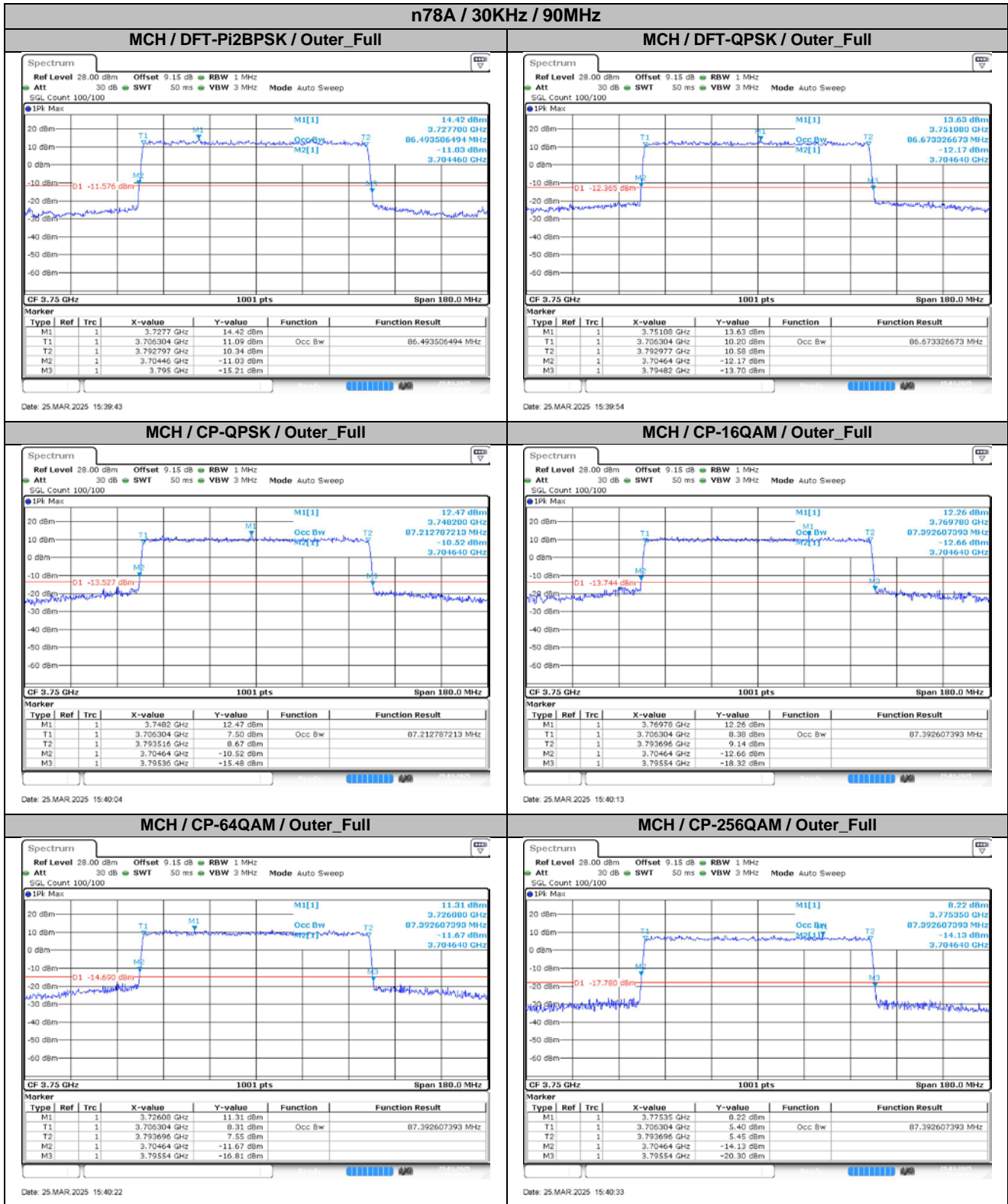
MCH / CP-256QAM / Outer_Full



Date: 25.MAR.2025 15:37:26

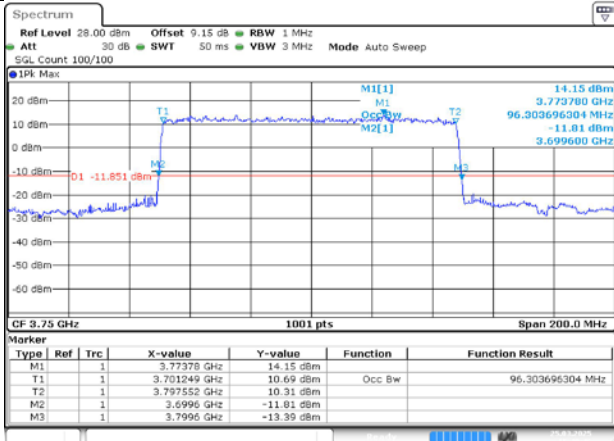






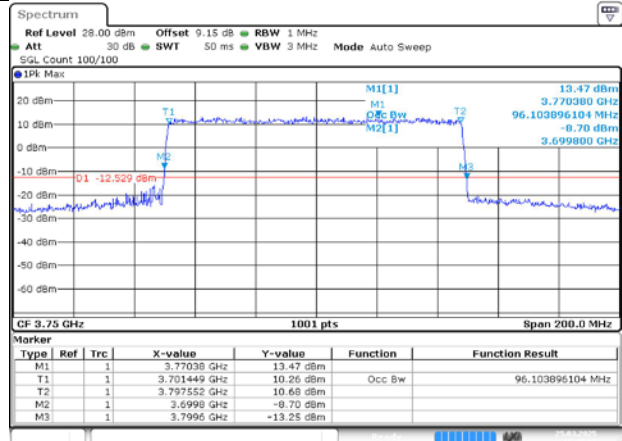
n78A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer_Full



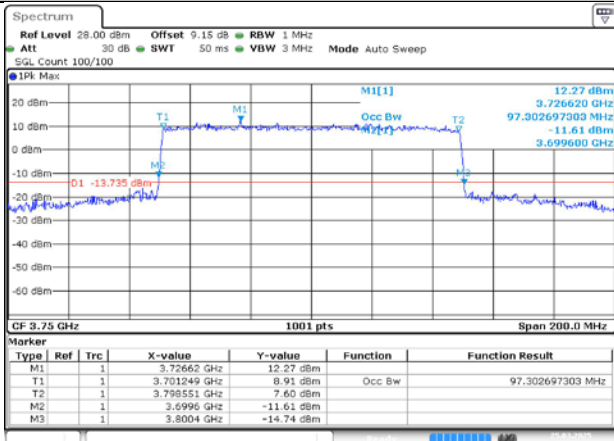
Date: 25.MAR.2025 15:40:46

MCH / DFT-QPSK / Outer_Full



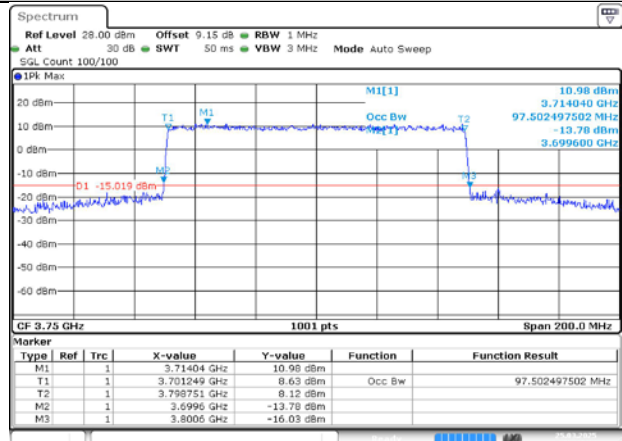
Date: 25.MAR.2025 15:40:58

MCH / CP-QPSK / Outer_Full



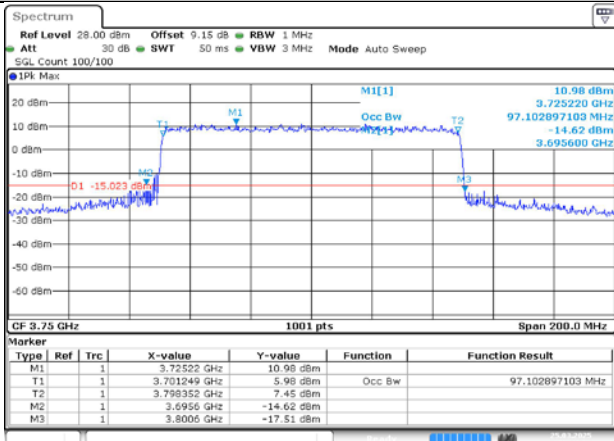
Date: 25.MAR.2025 15:41:07

MCH / CP-16QAM / Outer_Full



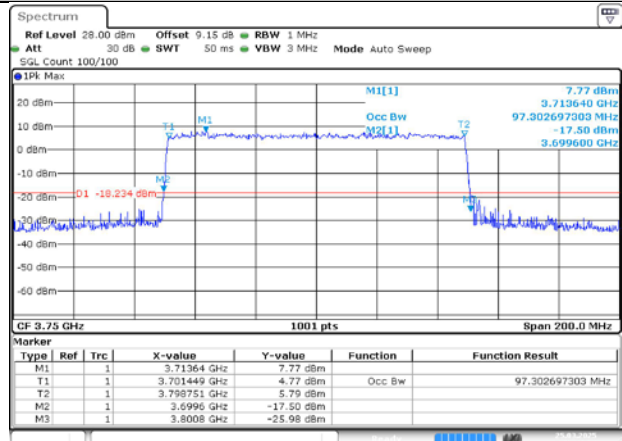
Date: 25.MAR.2025 15:41:16

MCH / CP-64QAM / Outer_Full



Date: 25.MAR.2025 15:41:25

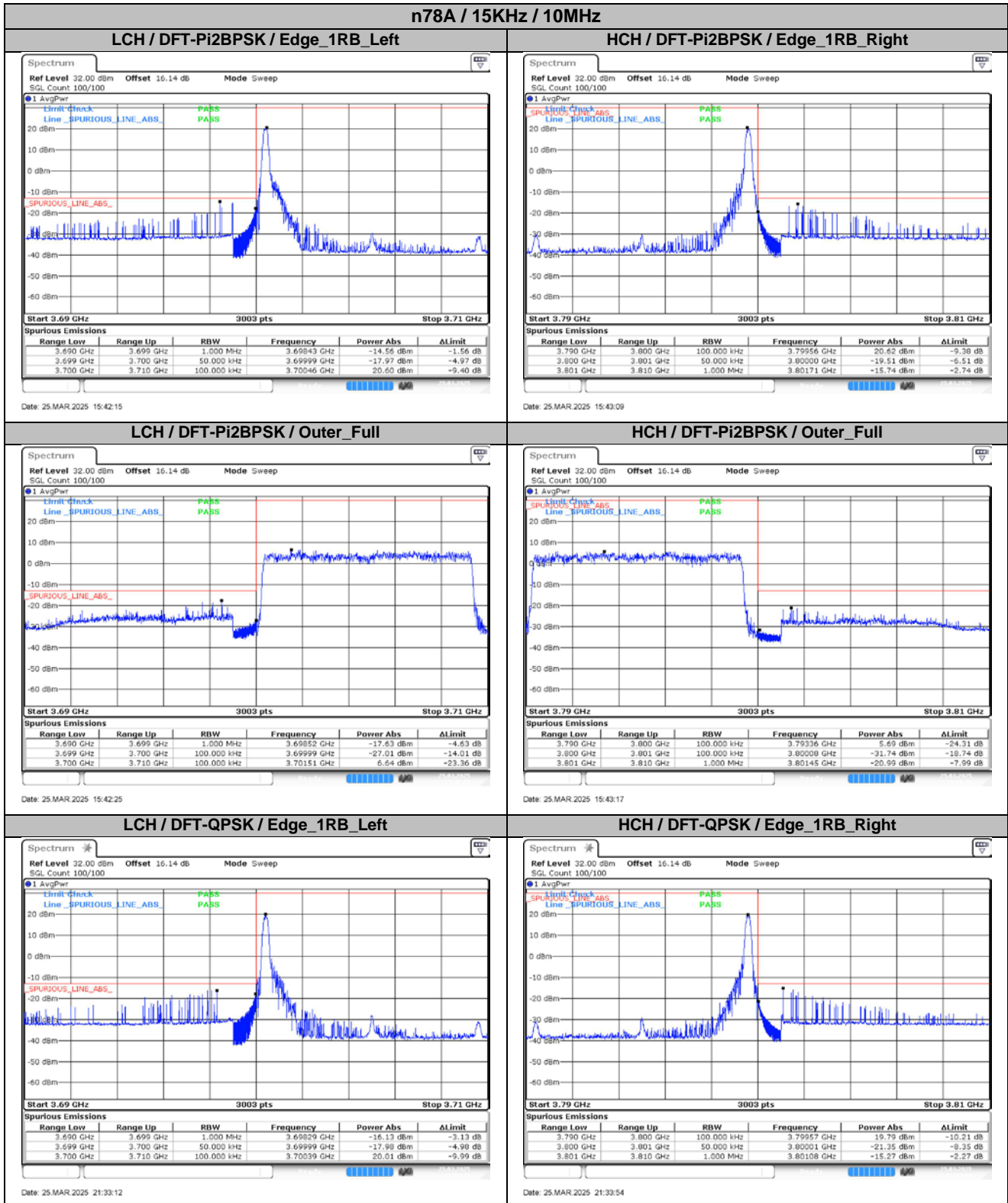
MCH / CP-256QAM / Outer_Full

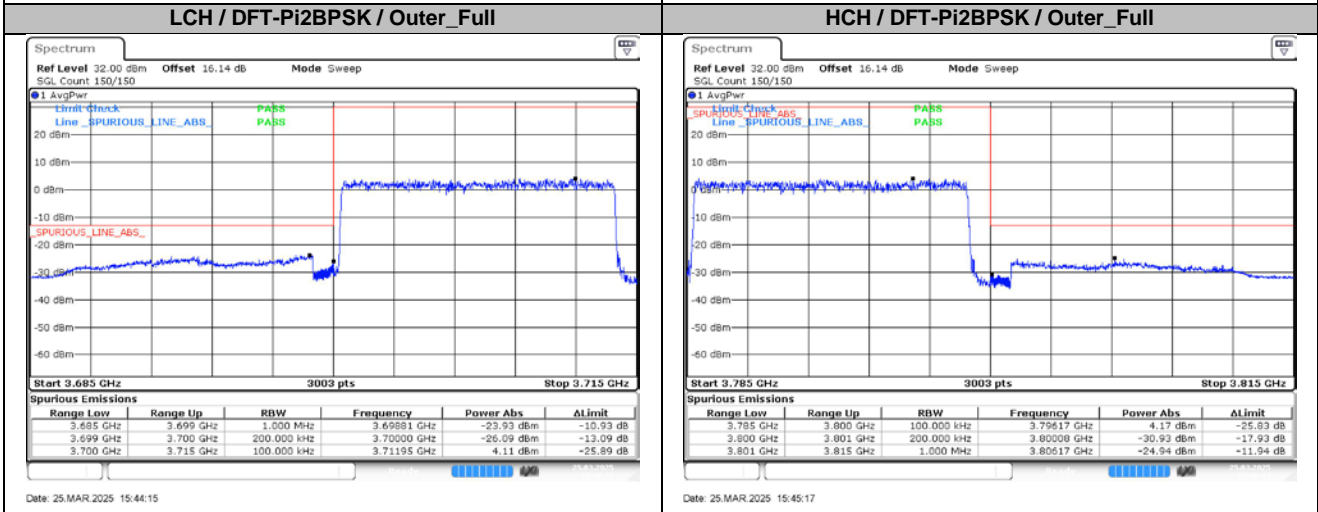
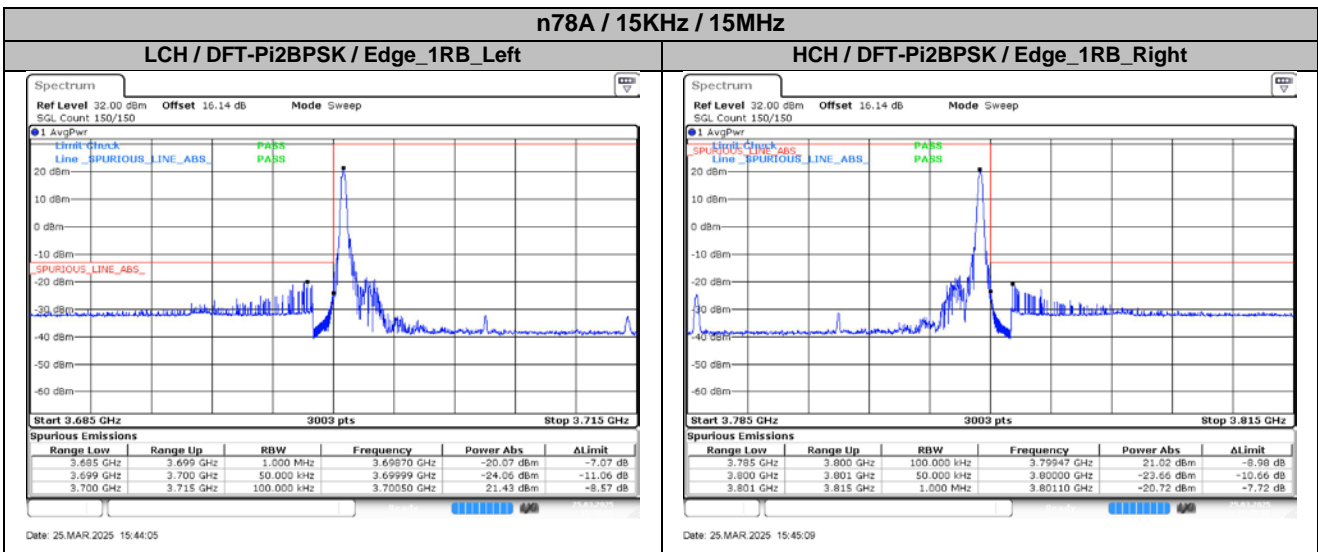
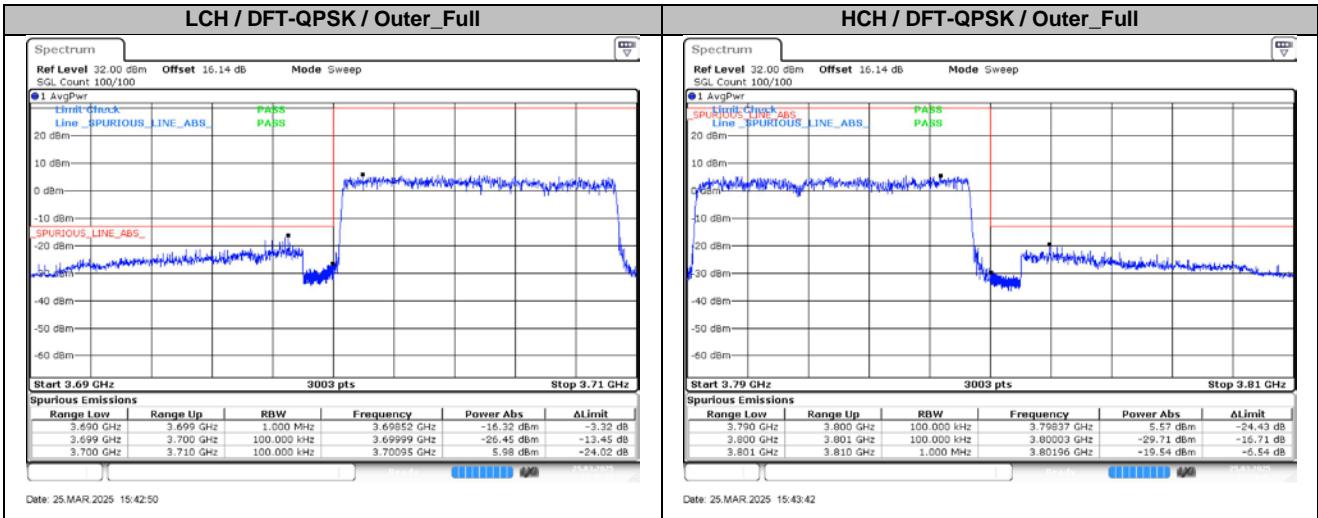


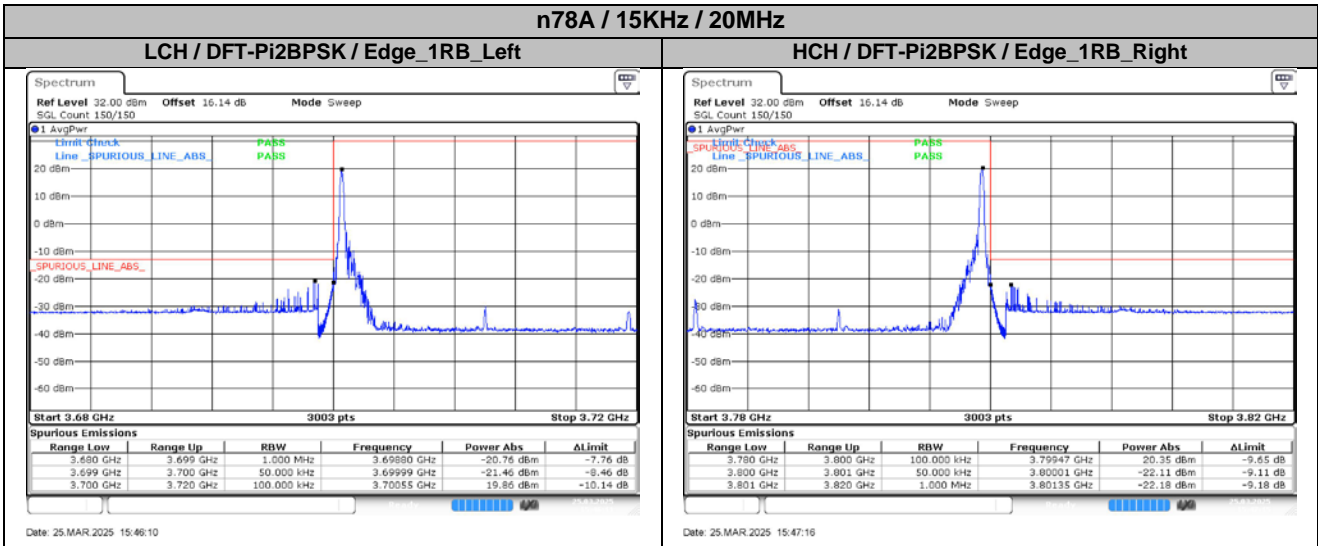
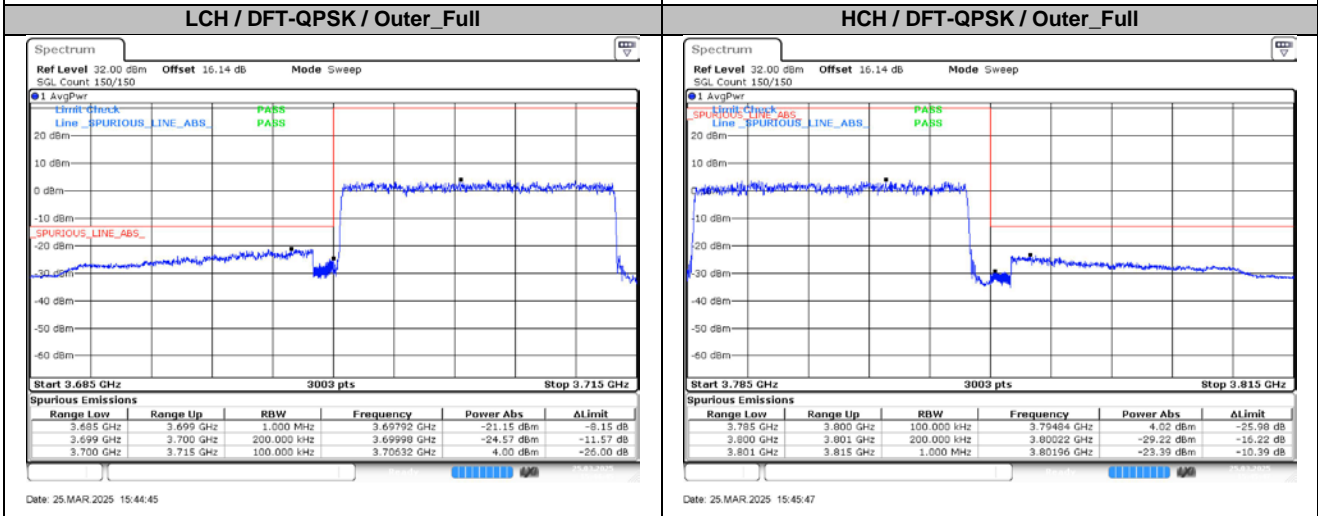
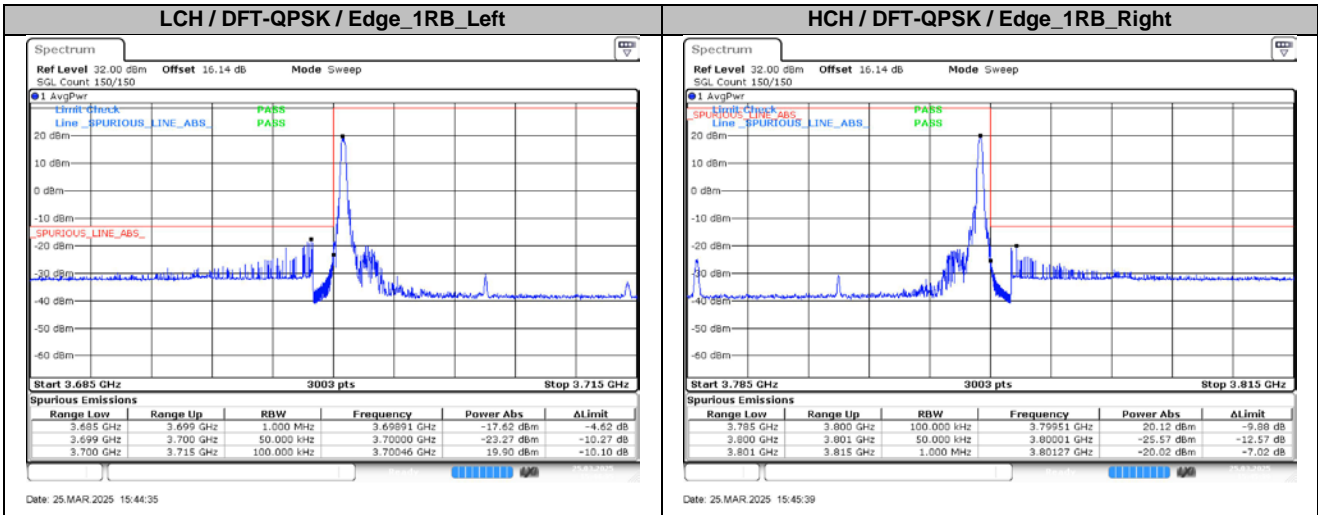
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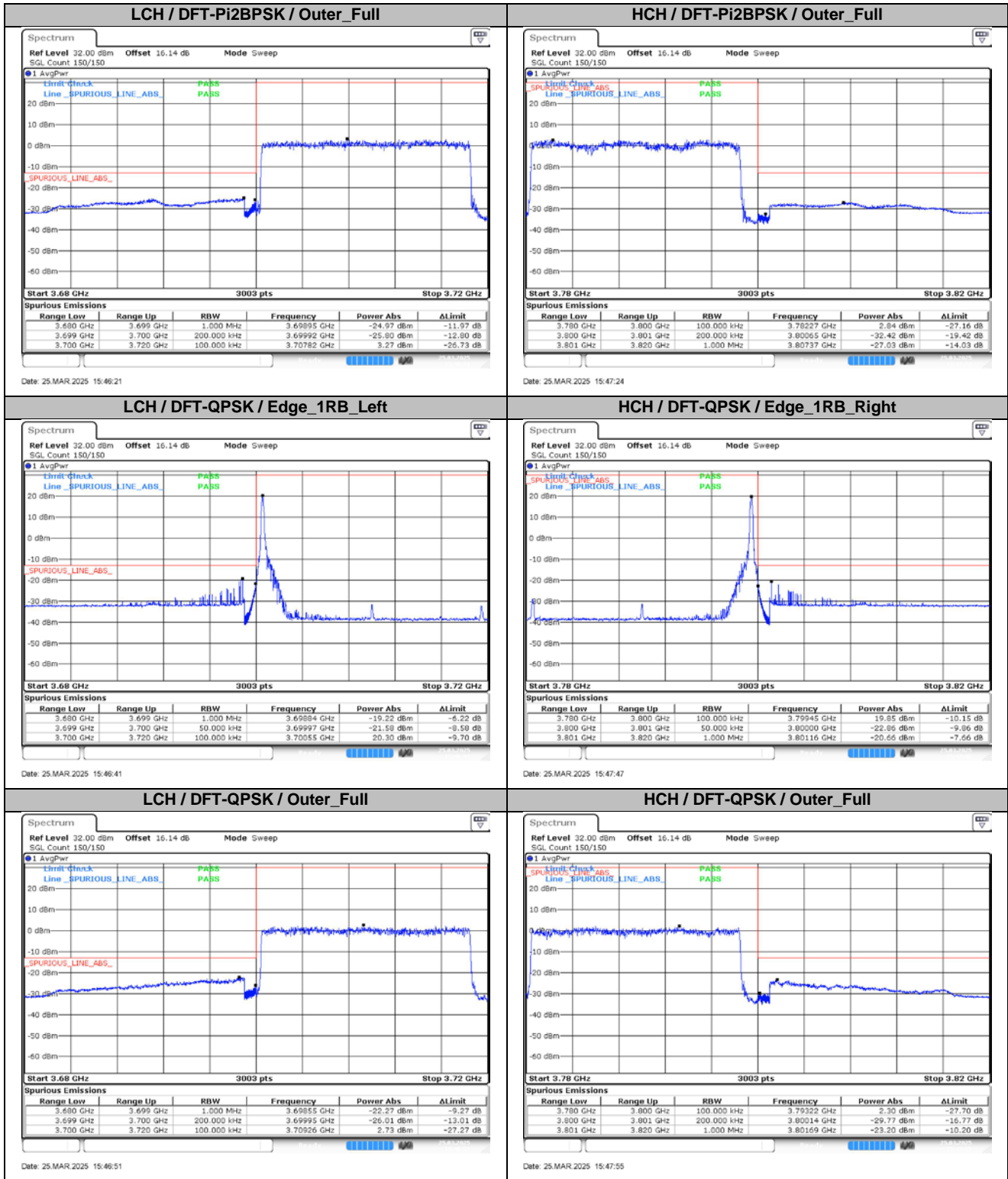
5. Conducted Band Edges

5.1. Test Plots for SCS=15KHz



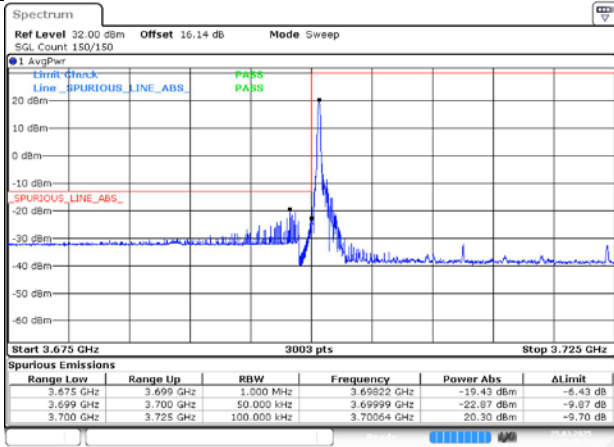






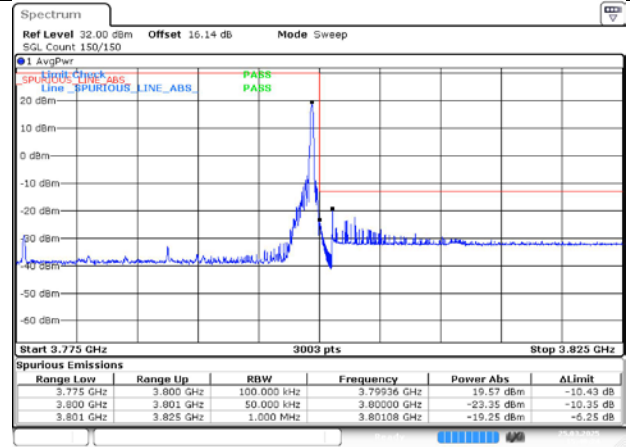
n78A / 15KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



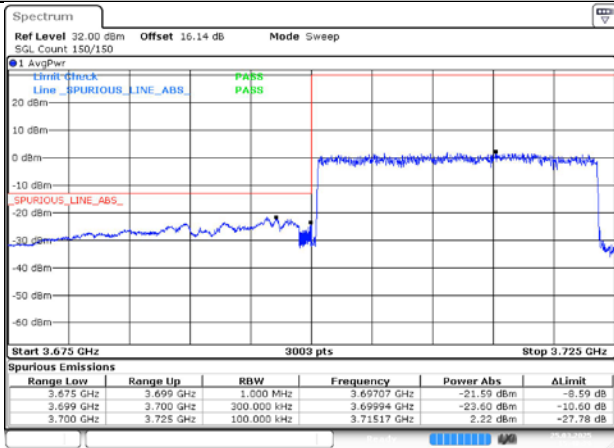
Date: 25.MAR.2025 15:48:19

HCH / DFT-Pi2BPSK / Edge_1RB_Right



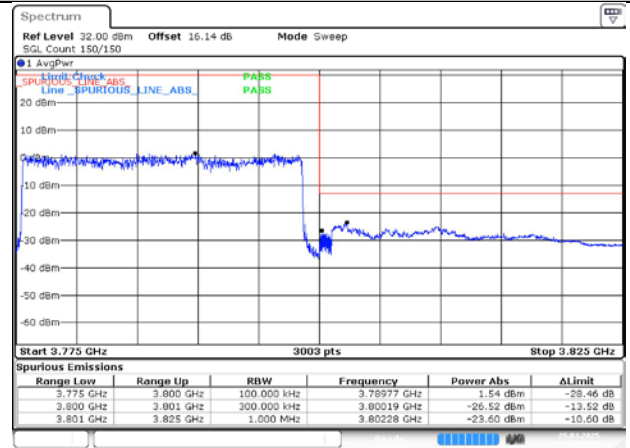
Date: 25.MAR.2025 15:49:25

LCH / DFT-Pi2BPSK / Outer_Full



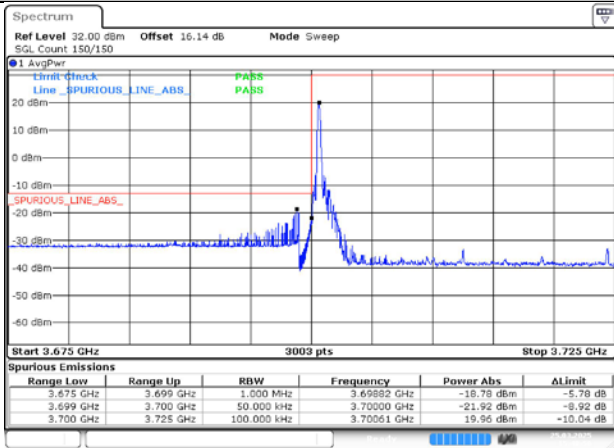
Date: 25.MAR.2025 15:48:29

HCH / DFT-Pi2BPSK / Outer_Full



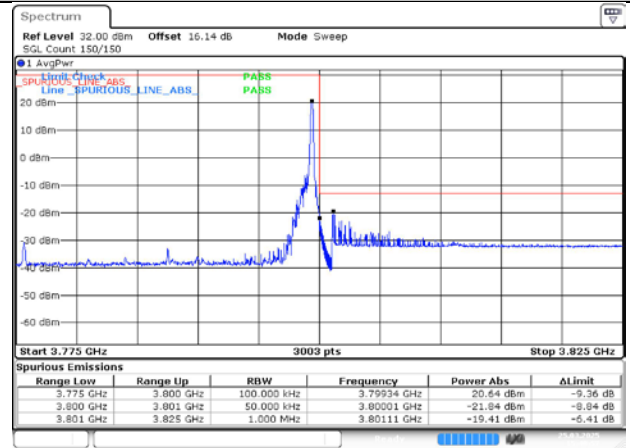
Date: 25.MAR.2025 15:49:34

LCH / DFT-QPSK / Edge_1RB_Left



Date: 25.MAR.2025 15:48:50

HCH / DFT-QPSK / Edge_1RB_Right



Date: 25.MAR.2025 15:49:57

