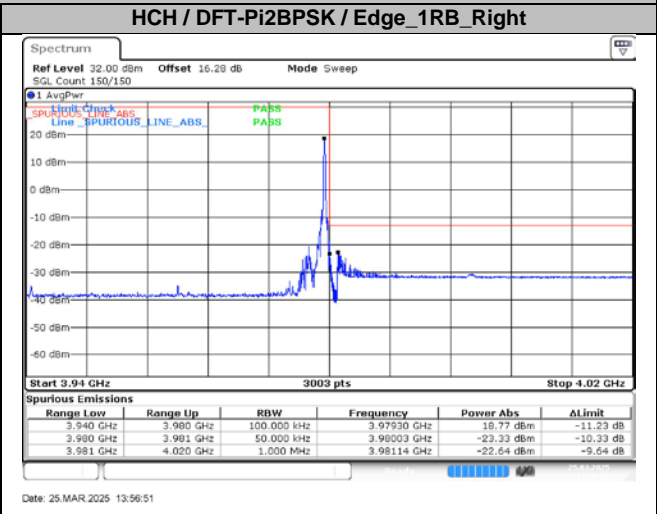
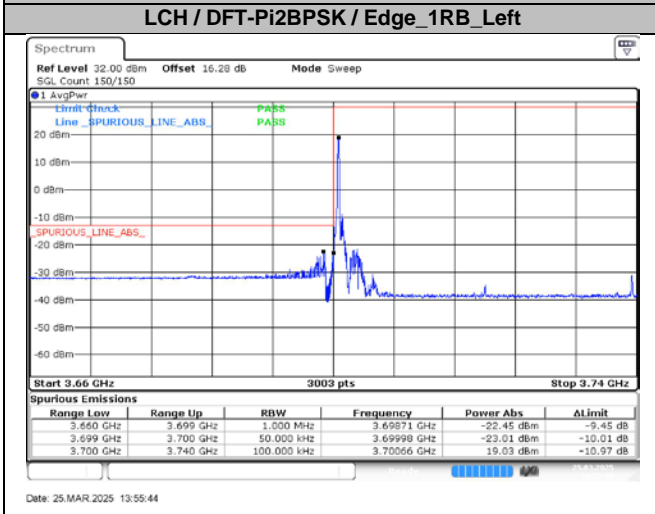
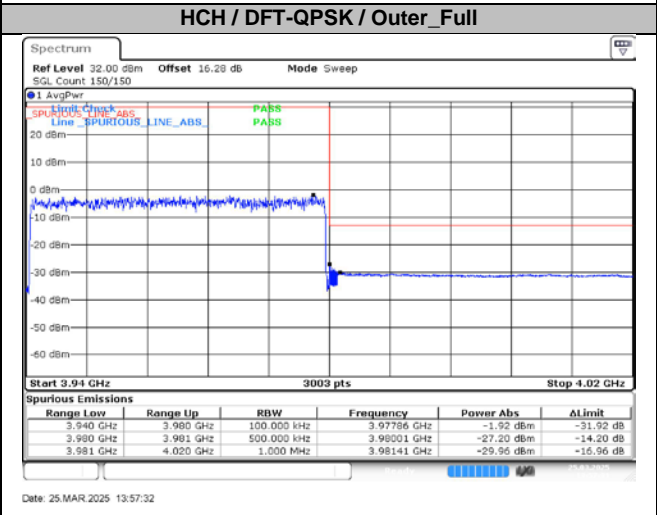
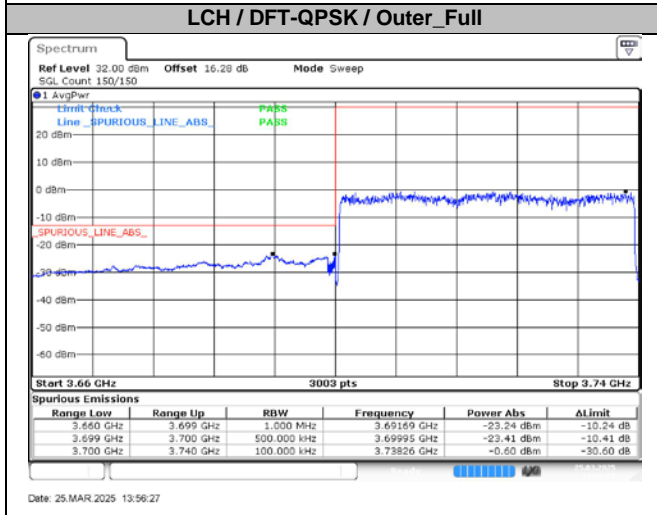
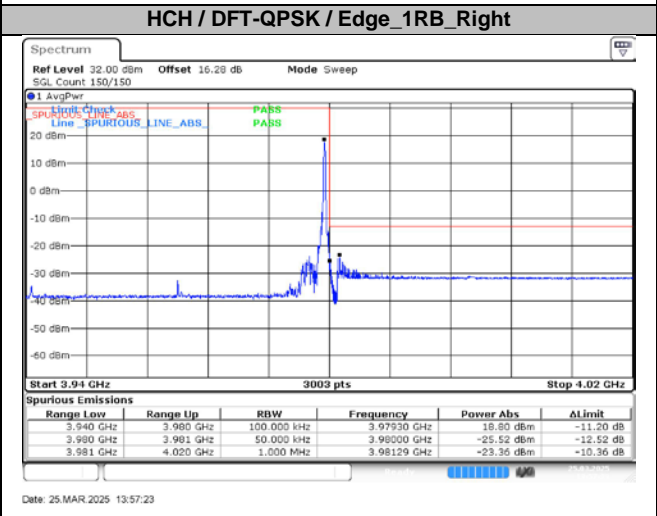
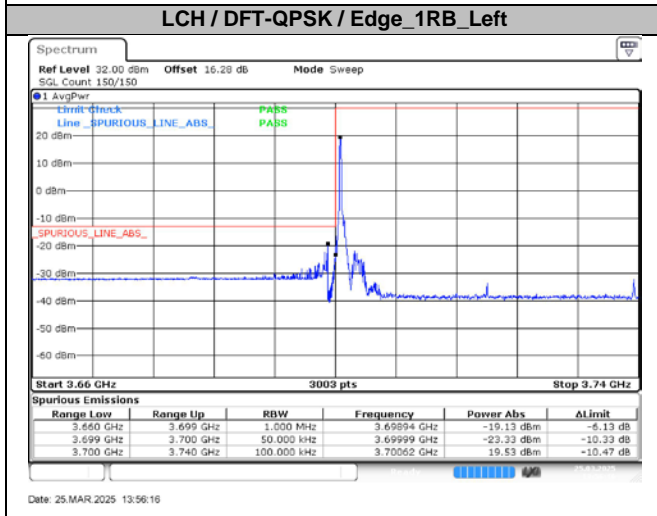
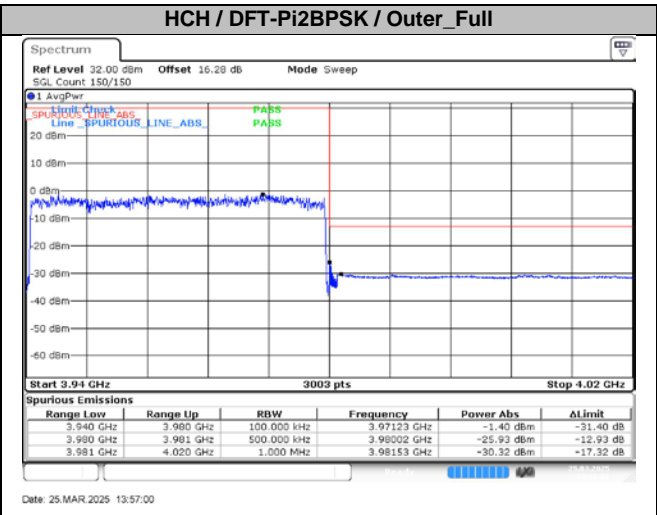
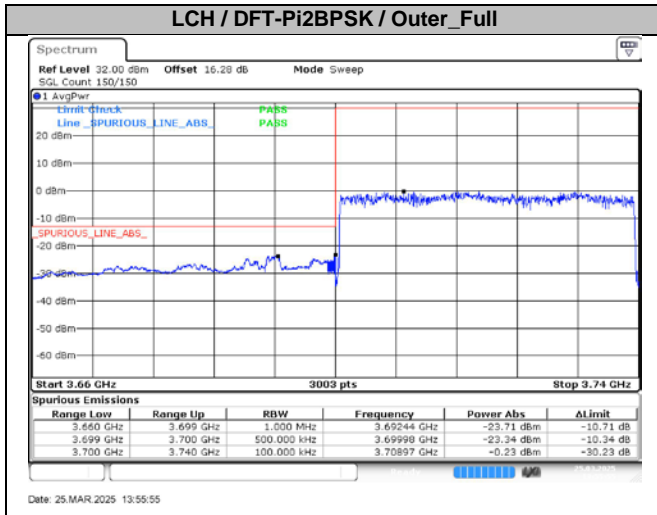


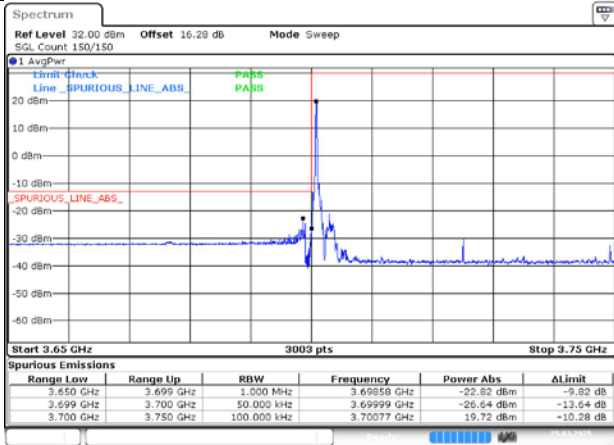
## n77A / 15KHz / 40MHz





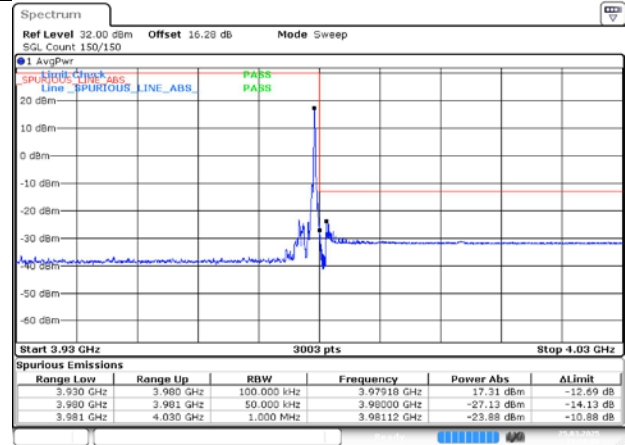
n77A / 15KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



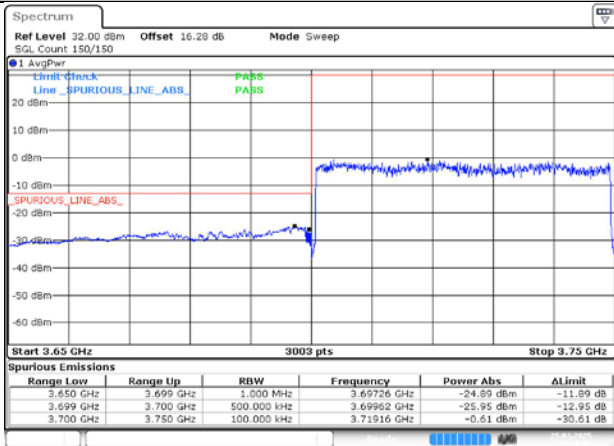
Date: 25.MAR.2025 13:57:57

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



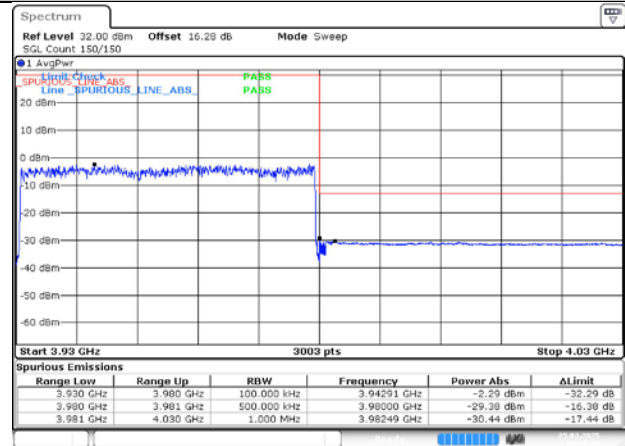
Date: 25.MAR.2025 13:59:09

LCH / DFT-Pi2BPSK / Outer\_Full



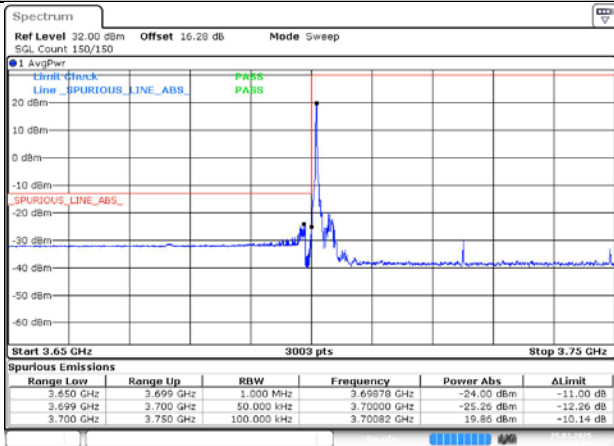
Date: 25.MAR.2025 13:58:08

HCH / DFT-Pi2BPSK / Outer\_Full



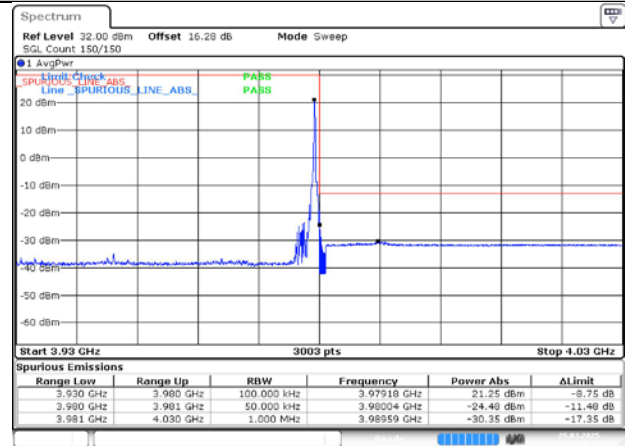
Date: 25.MAR.2025 13:59:18

LCH / DFT-QPSK / Edge\_1RB\_Left

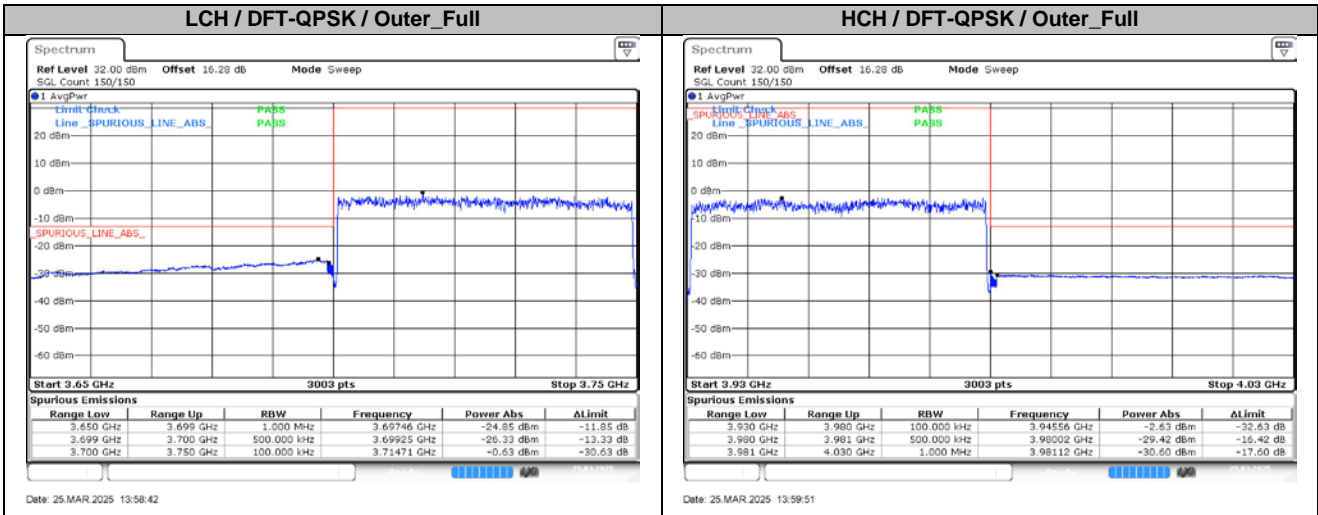


Date: 25.MAR.2025 13:58:29

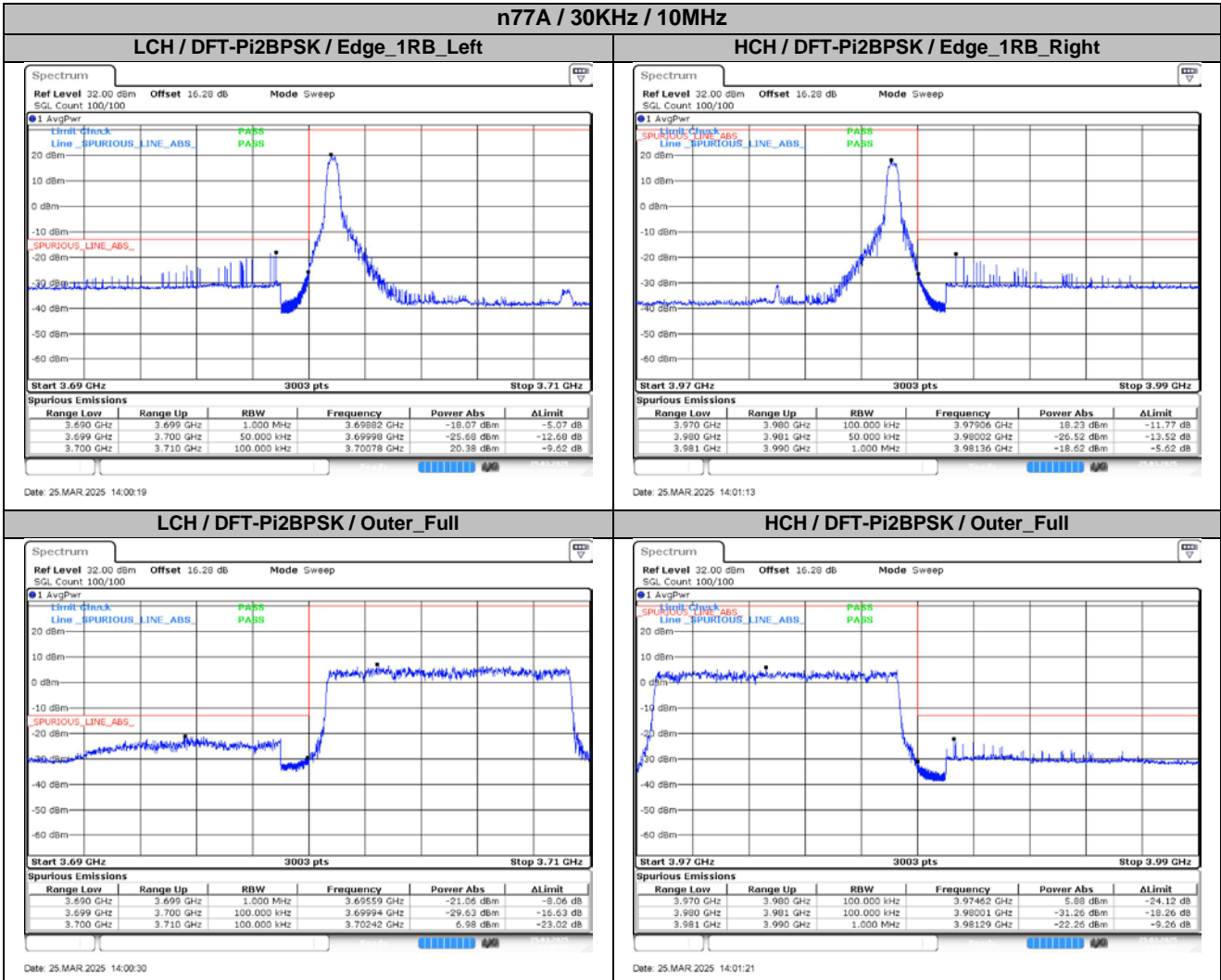
HCH / DFT-QPSK / Edge\_1RB\_Right

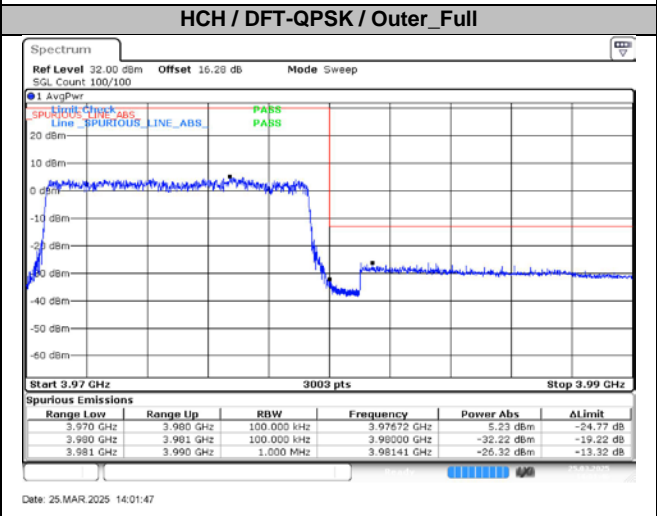
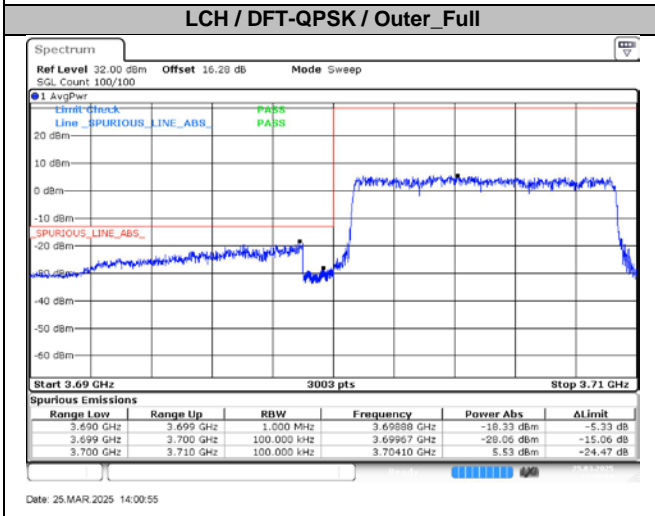
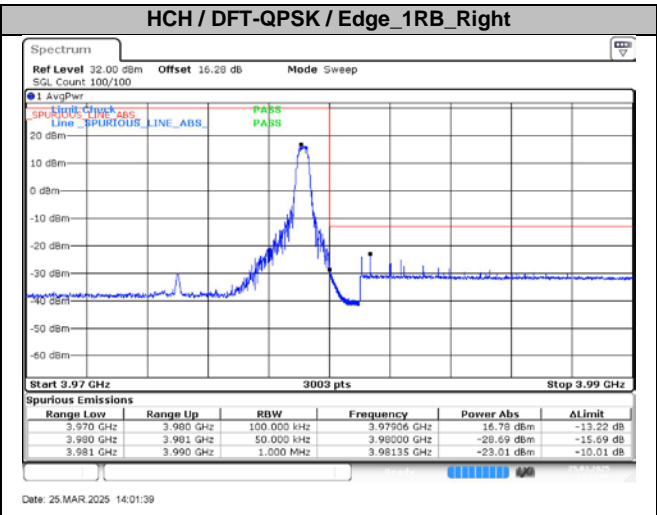
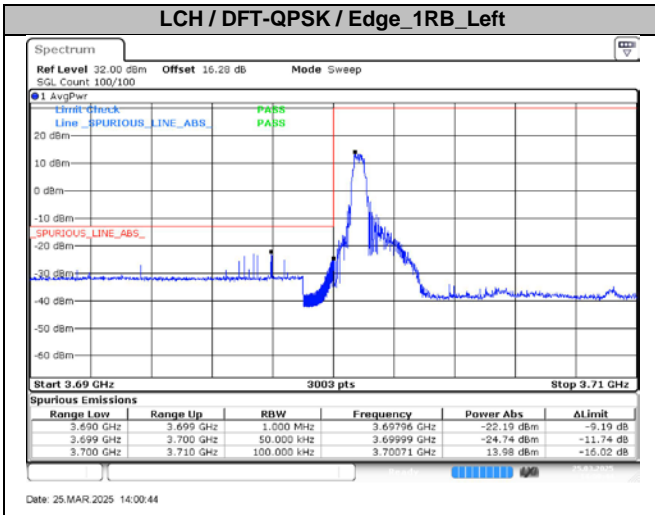


Date: 25.MAR.2025 13:59:42

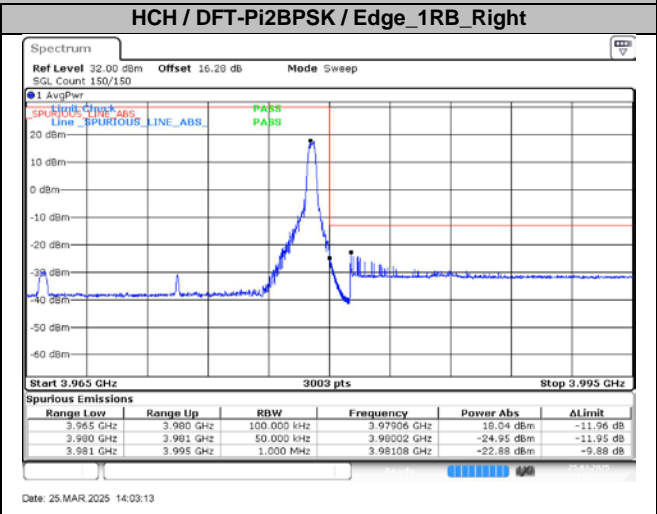
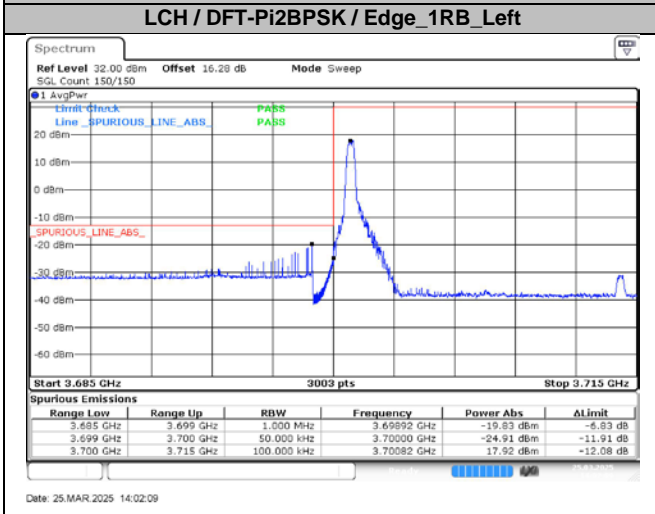


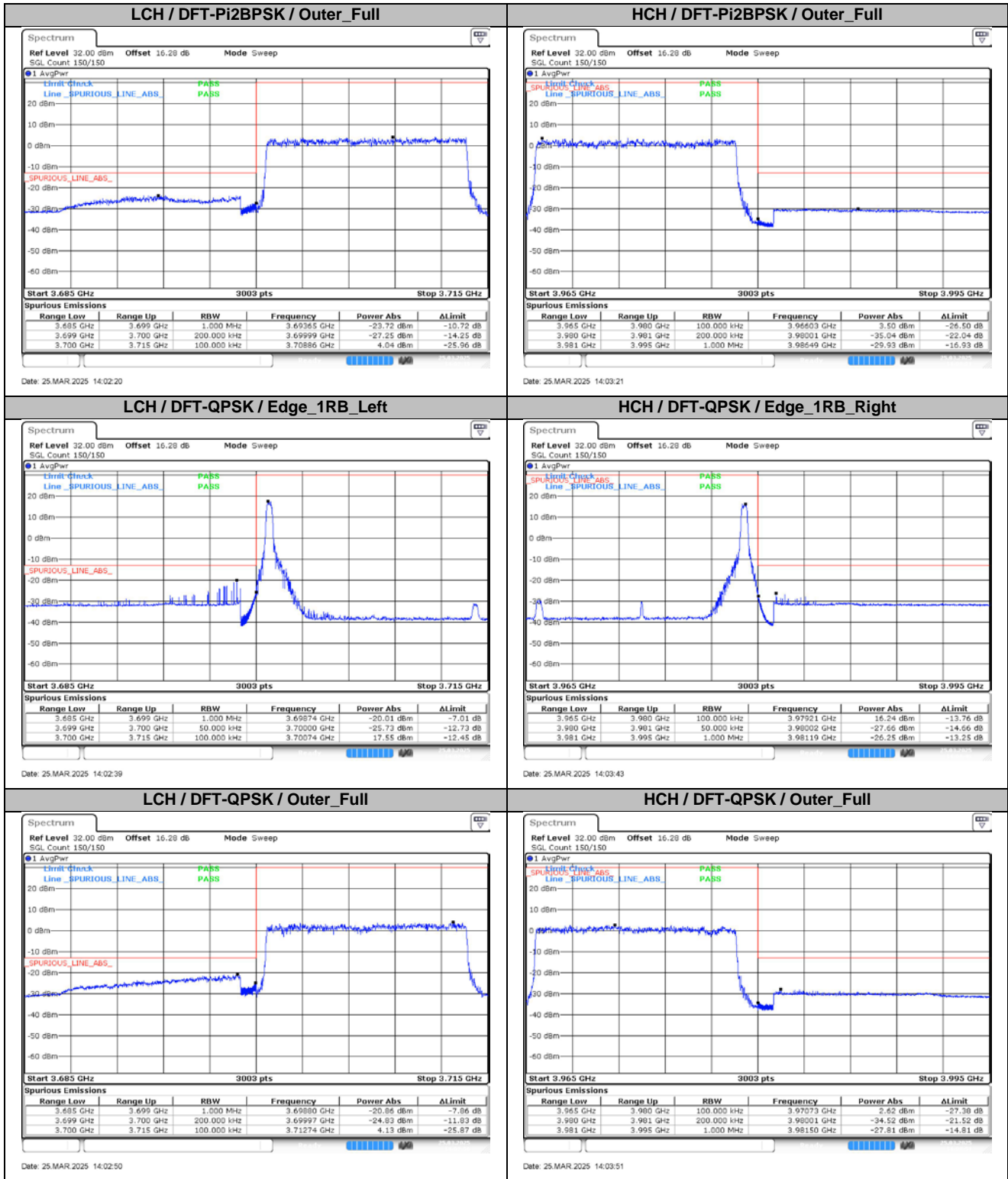
5.2. Test Plots for SCS=30KHz





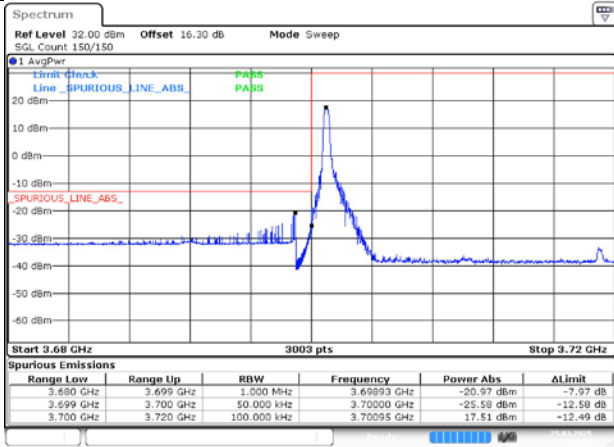
## n77A / 30KHz / 15MHz





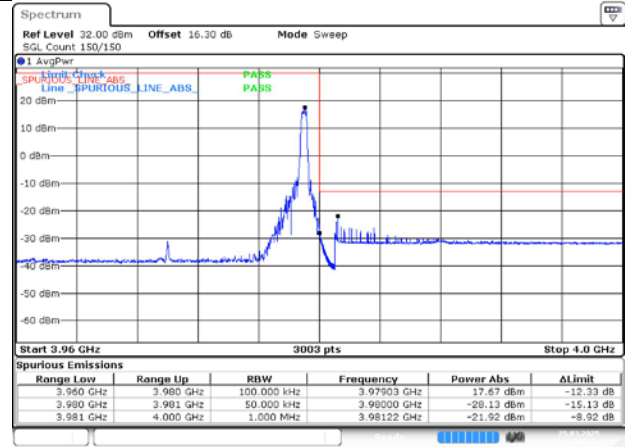
n77A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



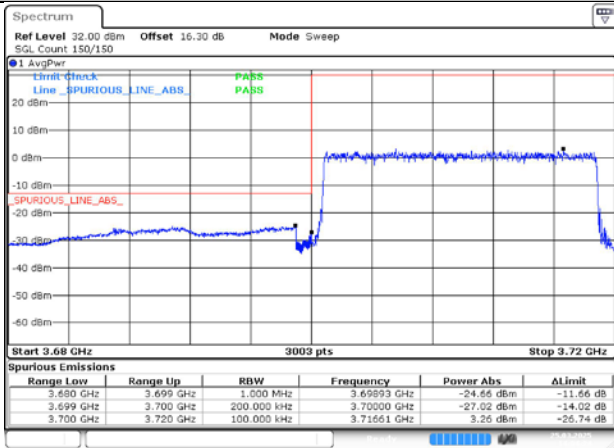
Date: 25.MAR.2025 14:04:14

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



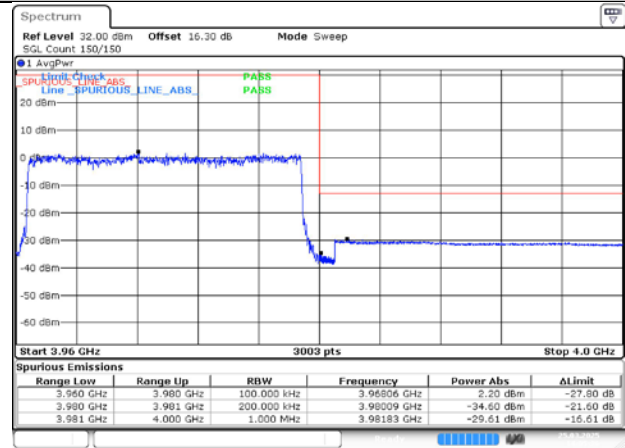
Date: 25.MAR.2025 14:05:18

LCH / DFT-Pi2BPSK / Outer\_Full



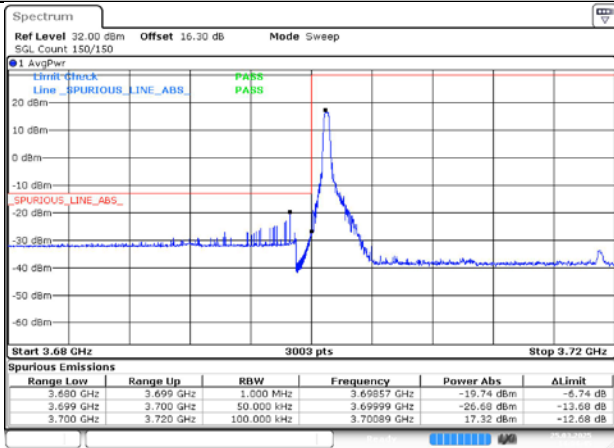
Date: 25.MAR.2025 14:04:24

HCH / DFT-Pi2BPSK / Outer\_Full



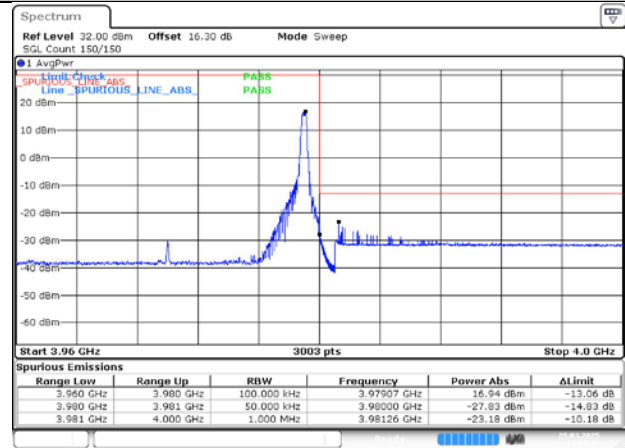
Date: 25.MAR.2025 14:05:26

LCH / DFT-QPSK / Edge\_1RB\_Left

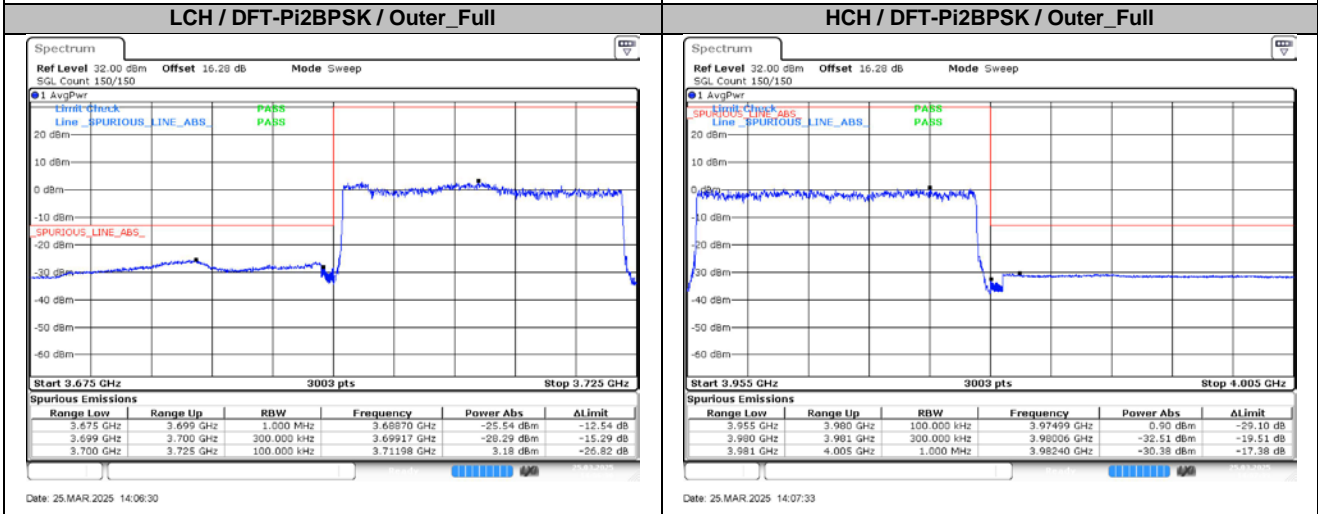
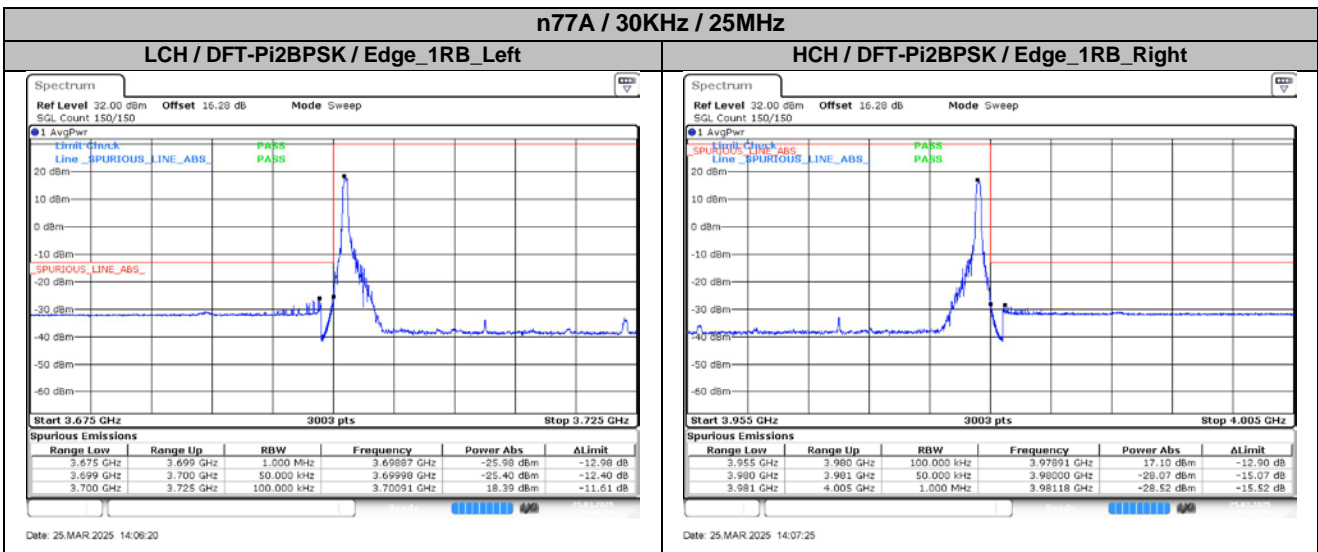
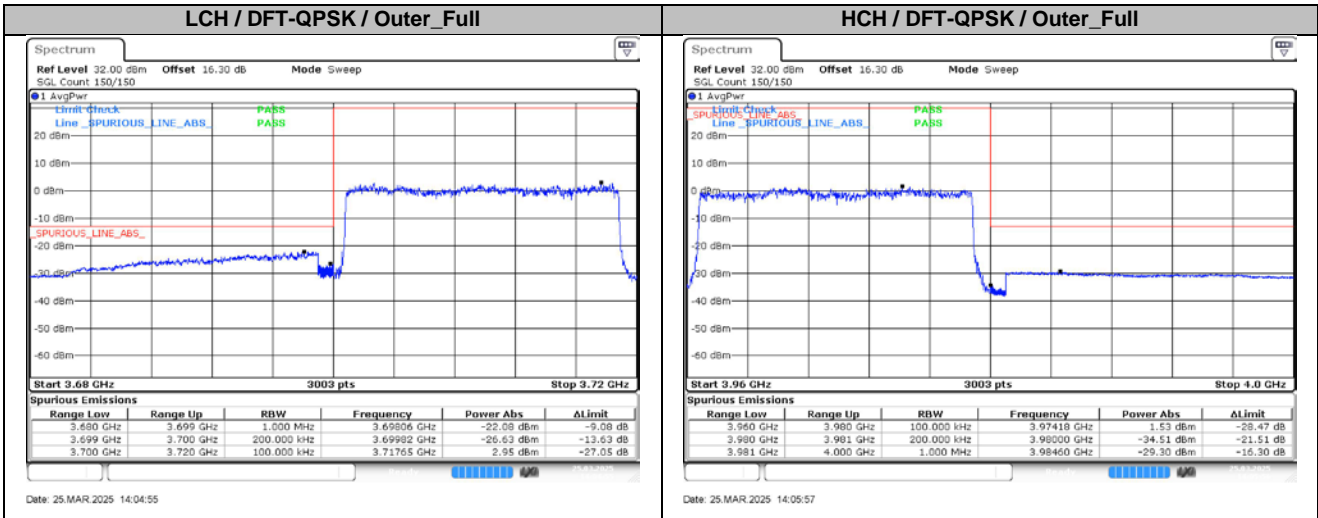


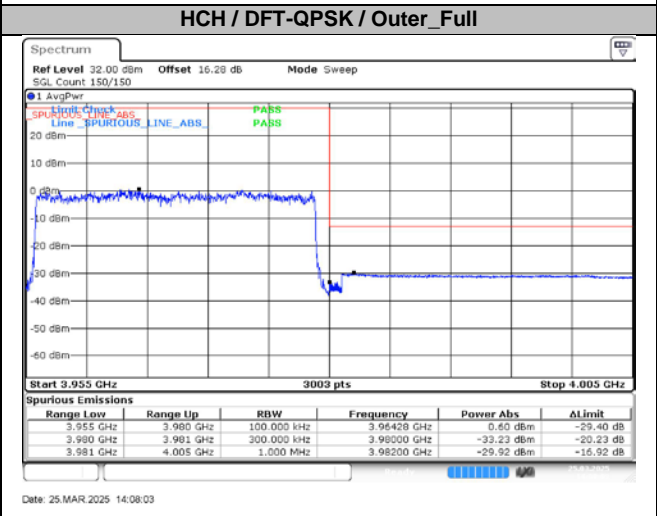
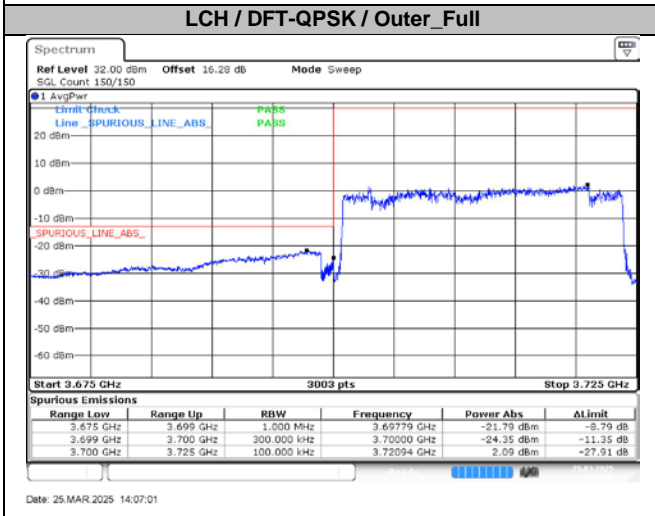
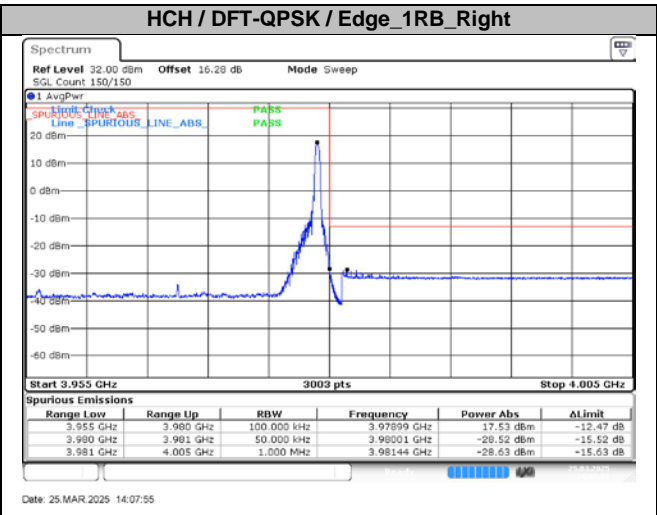
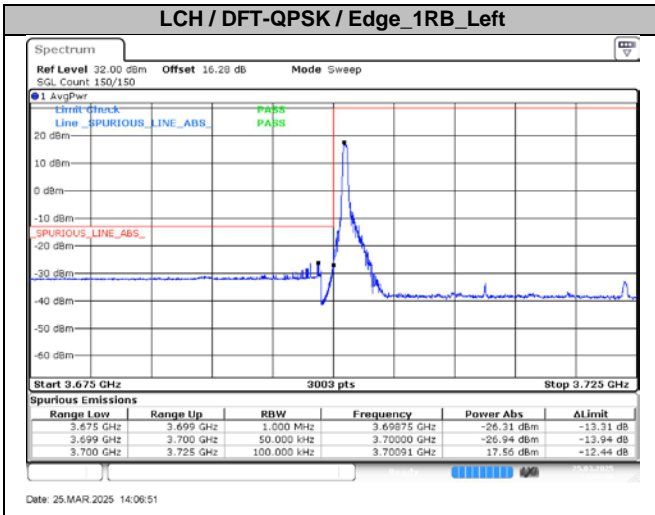
Date: 25.MAR.2025 14:04:44

HCH / DFT-QPSK / Edge\_1RB\_Right

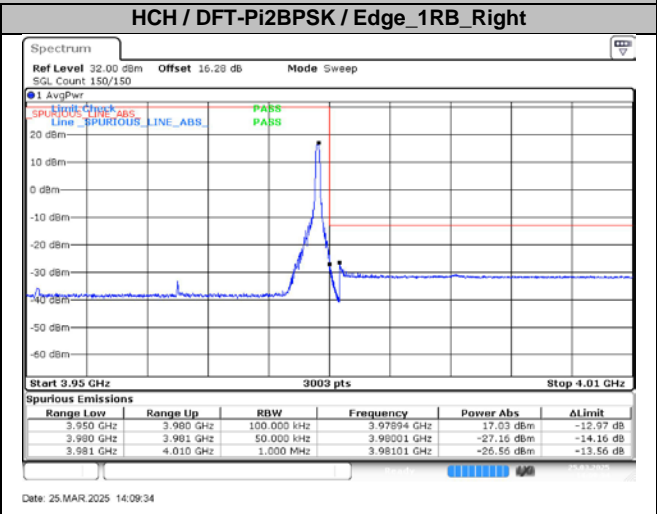
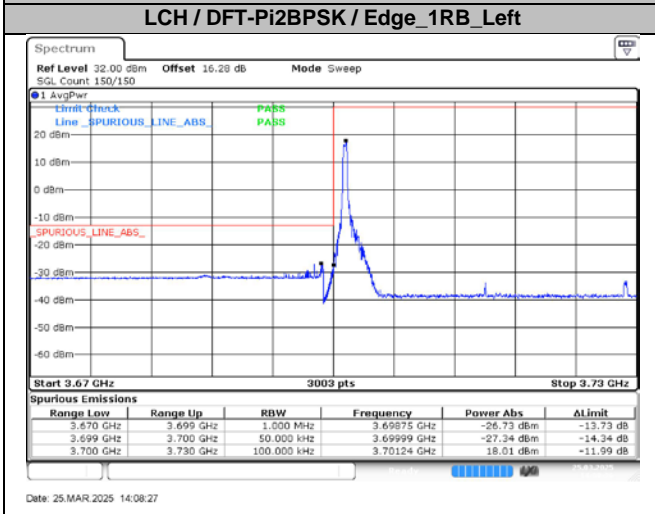


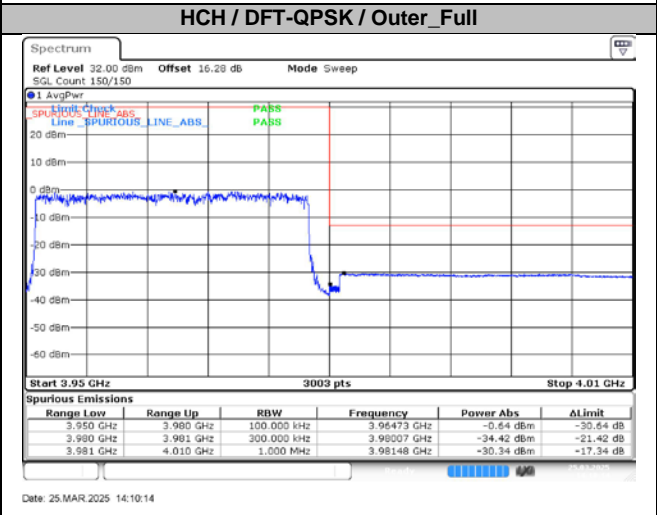
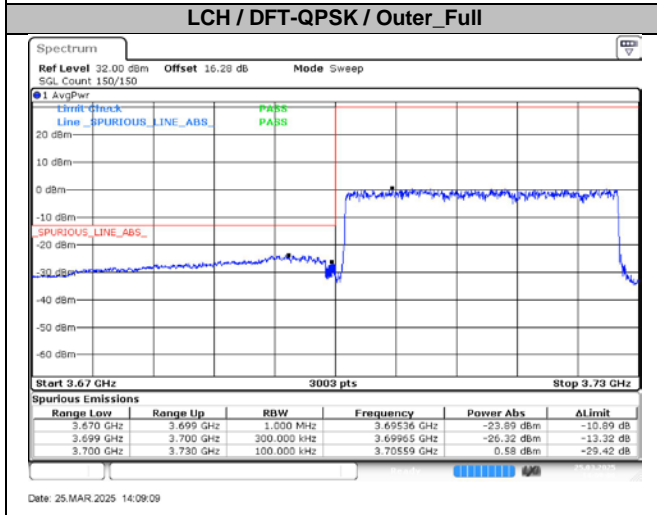
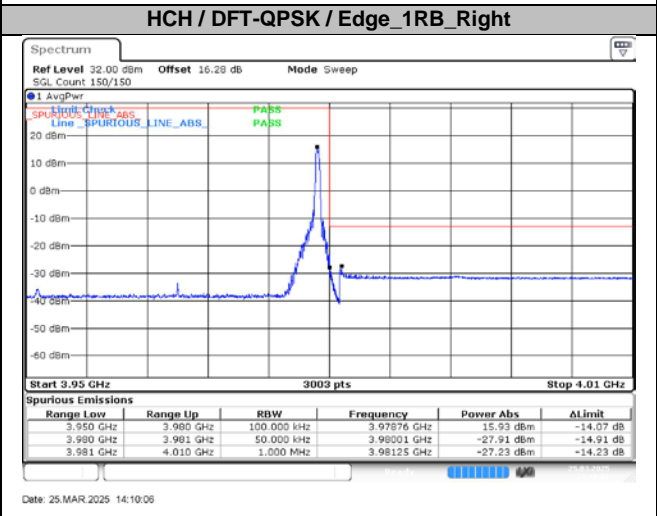
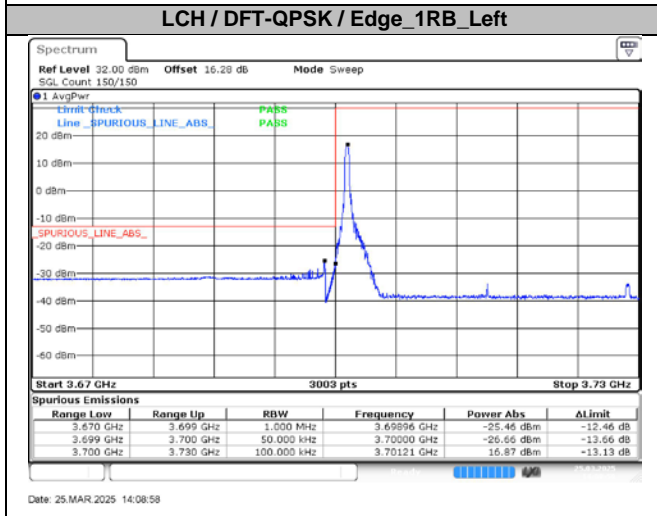
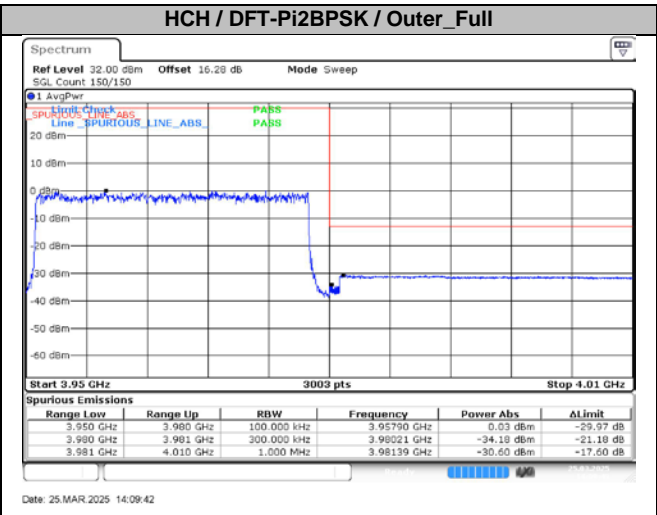
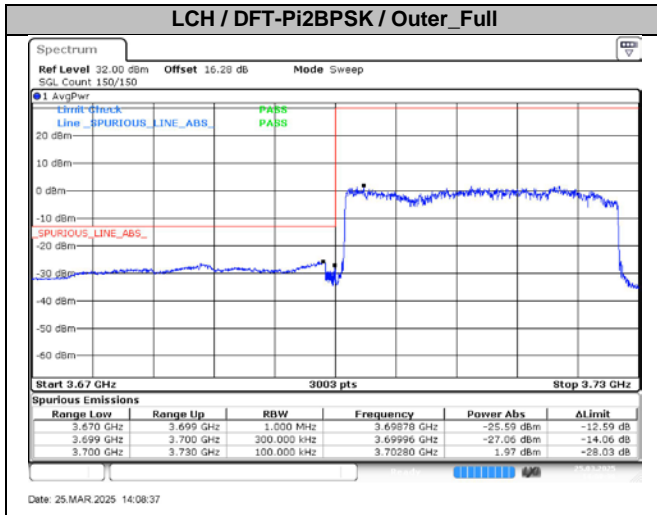
Date: 25.MAR.2025 14:05:49





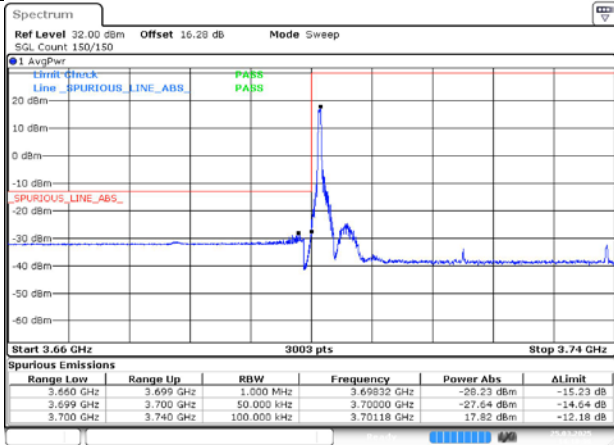
## n77A / 30kHz / 30MHz





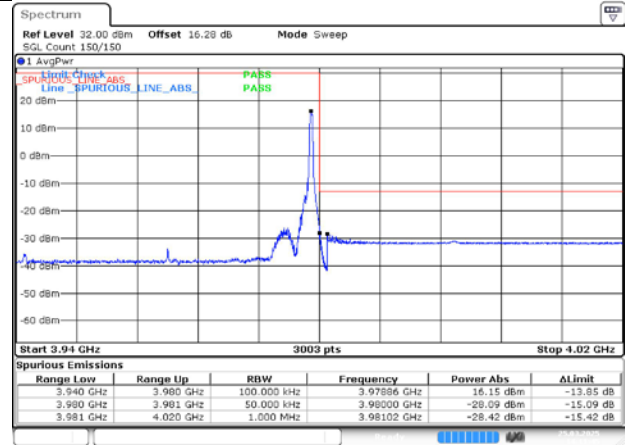
n77A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



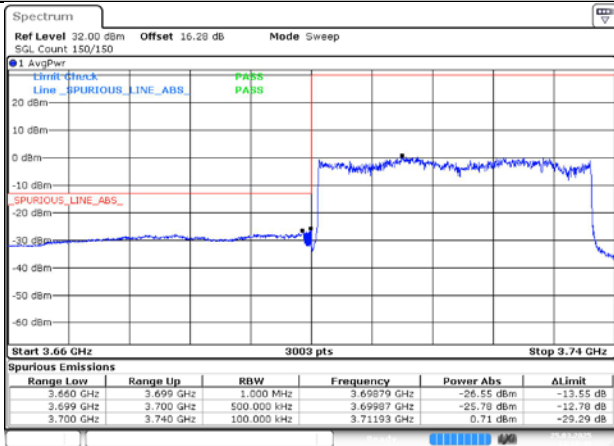
Date: 25.MAR.2025 14:10:39

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



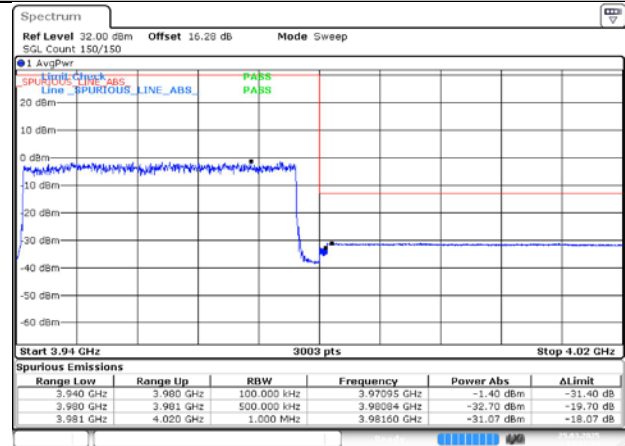
Date: 25.MAR.2025 14:11:46

LCH / DFT-Pi2BPSK / Outer\_Full



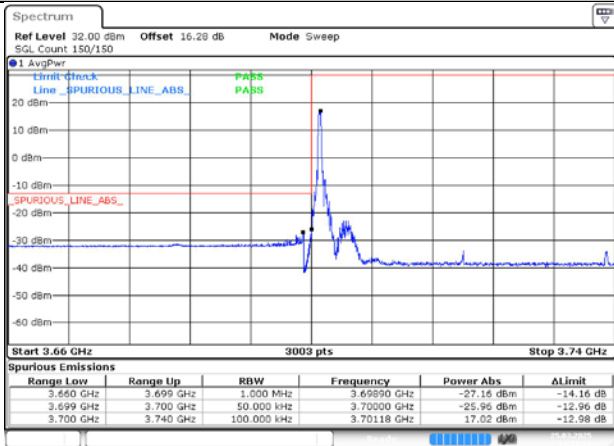
Date: 25.MAR.2025 14:10:49

HCH / DFT-Pi2BPSK / Outer\_Full



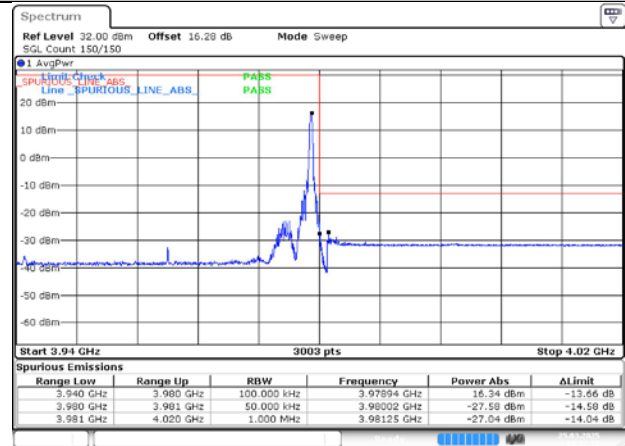
Date: 25.MAR.2025 14:11:54

LCH / DFT-QPSK / Edge\_1RB\_Left

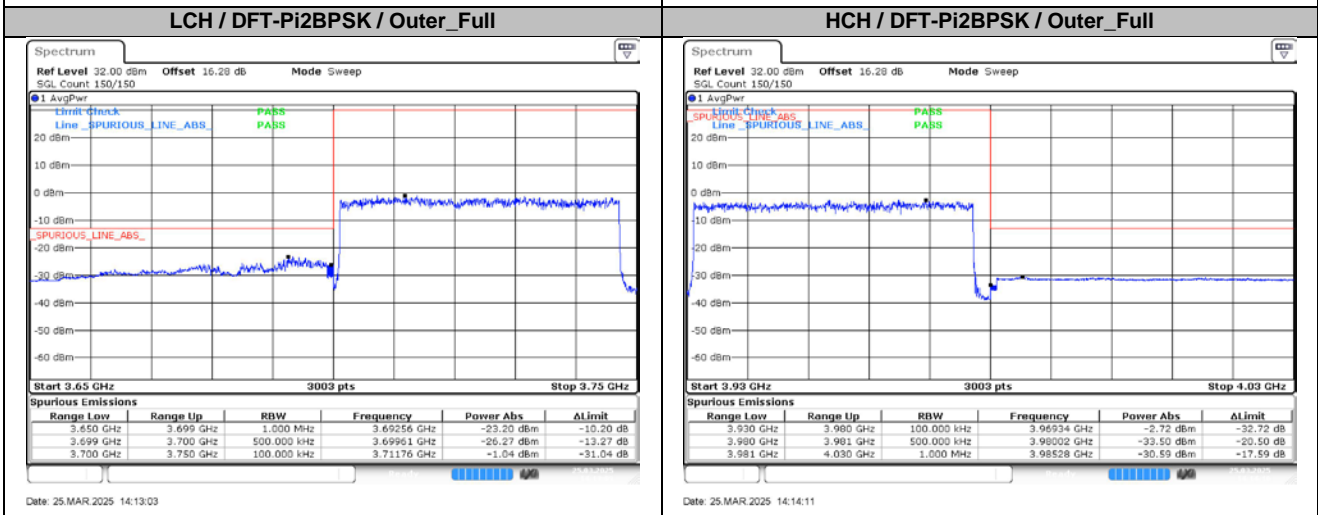
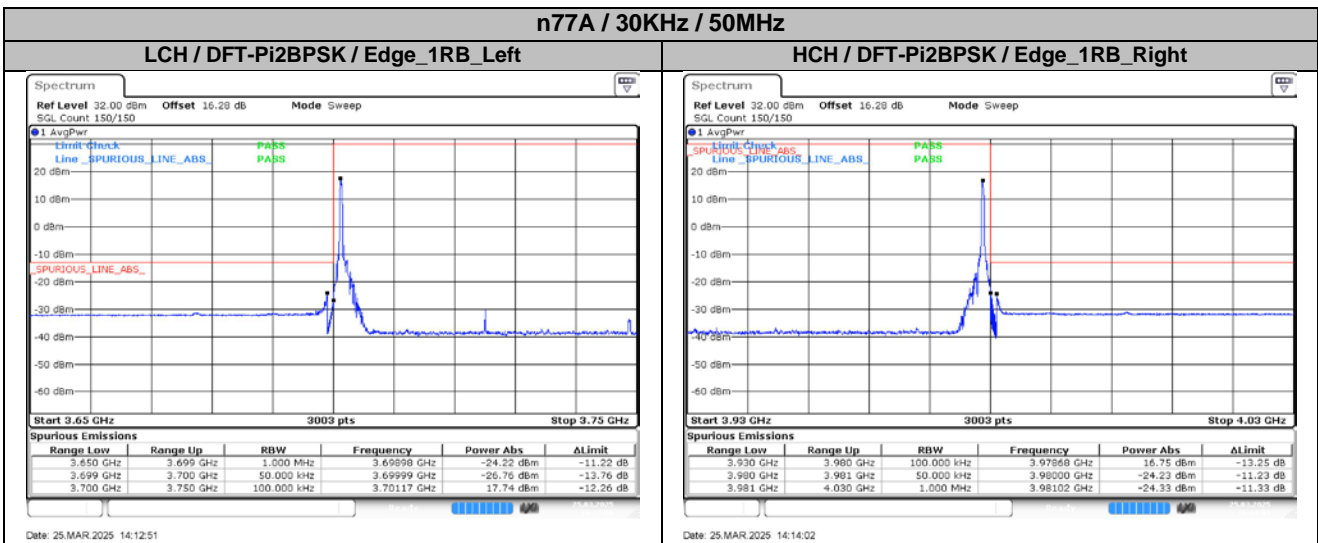
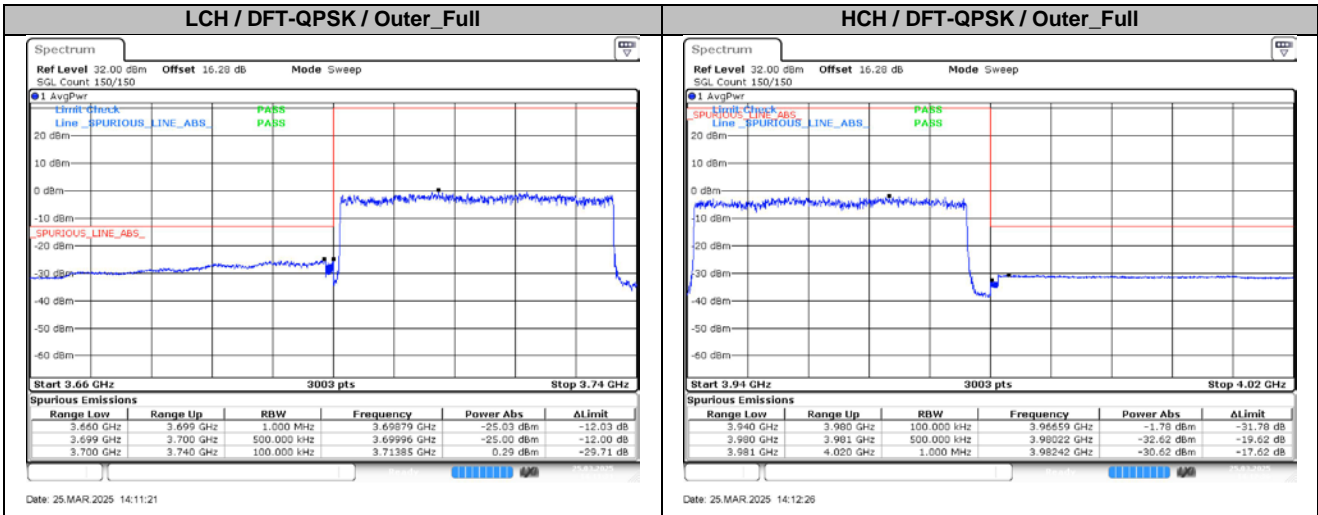


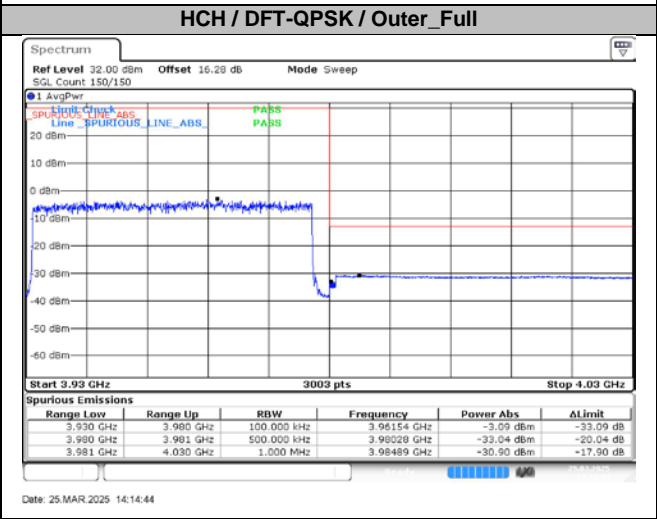
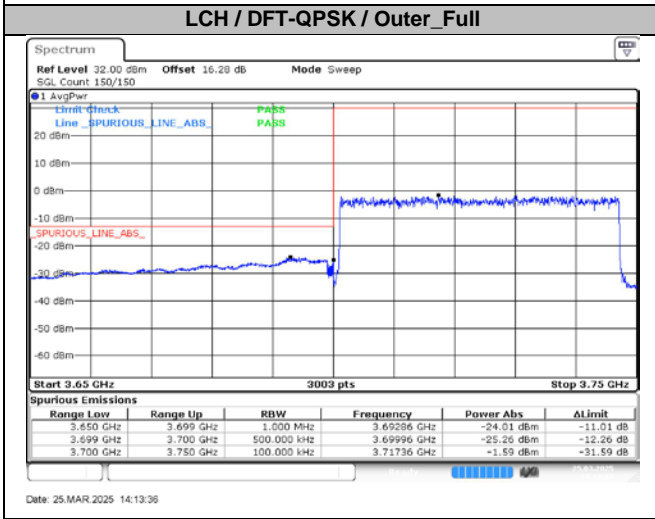
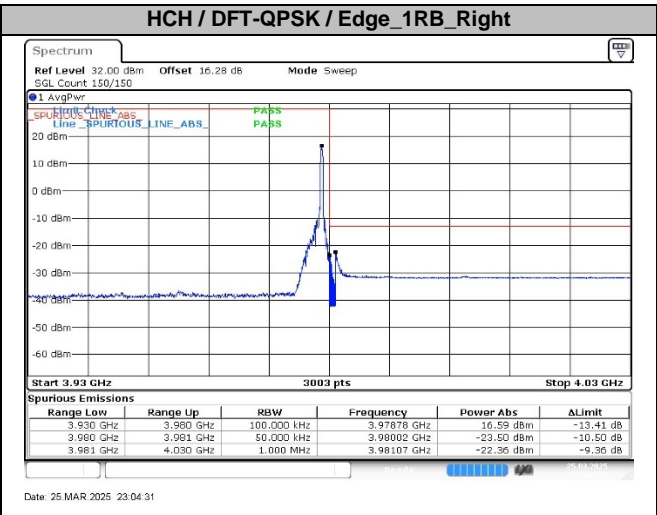
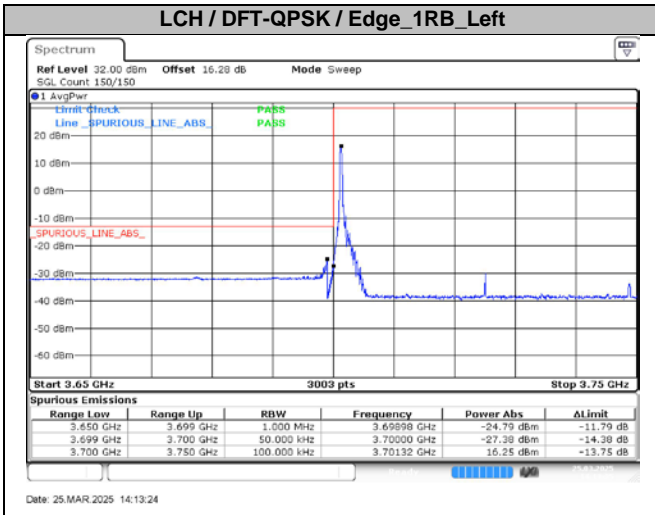
Date: 25.MAR.2025 14:11:10

HCH / DFT-QPSK / Edge\_1RB\_Right



Date: 25.MAR.2025 14:12:17





## n77A / 30KHz / 60MHz

