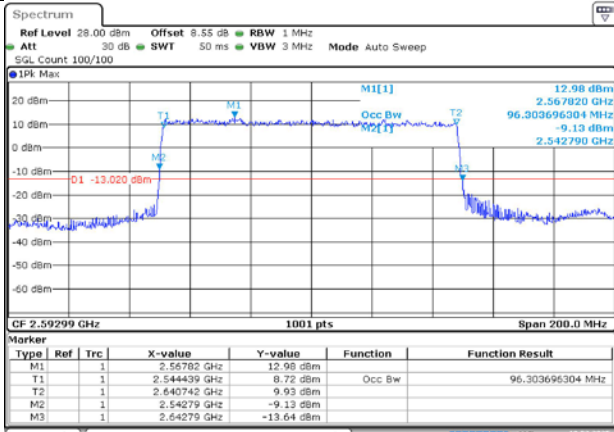


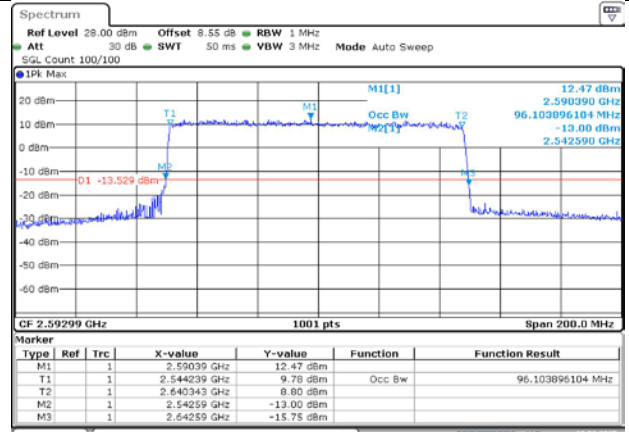
n41A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer_Full



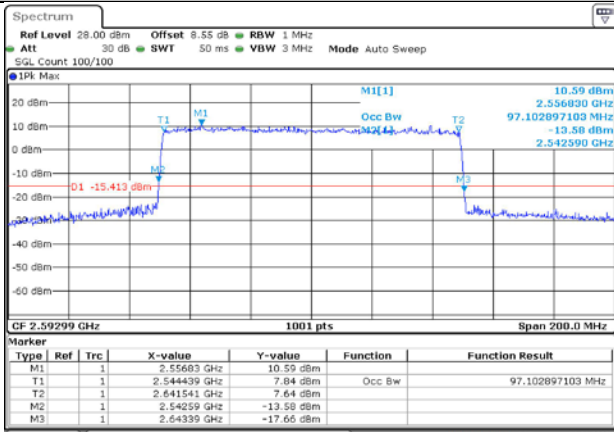
Date: 18MAR2025 10:02:41

MCH / DFT-QPSK / Outer_Full



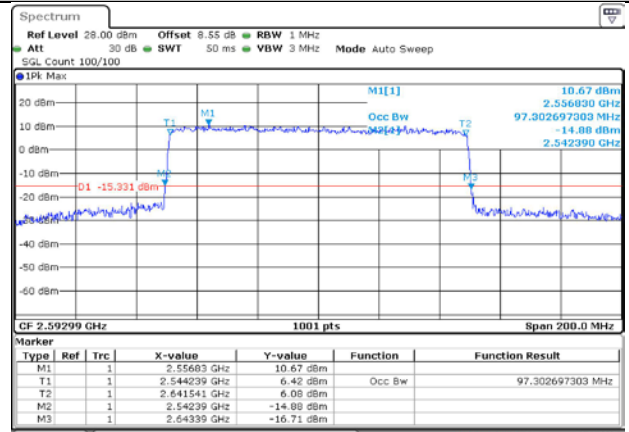
Date: 18MAR2025 10:02:52

MCH / CP-QPSK / Outer_Full



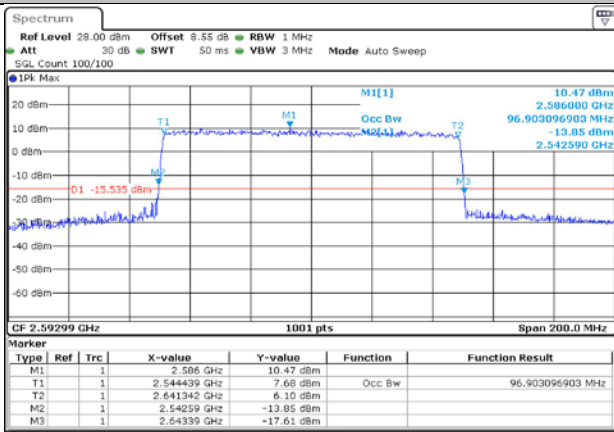
Date: 18MAR2025 10:03:01

MCH / CP-16QAM / Outer_Full



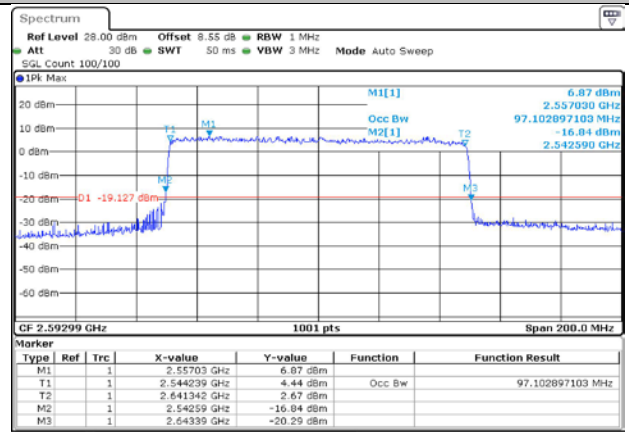
Date: 18MAR2025 10:03:10

MCH / CP-64QAM / Outer_Full



Date: 18MAR2025 10:03:19

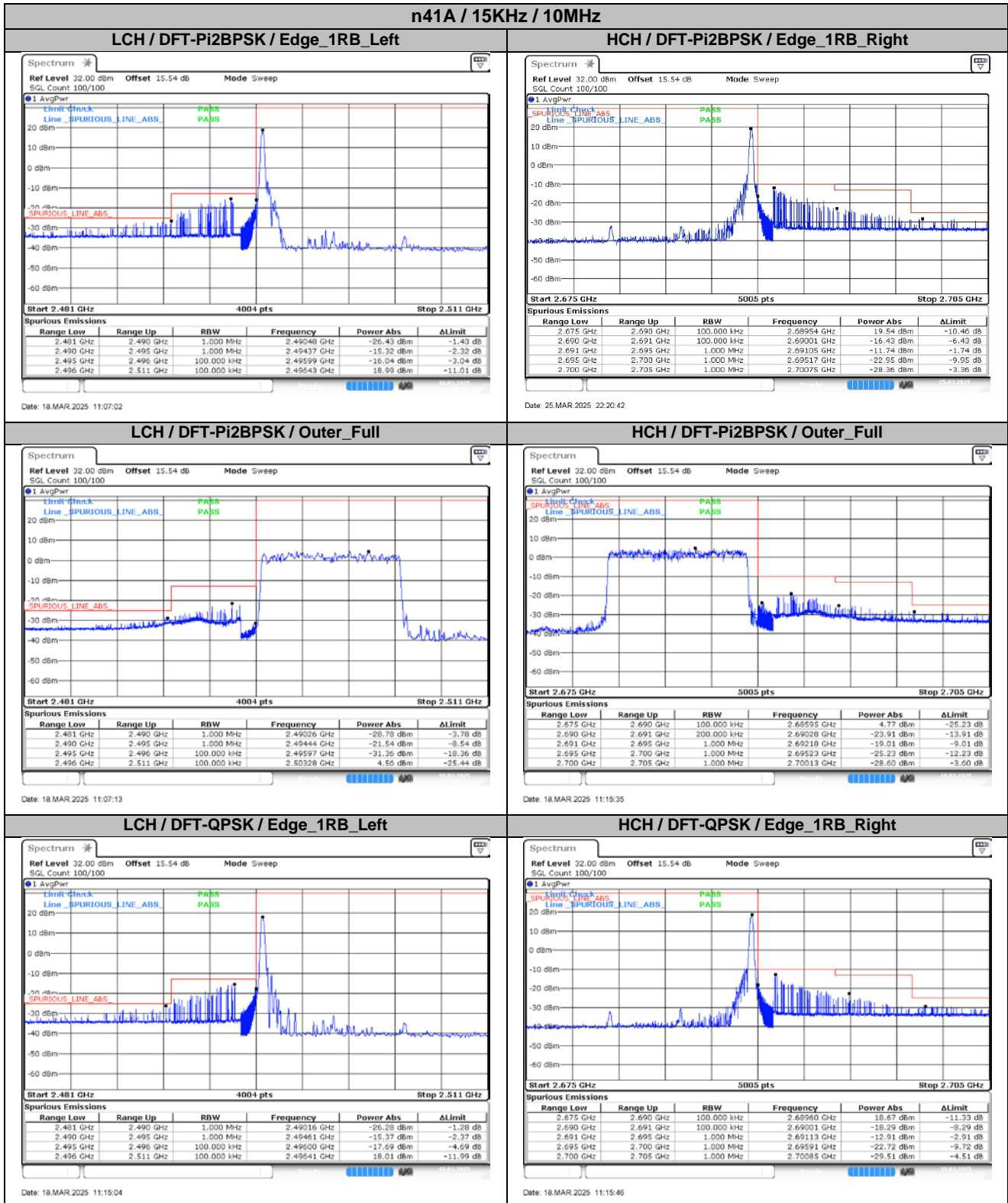
MCH / CP-256QAM / Outer_Full

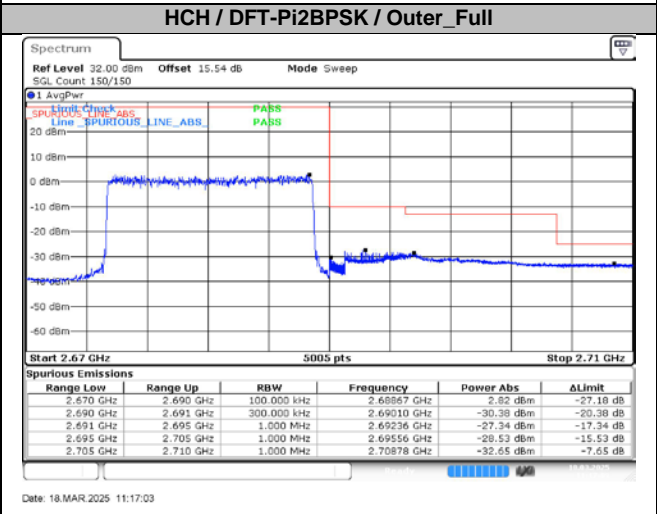
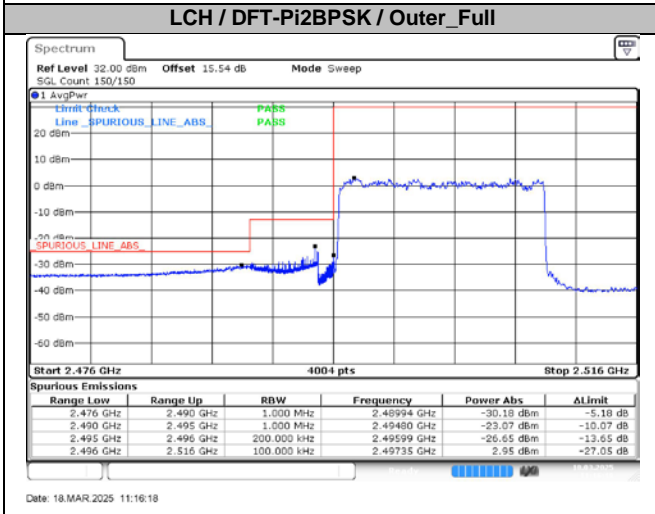
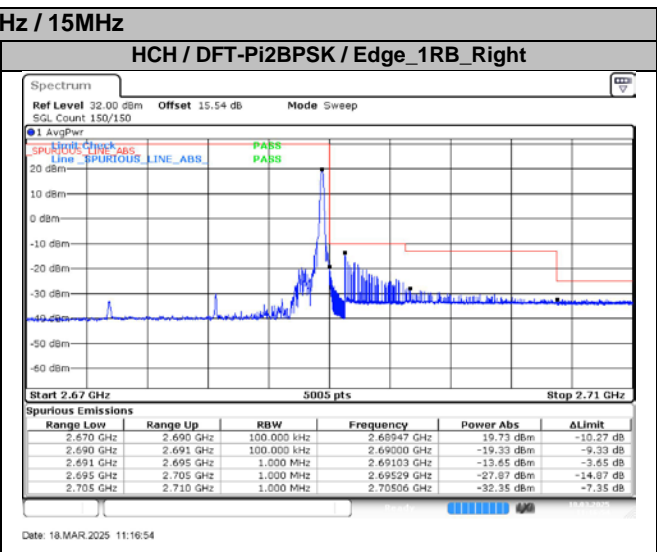
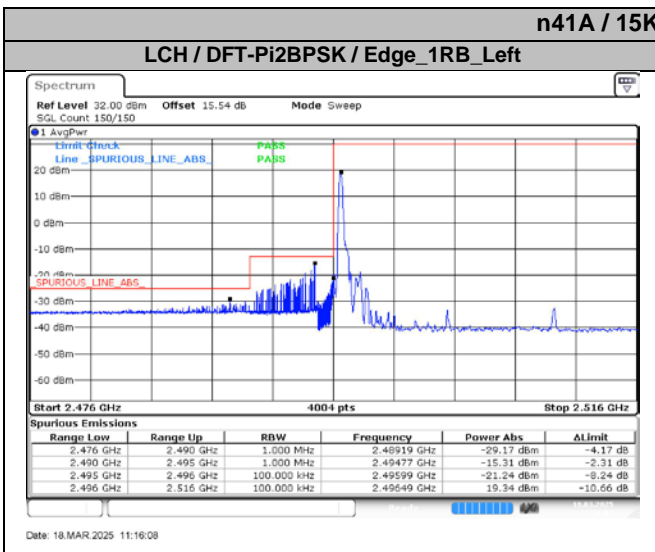
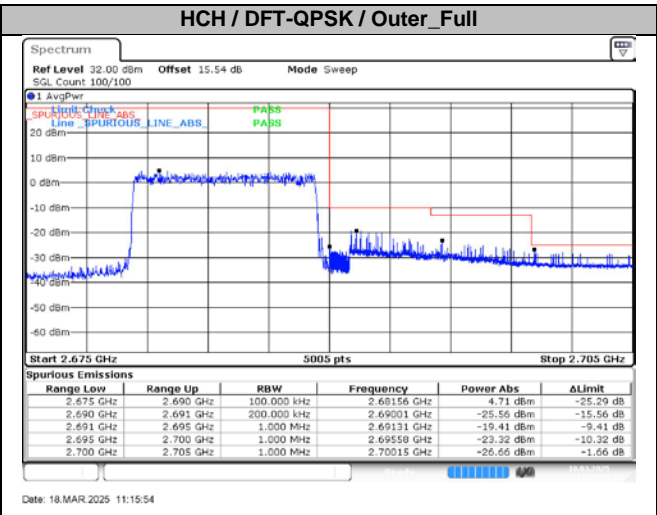
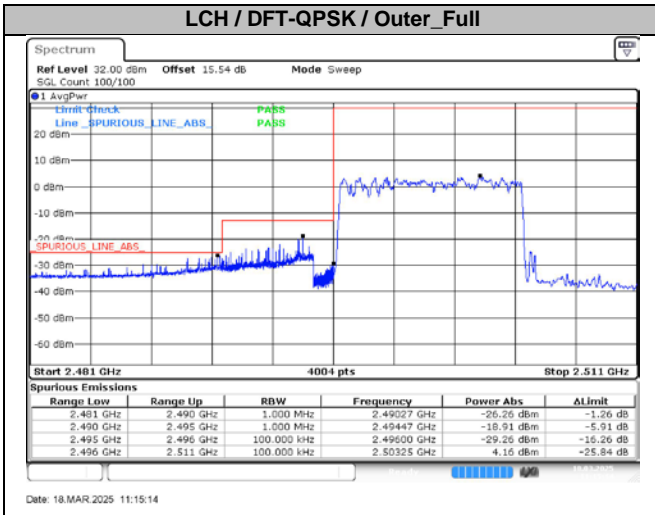


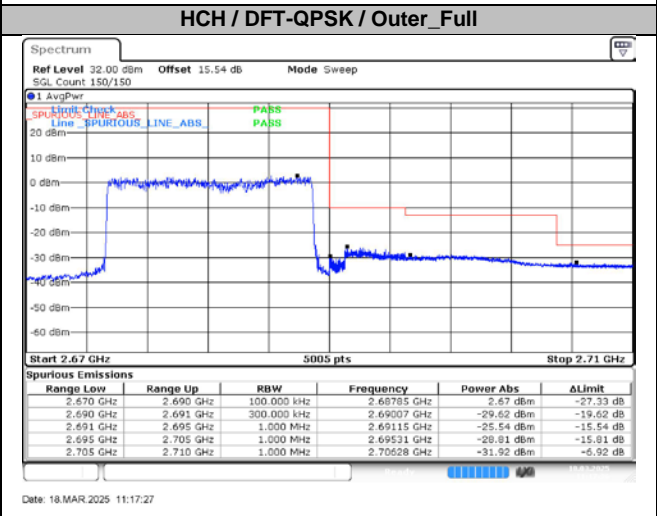
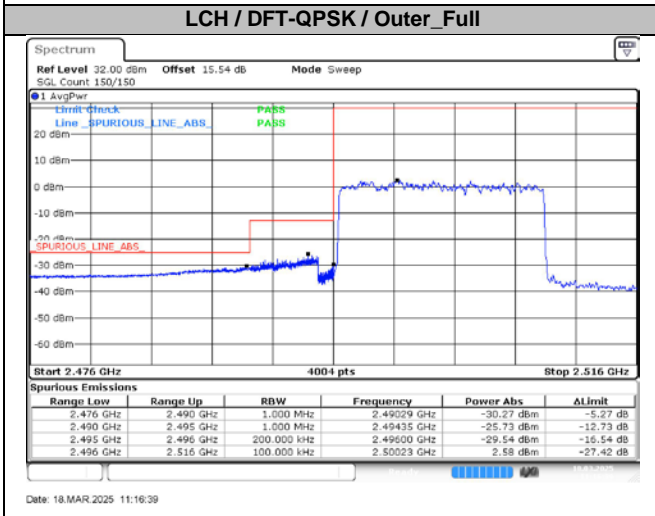
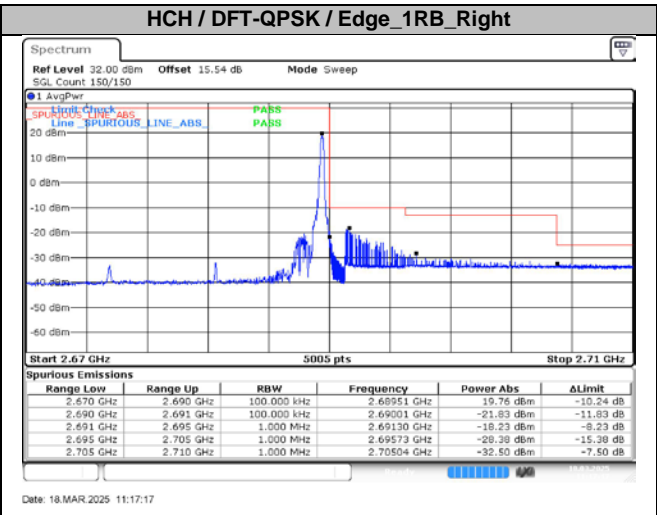
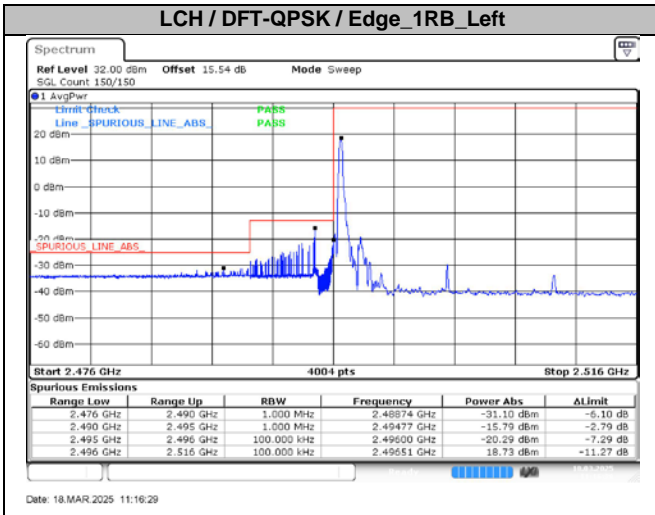
Date: 18MAR2025 10:03:29

5. Conducted Band Edges

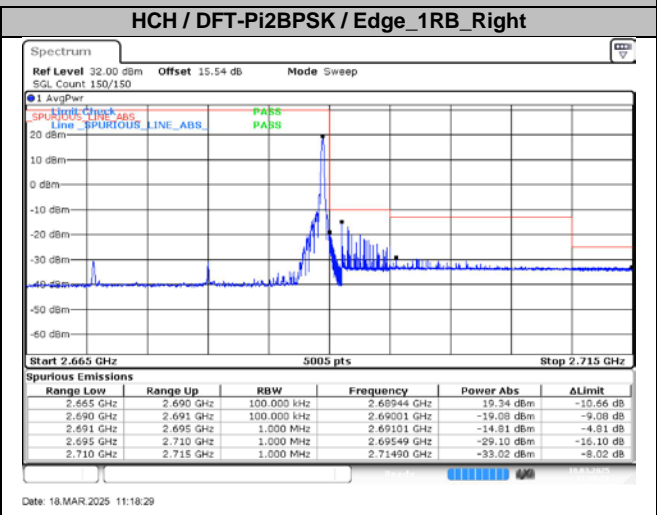
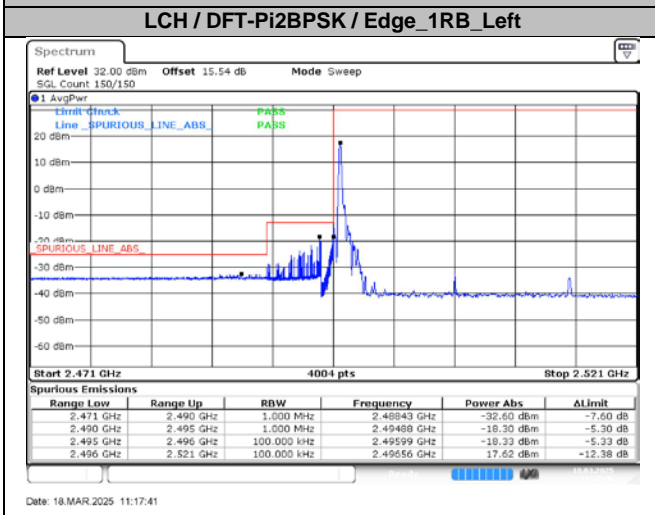
5.1. Test Plots for SCS=15KHz

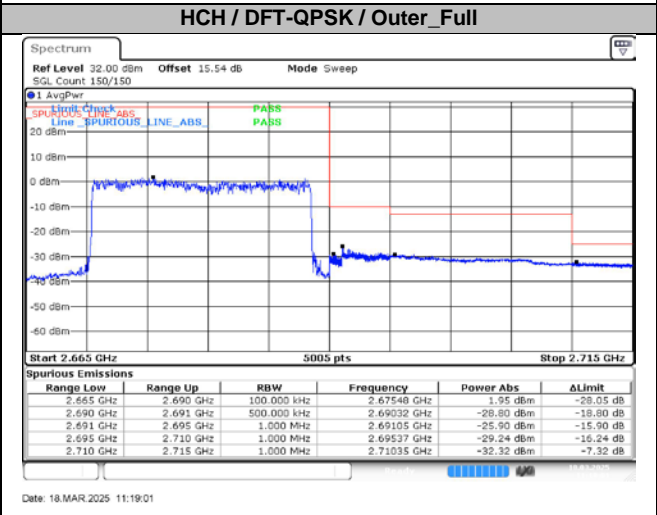
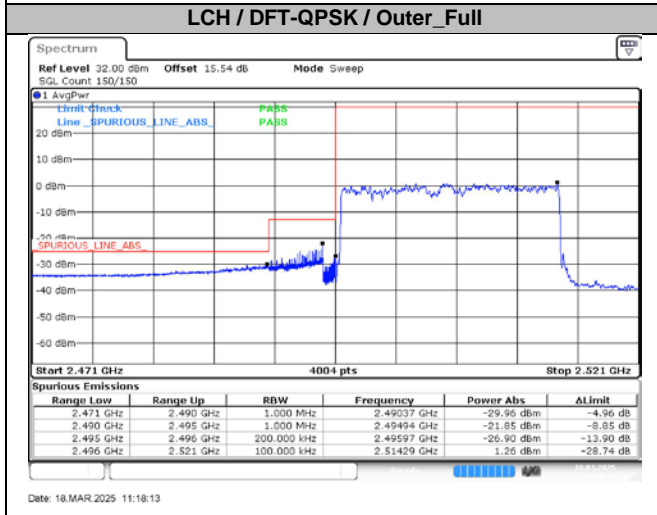
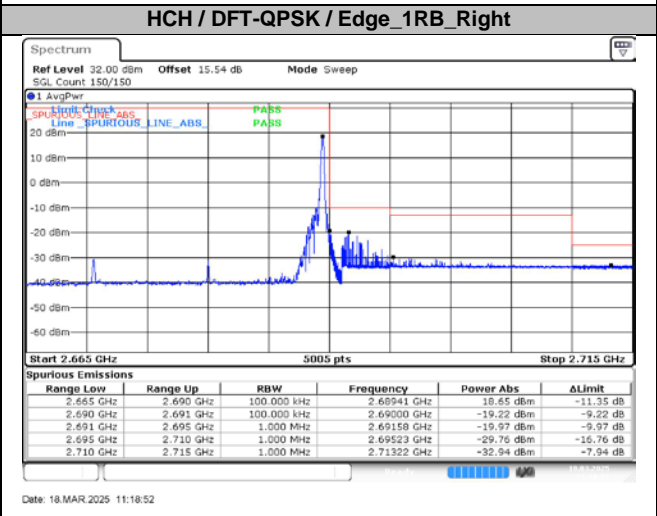
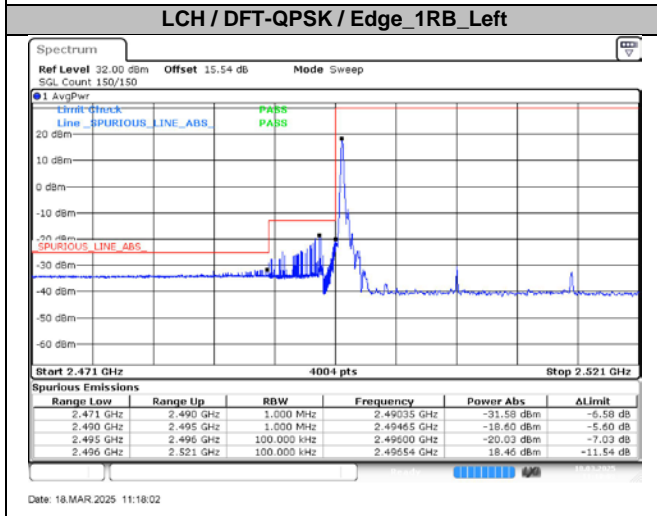
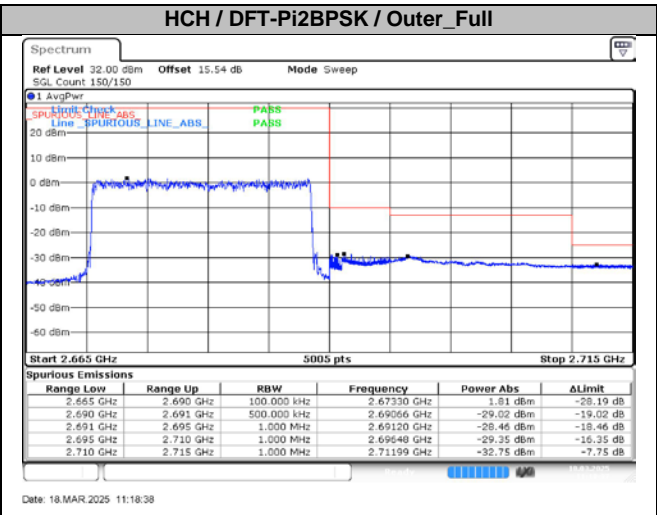
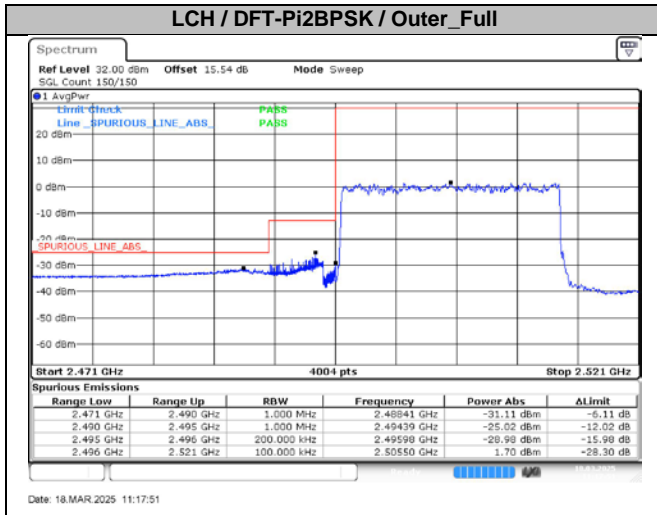






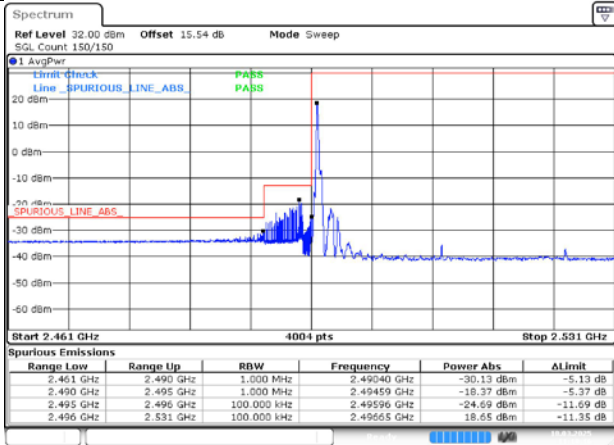
n41A / 15KHz / 20MHz





n41A / 15KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



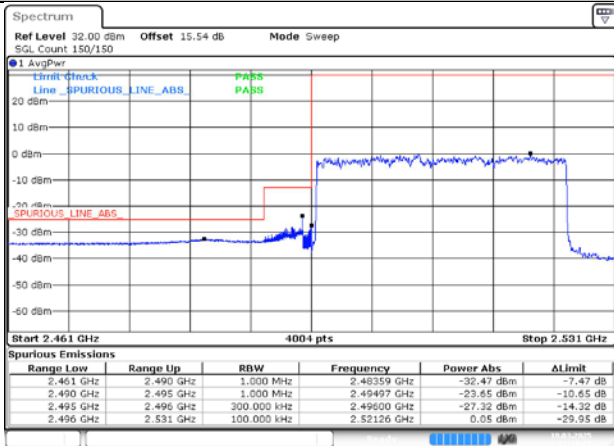
Date: 18.MAR.2025 11:19:16

HCH / DFT-Pi2BPSK / Edge_1RB_Right



Date: 18.MAR.2025 11:20:22

LCH / DFT-Pi2BPSK / Outer_Full



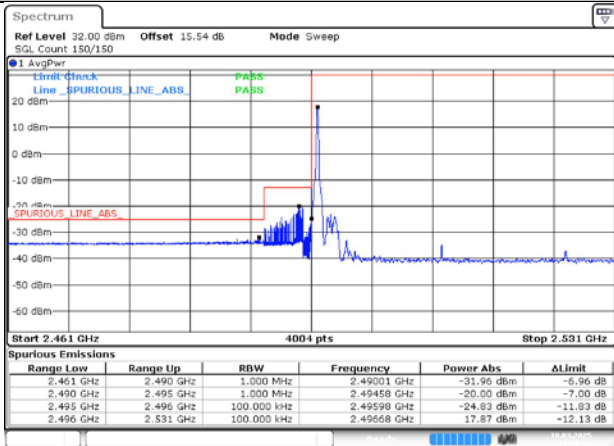
Date: 18.MAR.2025 11:19:27

HCH / DFT-Pi2BPSK / Outer_Full



Date: 18.MAR.2025 11:20:32

LCH / DFT-QPSK / Edge_1RB_Left



Date: 18.MAR.2025 11:19:38

HCH / DFT-QPSK / Edge_1RB_Right



Date: 18.MAR.2025 11:20:47