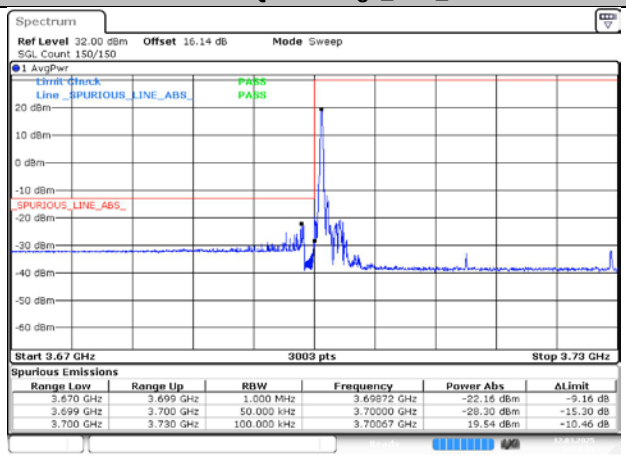
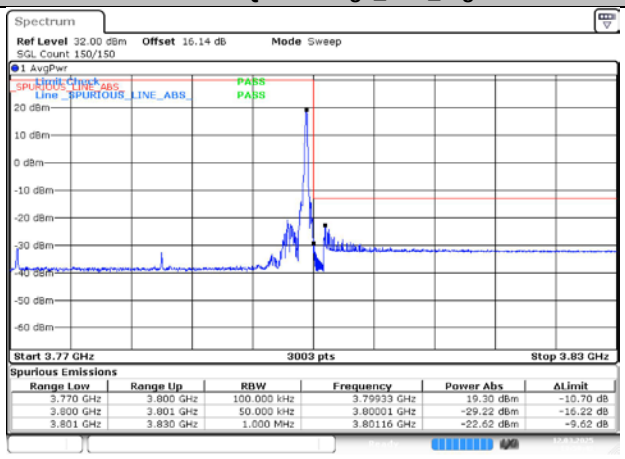
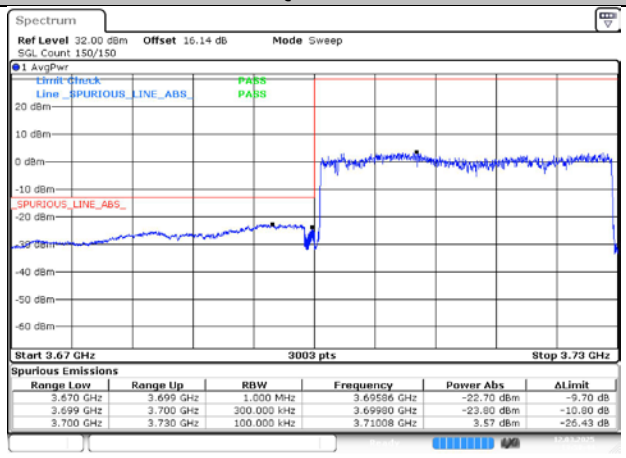
LCH / DFT-QPSK / Edge_1RB_Left



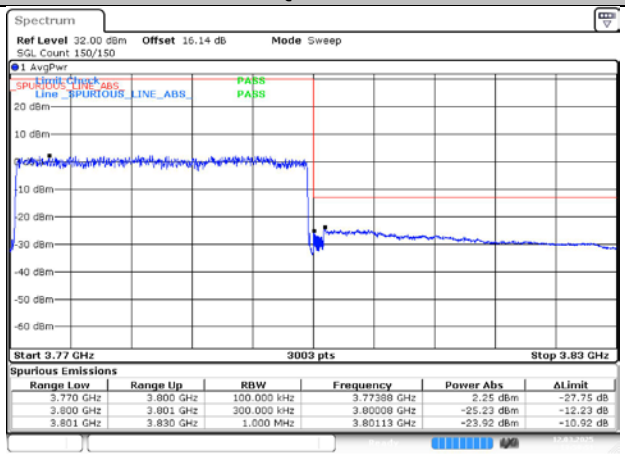
HCH / DFT-QPSK / Edge_1RB_Right



LCH / DFT-QPSK / Outer_Full

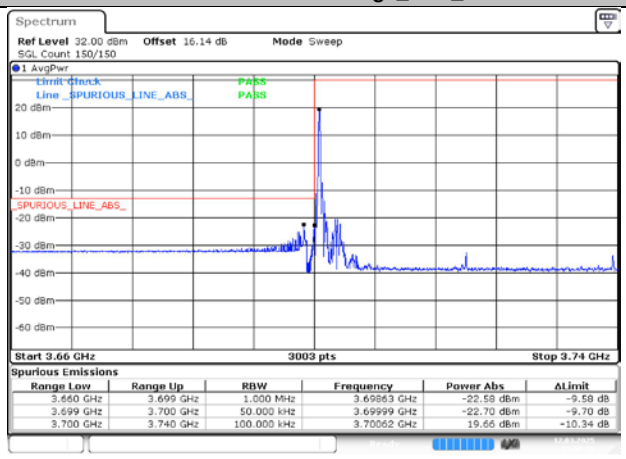


HCH / DFT-QPSK / Outer_Full

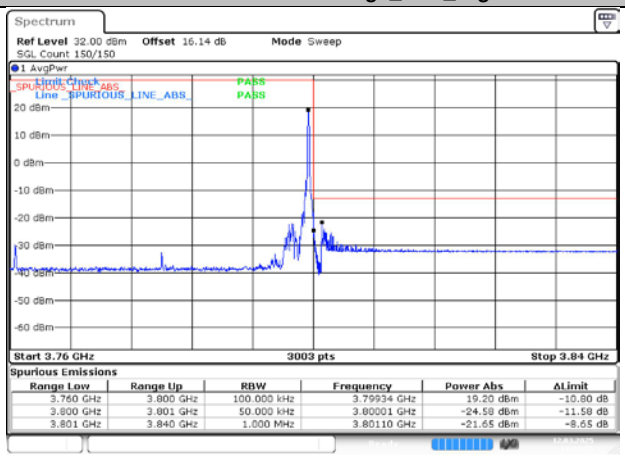


n78A / 15KHz / 40MHz

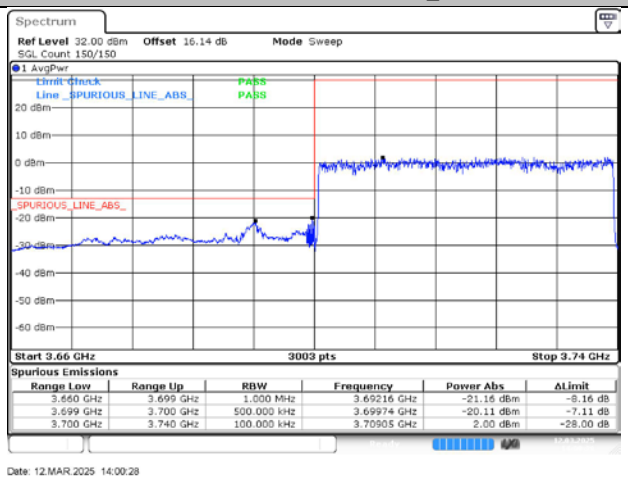
LCH / DFT-Pi2BPSK / Edge_1RB_Left



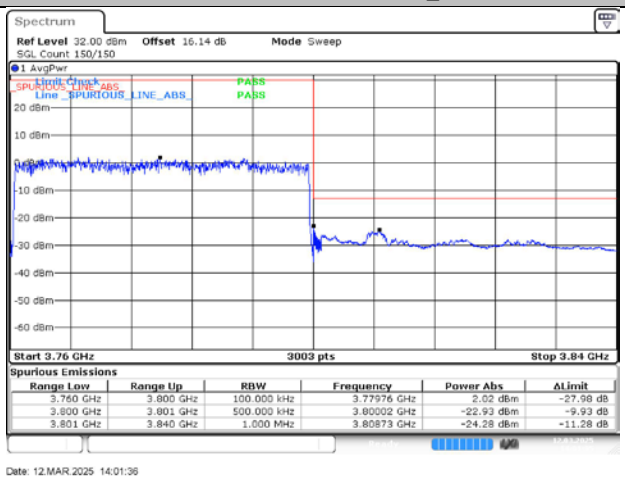
HCH / DFT-Pi2BPSK / Edge_1RB_Right



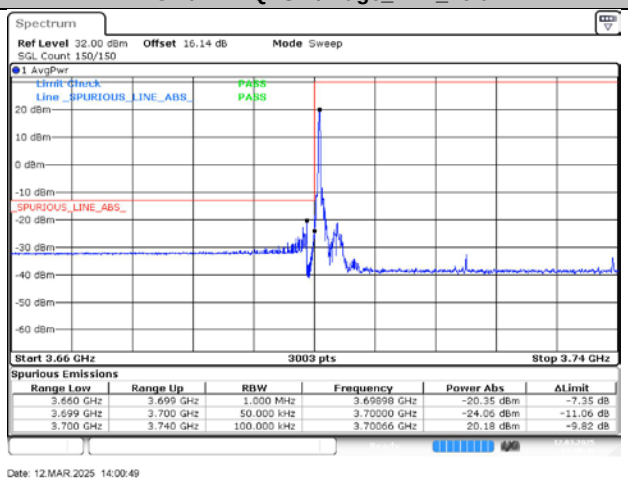
LCH / DFT-Pi2BPSK / Outer_Full



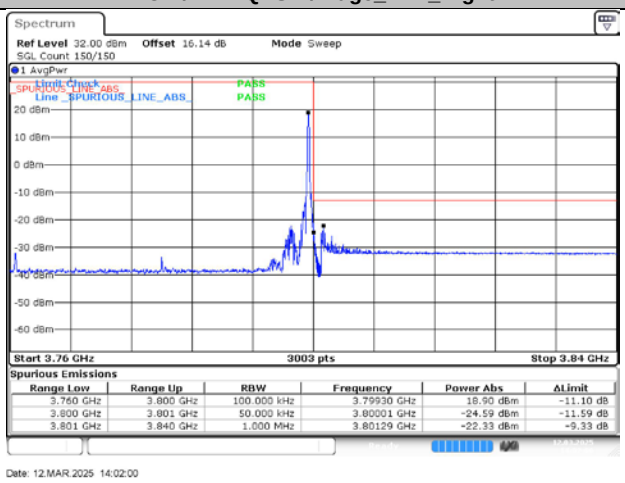
HCH / DFT-Pi2BPSK / Outer_Full



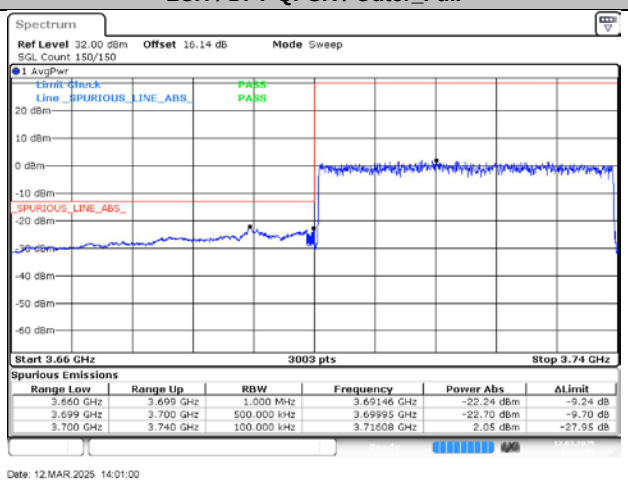
LCH / DFT-QPSK / Edge_1RB_Left



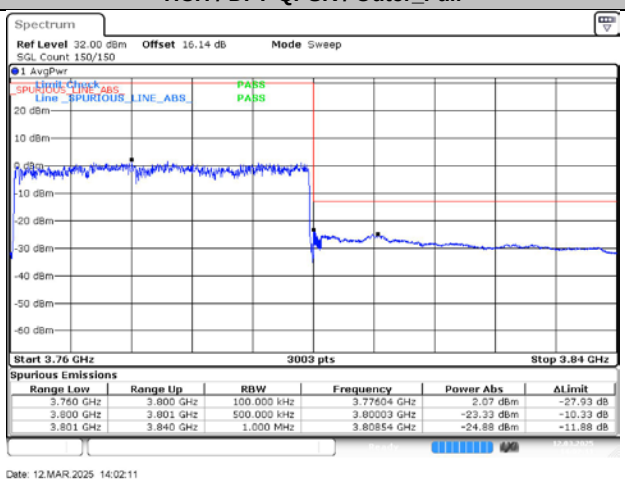
HCH / DFT-QPSK / Edge_1RB_Right

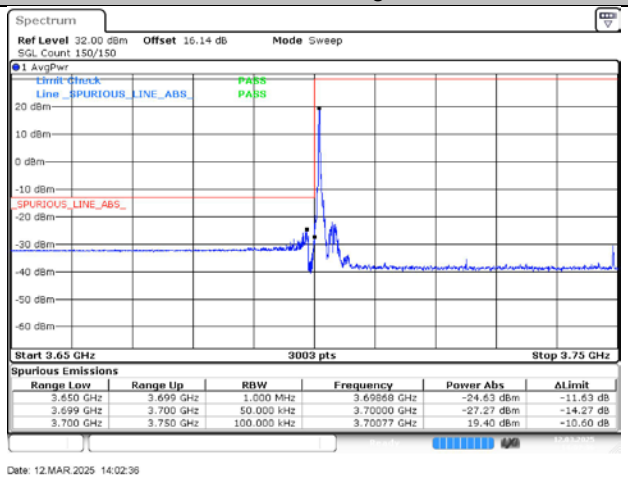
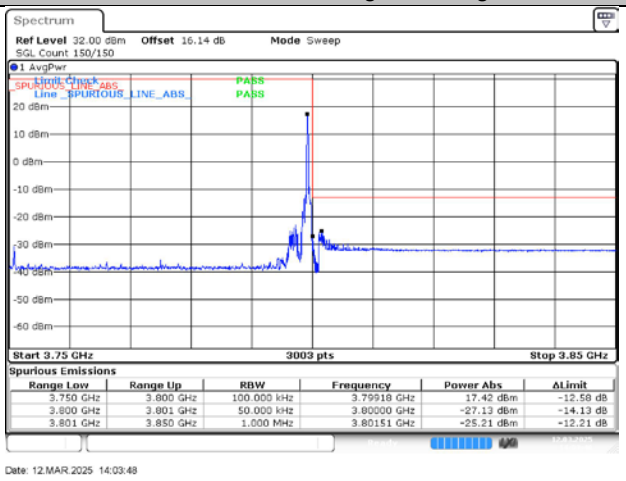
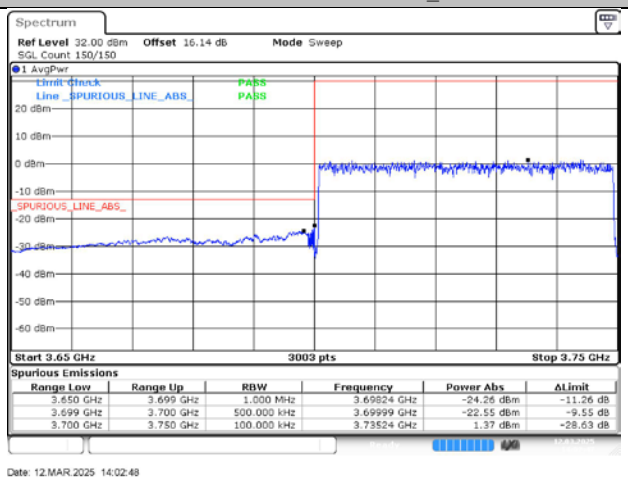
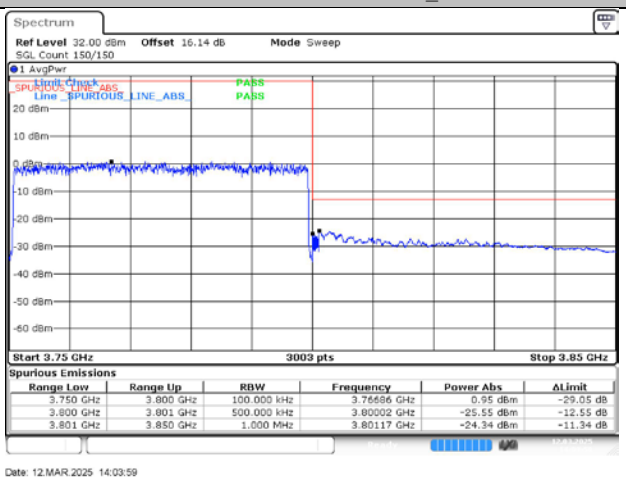
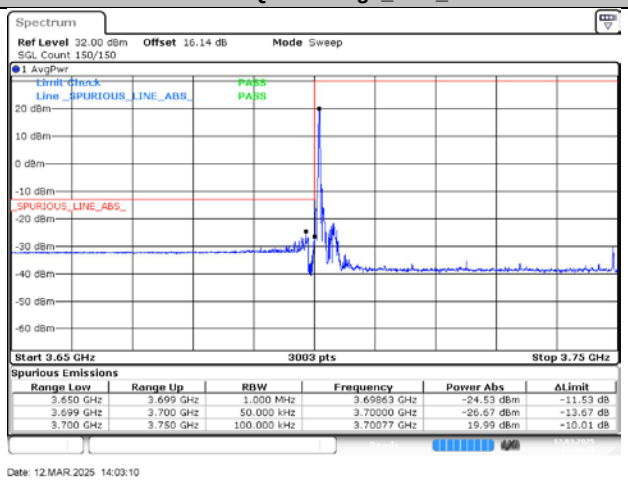
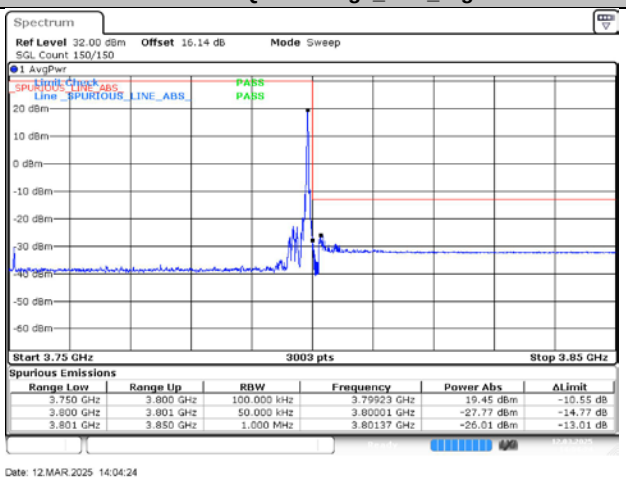


LCH / DFT-QPSK / Outer_Full

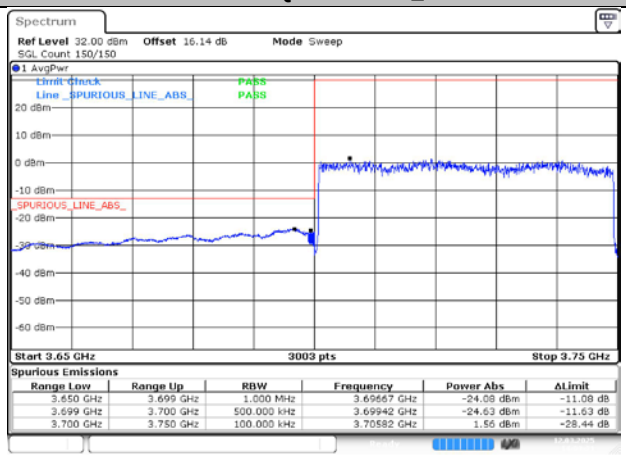


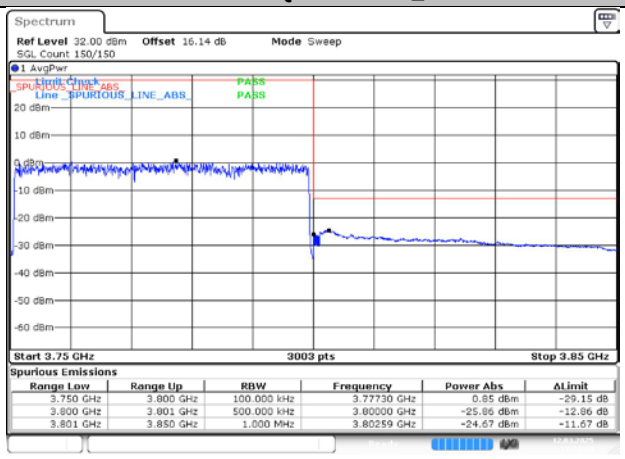
HCH / DFT-QPSK / Outer_Full



n78A / 15KHz / 50MHz	
LCH / DFT-Pi2BPSK / Edge_1RB_Left	HCH / DFT-Pi2BPSK / Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	
LCH / DFT-QPSK / Edge_1RB_Left	HCH / DFT-QPSK / Edge_1RB_Right
	

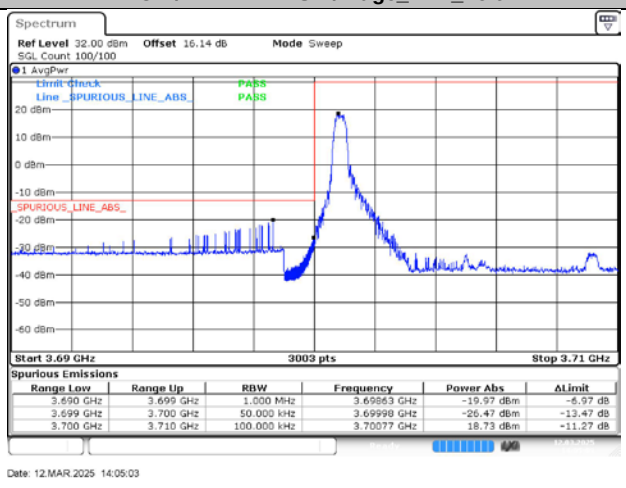
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full

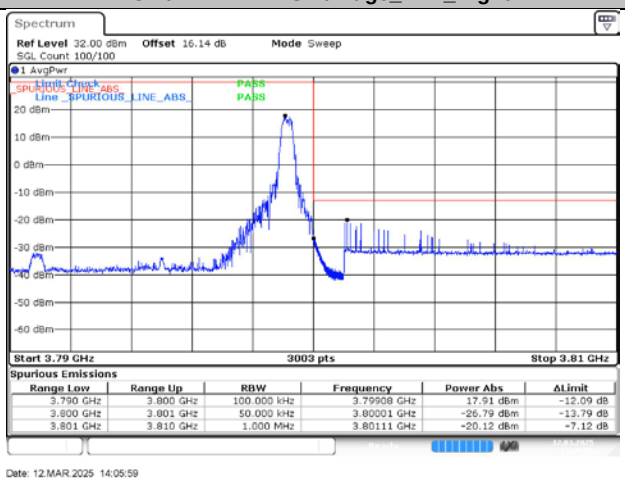




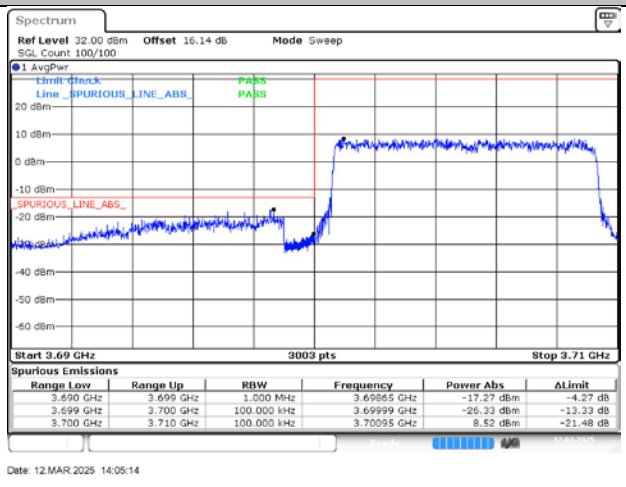
5.2. Test Plots for SCS=30KHz

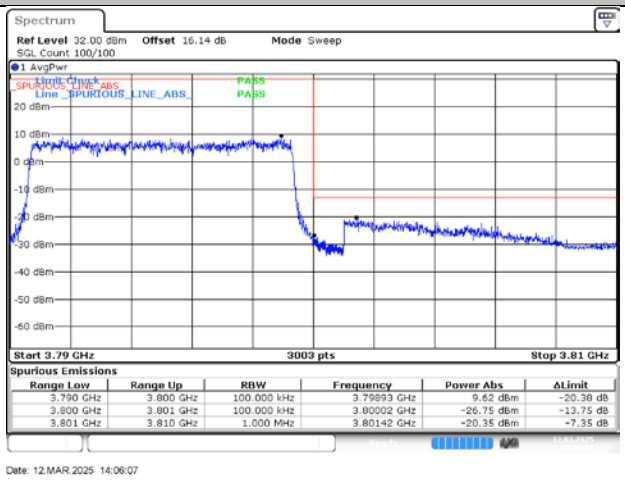
n78A / 30KHz / 10MHz	
LCH / DFT-Pi2BPSK / Edge_1RB_Left	HCH / DFT-Pi2BPSK / Edge_1RB_Right

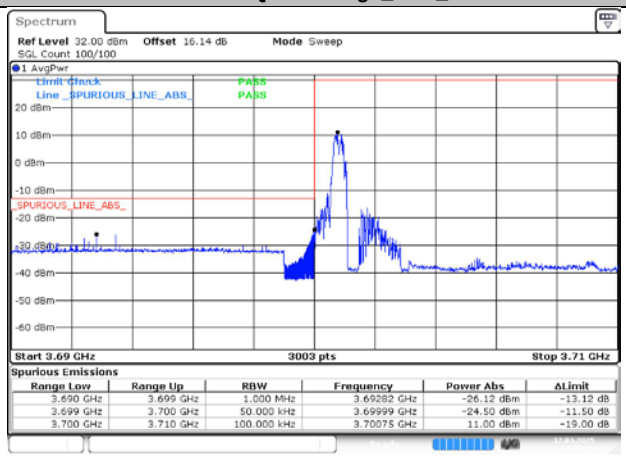
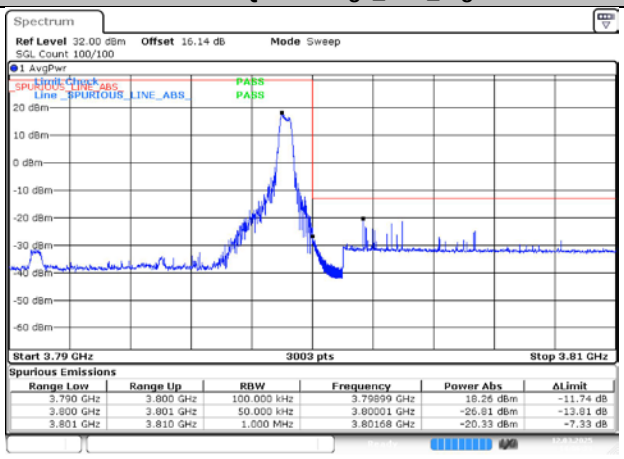
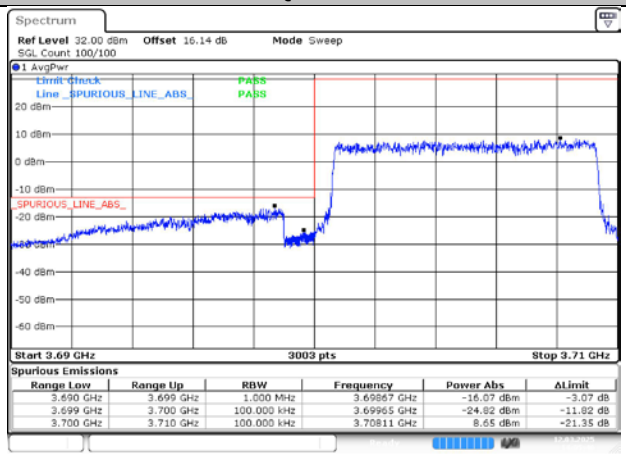
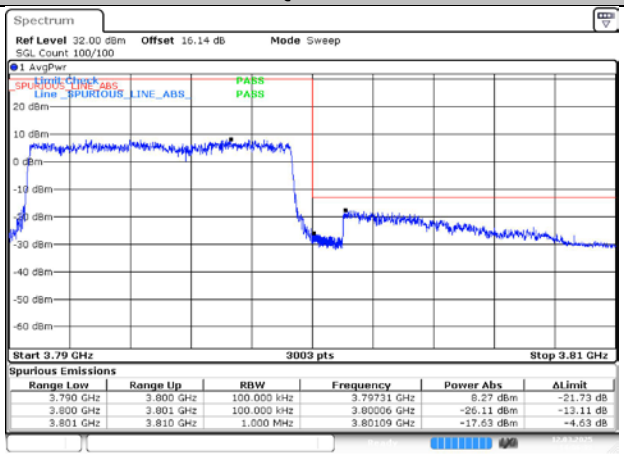


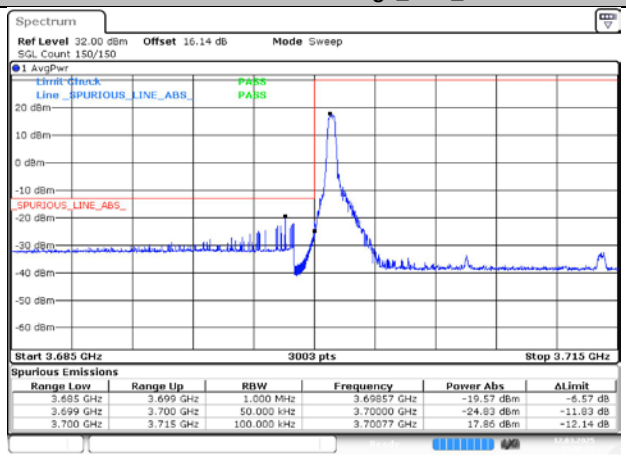
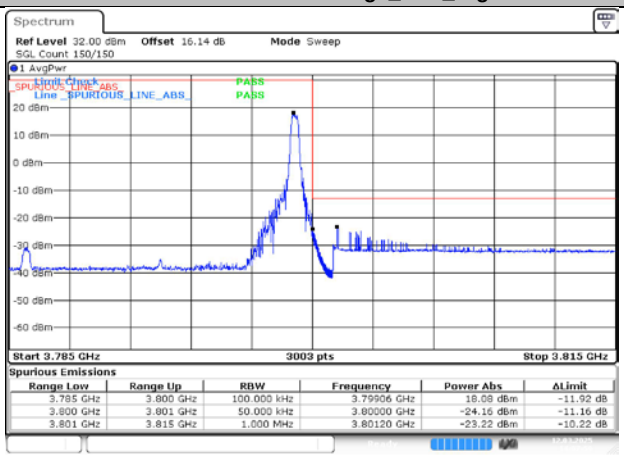


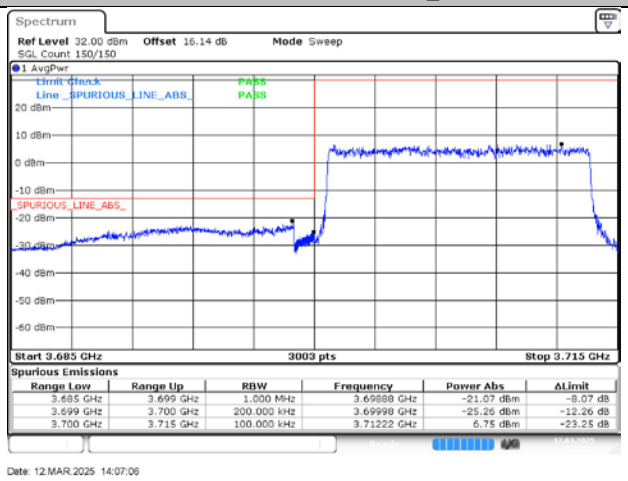
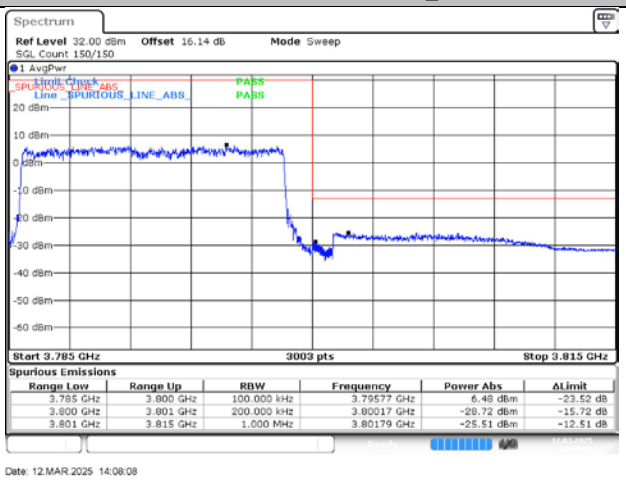
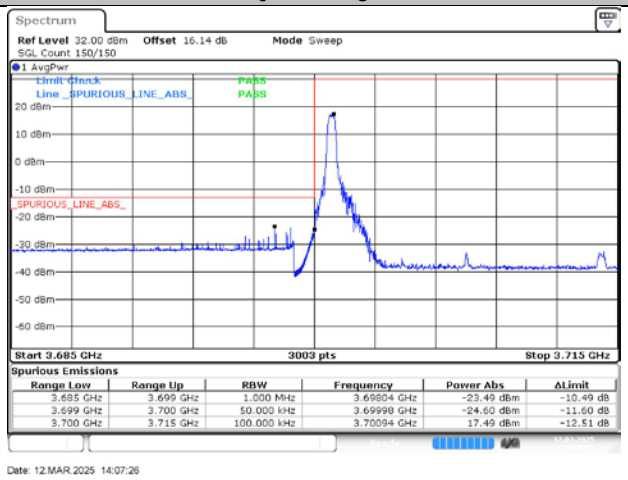
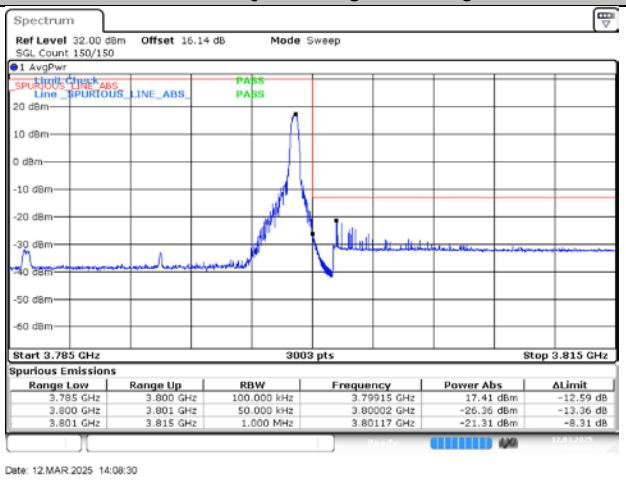
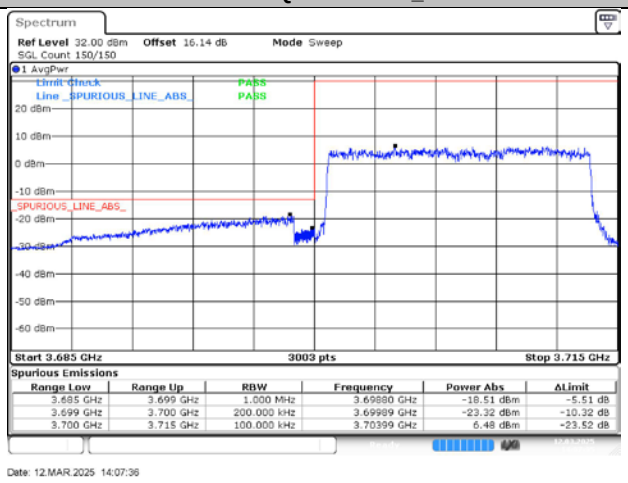
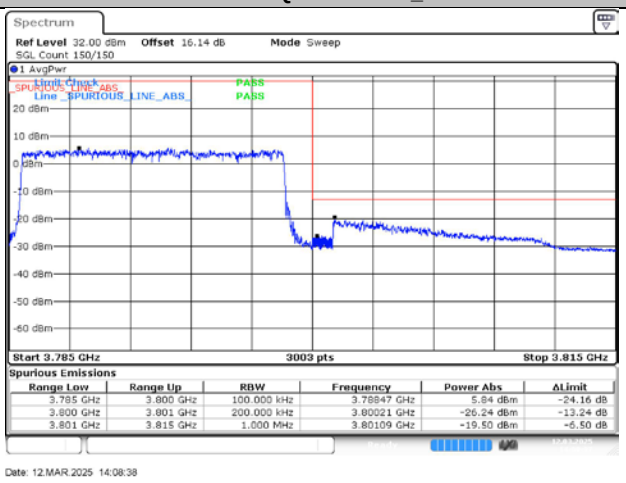
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full

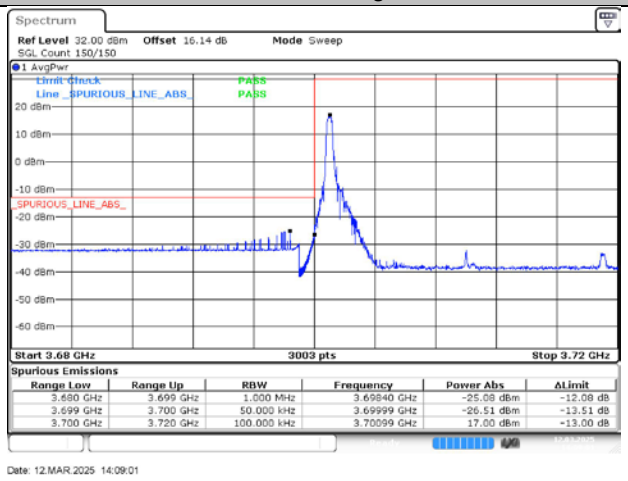
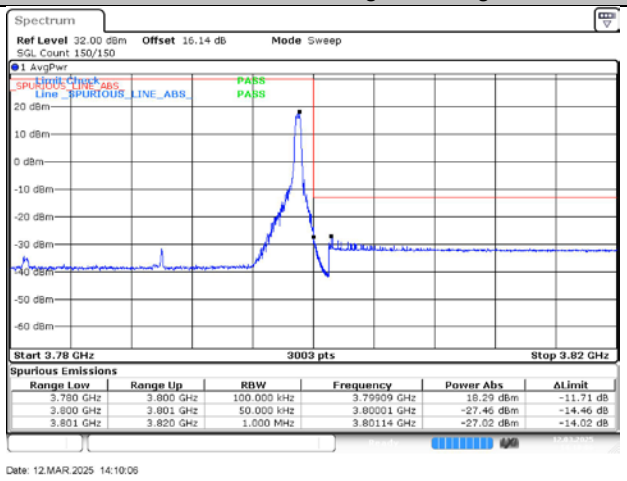
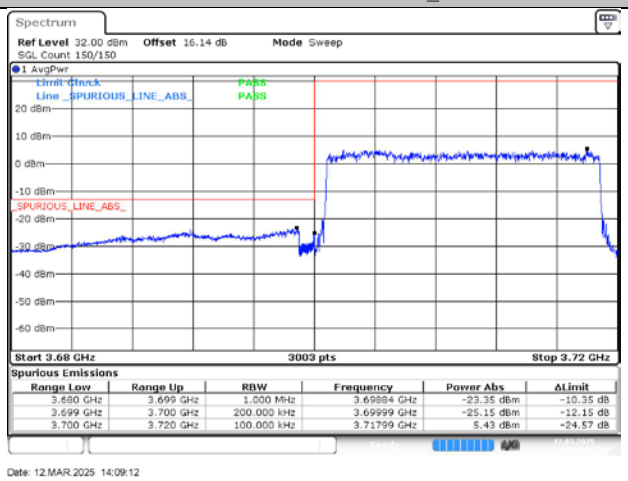
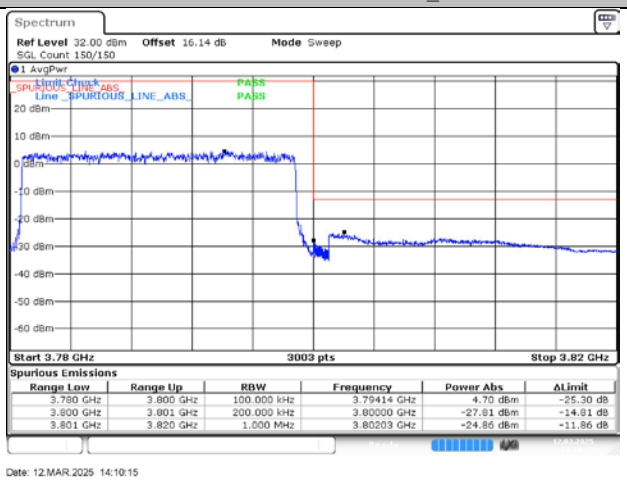
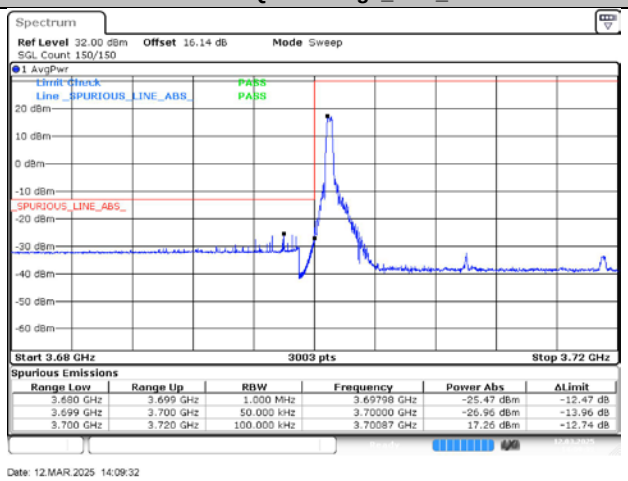
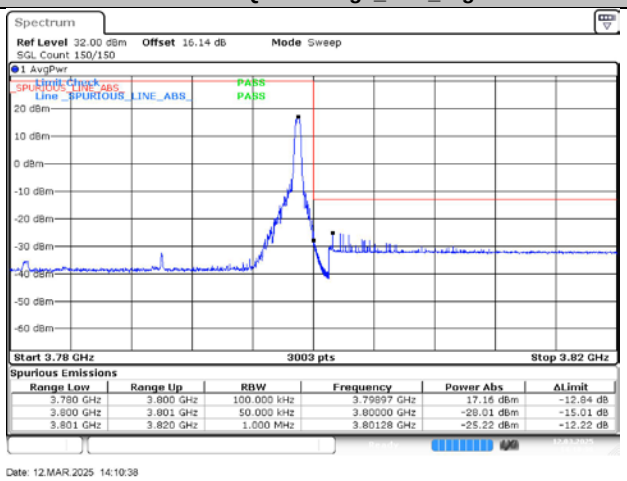


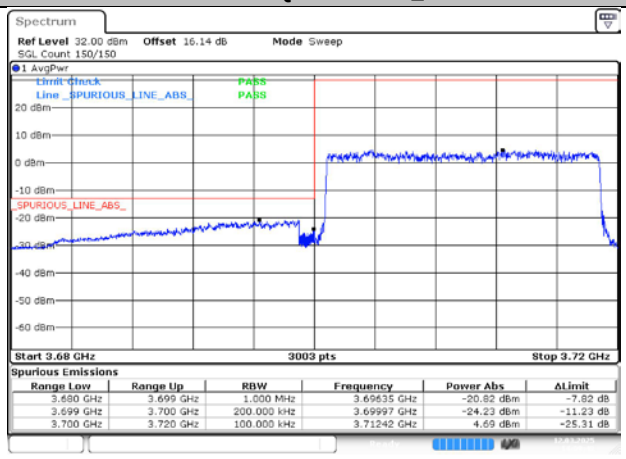
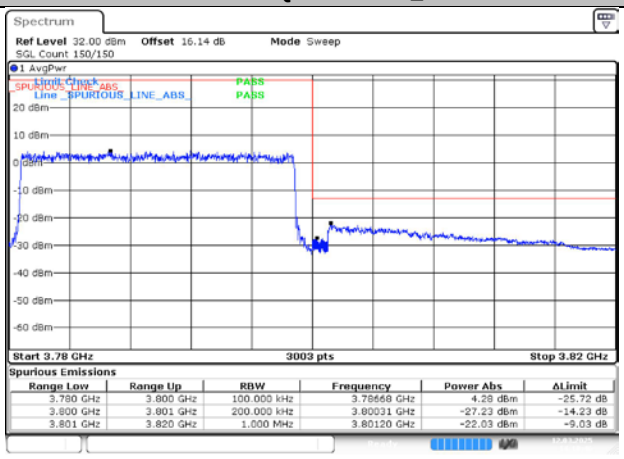


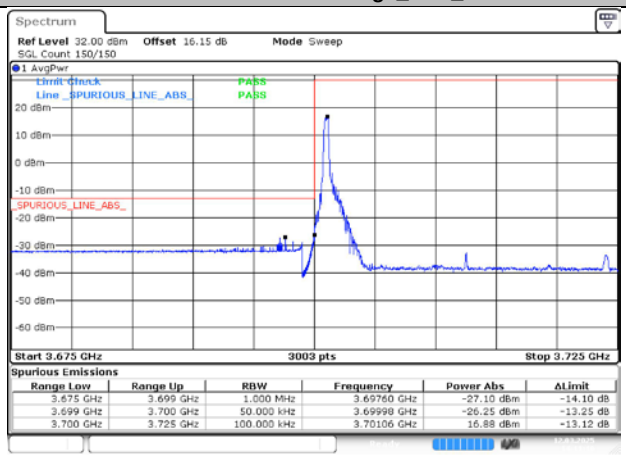
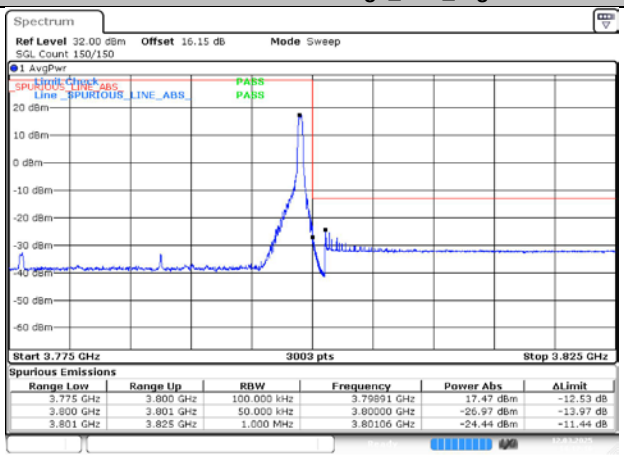
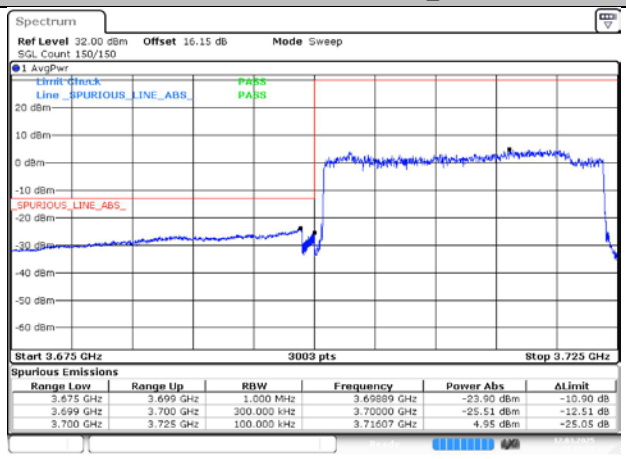
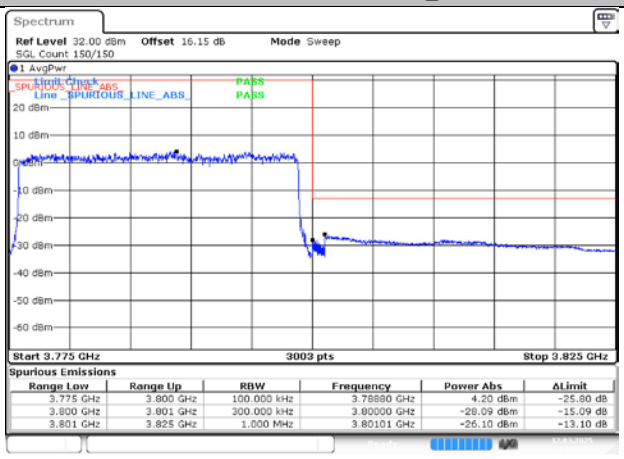
LCH / DFT-QPSK / Edge_1RB_Left	HCH / DFT-QPSK / Edge_1RB_Right
	
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

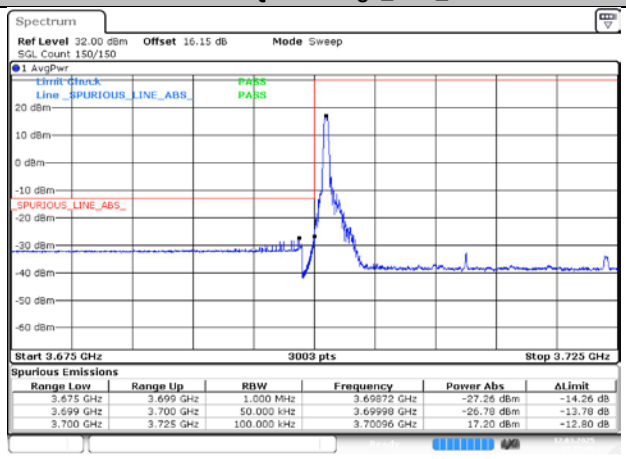
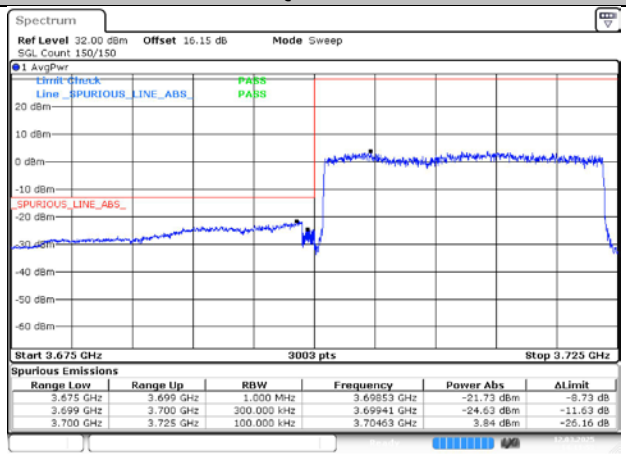
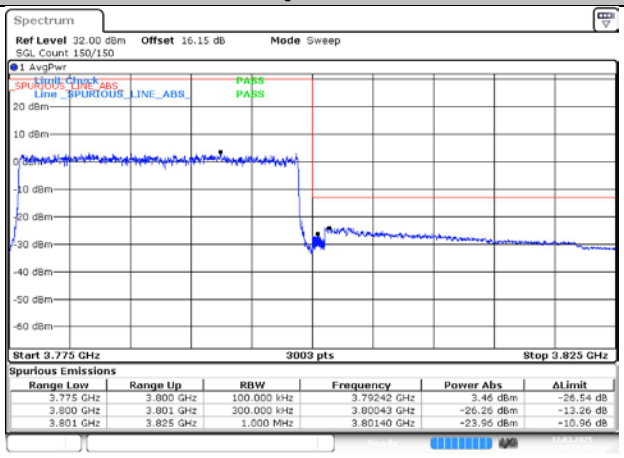
n78A / 30KHz / 15MHz	
LCH / DFT-Pi2BPSK / Edge_1RB_Left	HCH / DFT-Pi2BPSK / Edge_1RB_Right
	

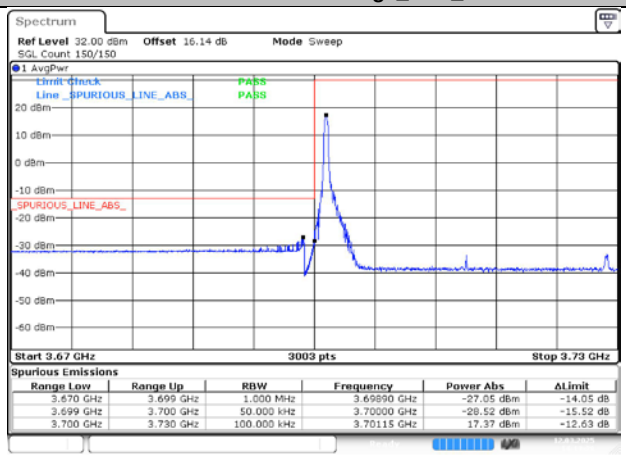
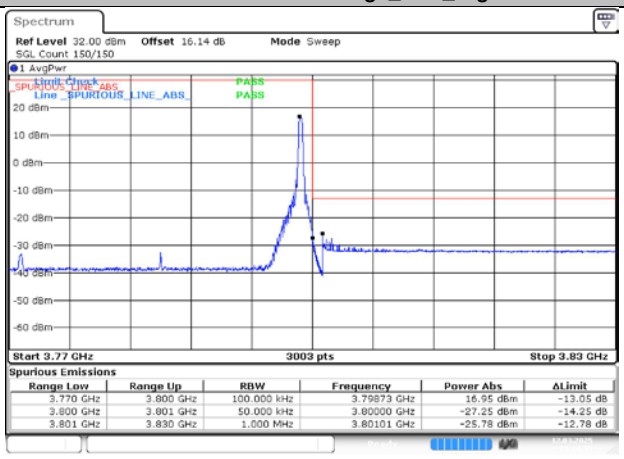
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	
LCH / DFT-QPSK / Edge_1RB_Left	HCH / DFT-QPSK / Edge_1RB_Right
	
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

n78A / 30KHz / 20MHz	
LCH / DFT-Pi2BPSK / Edge_1RB_Left	HCH / DFT-Pi2BPSK / Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	
LCH / DFT-QPSK / Edge_1RB_Left	HCH / DFT-QPSK / Edge_1RB_Right
	

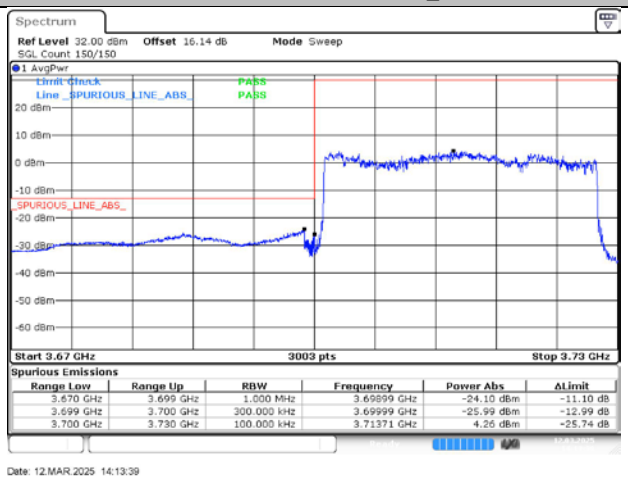
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

n78A / 30KHz / 25MHz	
LCH / DFT-Pi2BPSK / Edge_1RB_Left	HCH / DFT-Pi2BPSK / Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	

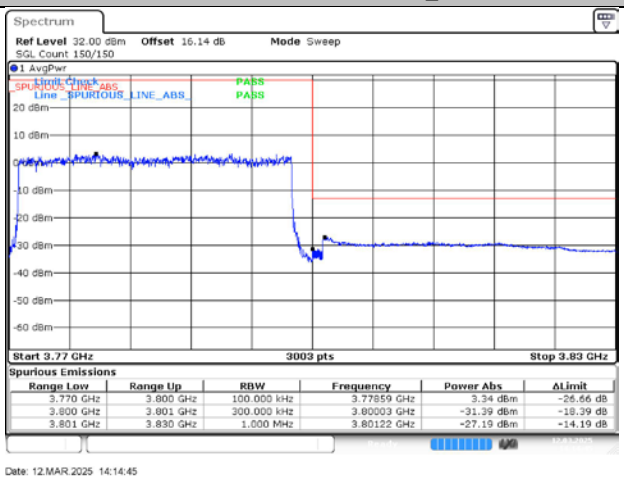
LCH / DFT-QPSK / Edge_1RB_Left	HCH / DFT-QPSK / Edge_1RB_Right
	
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

n78A / 30KHz / 30MHz	
LCH / DFT-Pi2BPSK / Edge_1RB_Left	HCH / DFT-Pi2BPSK / Edge_1RB_Right
	

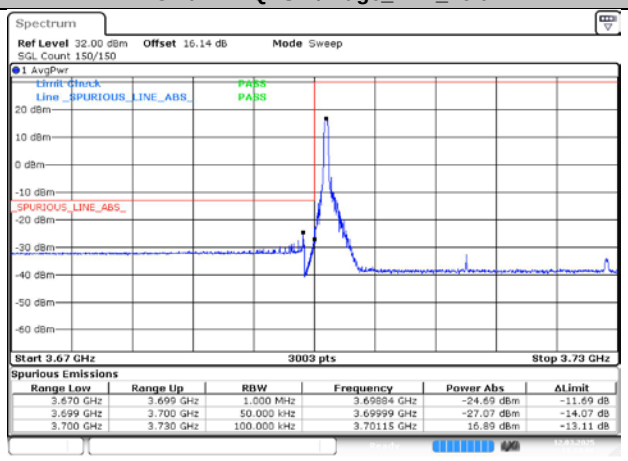
LCH / DFT-Pi2BPSK / Outer_Full



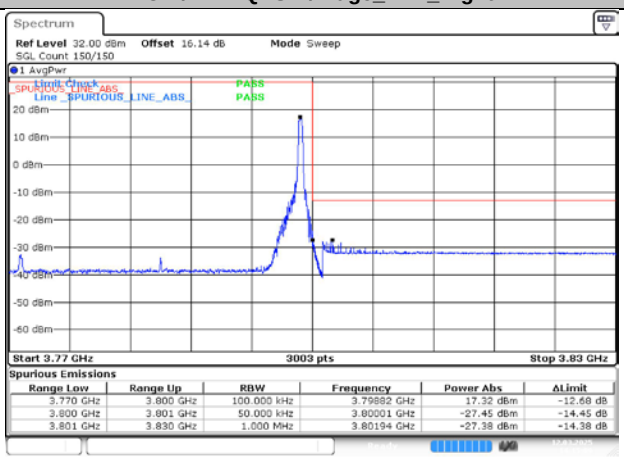
HCH / DFT-Pi2BPSK / Outer_Full



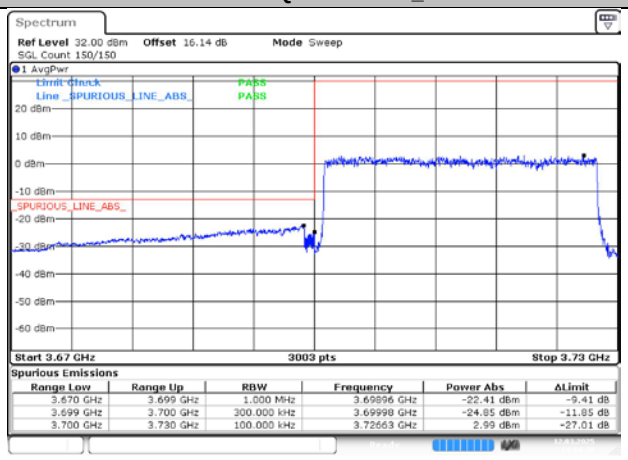
LCH / DFT-QPSK / Edge_1RB_Left



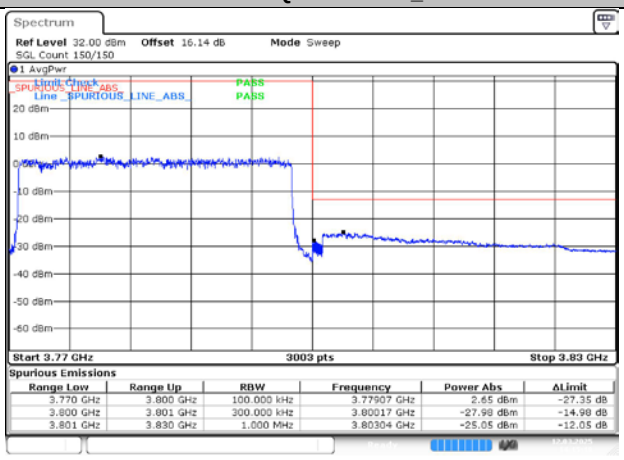
HCH / DFT-QPSK / Edge_1RB_Right

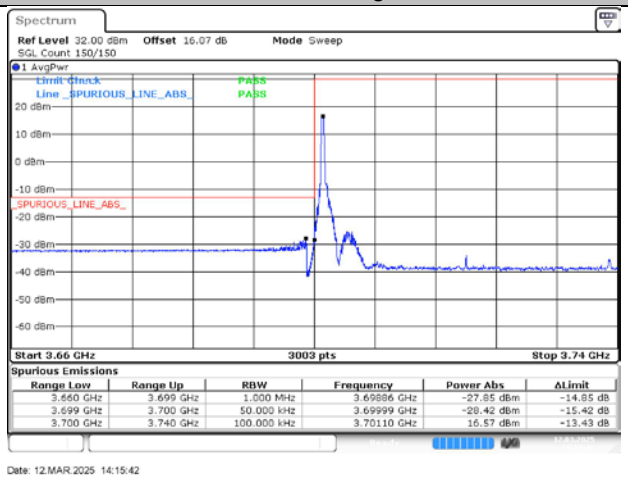
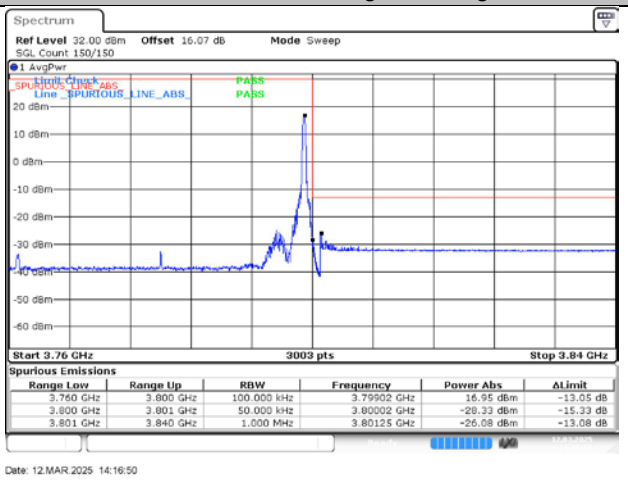
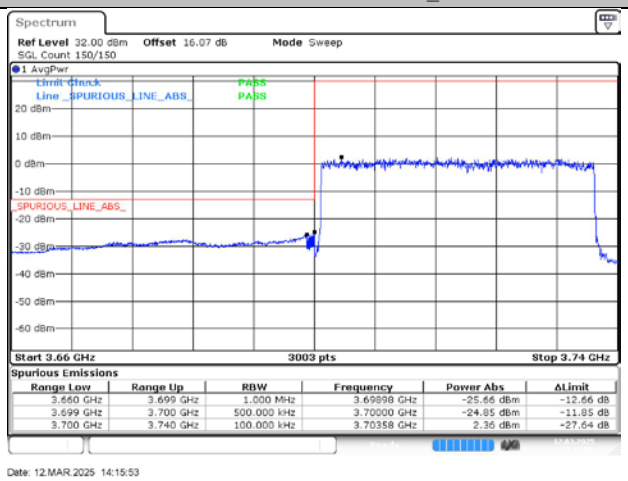
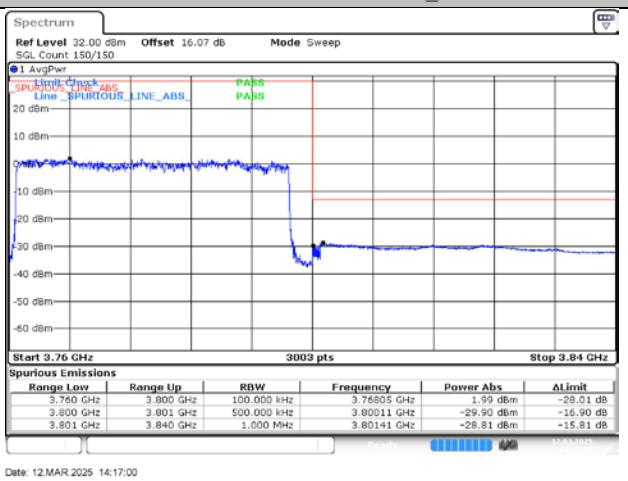
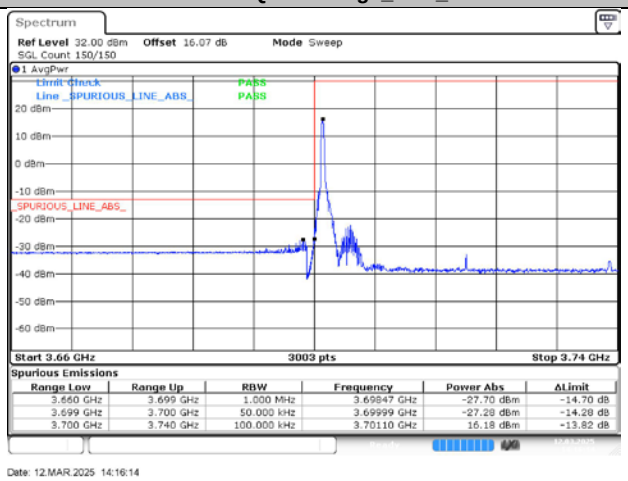
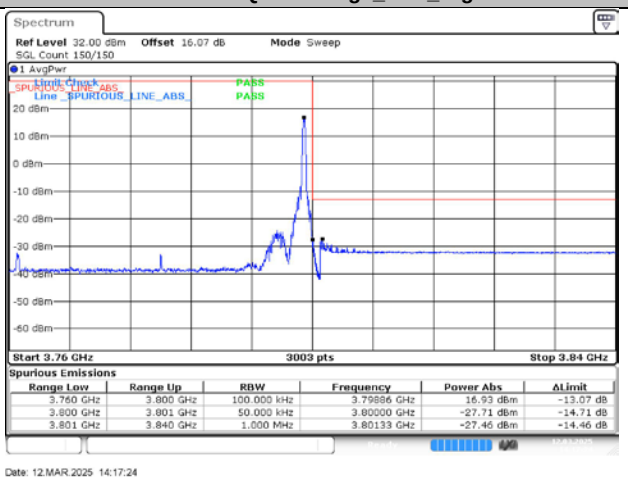


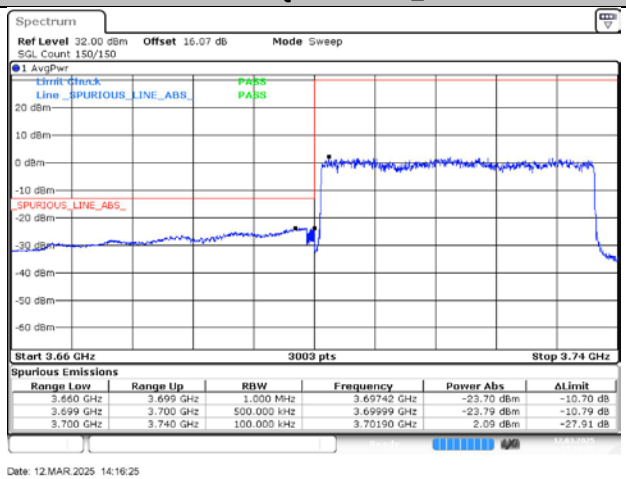
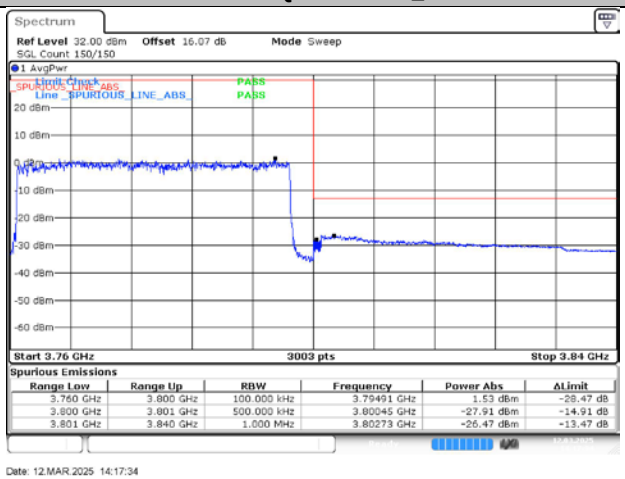
LCH / DFT-QPSK / Outer_Full

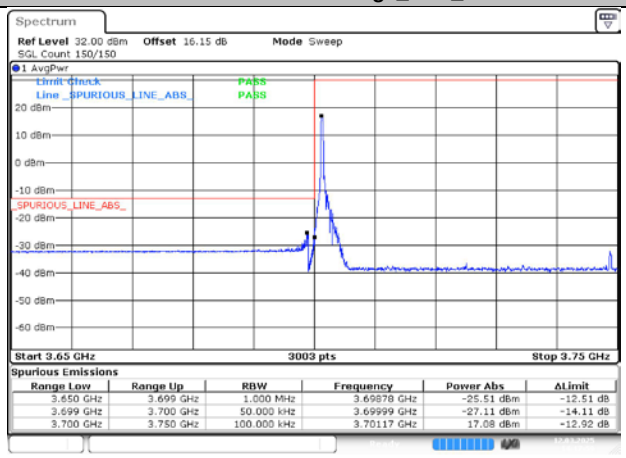
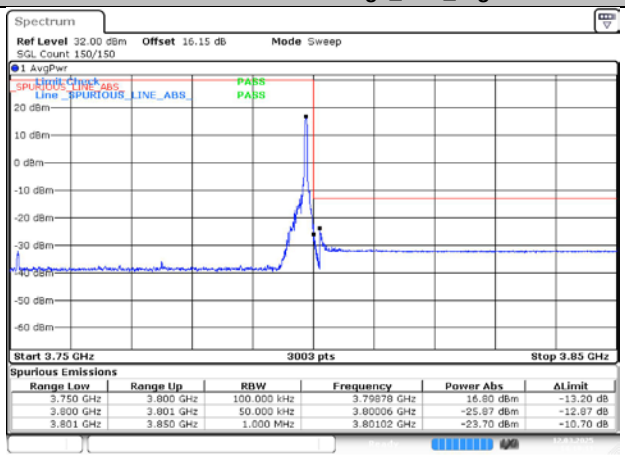
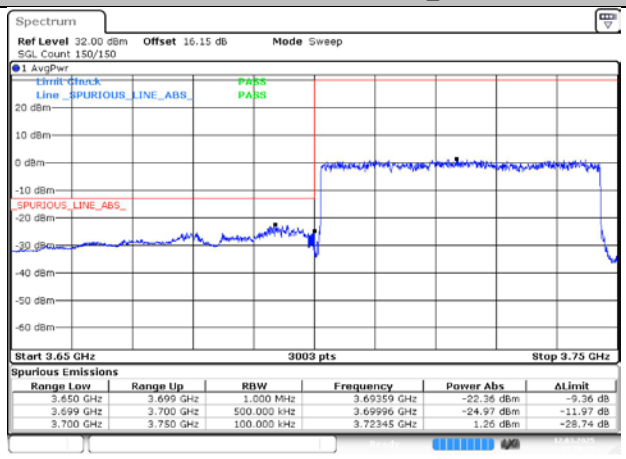
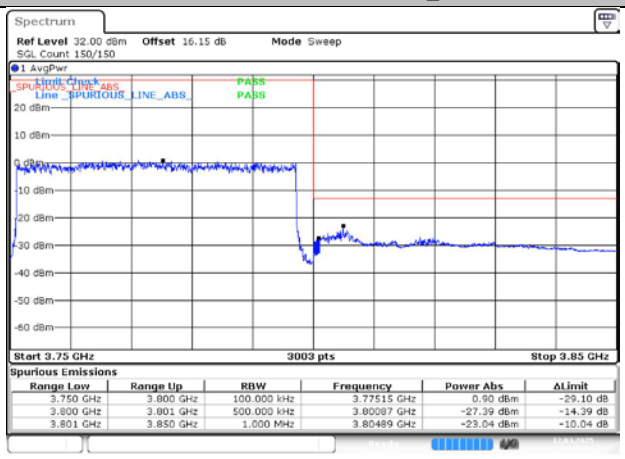


HCH / DFT-QPSK / Outer_Full

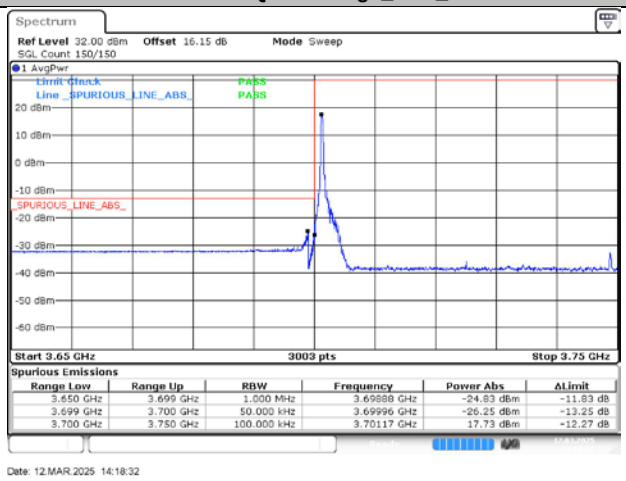


n78A / 30KHz / 40MHz	
LCH / DFT-Pi2BPSK / Edge_1RB_Left	HCH / DFT-Pi2BPSK / Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	
LCH / DFT-QPSK / Edge_1RB_Left	HCH / DFT-QPSK / Edge_1RB_Right
	

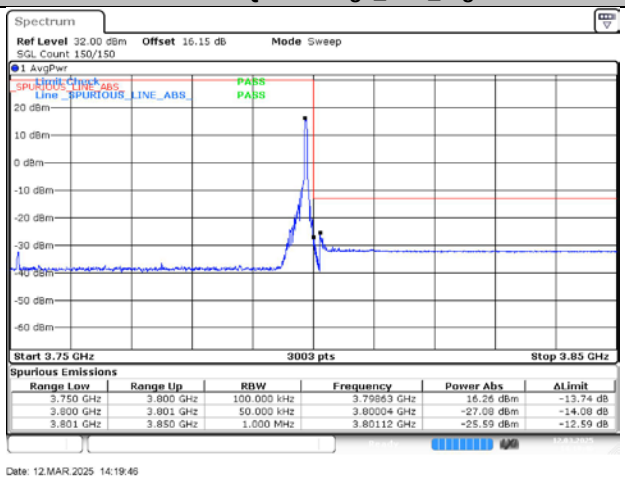
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

n78A / 30KHz / 50MHz	
LCH / DFT-Pi2BPSK / Edge_1RB_Left	HCH / DFT-Pi2BPSK / Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	

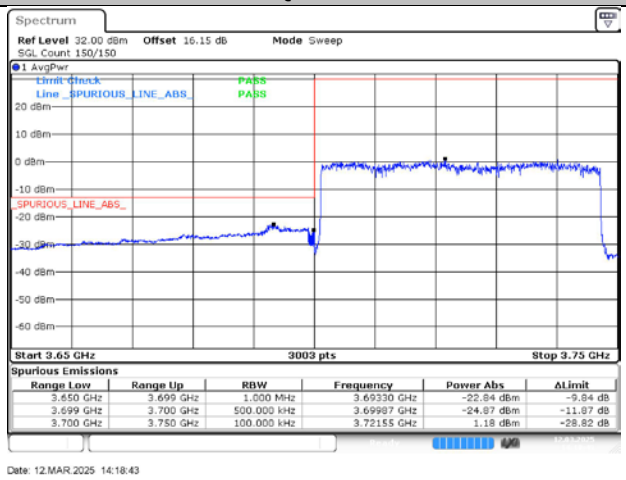
LCH / DFT-QPSK / Edge_1RB_Left



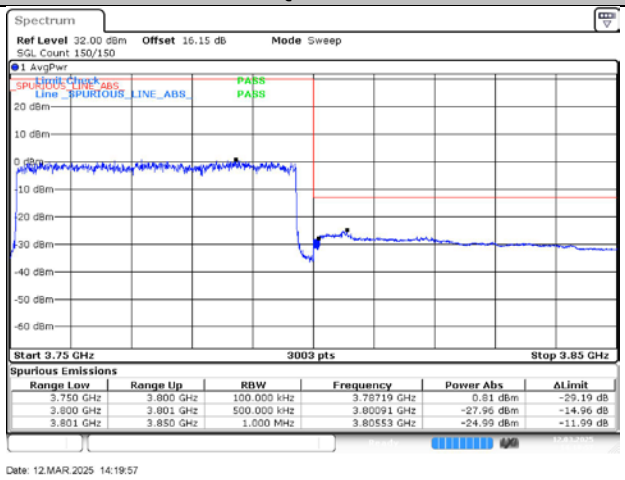
HCH / DFT-QPSK / Edge_1RB_Right



LCH / DFT-QPSK / Outer_Full

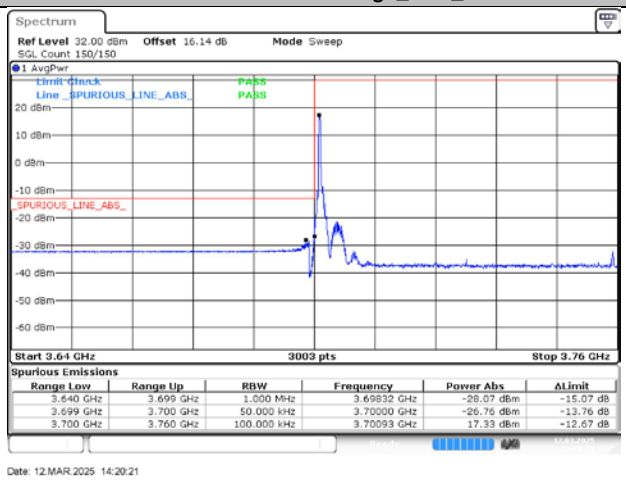


HCH / DFT-QPSK / Outer_Full

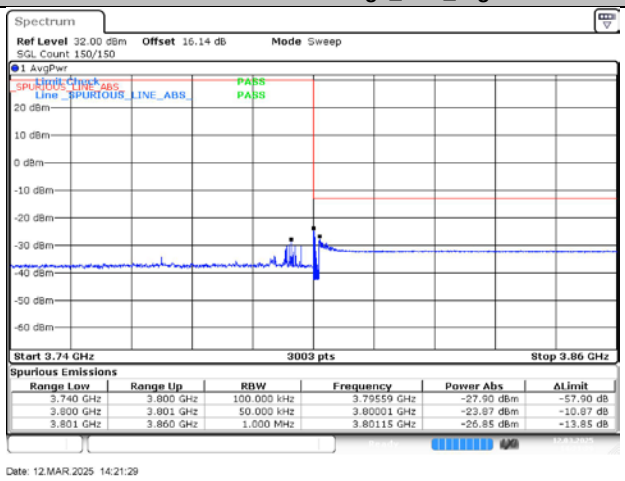


n78A / 30KHz / 60MHz

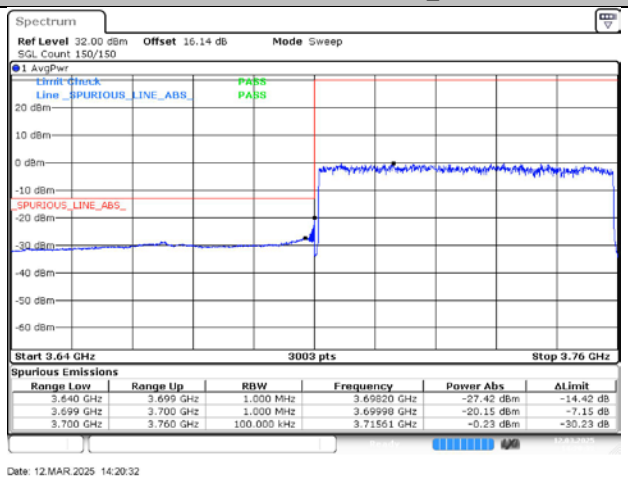
LCH / DFT-Pi2BPSK / Edge_1RB_Left



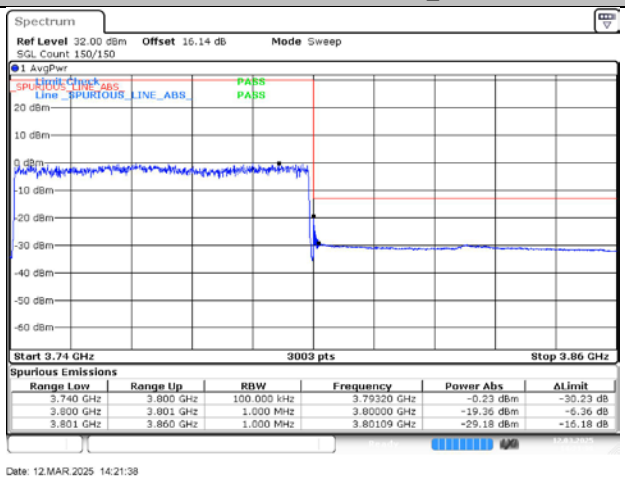
HCH / DFT-Pi2BPSK / Edge_1RB_Right



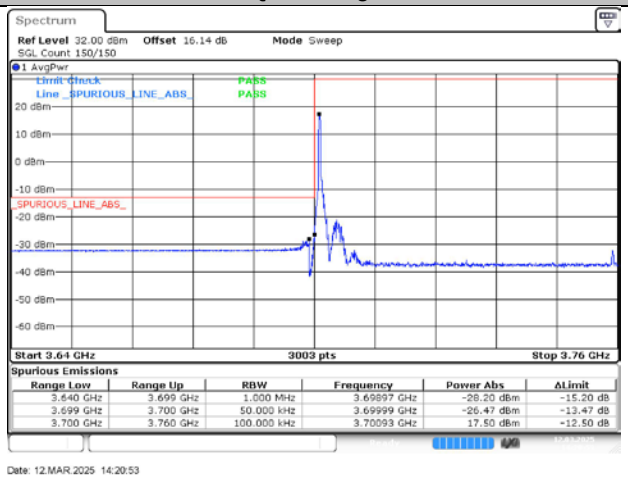
LCH / DFT-Pi2BPSK / Outer_Full



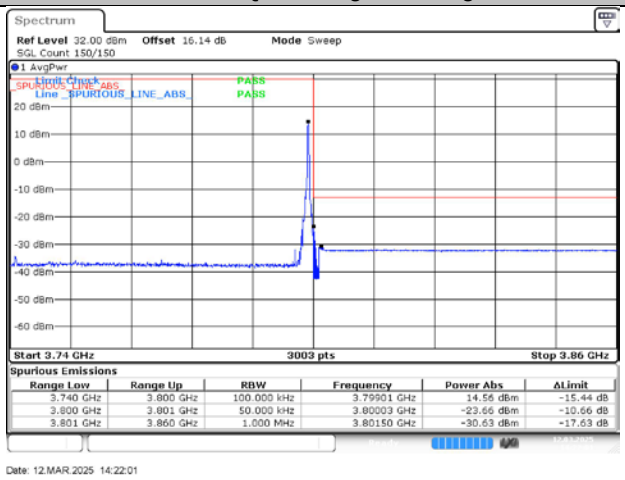
HCH / DFT-Pi2BPSK / Outer_Full



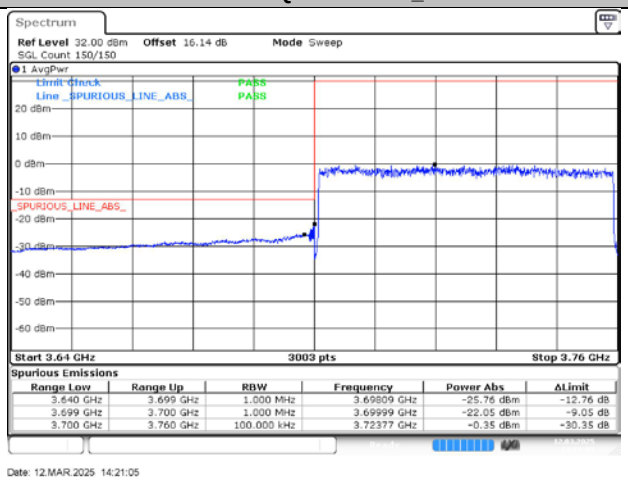
LCH / DFT-QPSK / Edge_1RB_Left



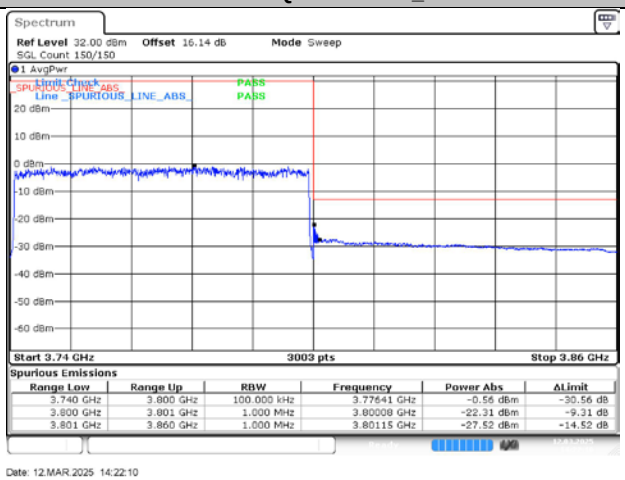
HCH / DFT-QPSK / Edge_1RB_Right

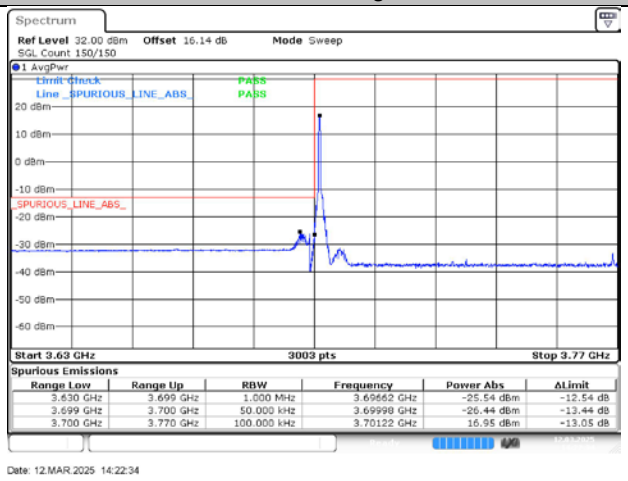
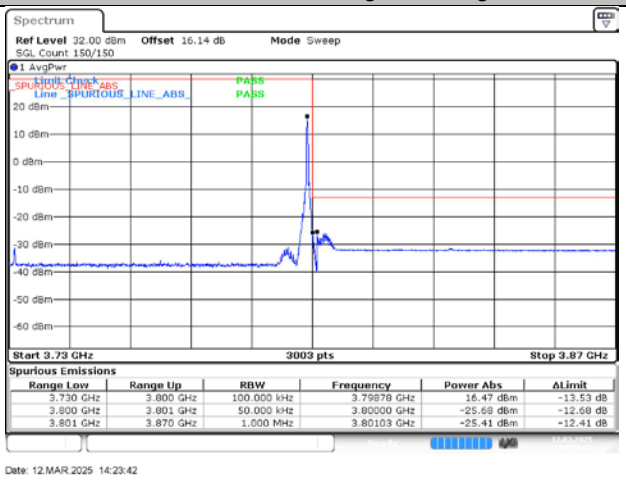
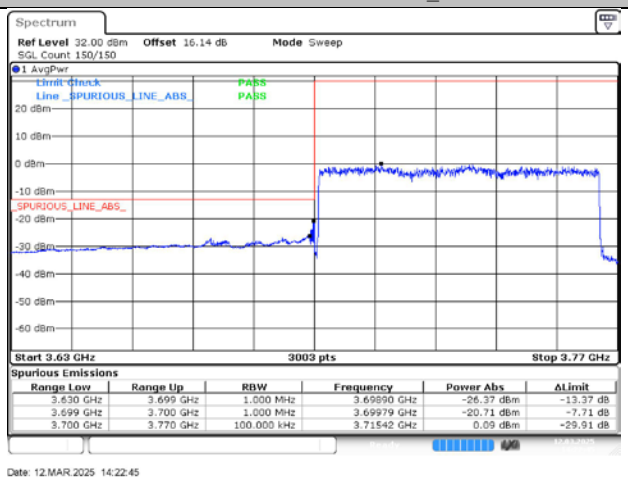
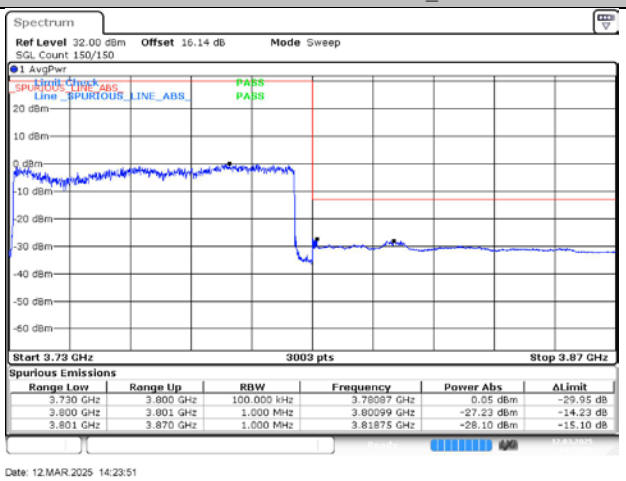


LCH / DFT-QPSK / Outer_Full



HCH / DFT-QPSK / Outer_Full



n78A / 30KHz / 70MHz	
LCH / DFT-Pi2BPSK / Edge_1RB_Left	HCH / DFT-Pi2BPSK / Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	
LCH / DFT-QPSK / Edge_1RB_Left	HCH / DFT-QPSK / Edge_1RB_Right