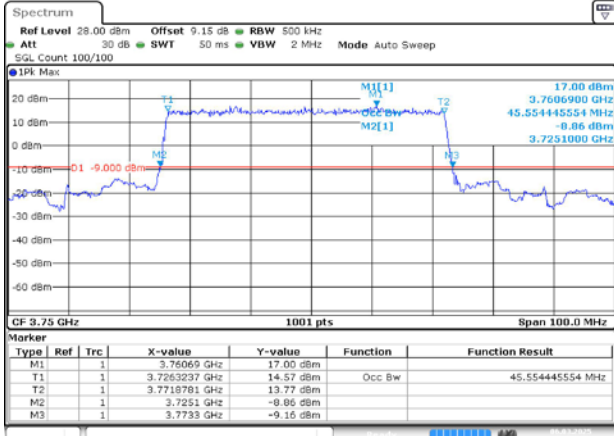


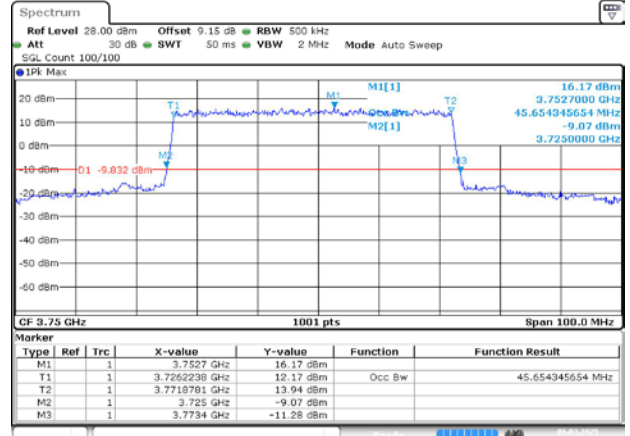
n78A / 30kHz / 50MHz

MCH / DFT-Pi2BPSK / Outer\_Full



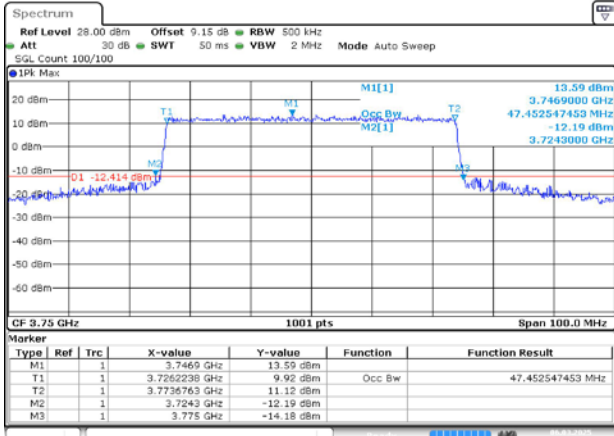
Date: 6 MAR 2025 18:38:37

MCH / DFT-QPSK / Outer\_Full



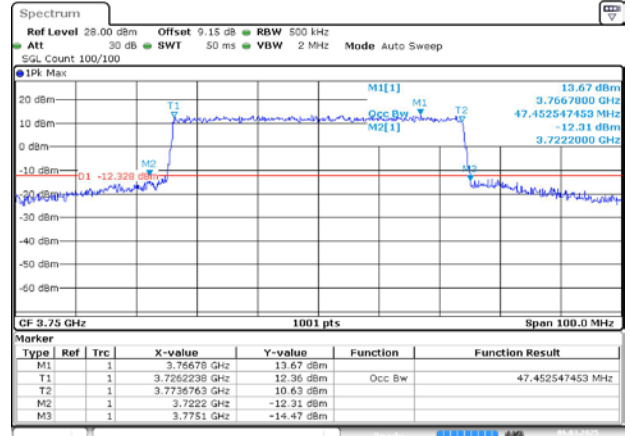
Date: 6 MAR 2025 18:38:48

MCH / CP-QPSK / Outer\_Full



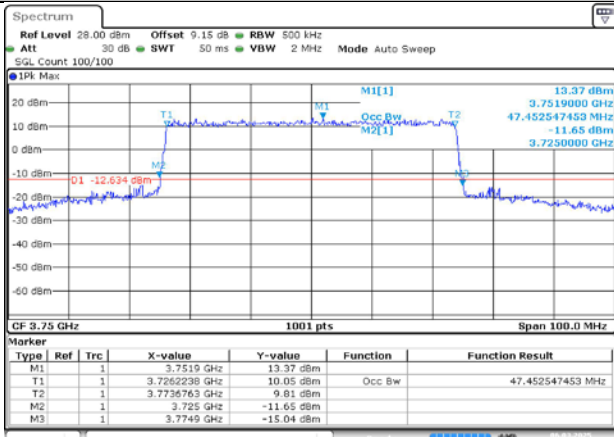
Date: 6 MAR 2025 18:38:57

MCH / CP-16QAM / Outer\_Full



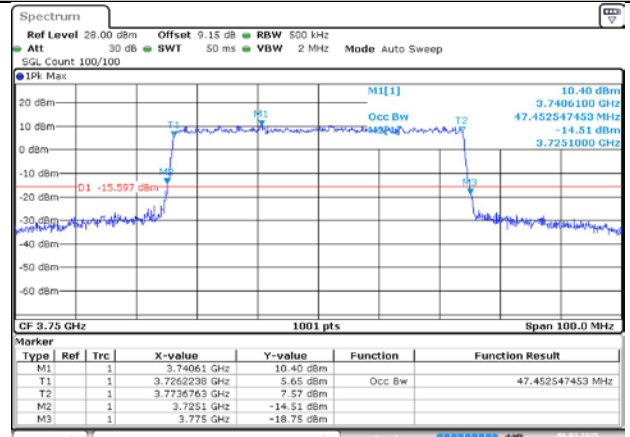
Date: 6 MAR 2025 18:39:06

MCH / CP-64QAM / Outer\_Full

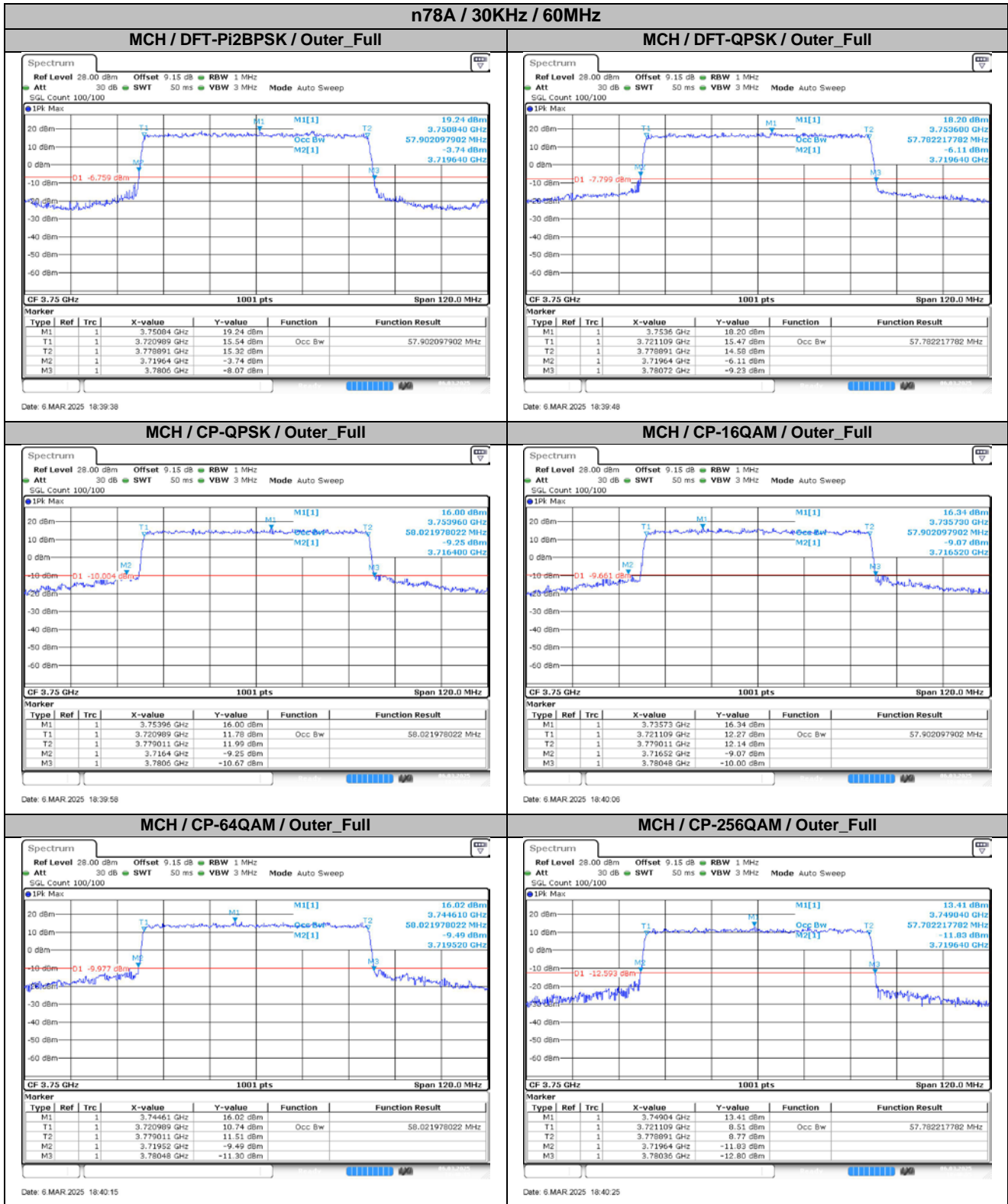


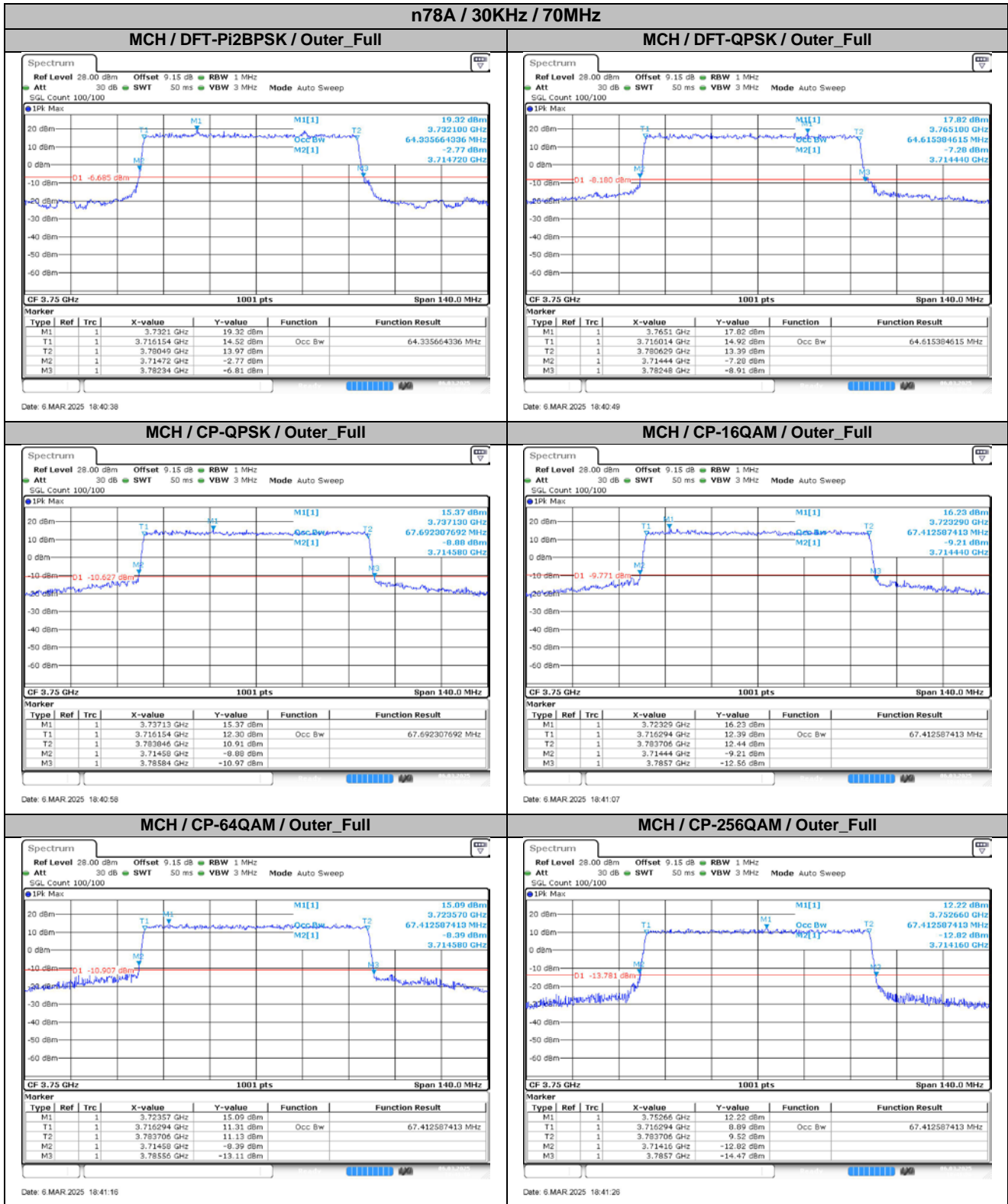
Date: 6 MAR 2025 18:39:14

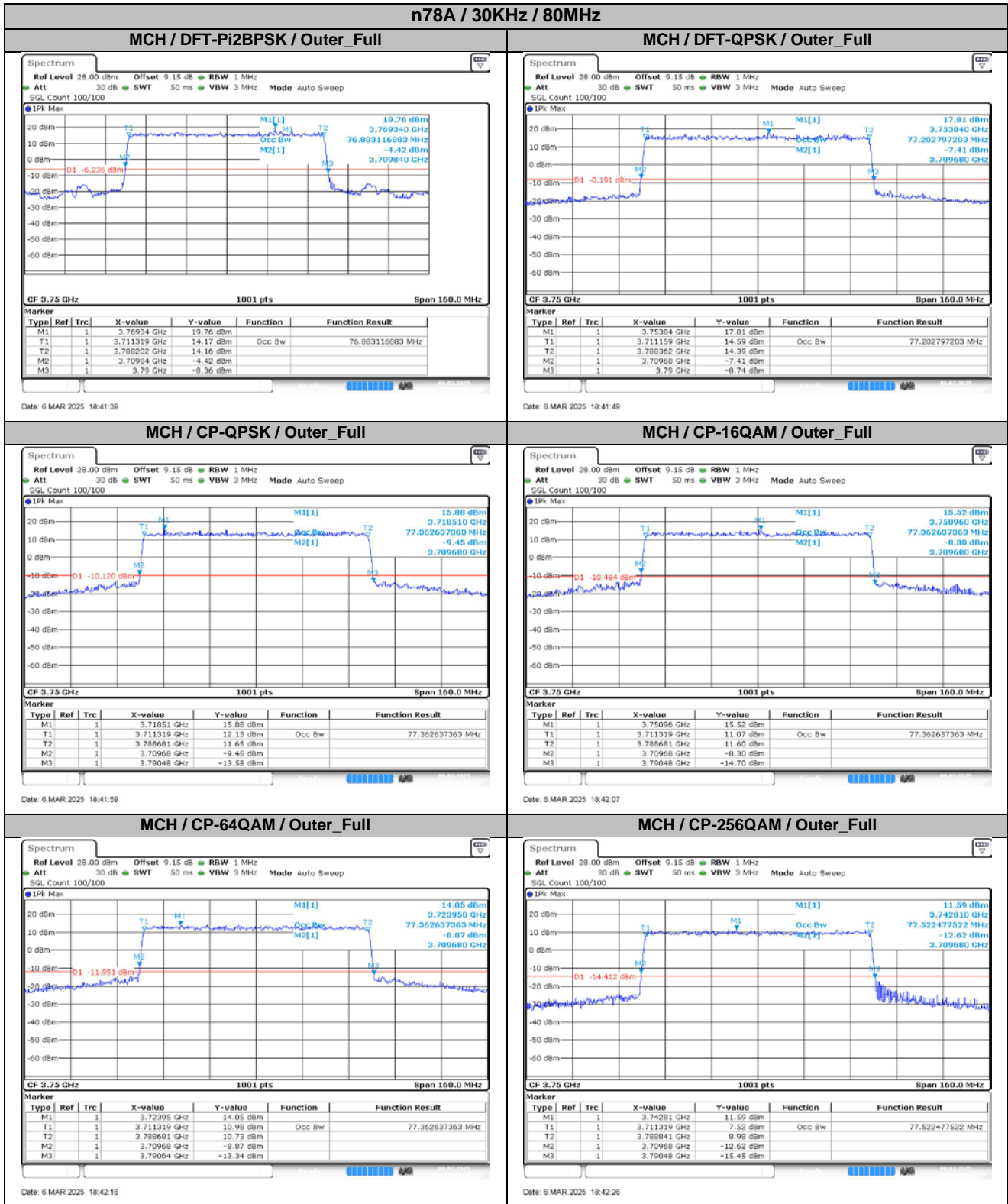
MCH / CP-256QAM / Outer\_Full

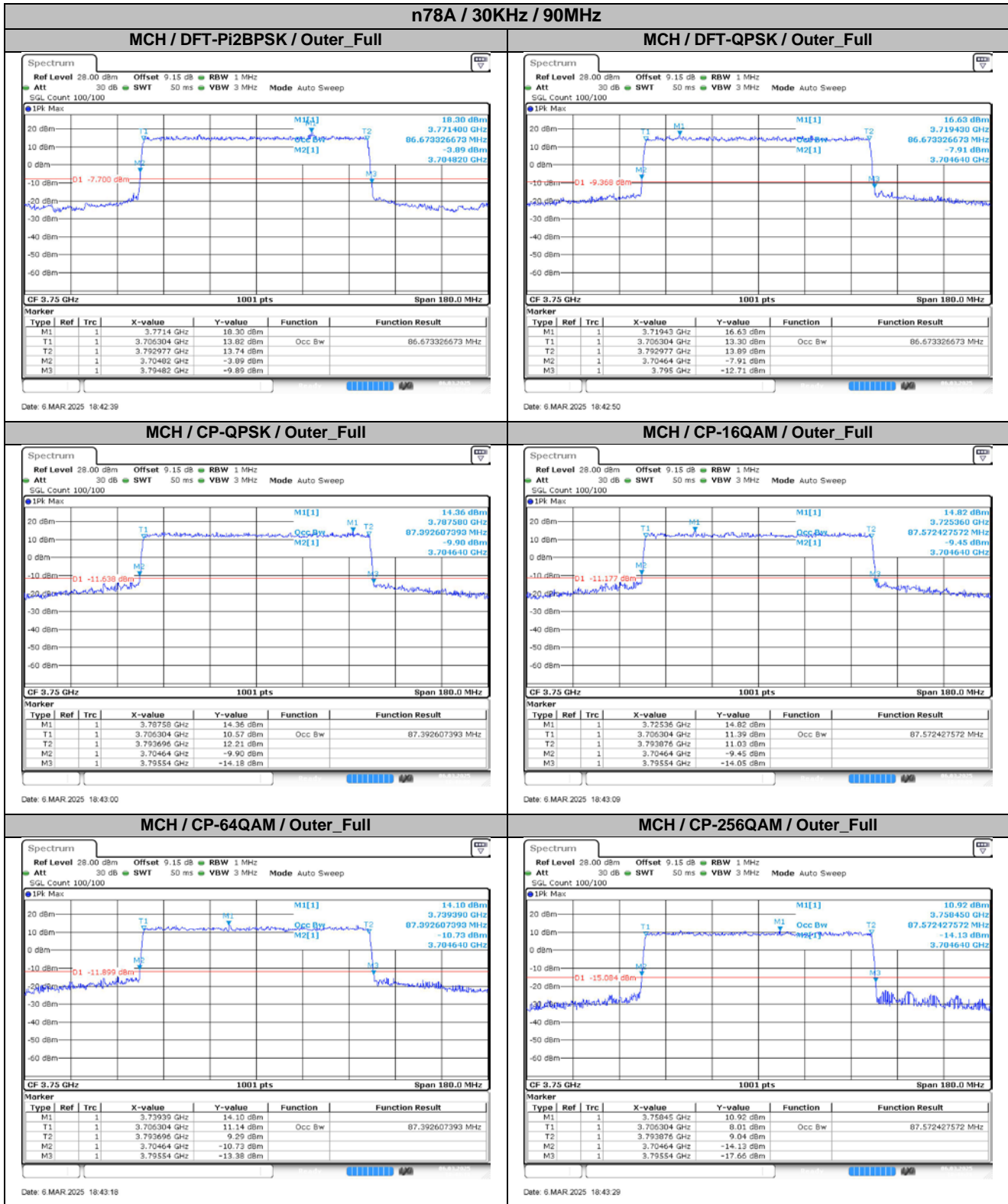


Date: 6 MAR 2025 18:39:25



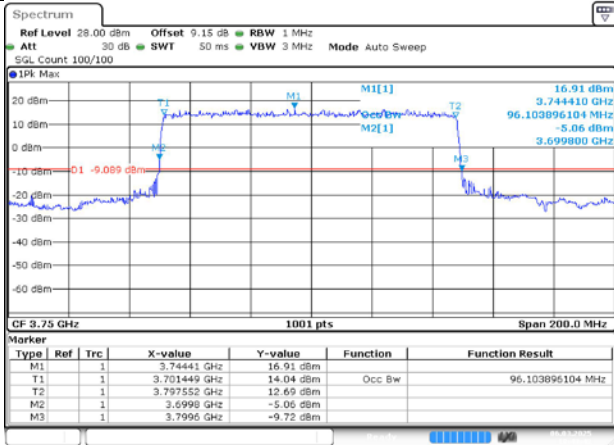






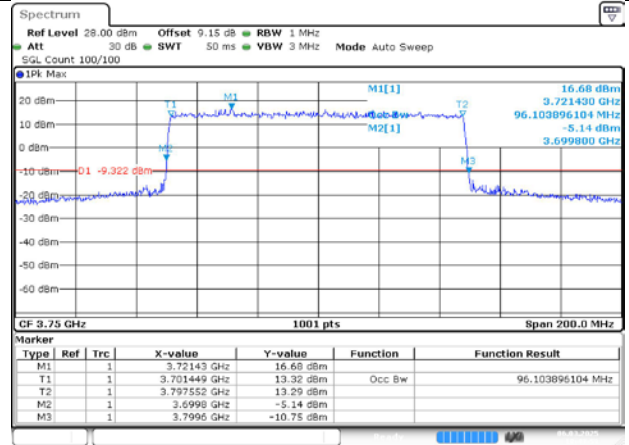
n78A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer\_Full



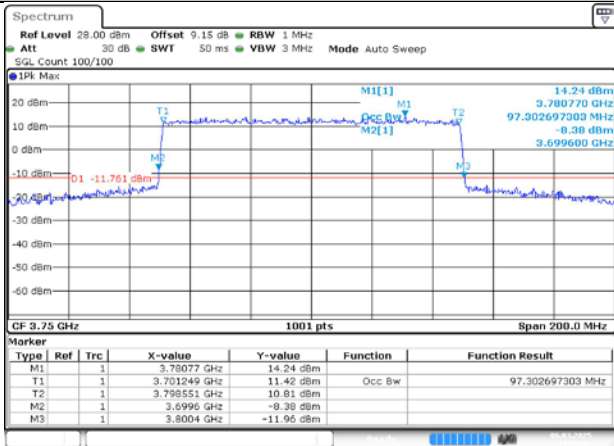
Date: 6 MAR 2025 18:43:42

MCH / DFT-QPSK / Outer\_Full



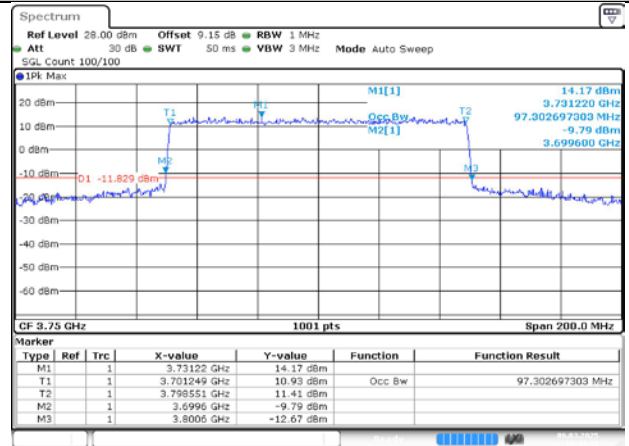
Date: 6 MAR 2025 18:43:53

MCH / CP-QPSK / Outer\_Full



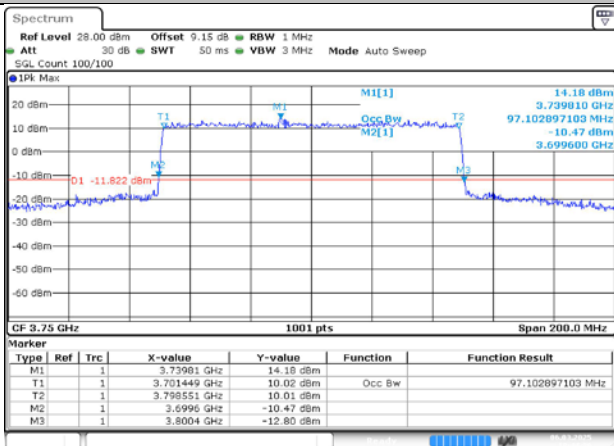
Date: 6 MAR 2025 18:44:02

MCH / CP-16QAM / Outer\_Full



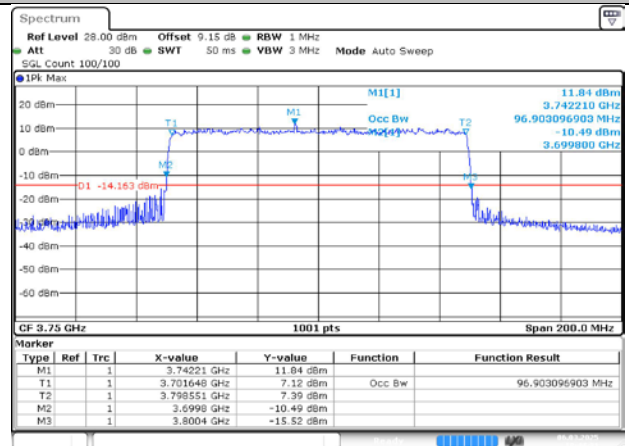
Date: 6 MAR 2025 18:44:11

MCH / CP-64QAM / Outer\_Full



Date: 6 MAR 2025 18:44:20

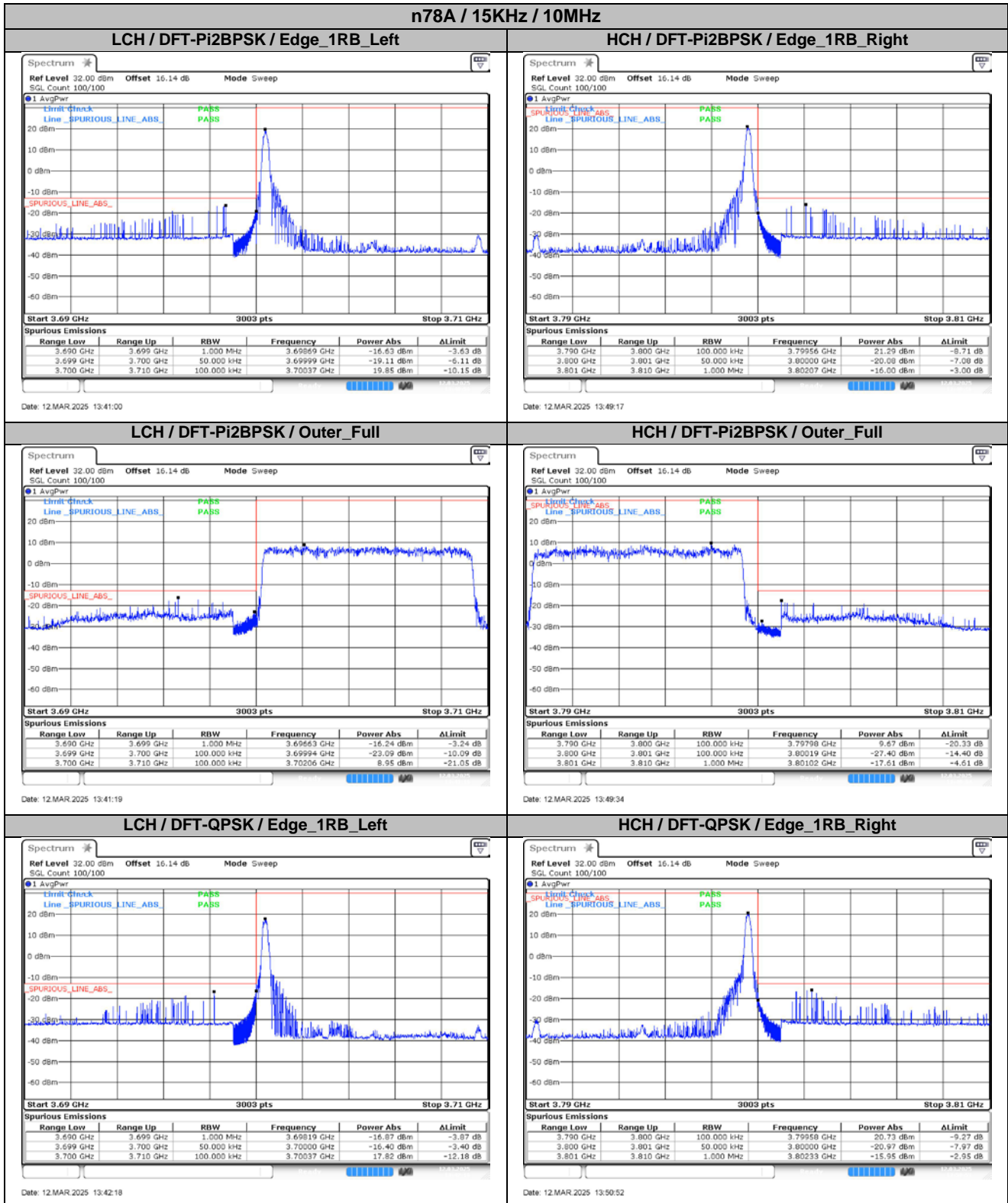
MCH / CP-256QAM / Outer\_Full

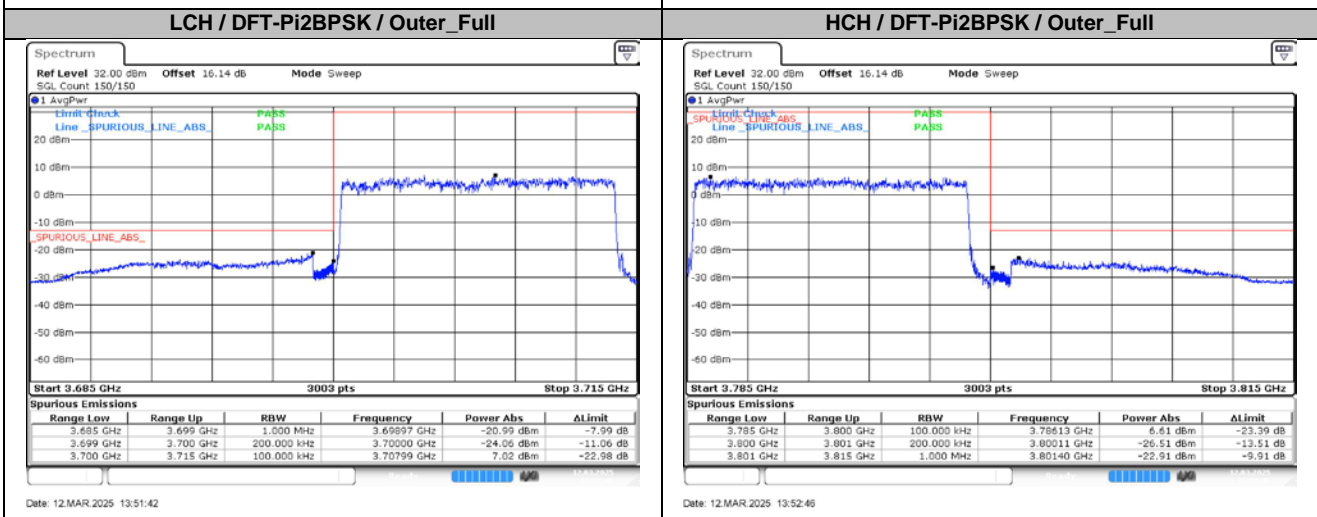
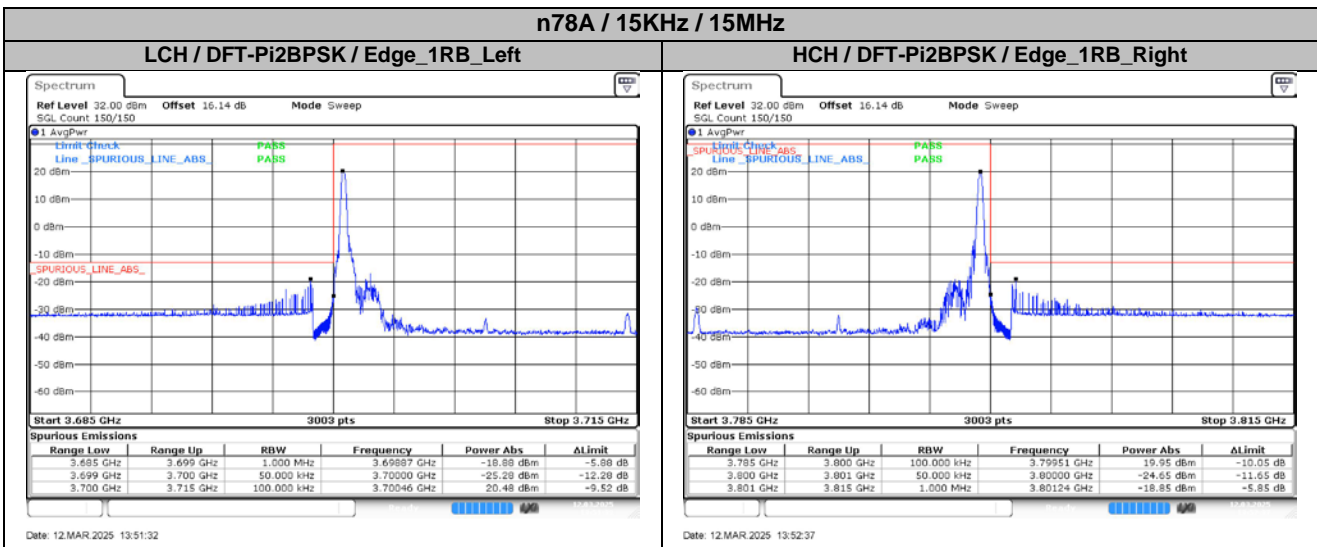
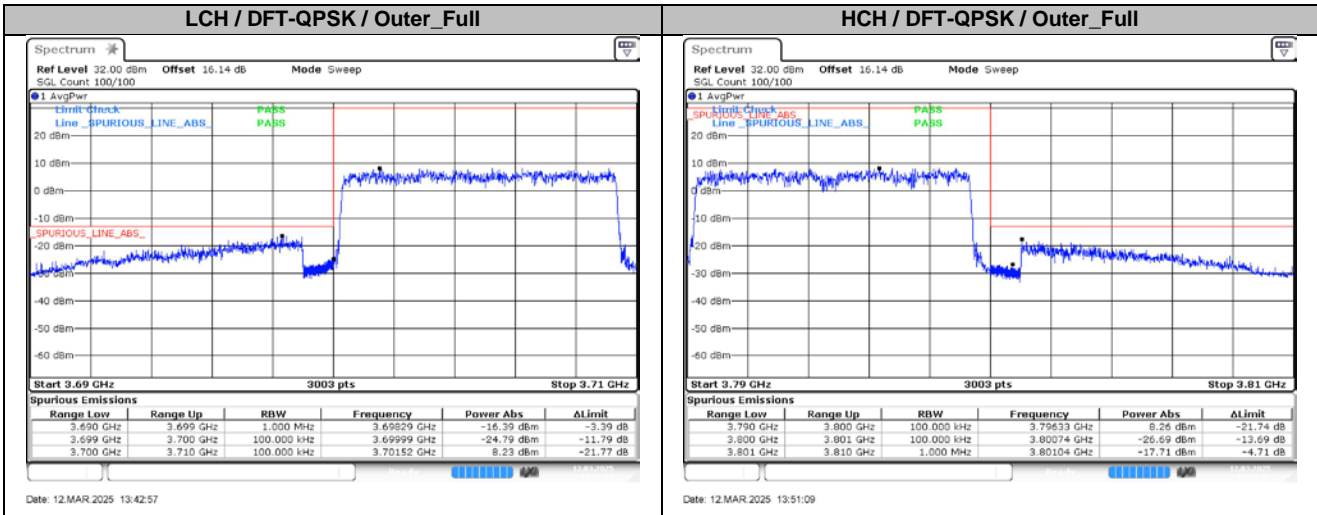


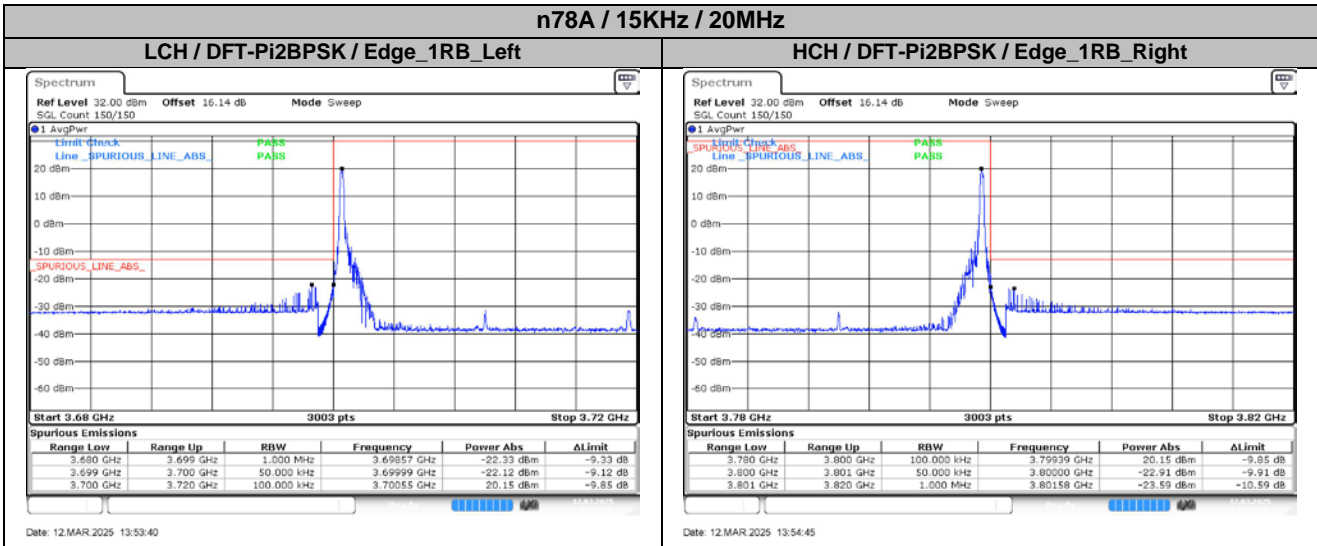
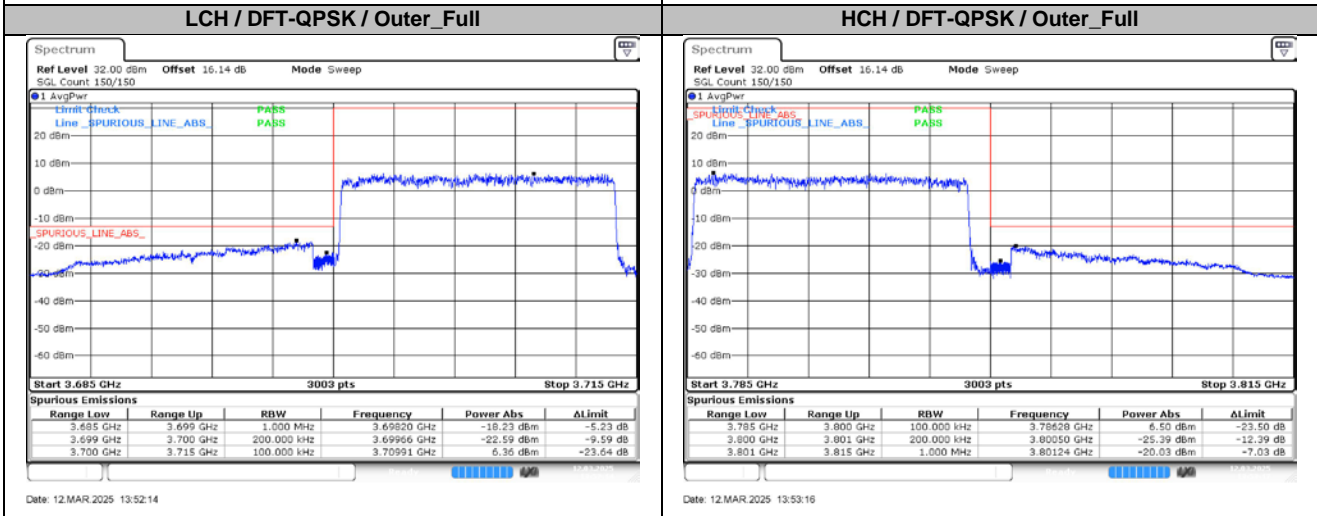
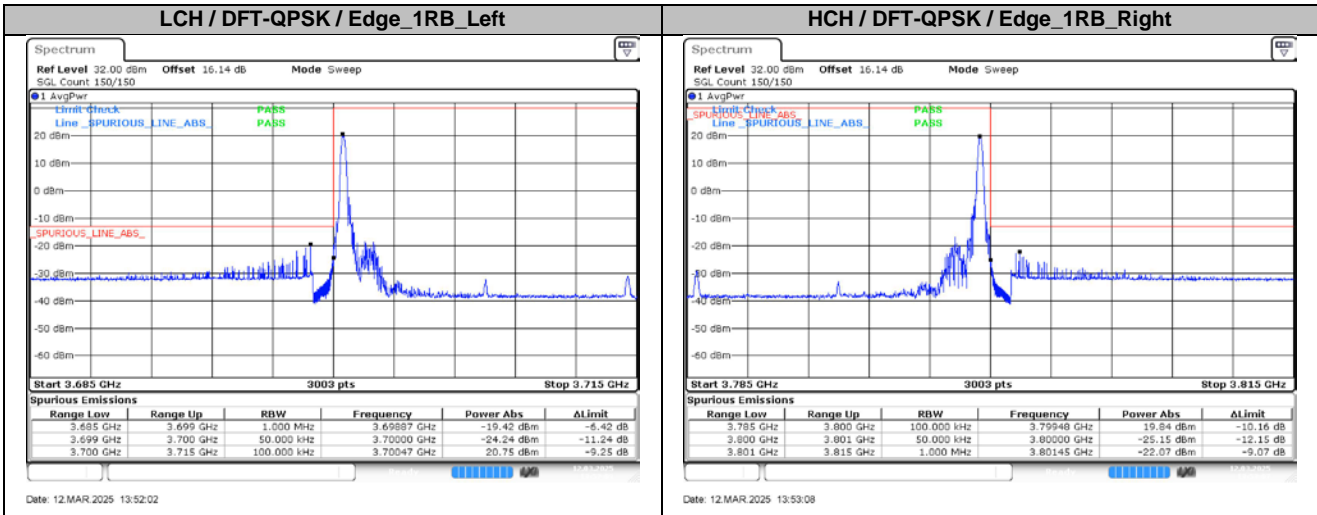
Date: 6 MAR 2025 18:44:31

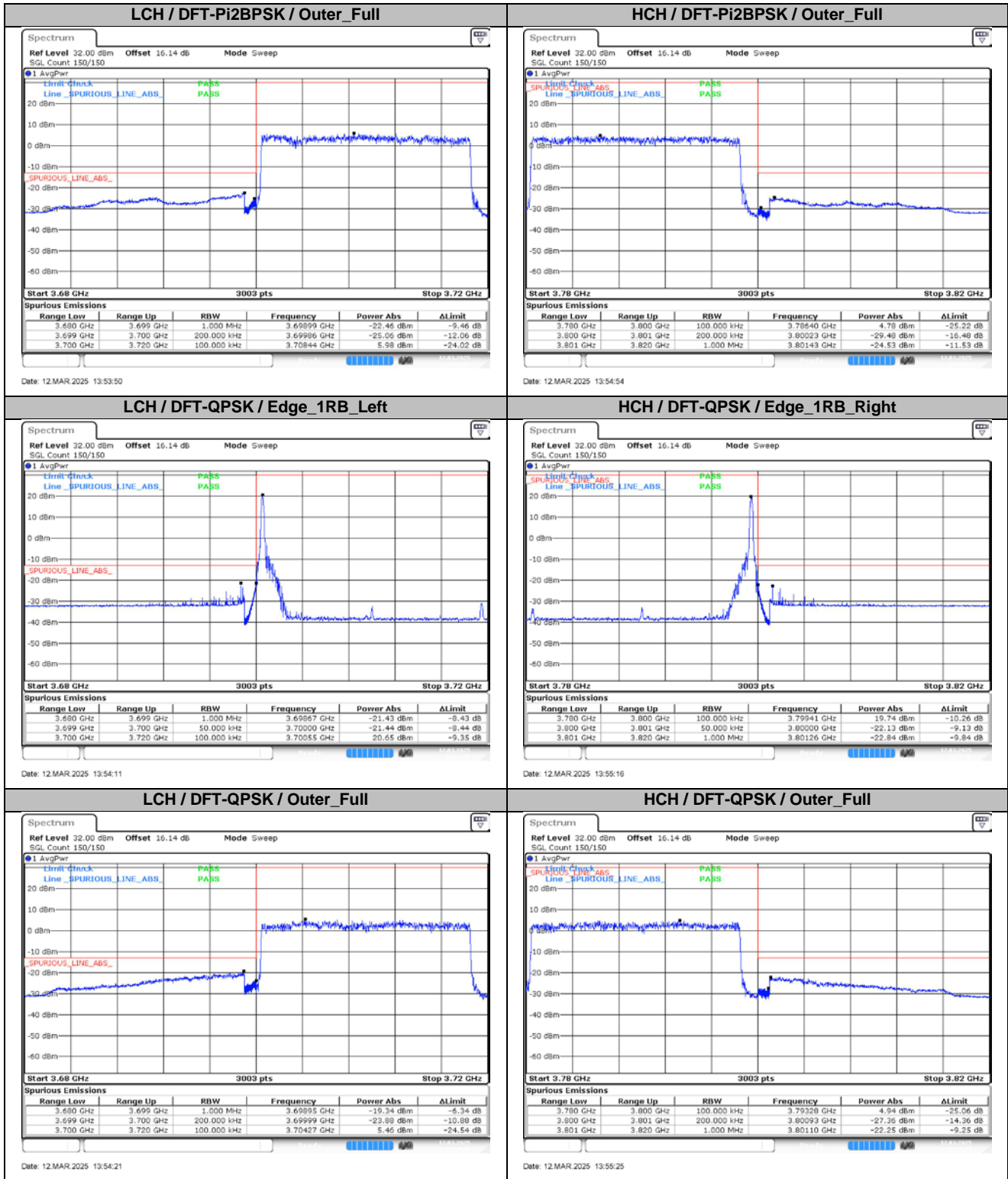
### 5. Conducted Band Edges

#### 5.1. Test Plots for SCS=15KHz



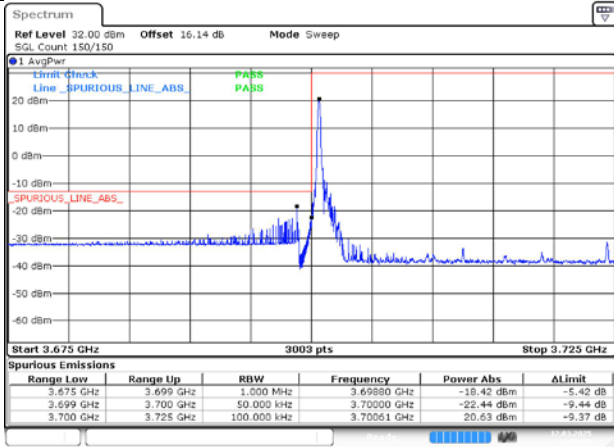






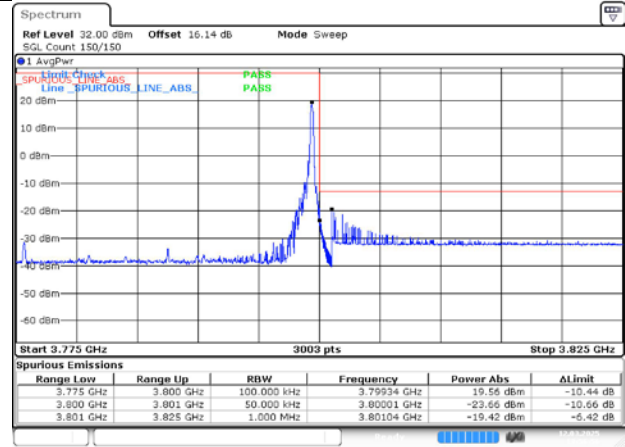
n78A / 15KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



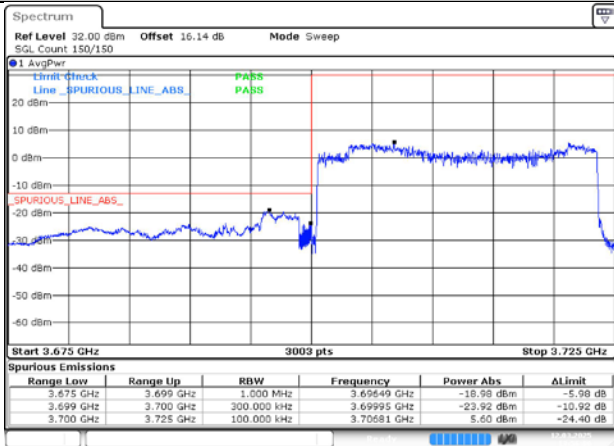
Date: 12.MAR.2025 13:55:50

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



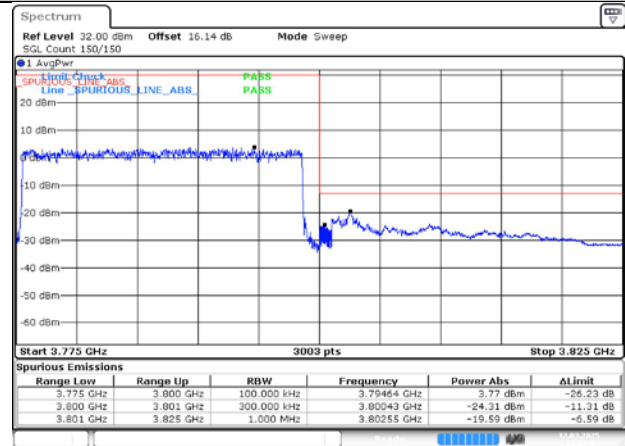
Date: 12.MAR.2025 13:56:56

LCH / DFT-Pi2BPSK / Outer\_Full



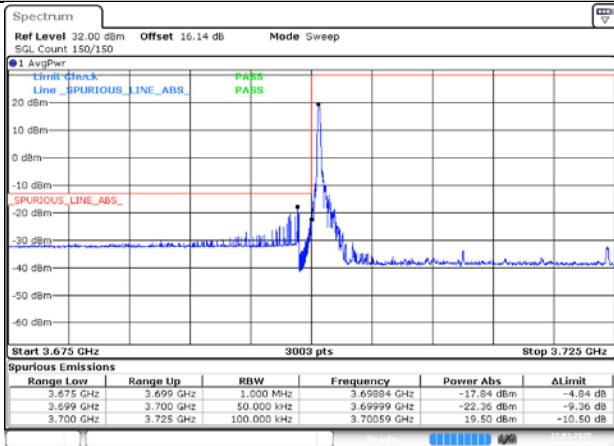
Date: 12.MAR.2025 13:56:00

HCH / DFT-Pi2BPSK / Outer\_Full



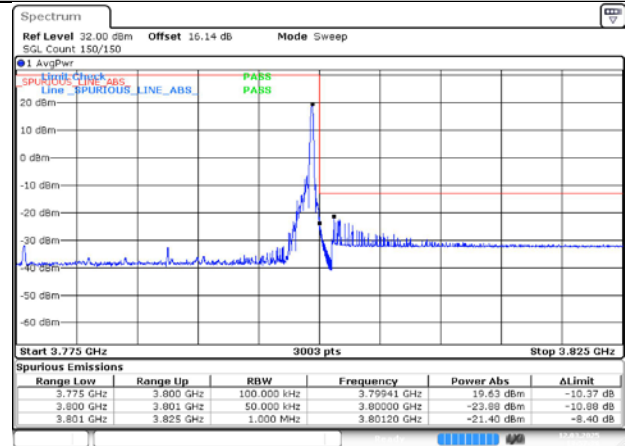
Date: 12.MAR.2025 13:57:05

LCH / DFT-QPSK / Edge\_1RB\_Left



Date: 12.MAR.2025 13:56:21

HCH / DFT-QPSK / Edge\_1RB\_Right



Date: 12.MAR.2025 13:57:28

