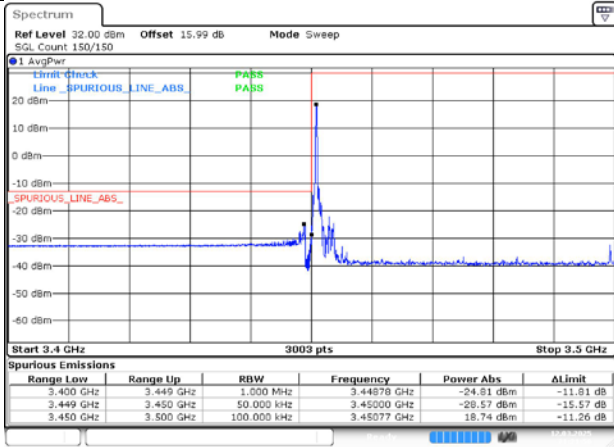


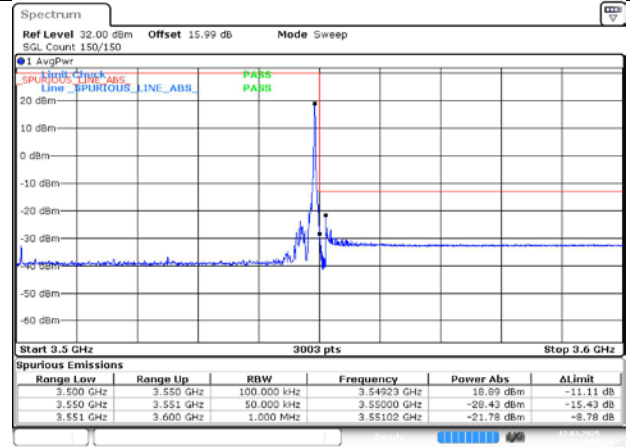
n78A / 15KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



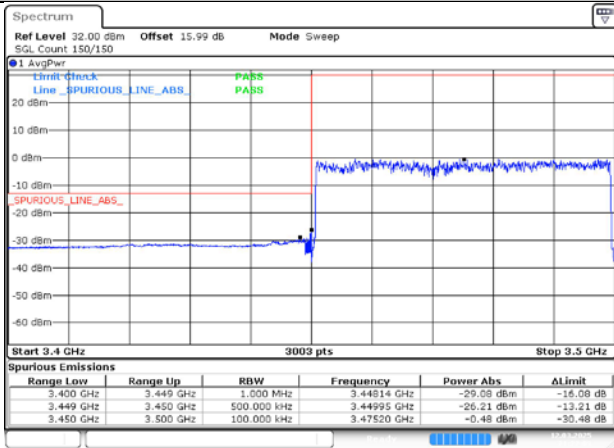
Date: 12.MAR.2025 21:21:50

HCH / DFT-Pi2BPSK / Edge_1RB_Right



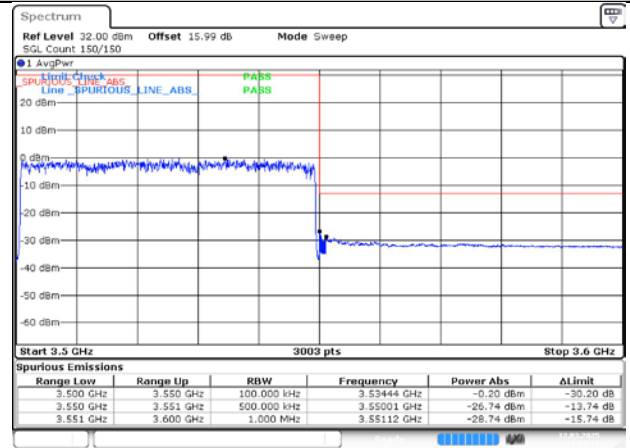
Date: 12.MAR.2025 21:23:03

LCH / DFT-Pi2BPSK / Outer_Full



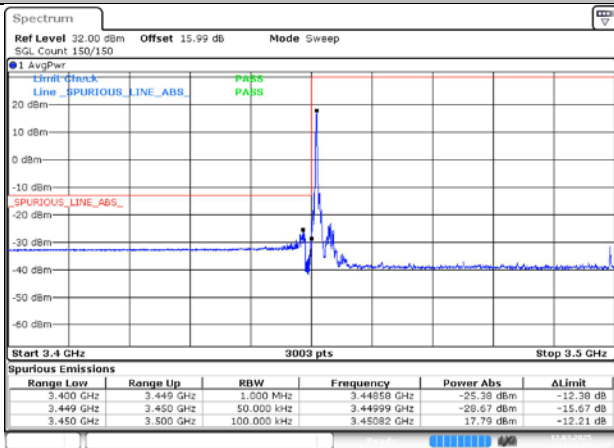
Date: 12.MAR.2025 21:22:03

HCH / DFT-Pi2BPSK / Outer_Full



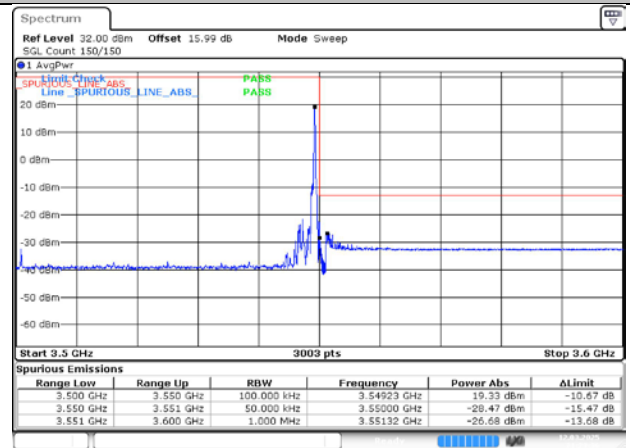
Date: 12.MAR.2025 21:23:12

LCH / DFT-QPSK / Edge_1RB_Left

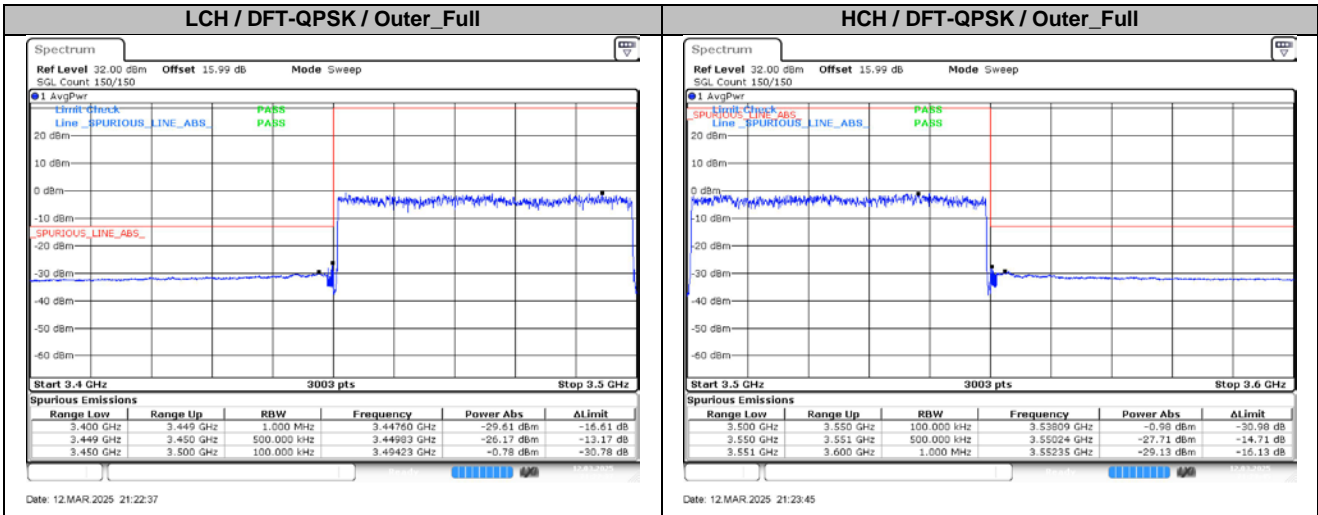


Date: 12.MAR.2025 21:22:25

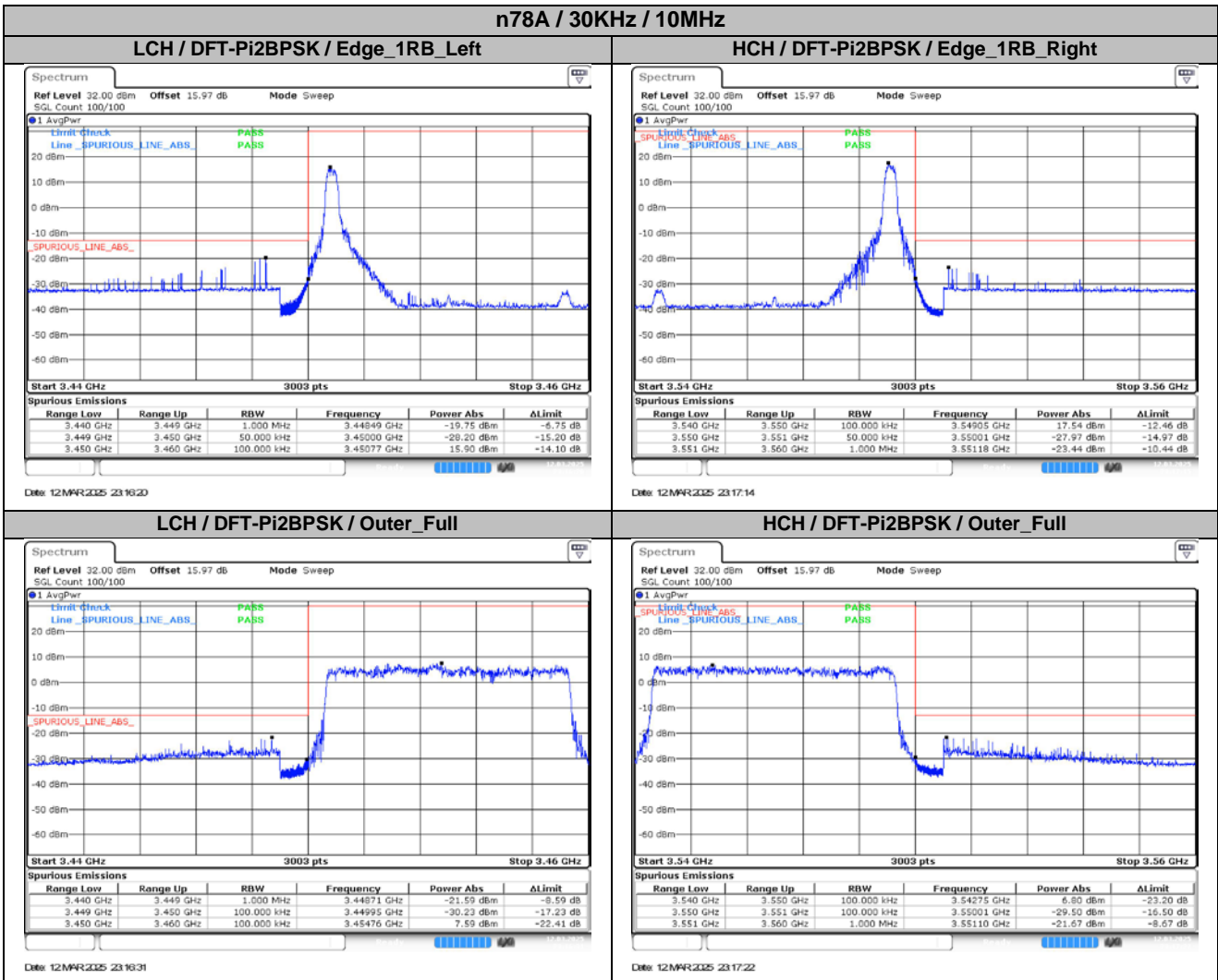
HCH / DFT-QPSK / Edge_1RB_Right

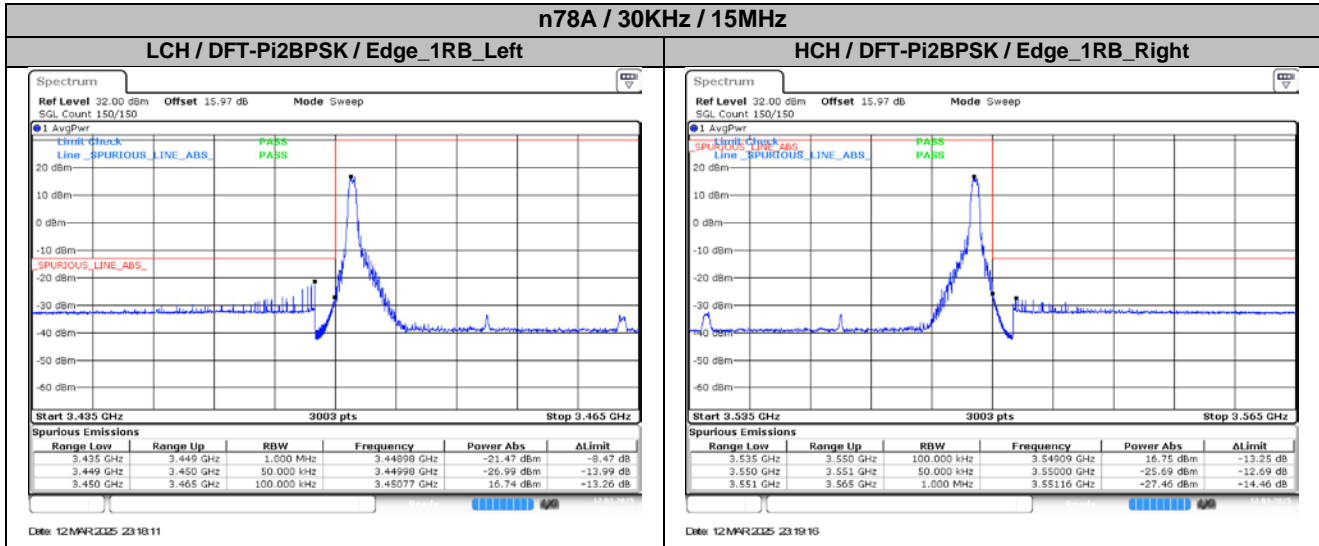
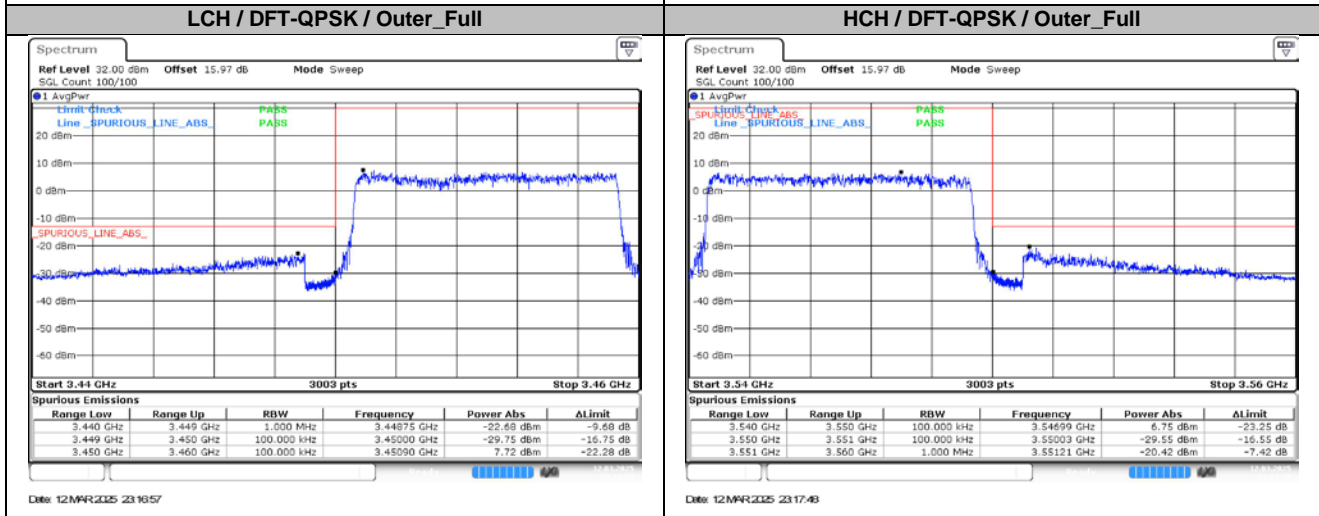
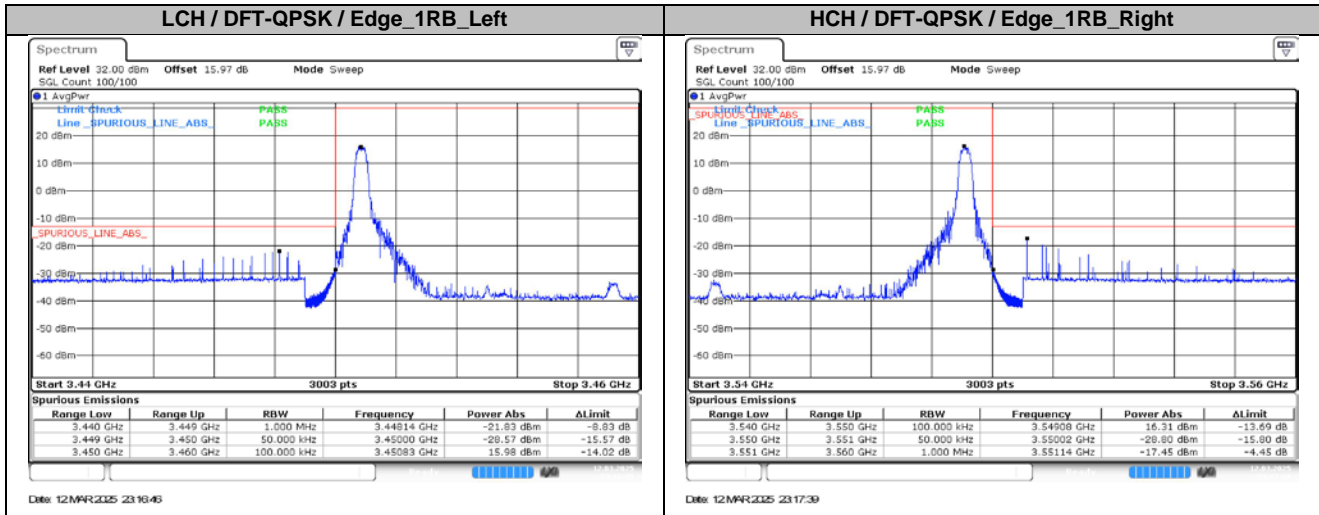


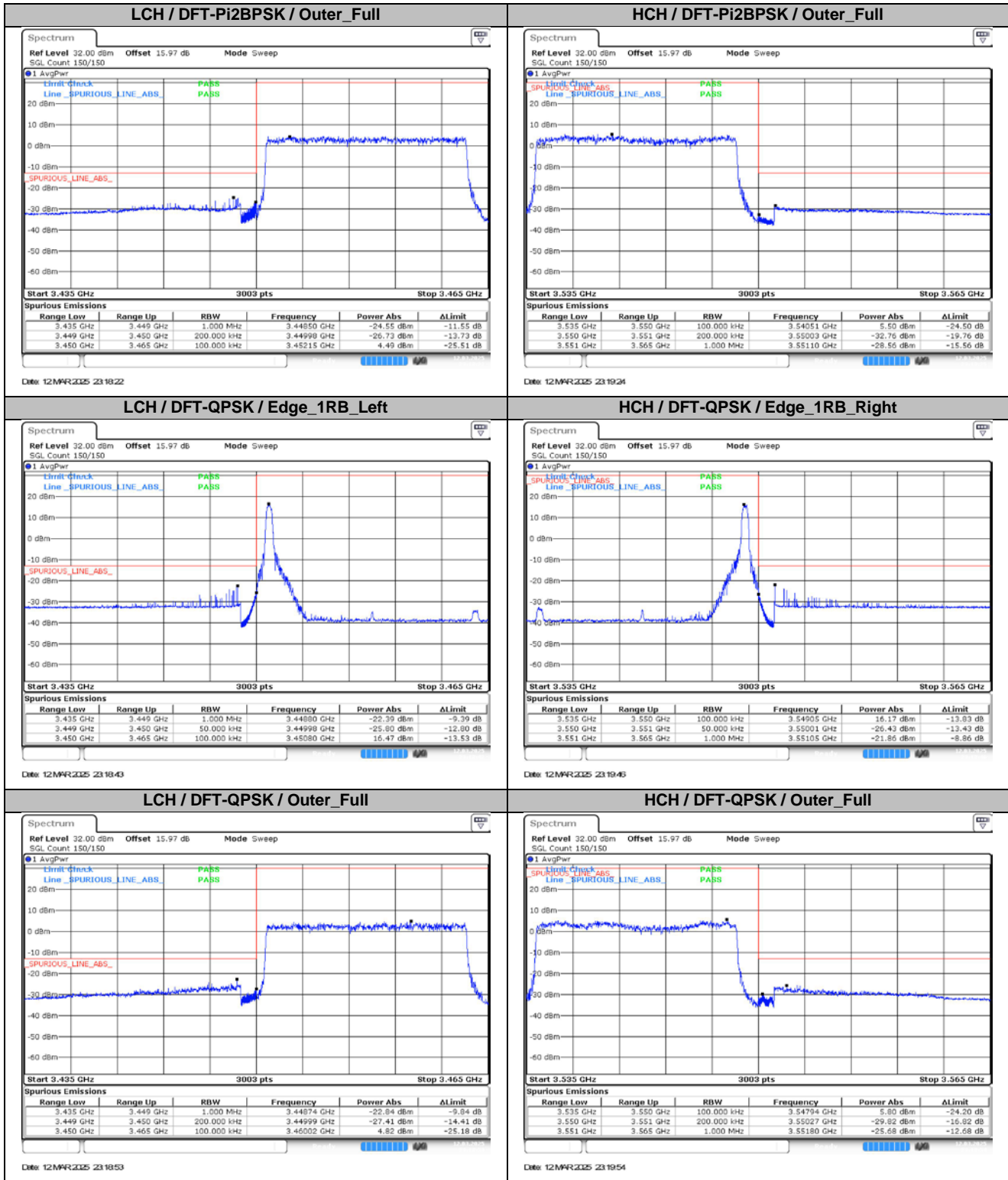
Date: 12.MAR.2025 21:23:36



5.2. Test Plots for SCS=30KHz

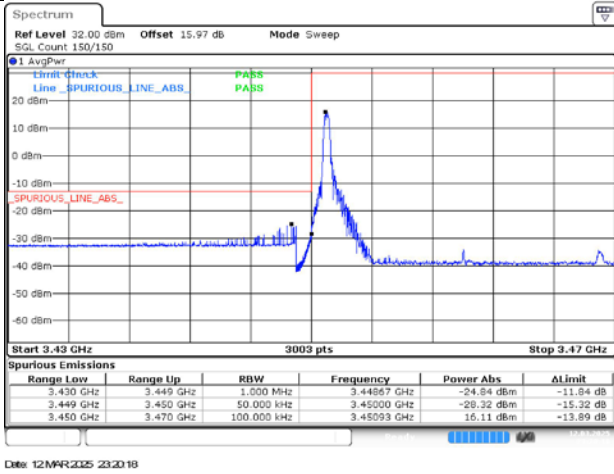




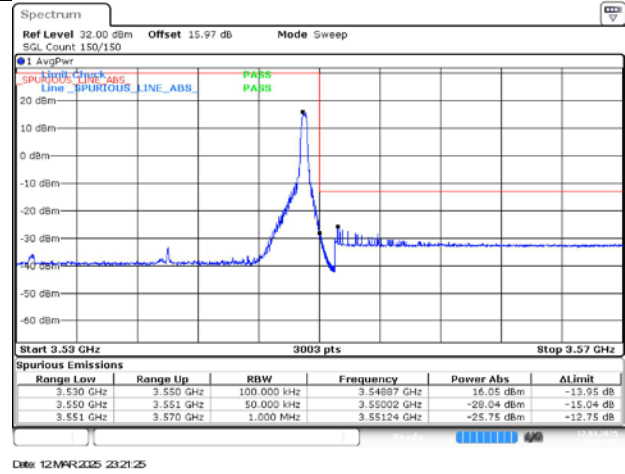


n78A / 30KHz / 20MHz

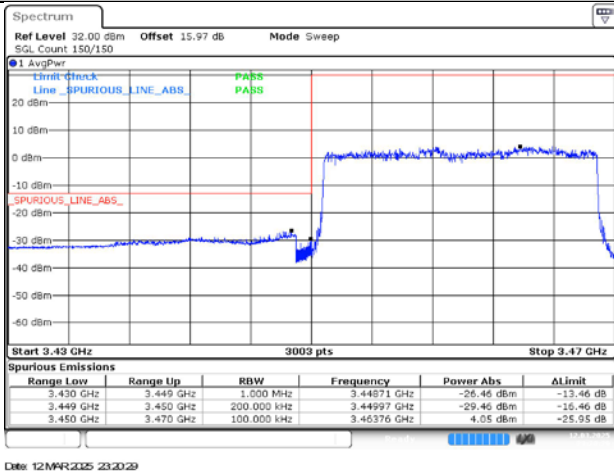
LCH / DFT-Pi2BPSK / Edge_1RB_Left



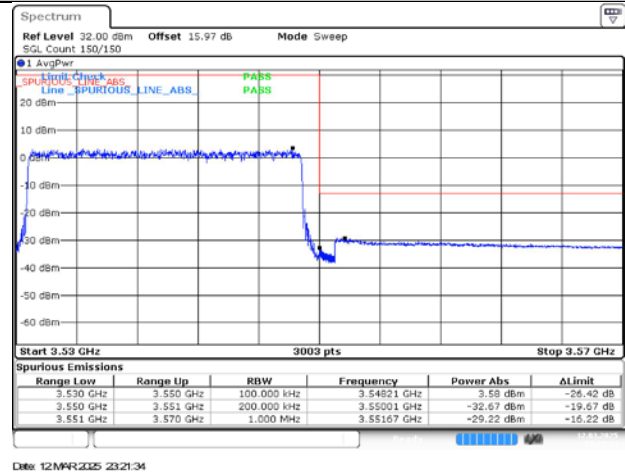
HCH / DFT-Pi2BPSK / Edge_1RB_Right



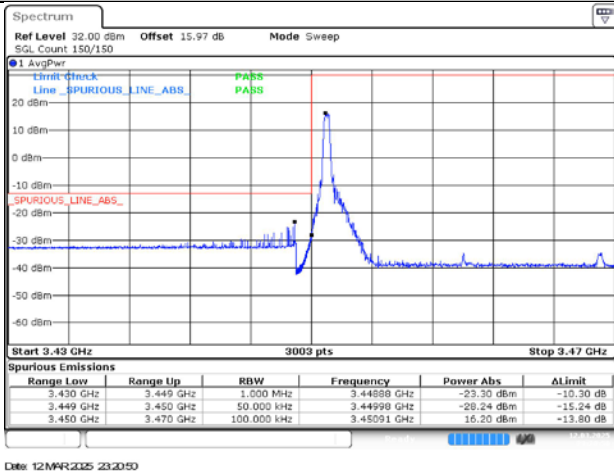
LCH / DFT-Pi2BPSK / Outer_Full



HCH / DFT-Pi2BPSK / Outer_Full



LCH / DFT-QPSK / Edge_1RB_Left



HCH / DFT-QPSK / Edge_1RB_Right

