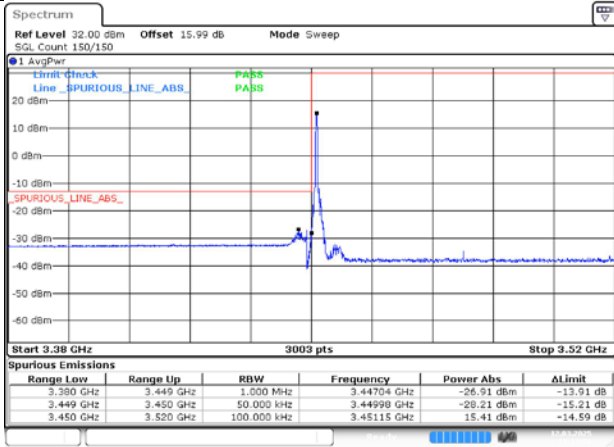


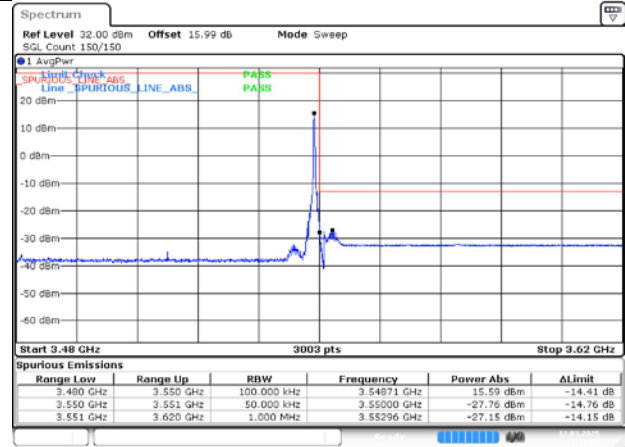
n77A / 30KHz / 70MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



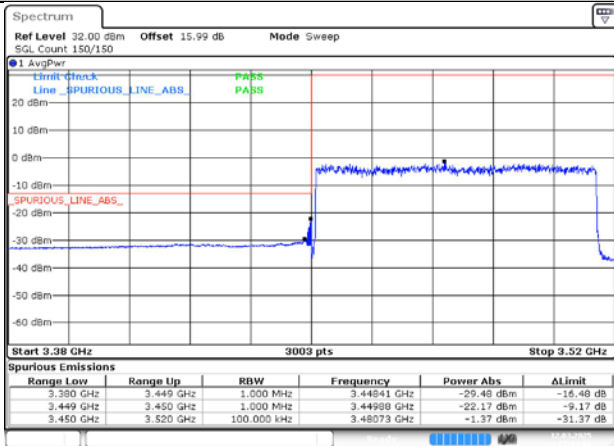
Date: 12.MAR.2025 17:13:58

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



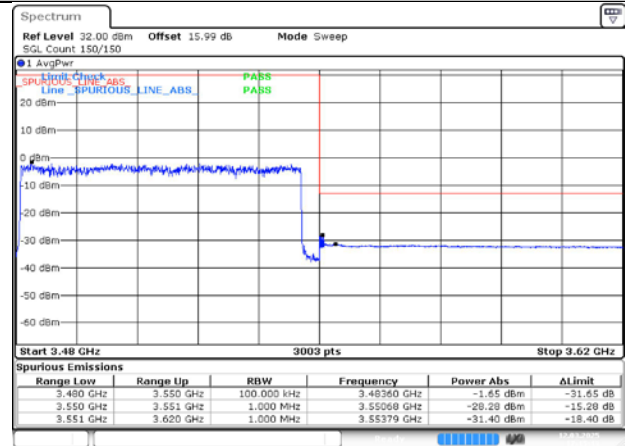
Date: 12.MAR.2025 17:15:05

LCH / DFT-Pi2BPSK / Outer\_Full



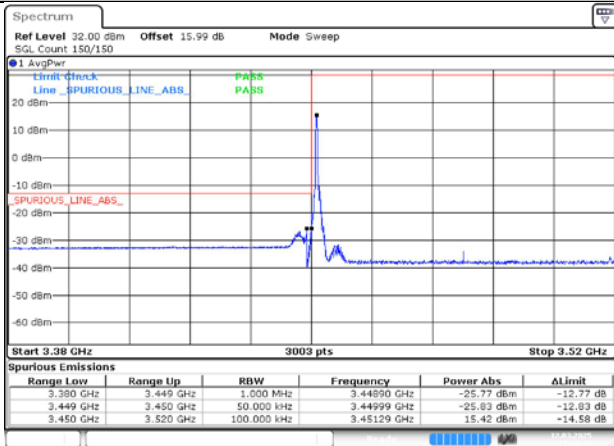
Date: 12.MAR.2025 17:14:08

HCH / DFT-Pi2BPSK / Outer\_Full



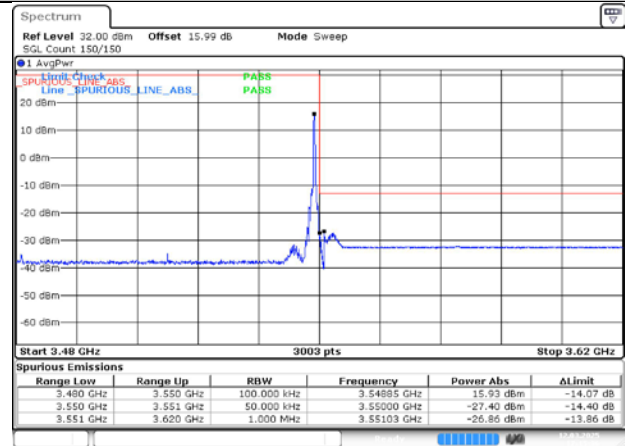
Date: 12.MAR.2025 17:15:13

LCH / DFT-QPSK / Edge\_1RB\_Left

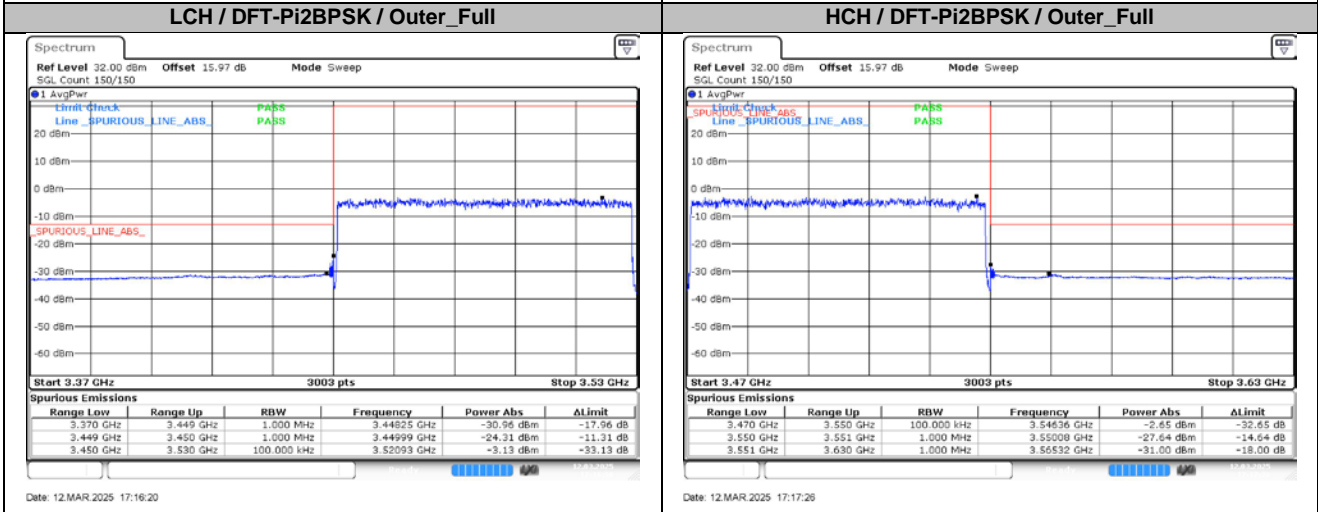
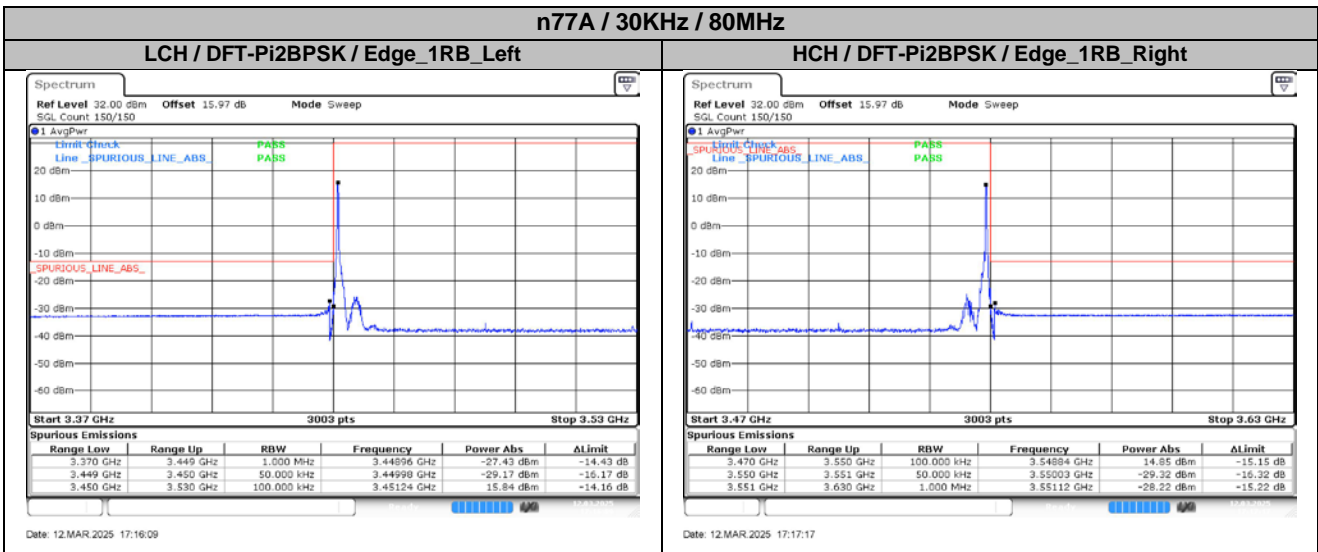
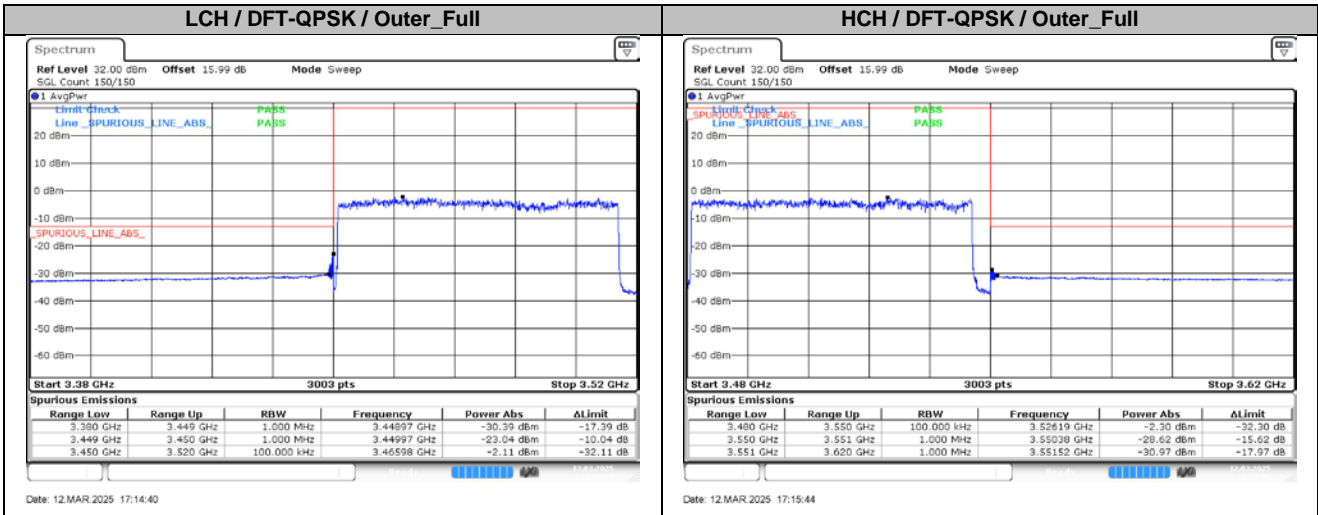


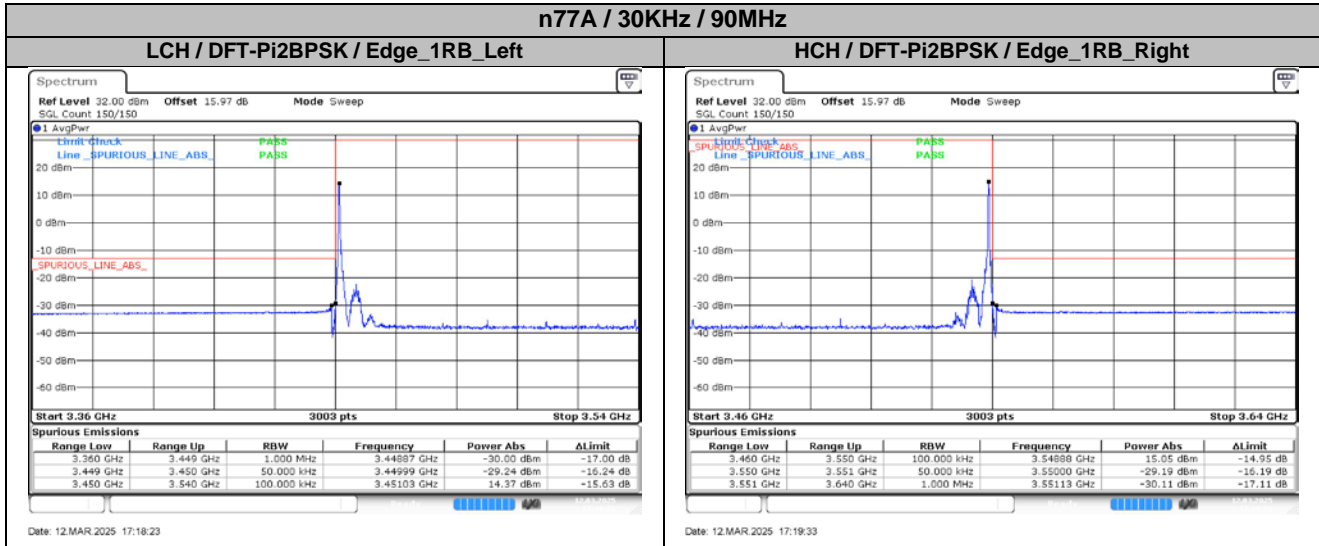
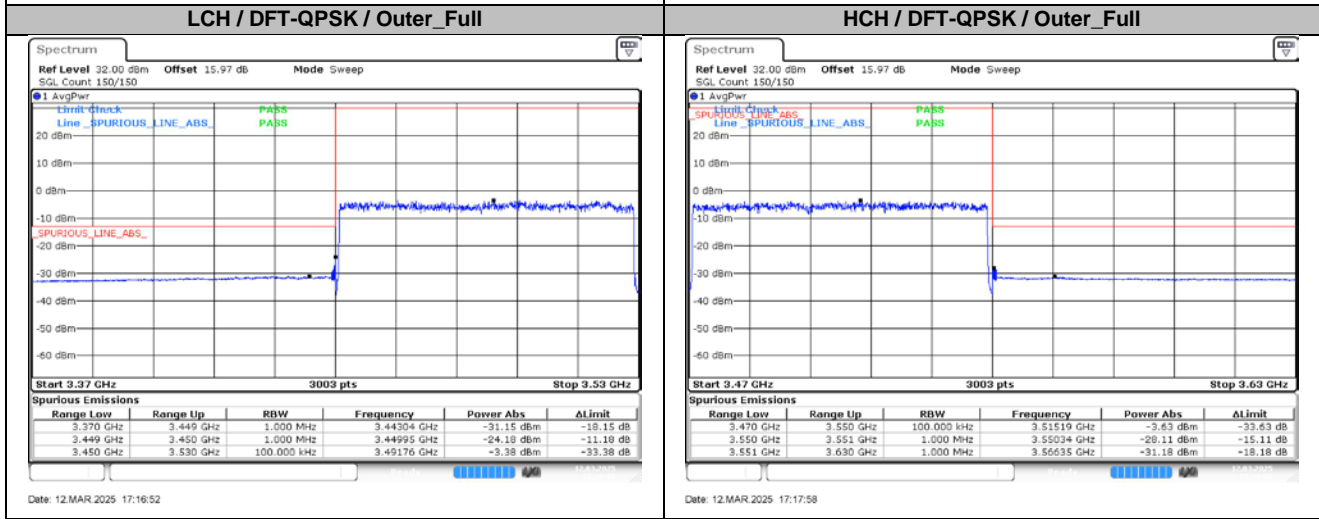
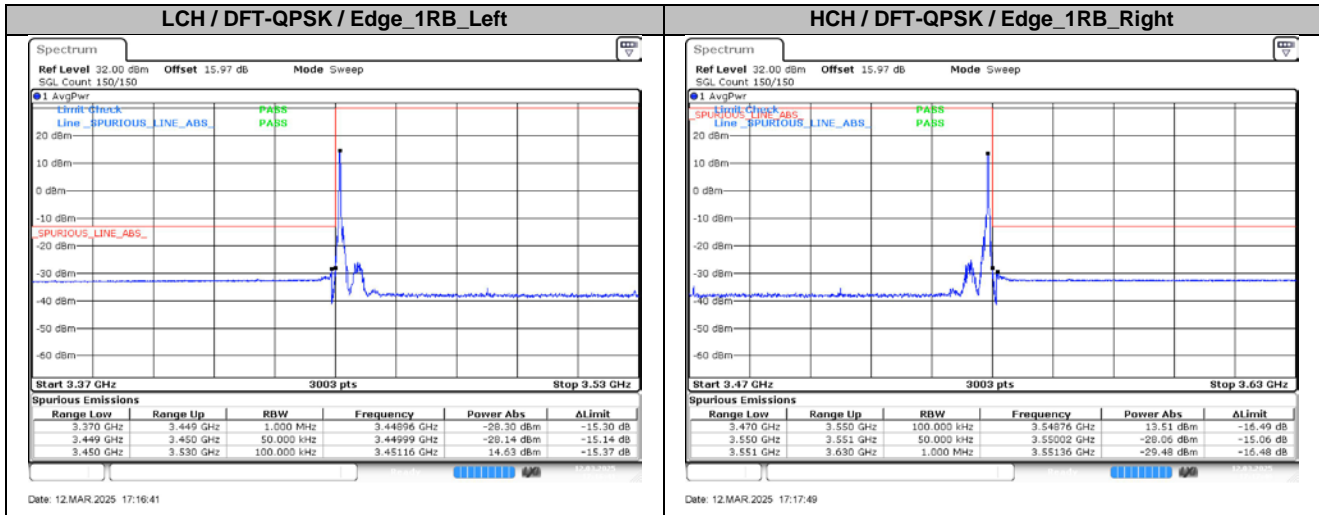
Date: 12.MAR.2025 17:14:30

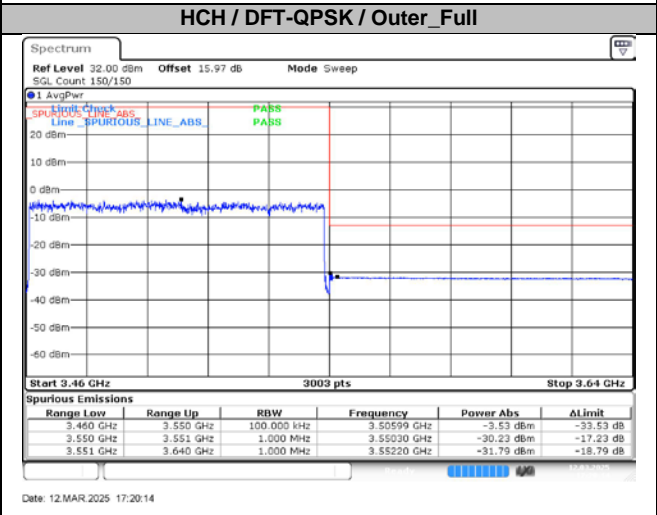
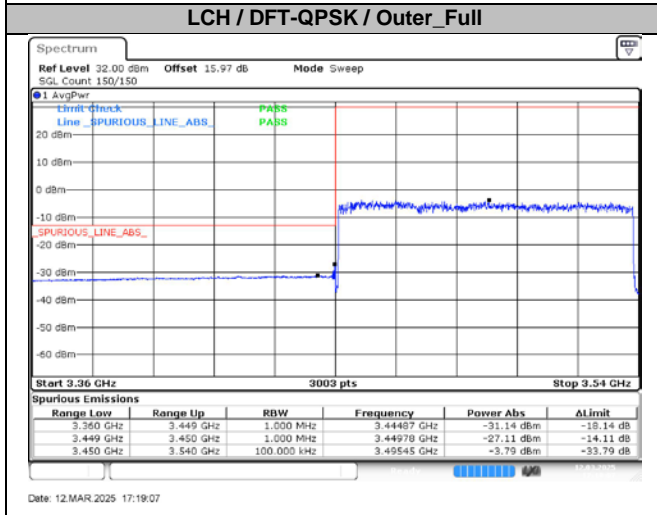
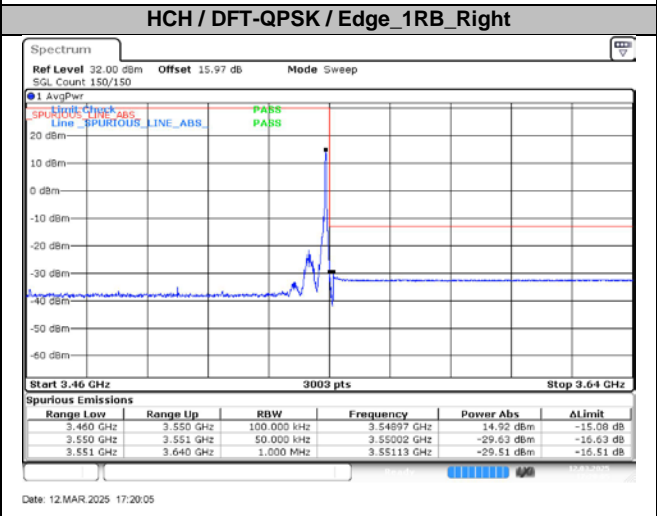
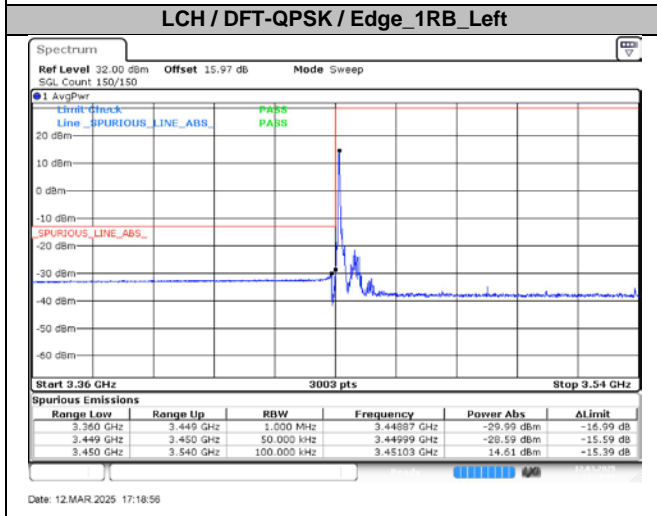
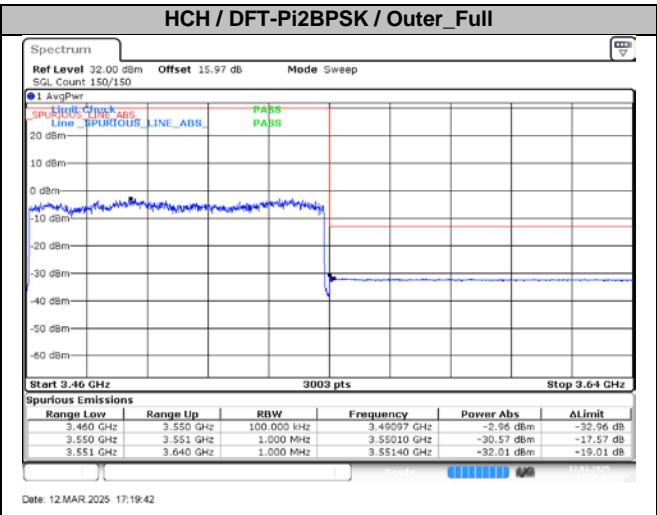
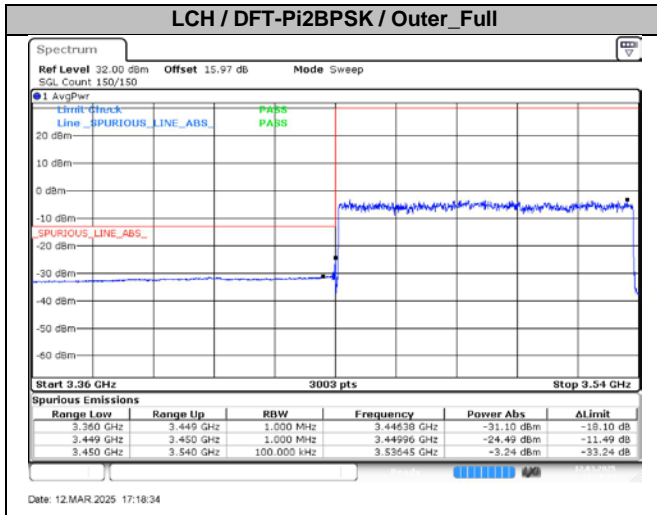
HCH / DFT-QPSK / Edge\_1RB\_Right



Date: 12.MAR.2025 17:15:36

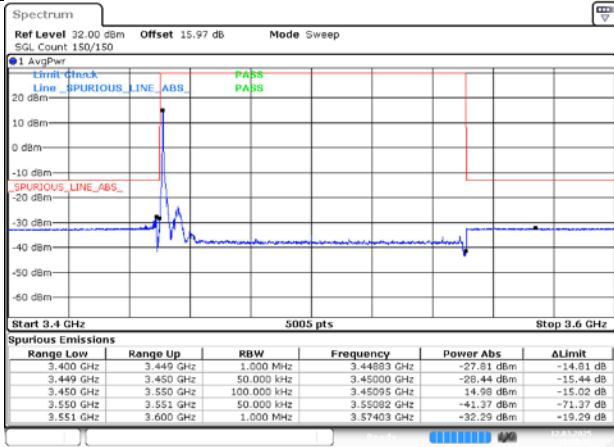






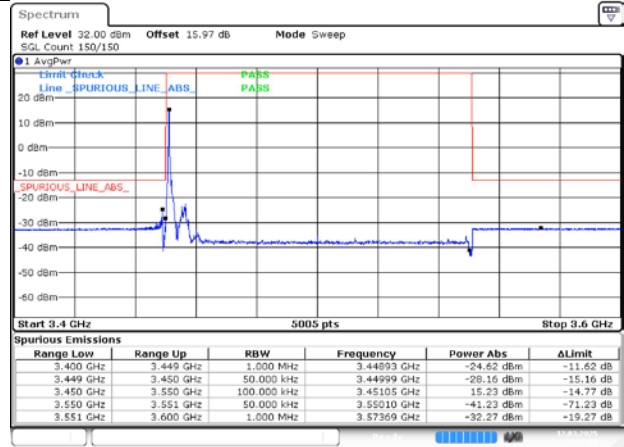
n77A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



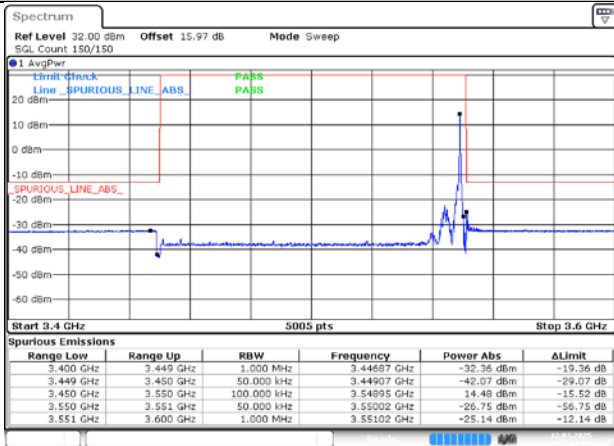
Date: 12.MAR.2025 17:20:41

MCH / DFT-QPSK / Edge\_1RB\_Left



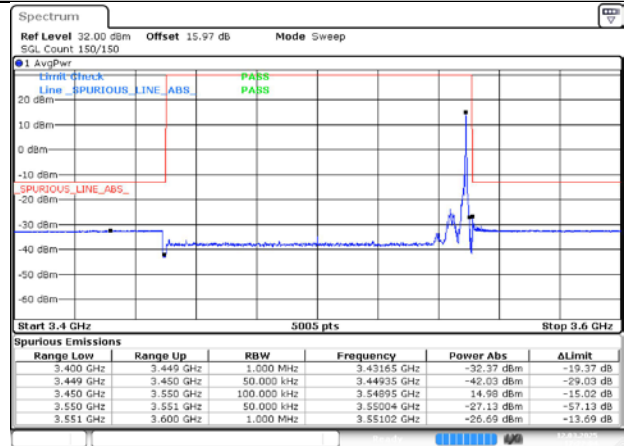
Date: 12.MAR.2025 17:21:40

MCH / DFT-Pi2BPSK / Edge\_1RB\_Right



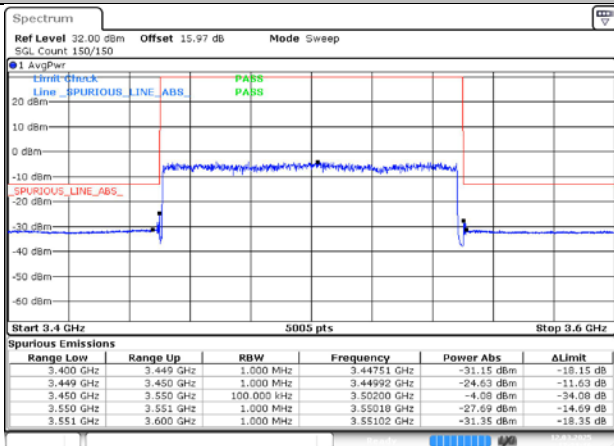
Date: 12.MAR.2025 17:21:05

MCH / DFT-QPSK / Edge\_1RB\_Right



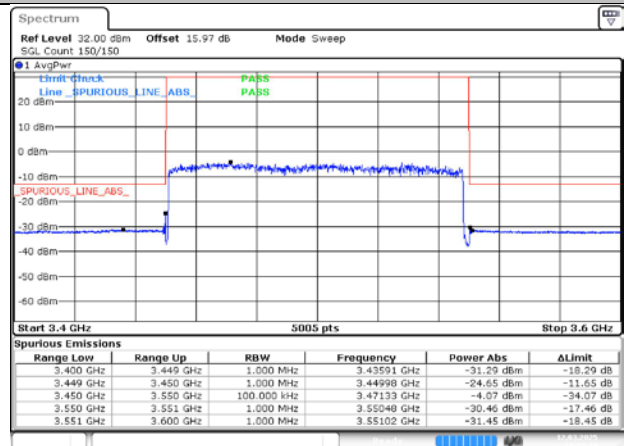
Date: 12.MAR.2025 17:22:04

MCH / DFT-Pi2BPSK / Outer\_Full



Date: 12.MAR.2025 17:21:16

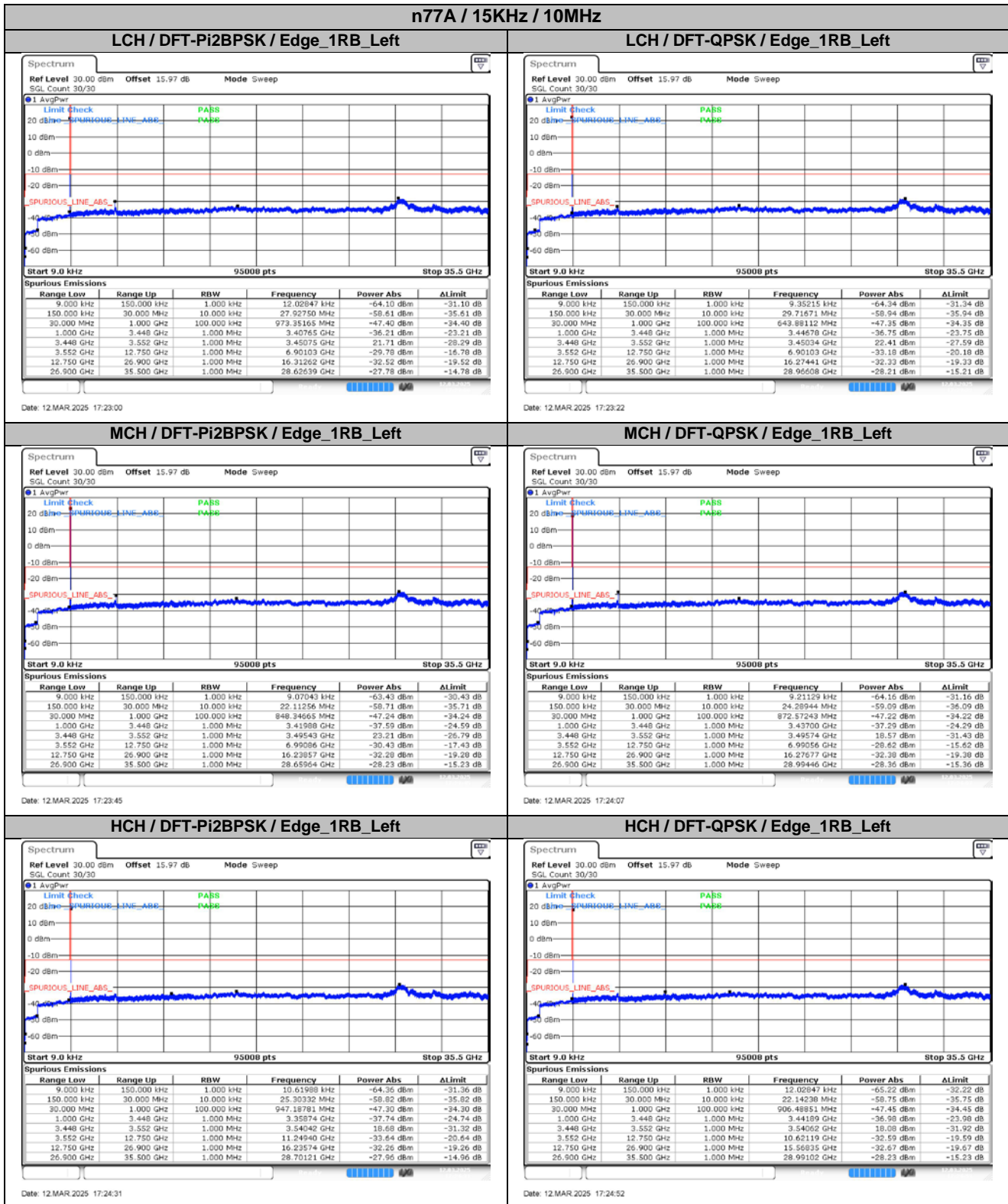
MCH / DFT-QPSK / Outer\_Full

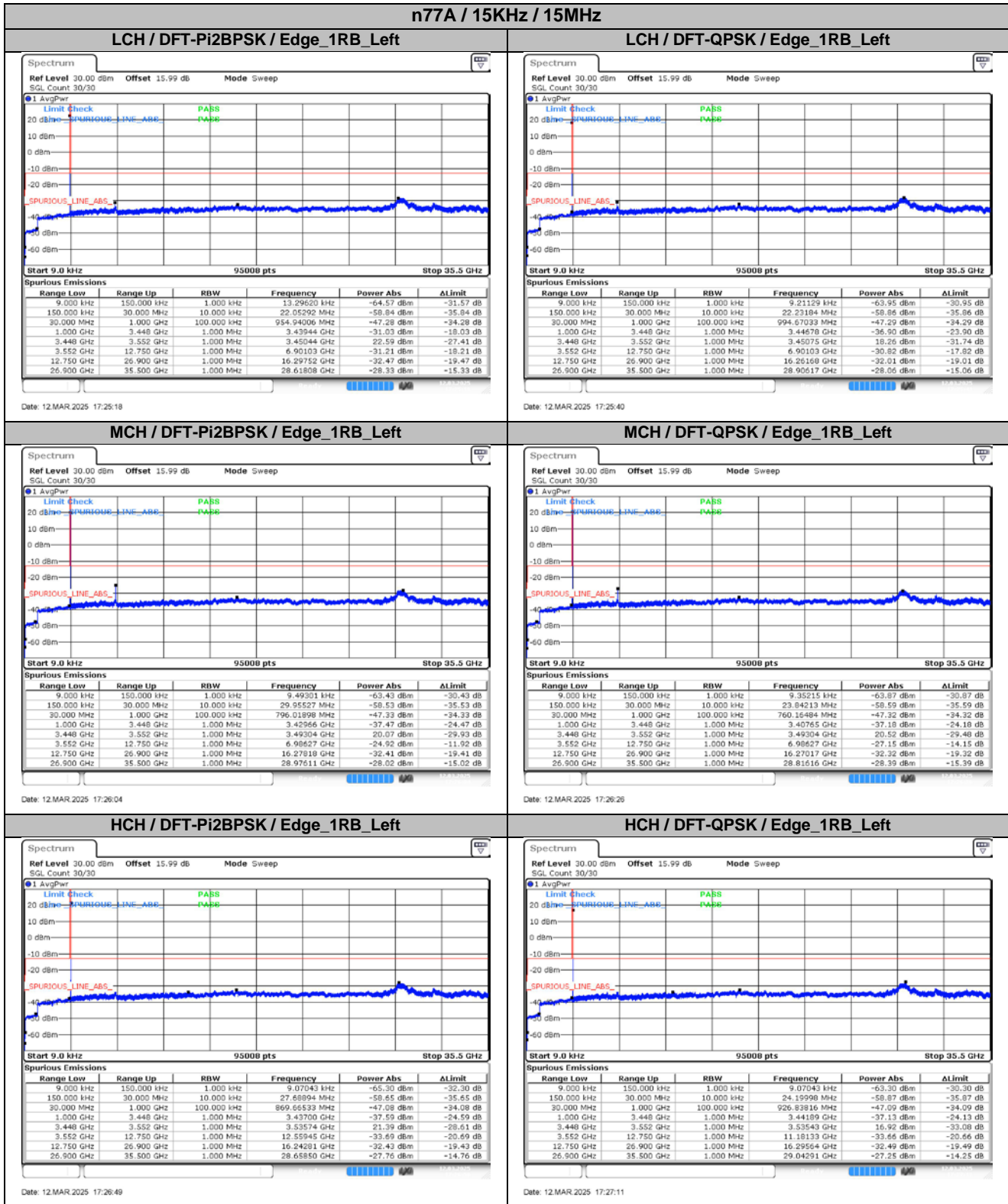


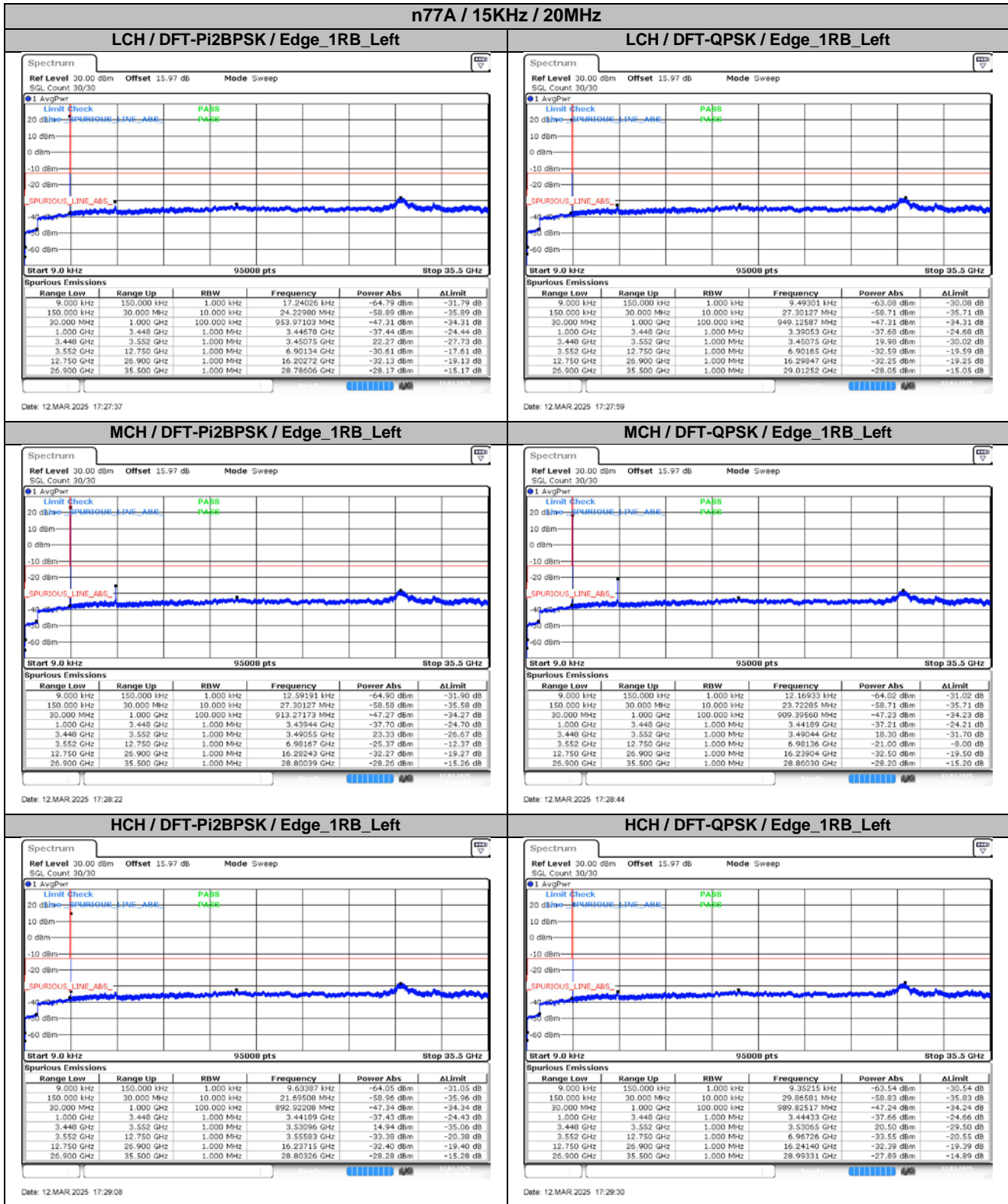
Date: 12.MAR.2025 17:22:14

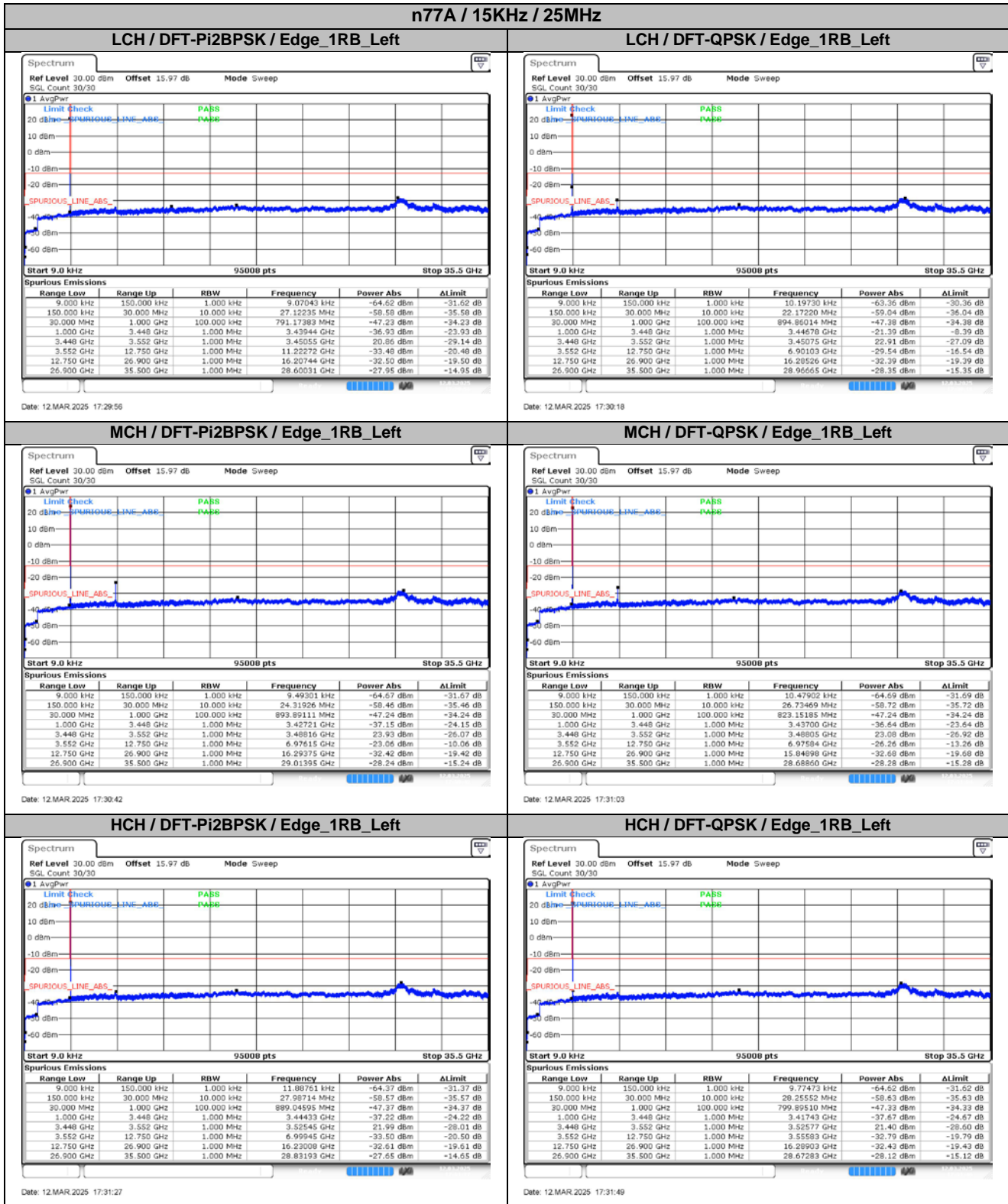
## 6. Conducted Spurious Emission

### 6.1. Test Plots for SCS=15KHz



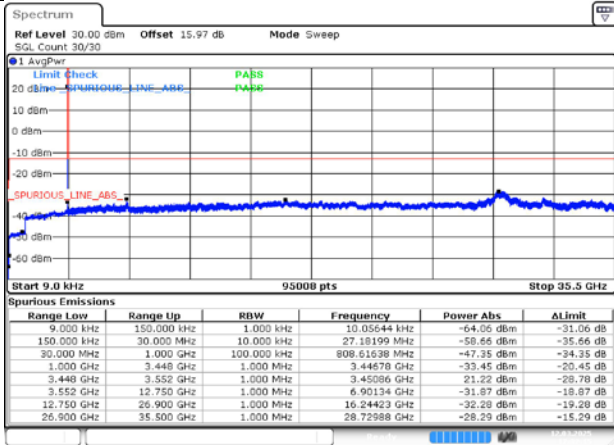






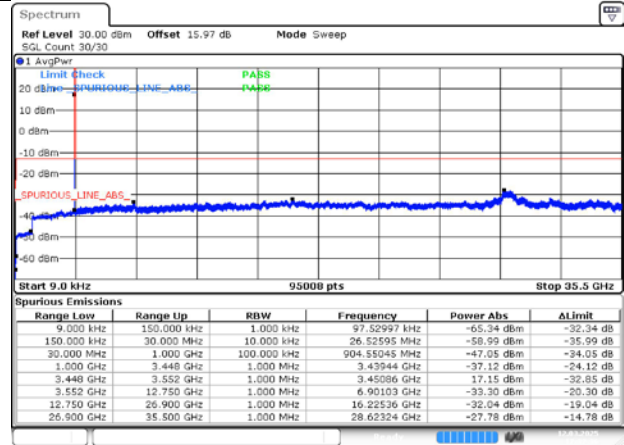
n77A / 15KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



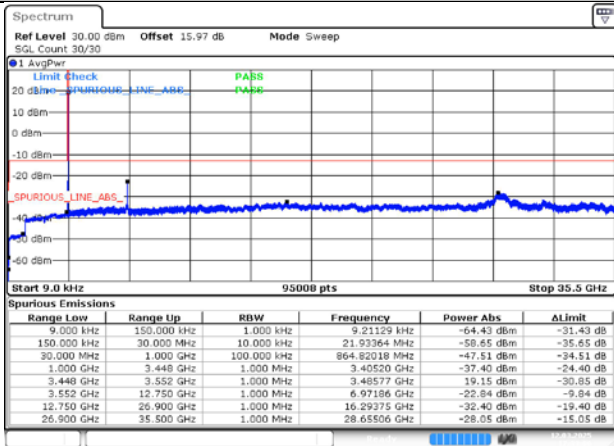
Date: 12.MAR.2025 17:32:15

LCH / DFT-QPSK / Edge\_1RB\_Left



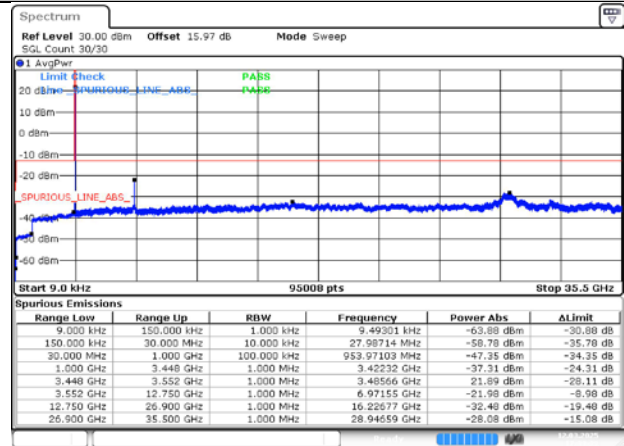
Date: 12.MAR.2025 17:32:37

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



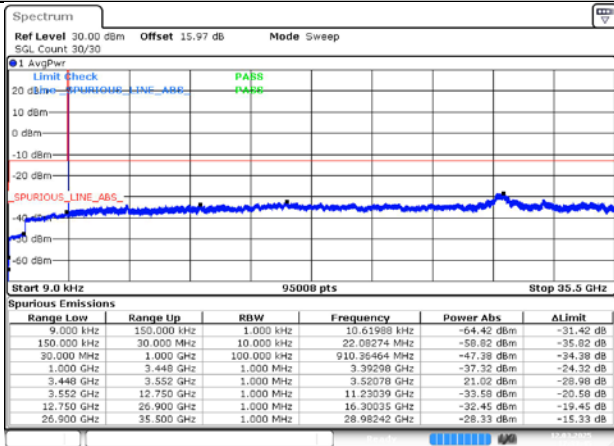
Date: 12.MAR.2025 17:33:00

MCH / DFT-QPSK / Edge\_1RB\_Left



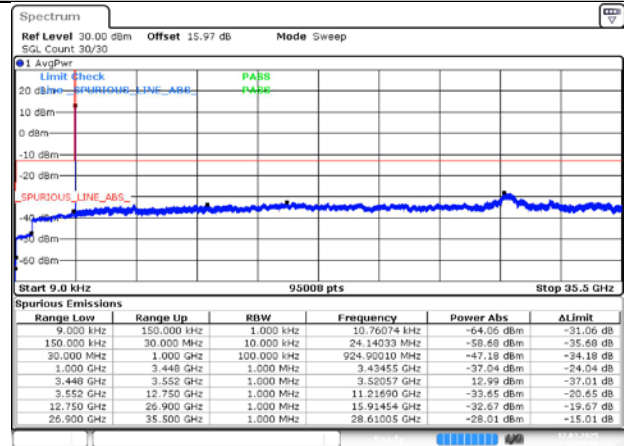
Date: 12.MAR.2025 17:33:22

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 12.MAR.2025 17:33:45

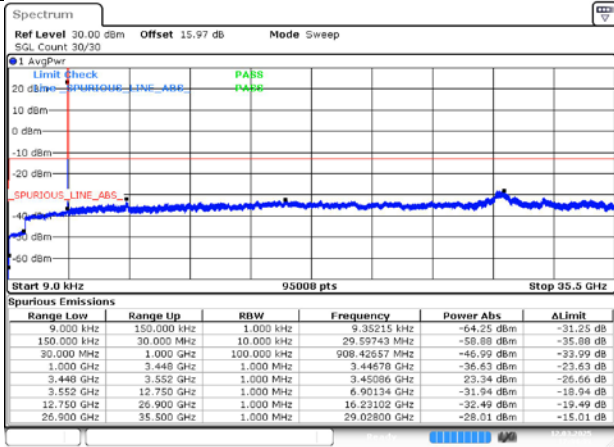
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 12.MAR.2025 17:34:07

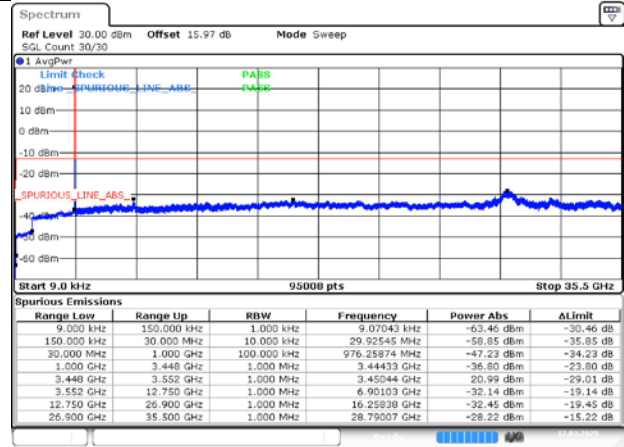
n77A / 15KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



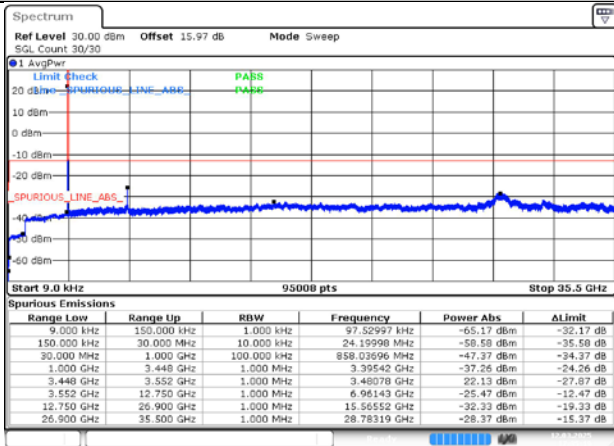
Date: 12.MAR.2025 17:34:33

LCH / DFT-QPSK / Edge\_1RB\_Left



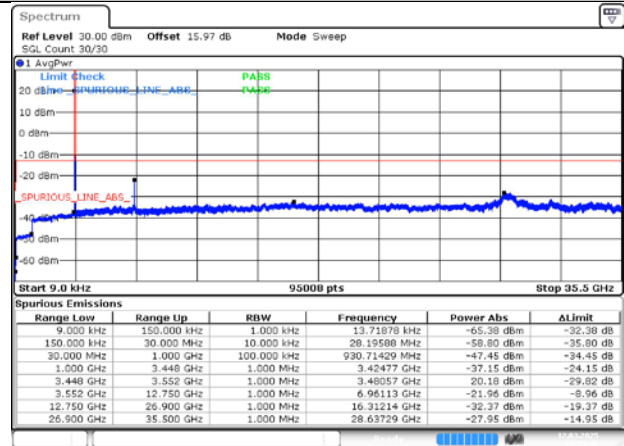
Date: 12.MAR.2025 17:34:55

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



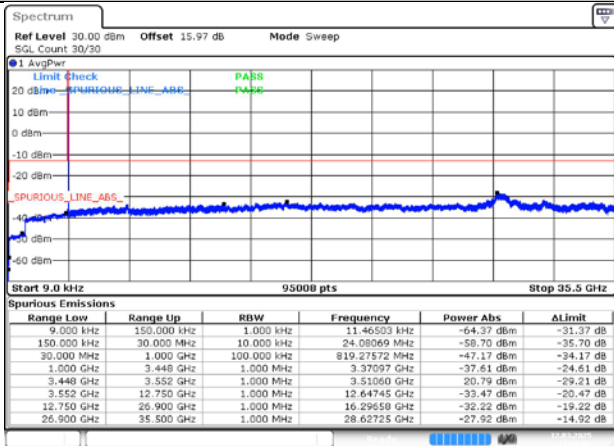
Date: 12.MAR.2025 17:35:18

MCH / DFT-QPSK / Edge\_1RB\_Left



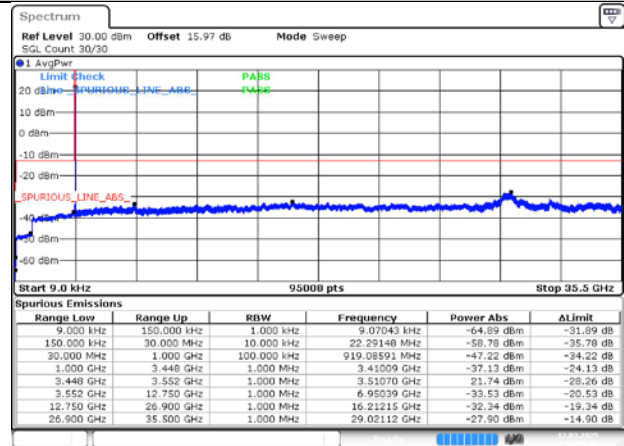
Date: 12.MAR.2025 17:35:40

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 12.MAR.2025 17:36:09

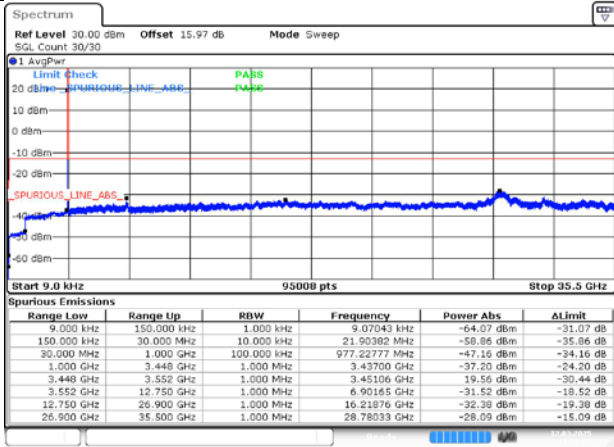
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 12.MAR.2025 17:36:31

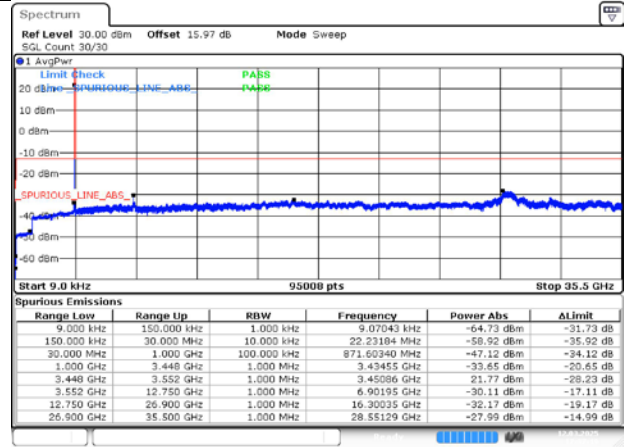
n77A / 15KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



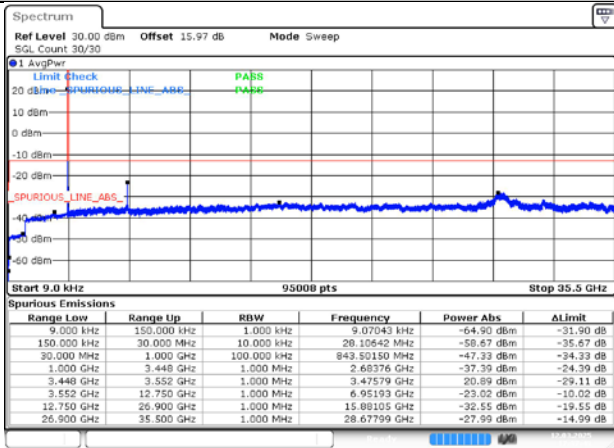
Date: 12.MAR.2025 17:36:57

LCH / DFT-QPSK / Edge\_1RB\_Left



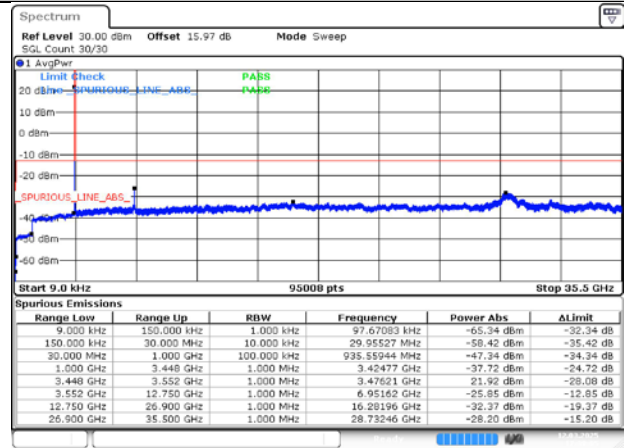
Date: 12.MAR.2025 17:37:19

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



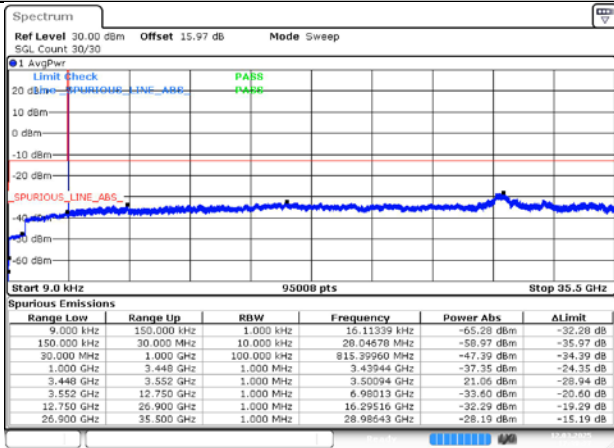
Date: 12.MAR.2025 17:37:43

MCH / DFT-QPSK / Edge\_1RB\_Left



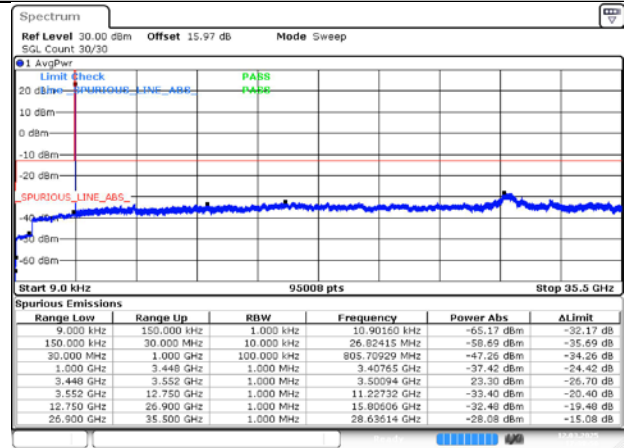
Date: 12.MAR.2025 17:38:04

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



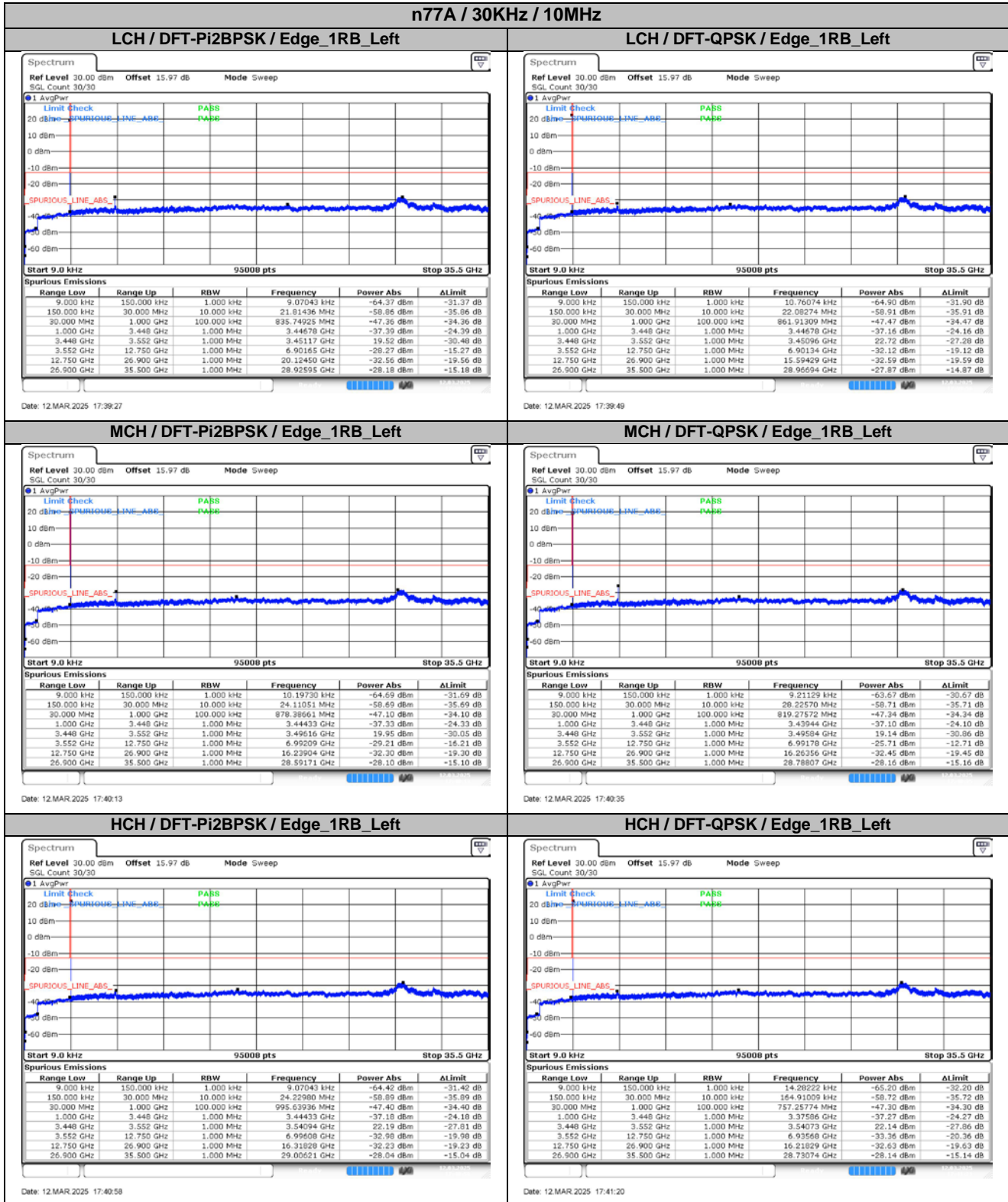
Date: 12.MAR.2025 17:38:28

HCH / DFT-QPSK / Edge\_1RB\_Left



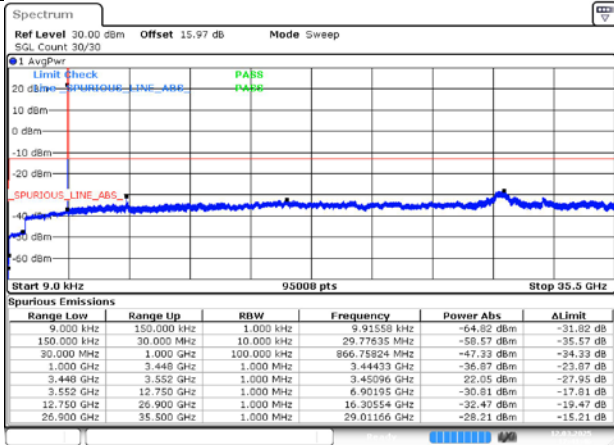
Date: 12.MAR.2025 17:38:49

6.2. Test Plots for SCS=30KHz



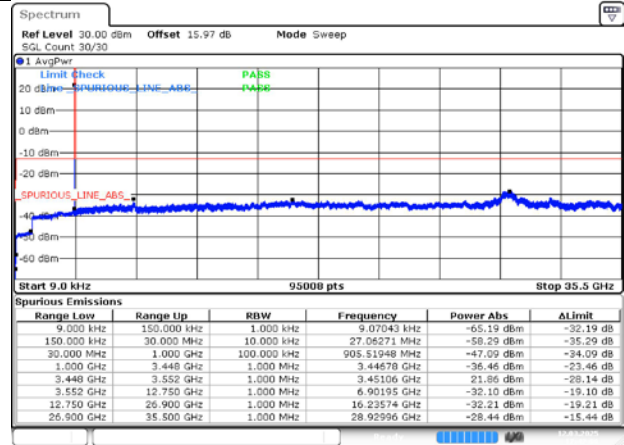
n77A / 30KHz / 15MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



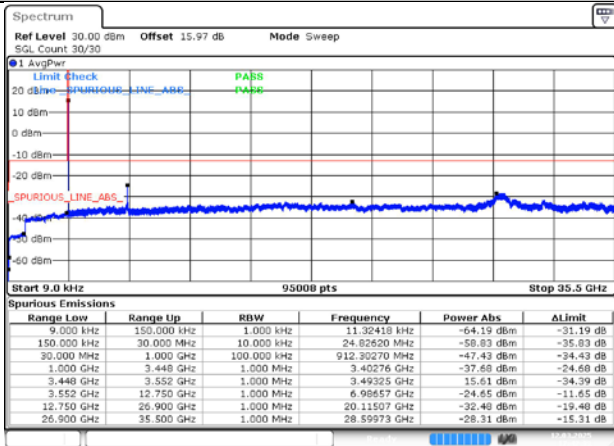
Date: 12.MAR.2025 17:41:46

LCH / DFT-QPSK / Edge\_1RB\_Left



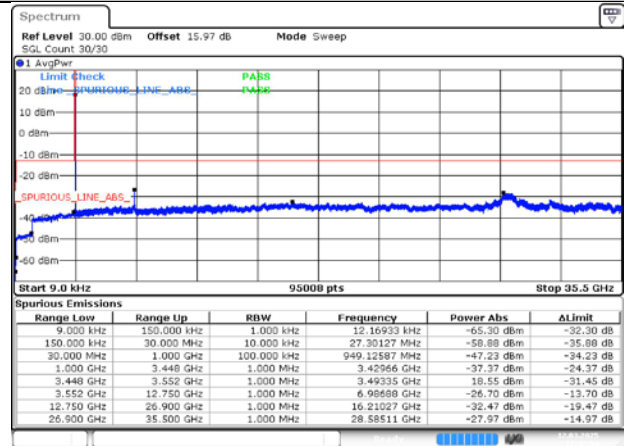
Date: 12.MAR.2025 17:42:08

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



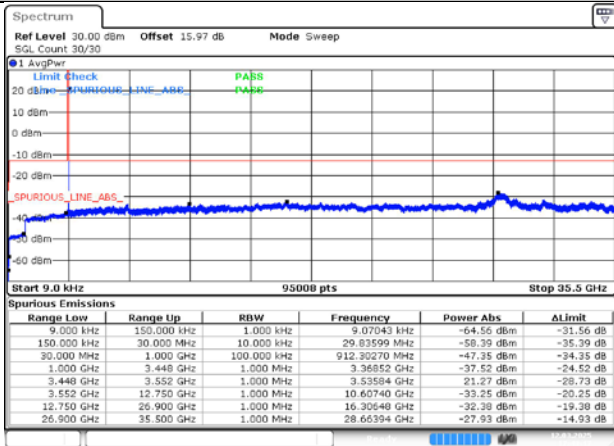
Date: 12.MAR.2025 17:42:32

MCH / DFT-QPSK / Edge\_1RB\_Left



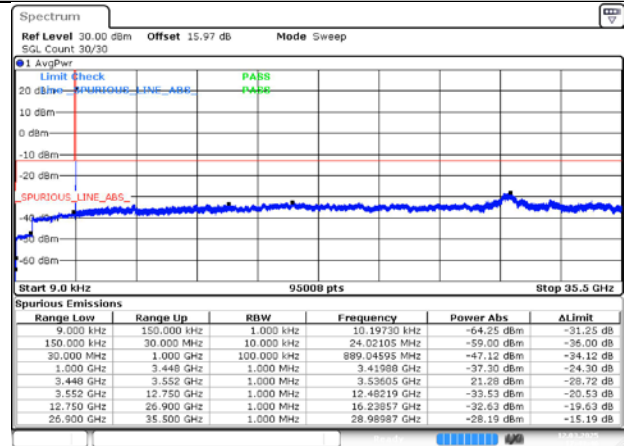
Date: 12.MAR.2025 17:42:54

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 12.MAR.2025 17:43:17

HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 12.MAR.2025 17:43:39