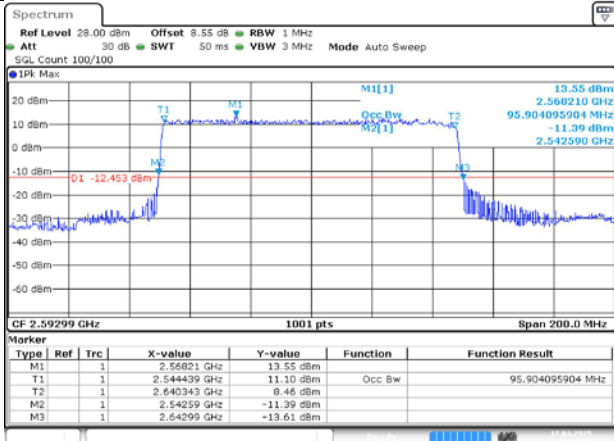


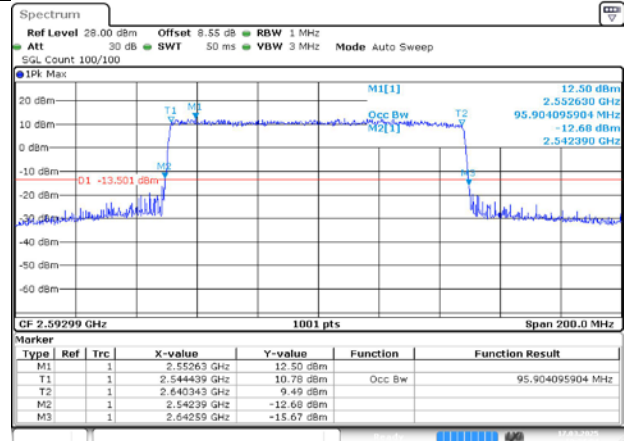
n41A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer_Full



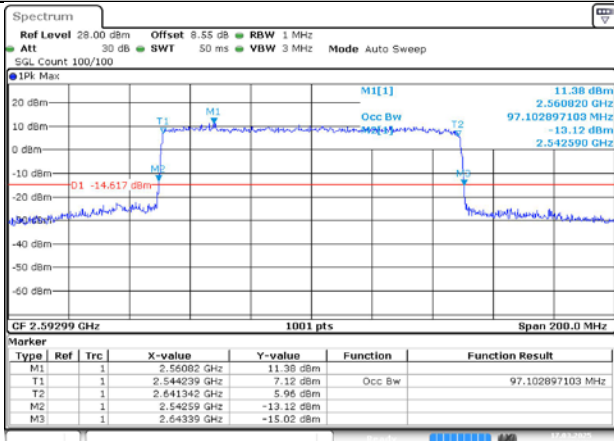
Date: 17.MAR.2025 19:33:53

MCH / DFT-QPSK / Outer_Full



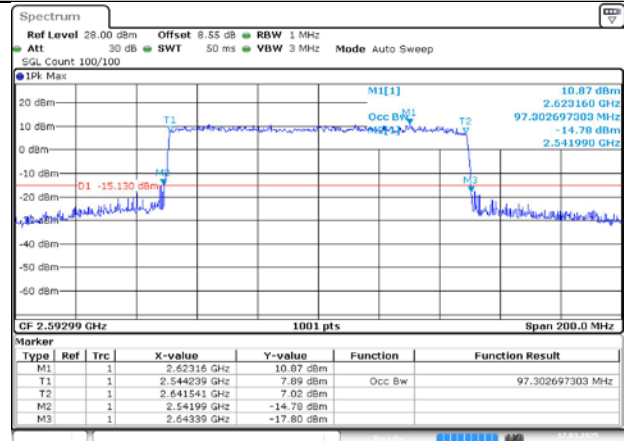
Date: 17.MAR.2025 19:34:04

MCH / CP-QPSK / Outer_Full



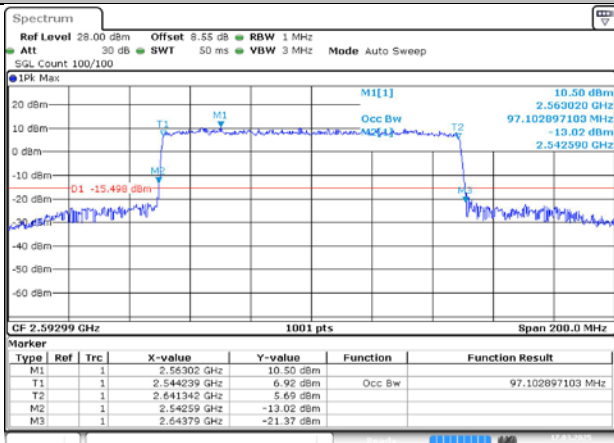
Date: 17.MAR.2025 19:34:14

MCH / CP-16QAM / Outer_Full



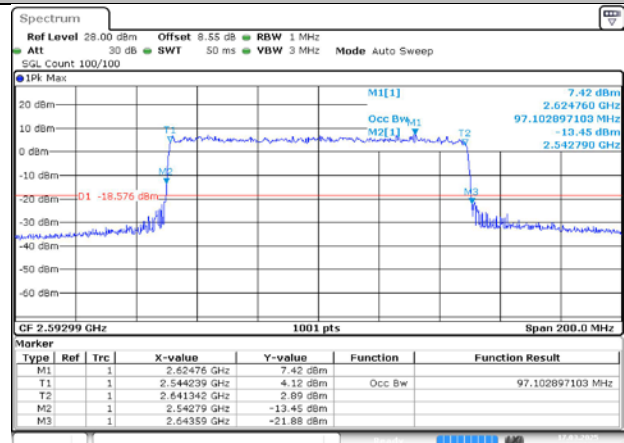
Date: 17.MAR.2025 19:34:23

MCH / CP-64QAM / Outer_Full



Date: 17.MAR.2025 19:34:32

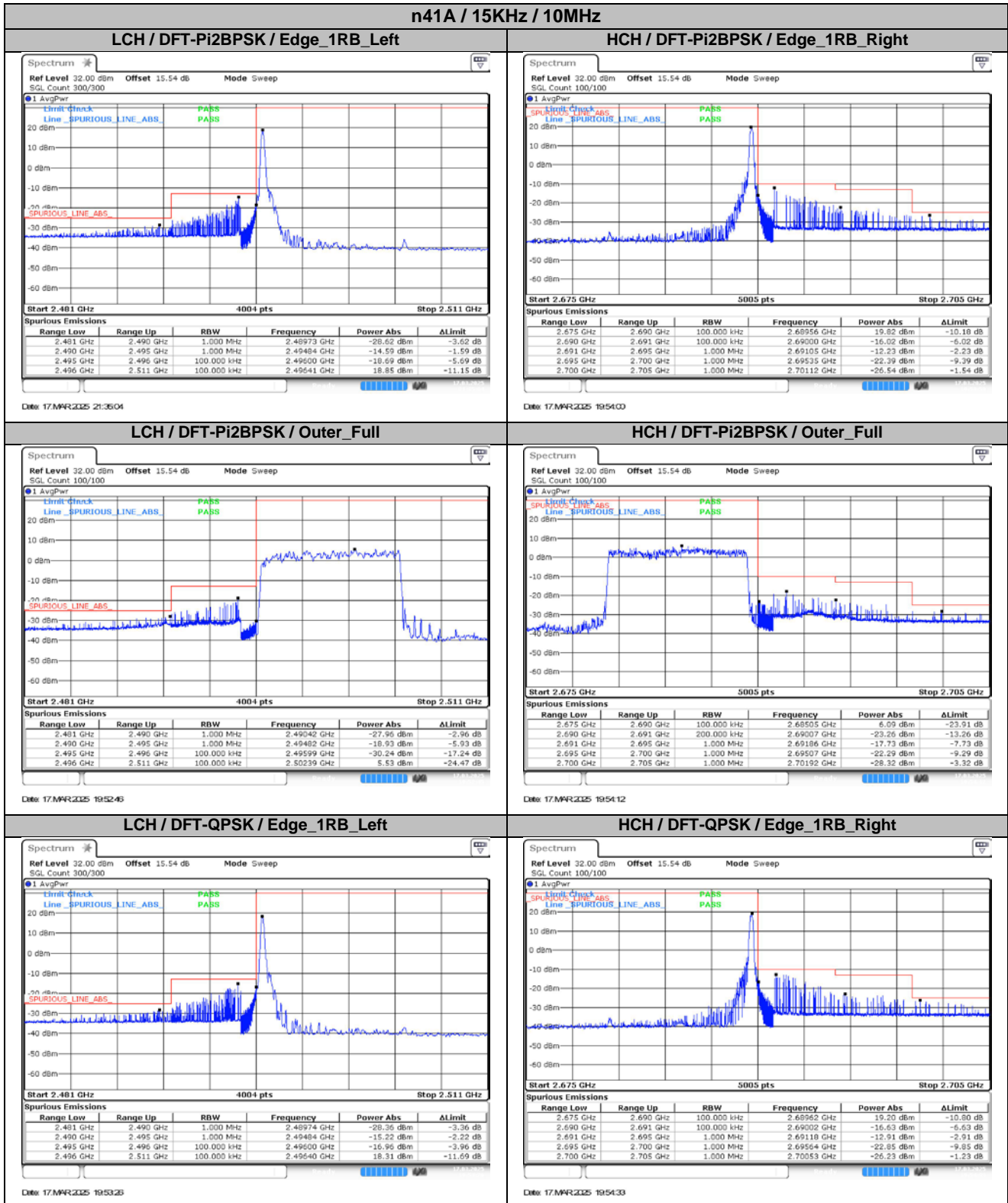
MCH / CP-256QAM / Outer_Full

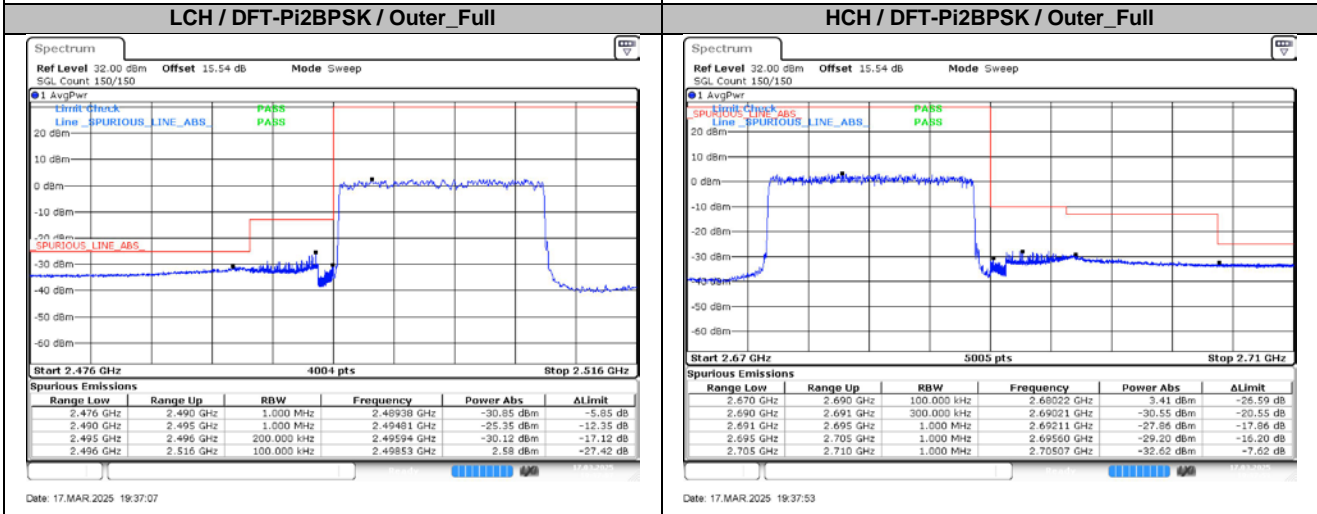
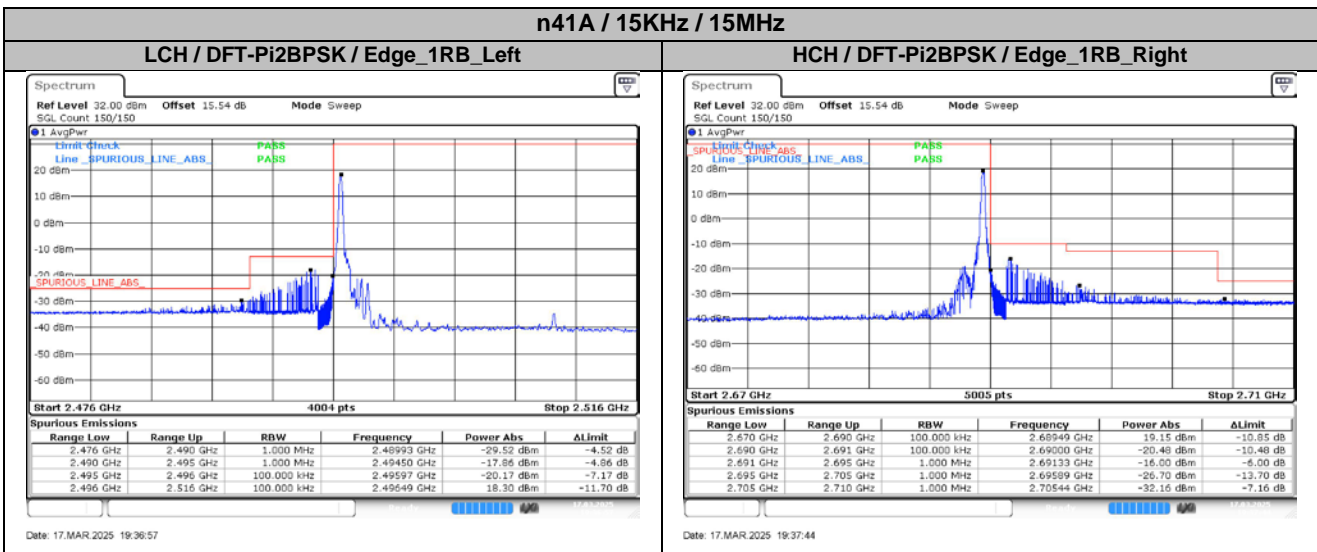
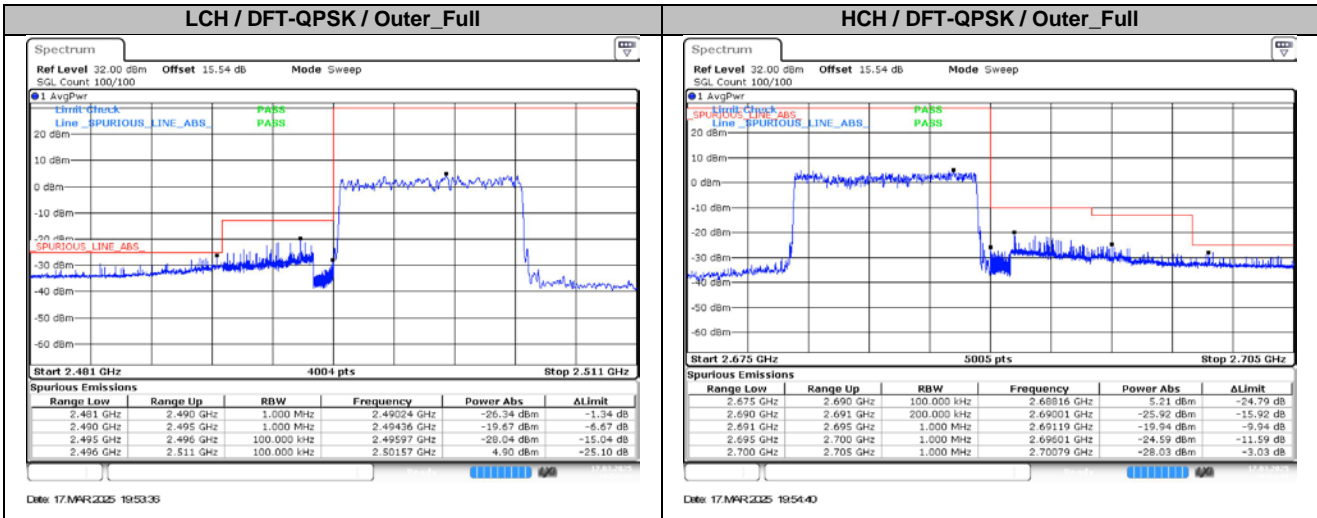


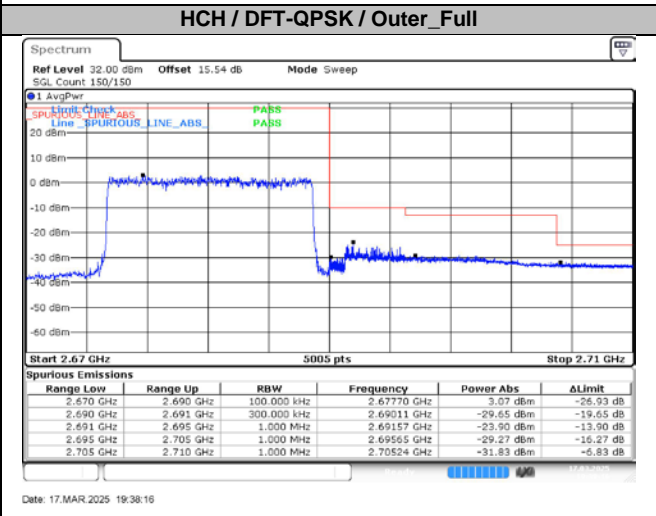
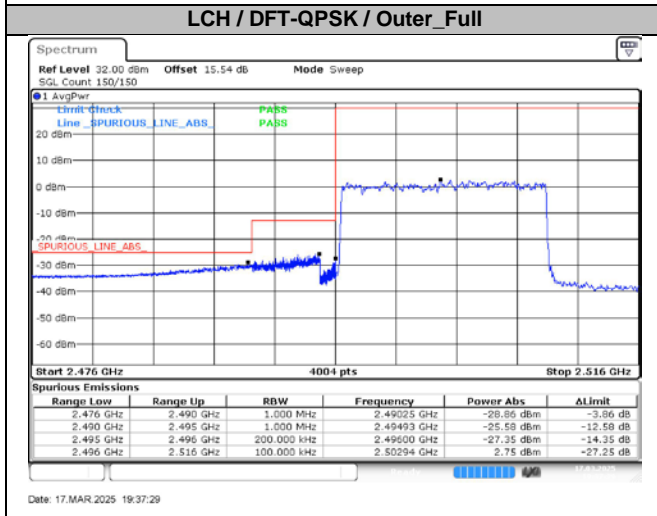
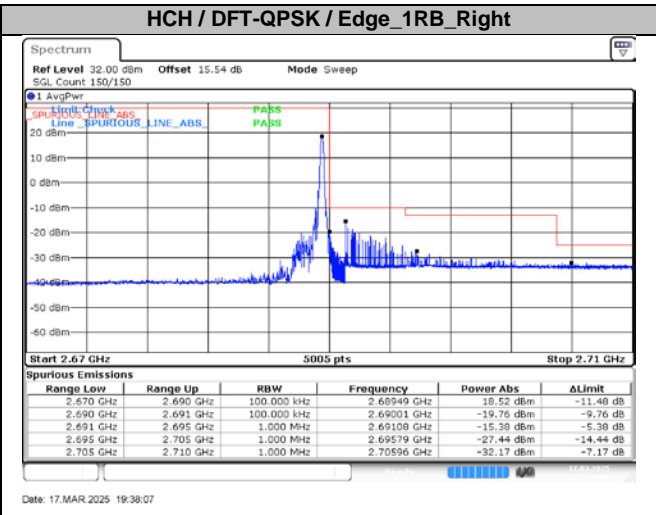
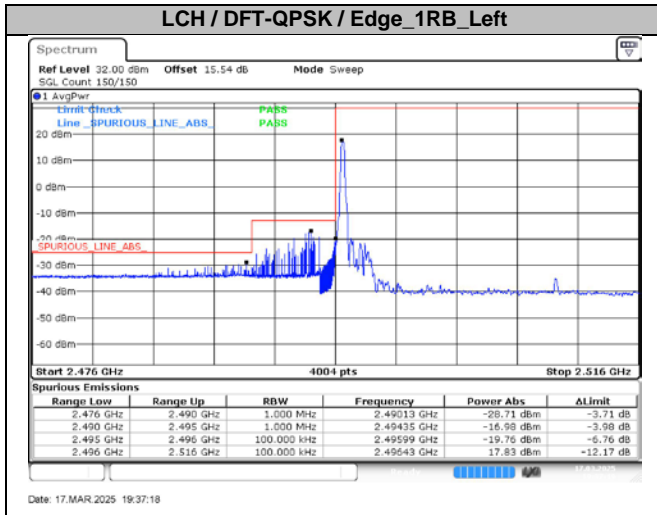
Date: 17.MAR.2025 19:34:42

5. Conducted Band Edges

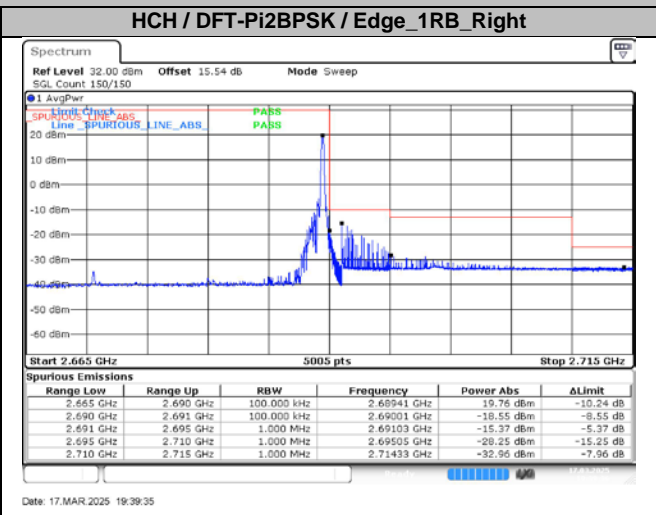
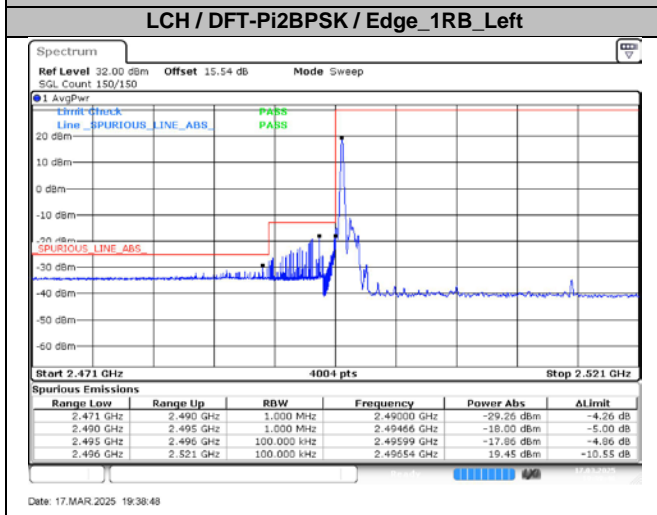
5.1. Test Plots for SCS=15KHz

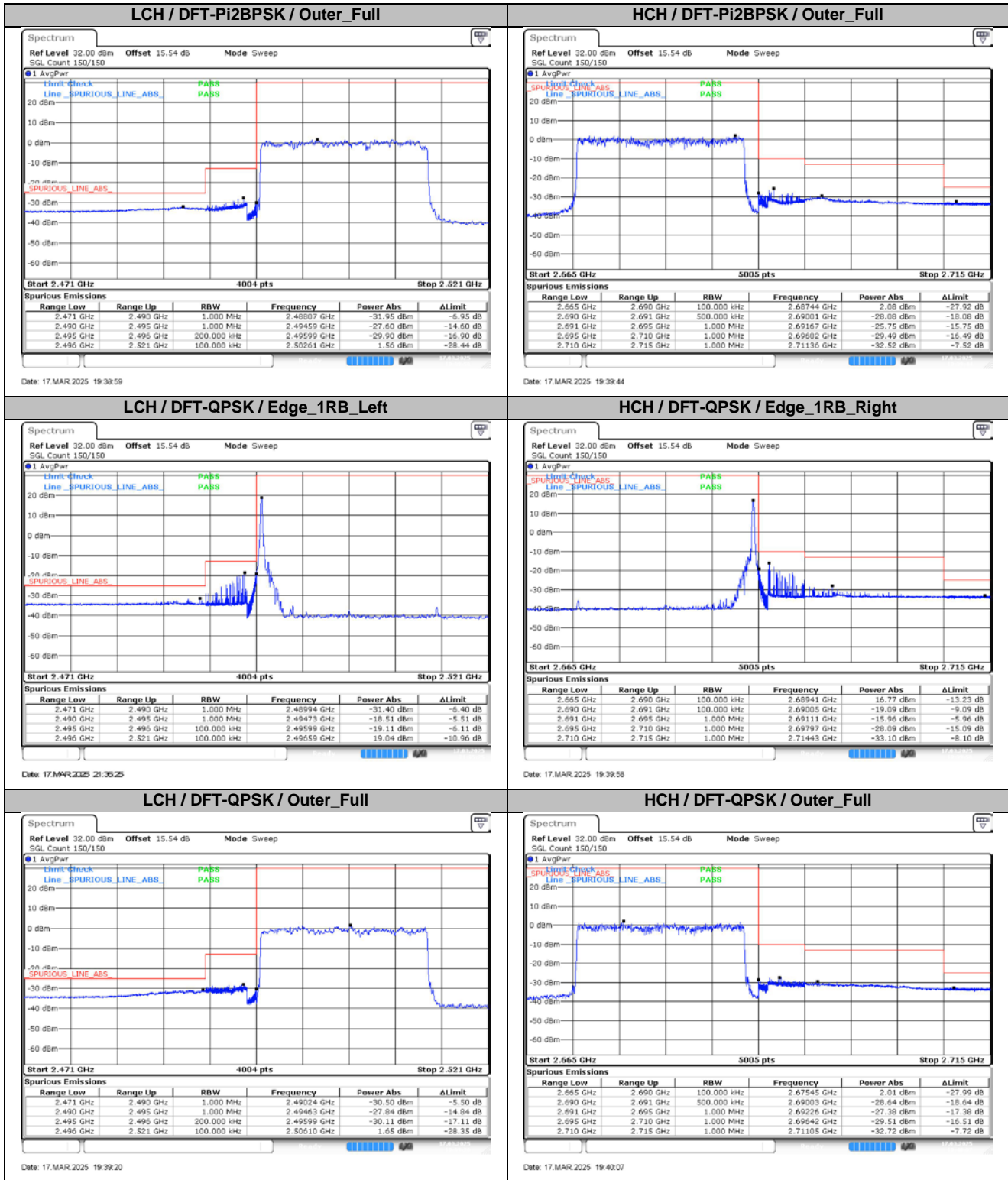






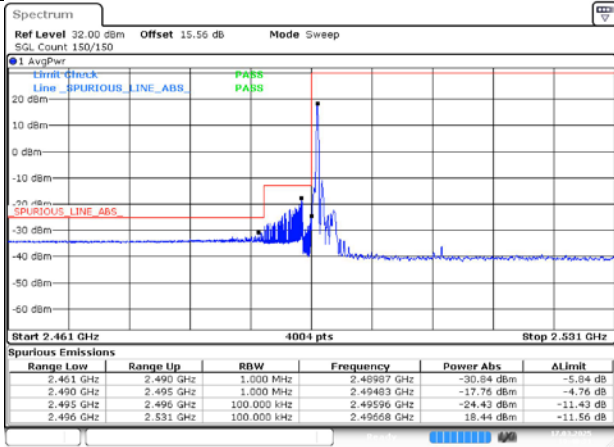
n41A / 15KHz / 20MHz





n41A / 15KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



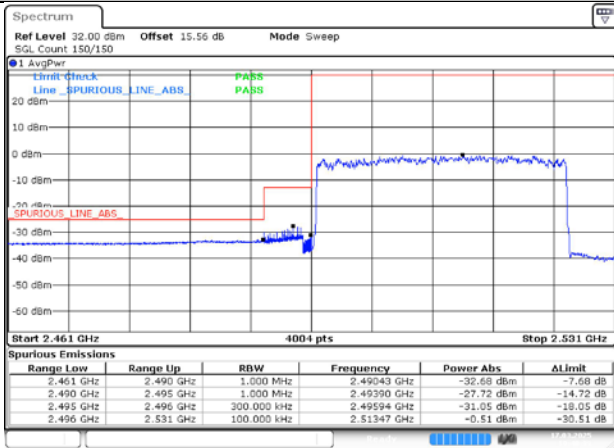
Date: 17.MAR.2025 19:40:22

HCH / DFT-Pi2BPSK / Edge_1RB_Right



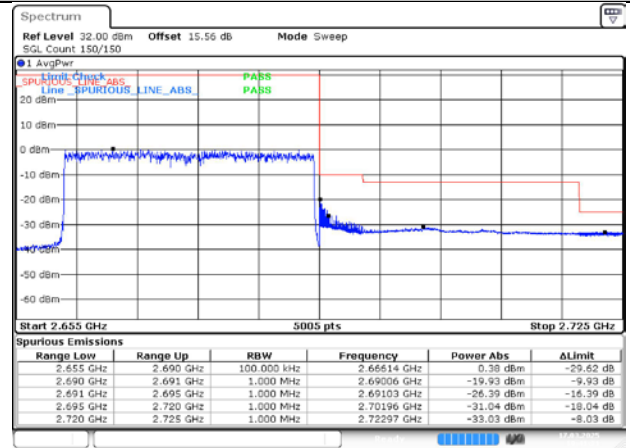
Date: 17.MAR.2025 19:41:11

LCH / DFT-Pi2BPSK / Outer_Full



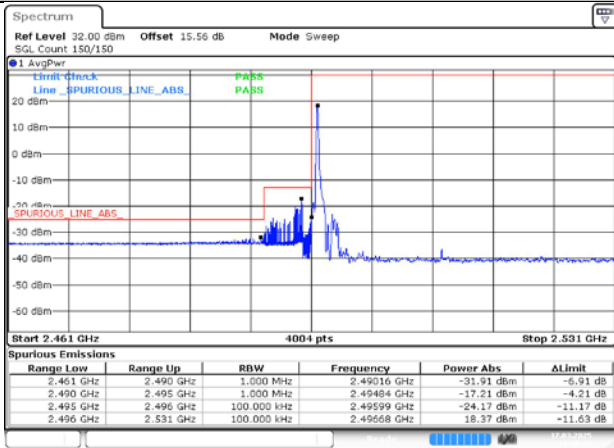
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HCH / DFT-Pi2BPSK / Outer_Full



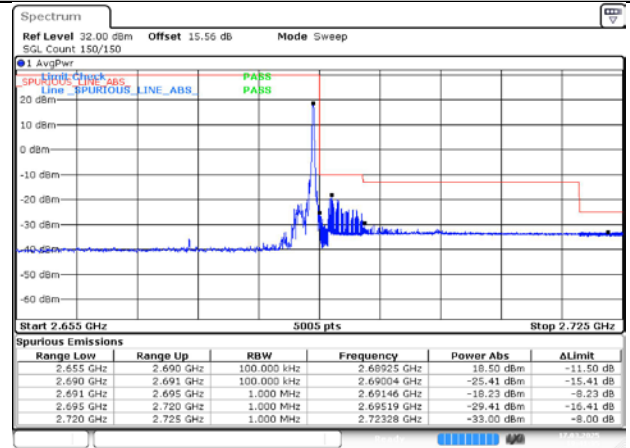
Date: 17.MAR.2025 19:41:21

LCH / DFT-QPSK / Edge_1RB_Left



Date: 17.MAR.2025 19:40:44

HCH / DFT-QPSK / Edge_1RB_Right



Date: 17.MAR.2025 19:41:36