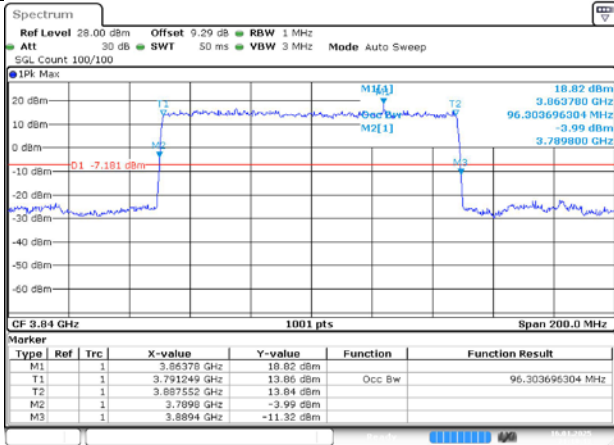


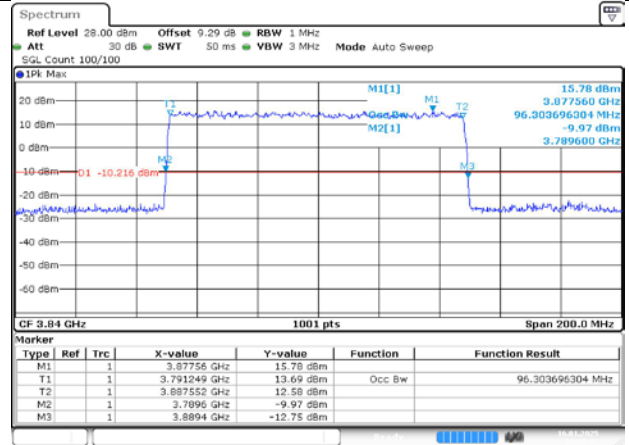
n77A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer_Full



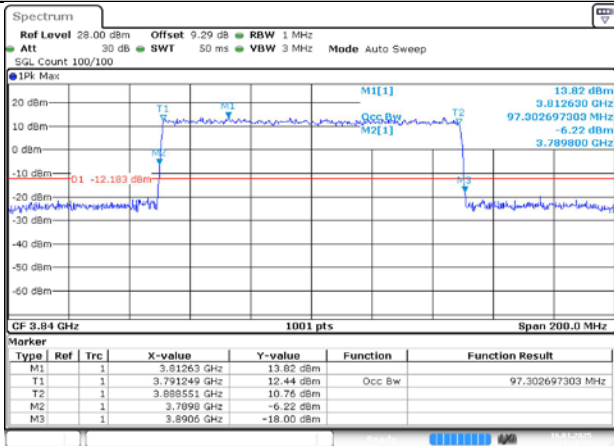
Date: 16, JAN 2025 21:19:12

MCH / DFT-QPSK / Outer_Full



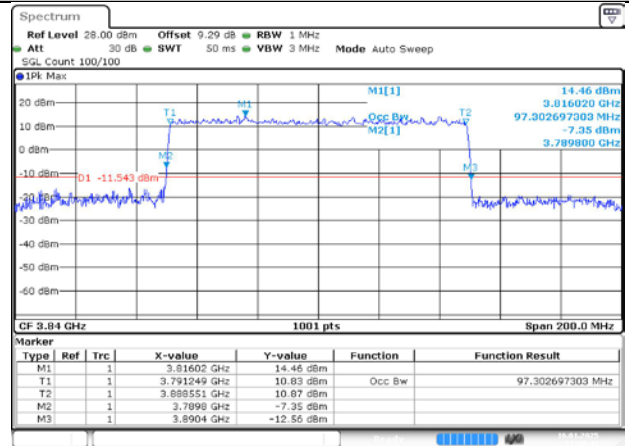
Date: 16, JAN 2025 21:19:22

MCH / CP-QPSK / Outer_Full



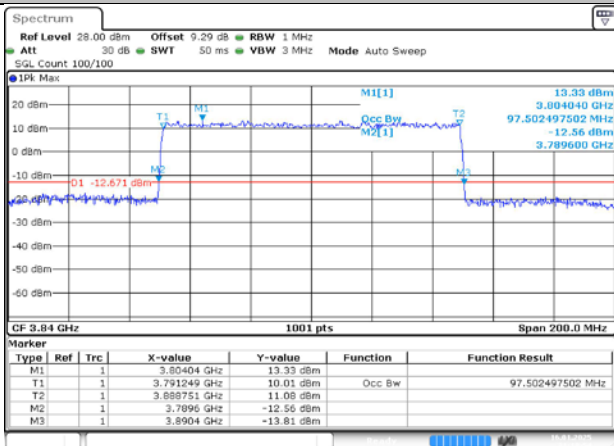
Date: 16, JAN 2025 21:19:33

MCH / CP-16QAM / Outer_Full



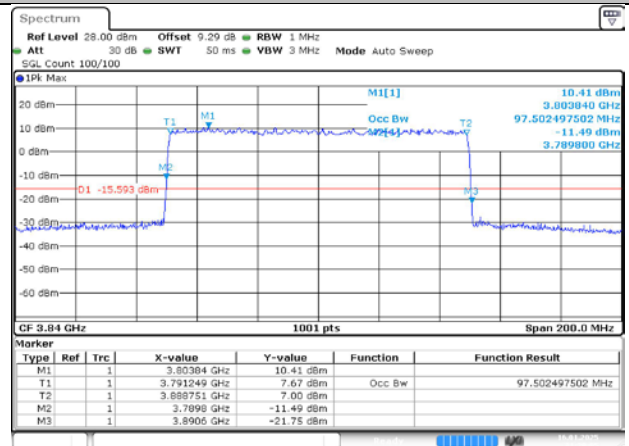
Date: 16, JAN 2025 21:19:43

MCH / CP-64QAM / Outer_Full



Date: 16, JAN 2025 21:19:54

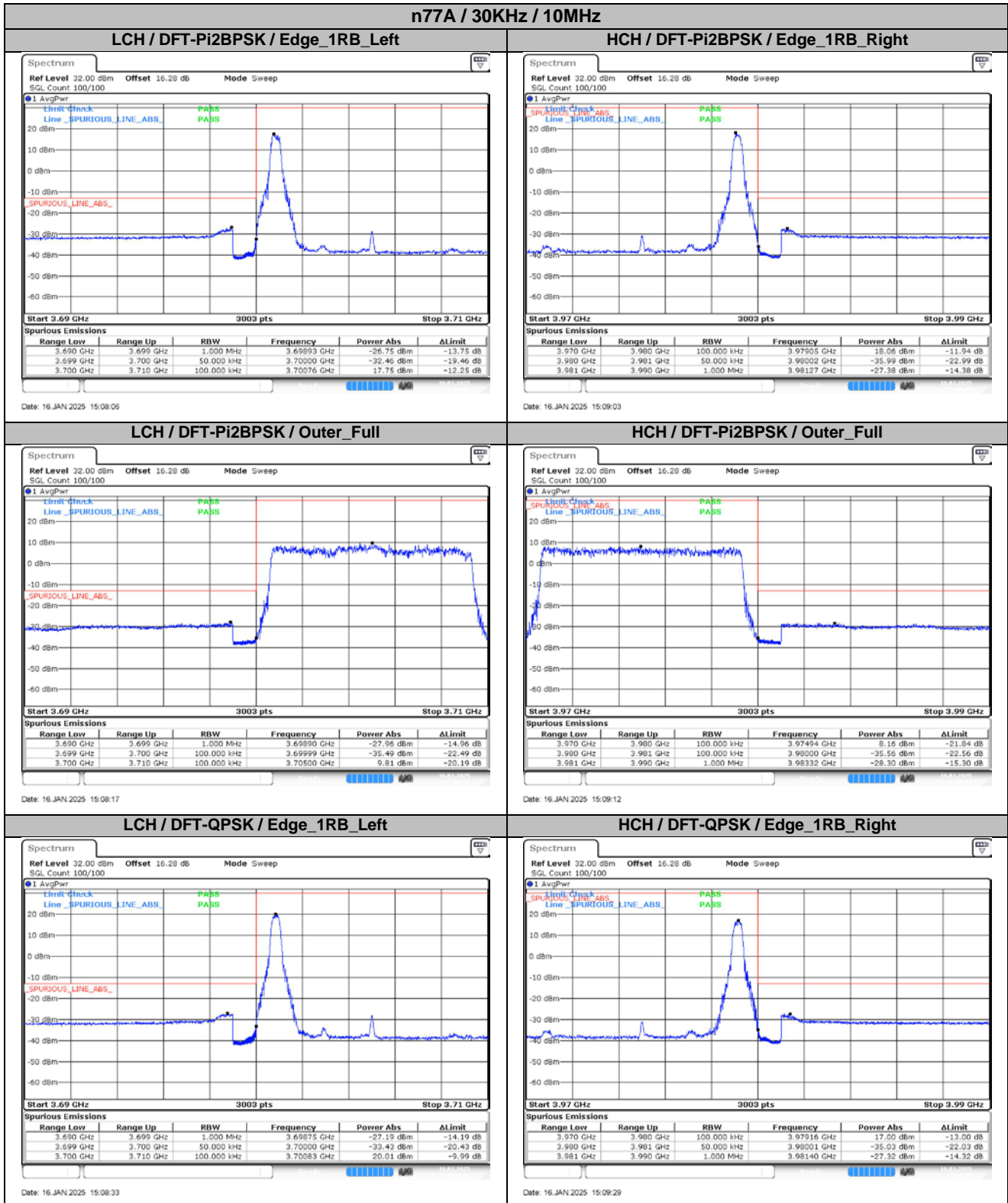
MCH / CP-256QAM / Outer_Full

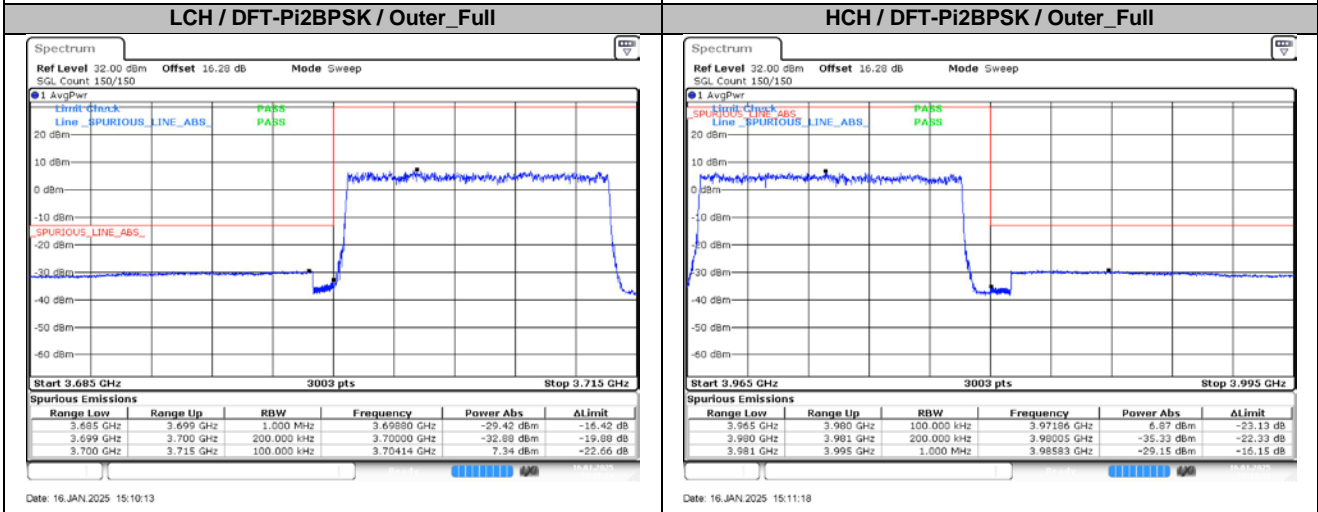
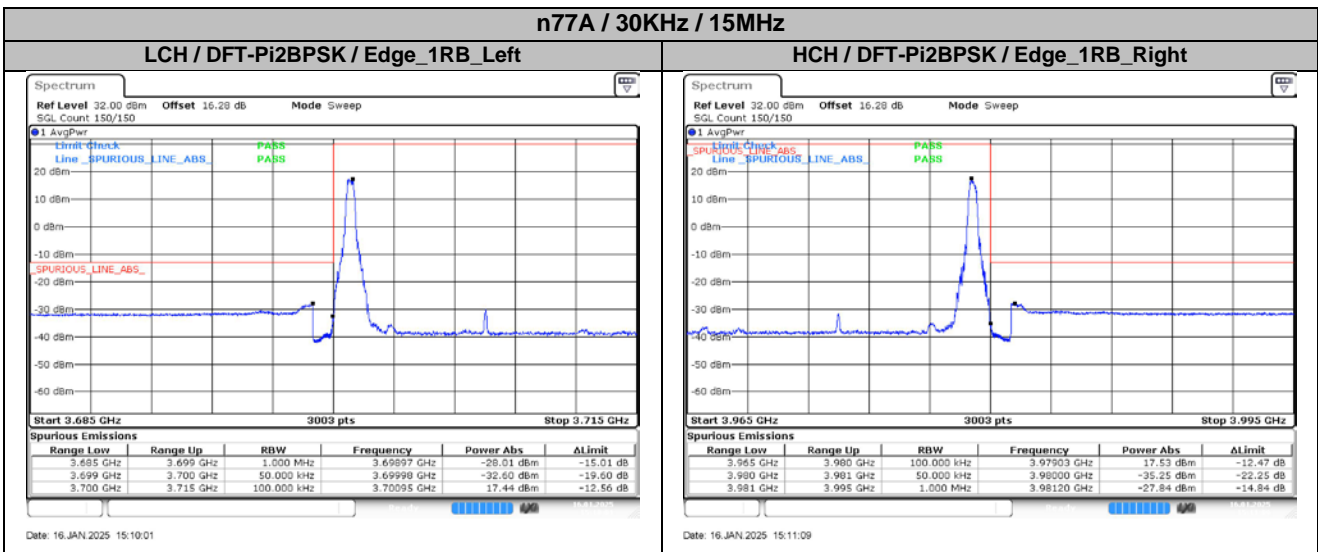
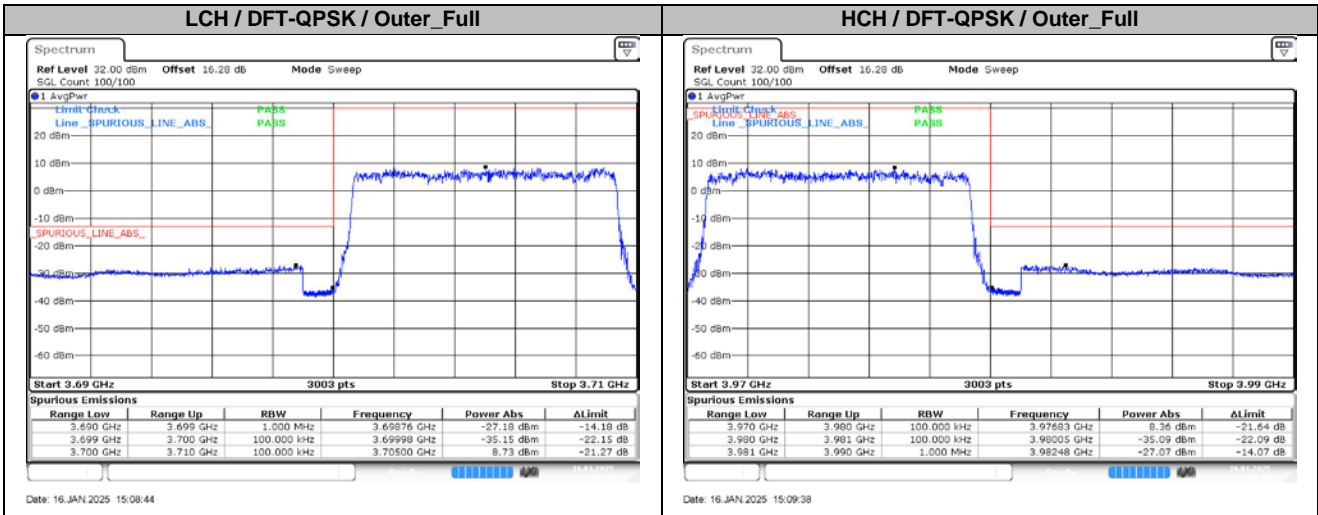


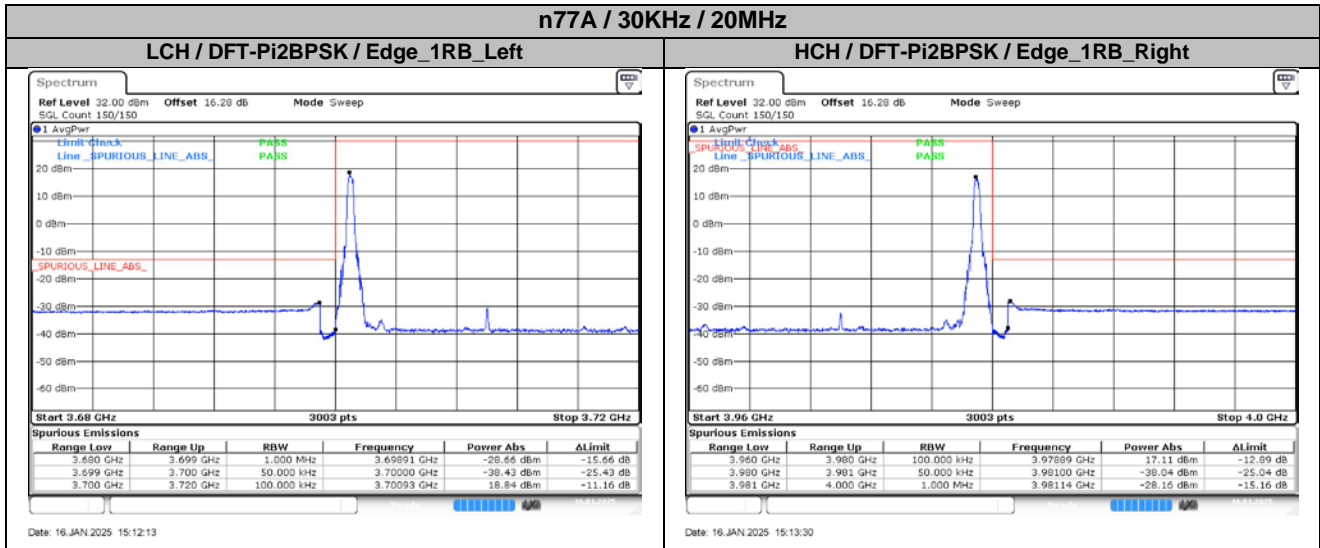
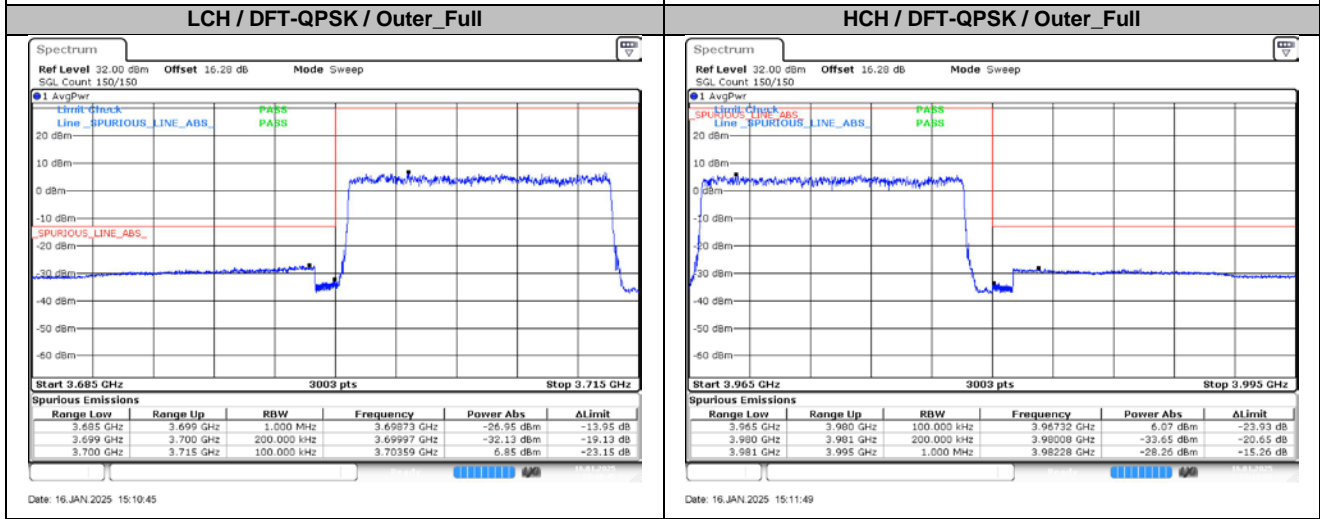
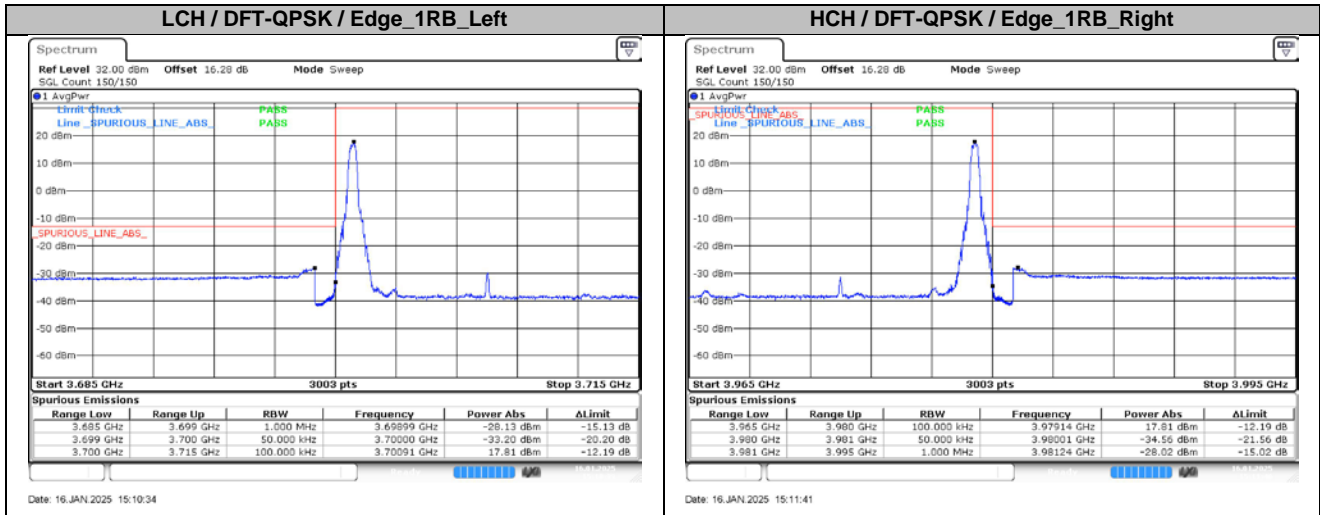
Date: 16, JAN 2025 21:20:05

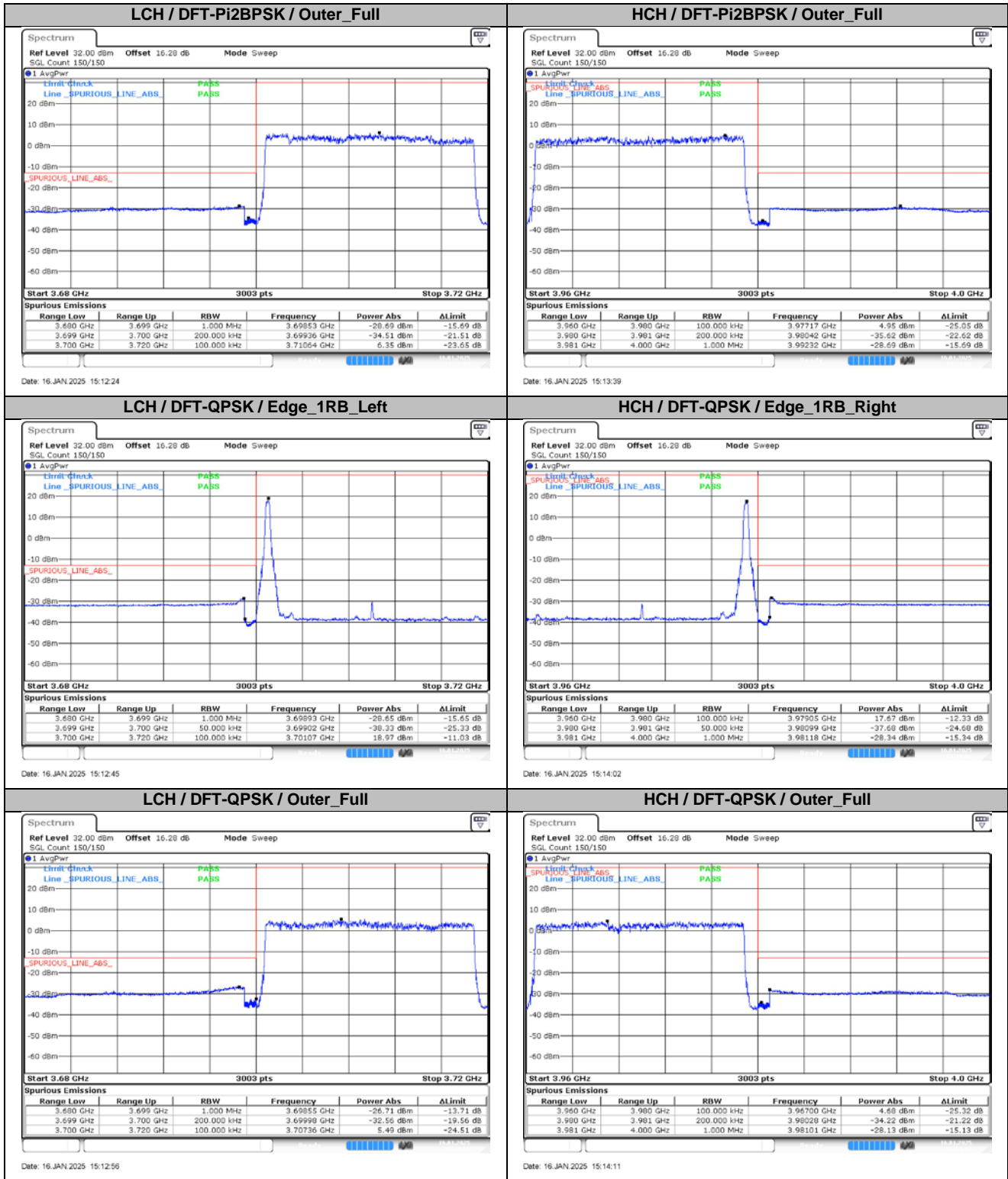
5. Conducted Band Edges

5.1. Test Plots for SCS=30KHz



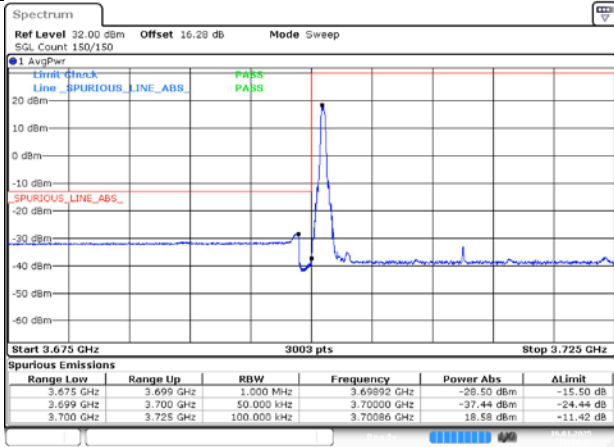






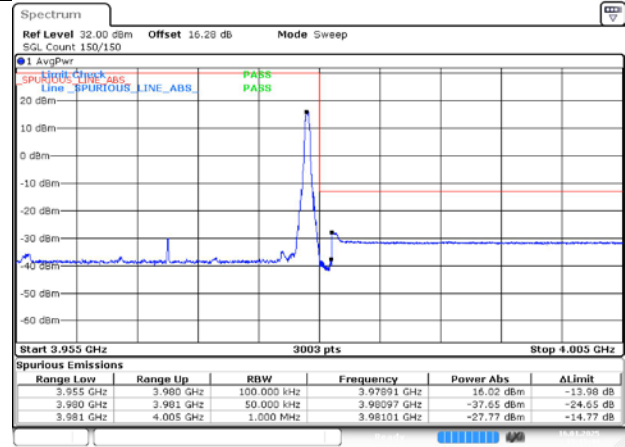
n77A / 30KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



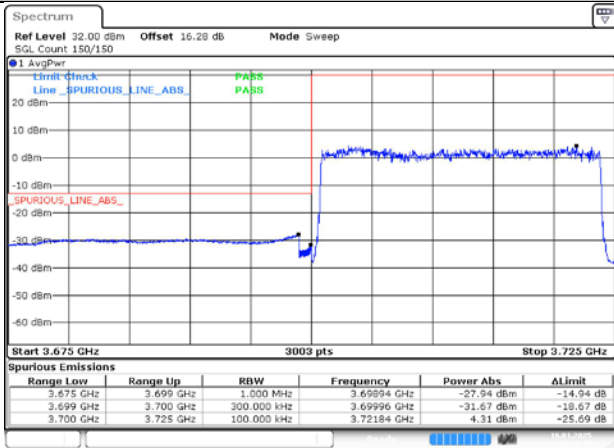
Date: 16, JAN 2025 15:14:35

HCH / DFT-Pi2BPSK / Edge_1RB_Right



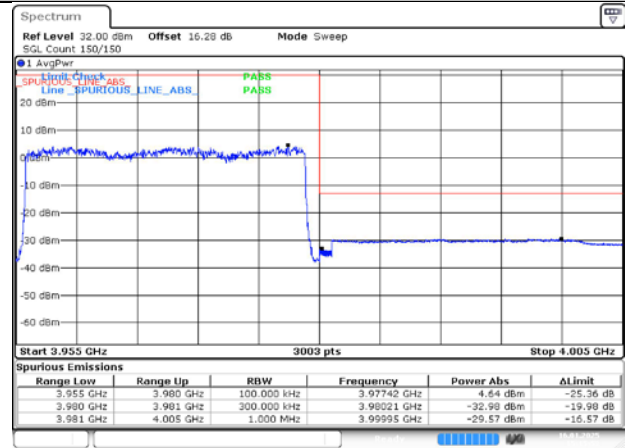
Date: 16, JAN 2025 15:15:44

LCH / DFT-Pi2BPSK / Outer_Full



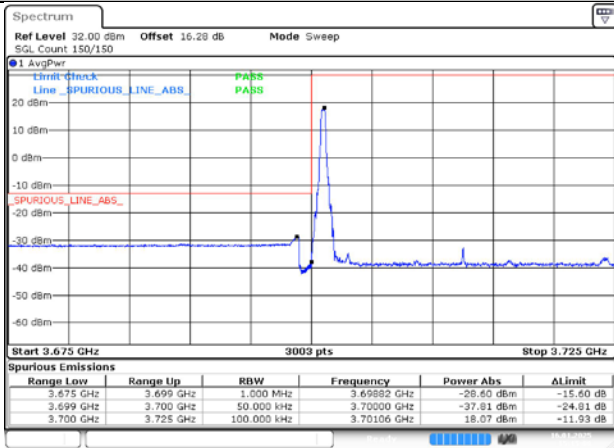
Date: 16, JAN 2025 15:14:47

HCH / DFT-Pi2BPSK / Outer_Full



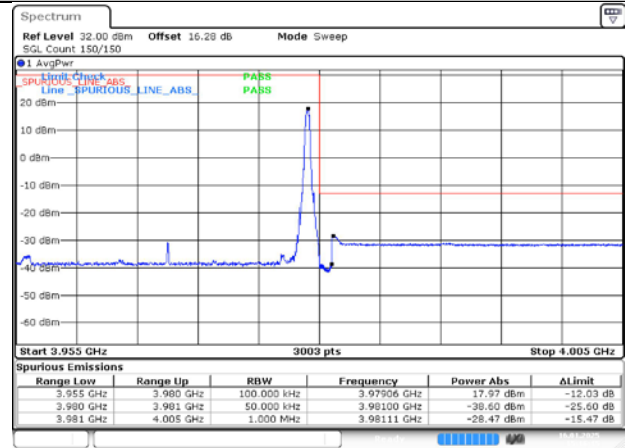
Date: 16, JAN 2025 15:15:52

LCH / DFT-QPSK / Edge_1RB_Left

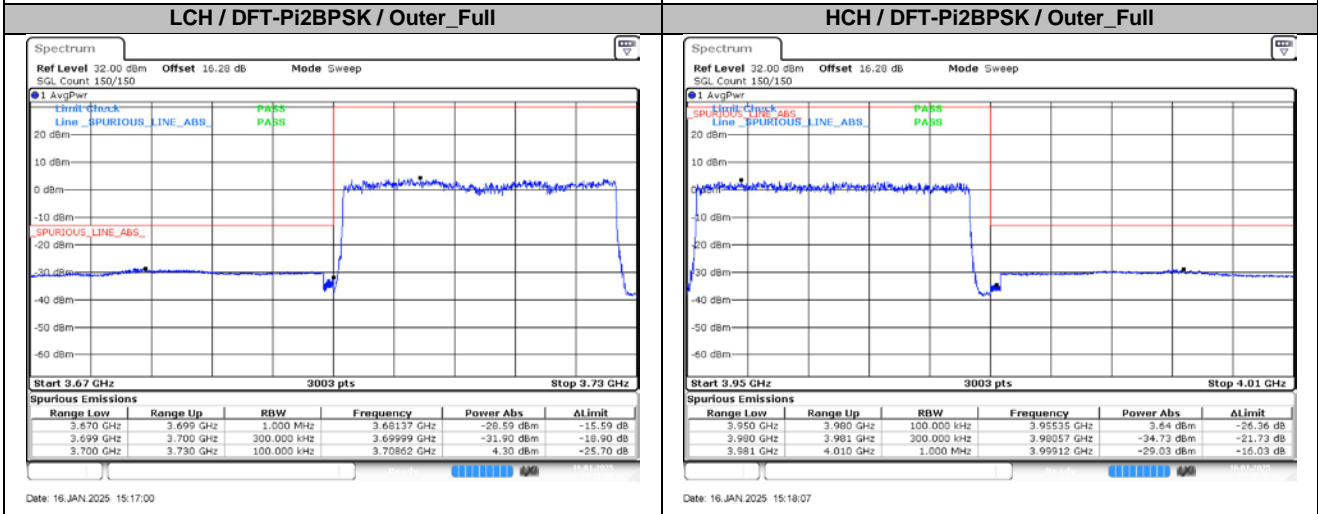
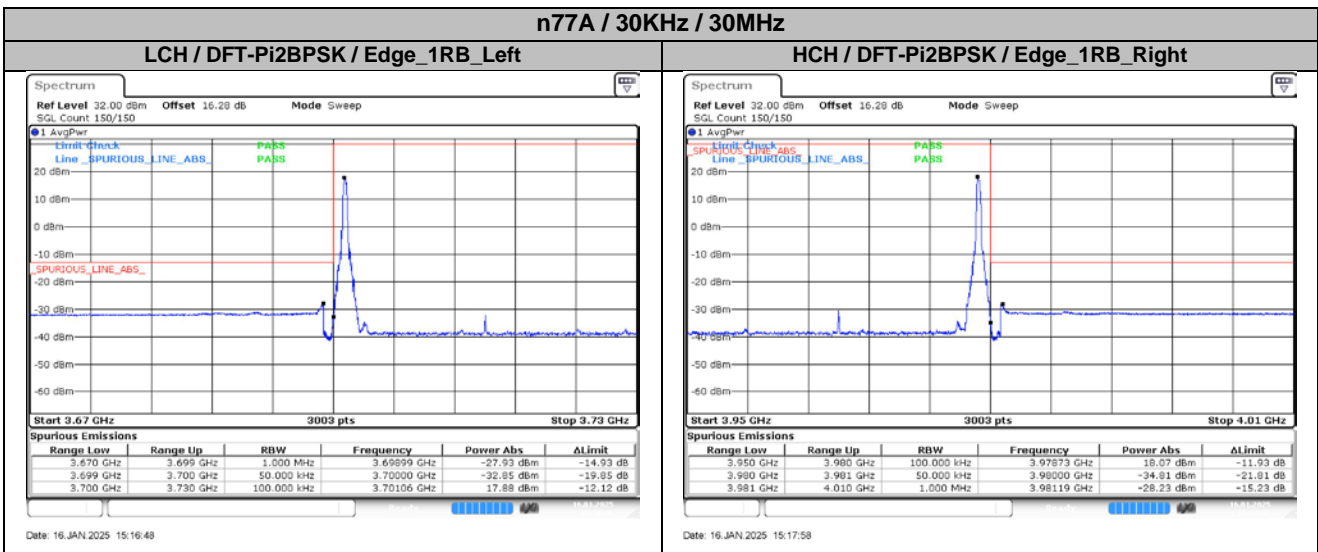
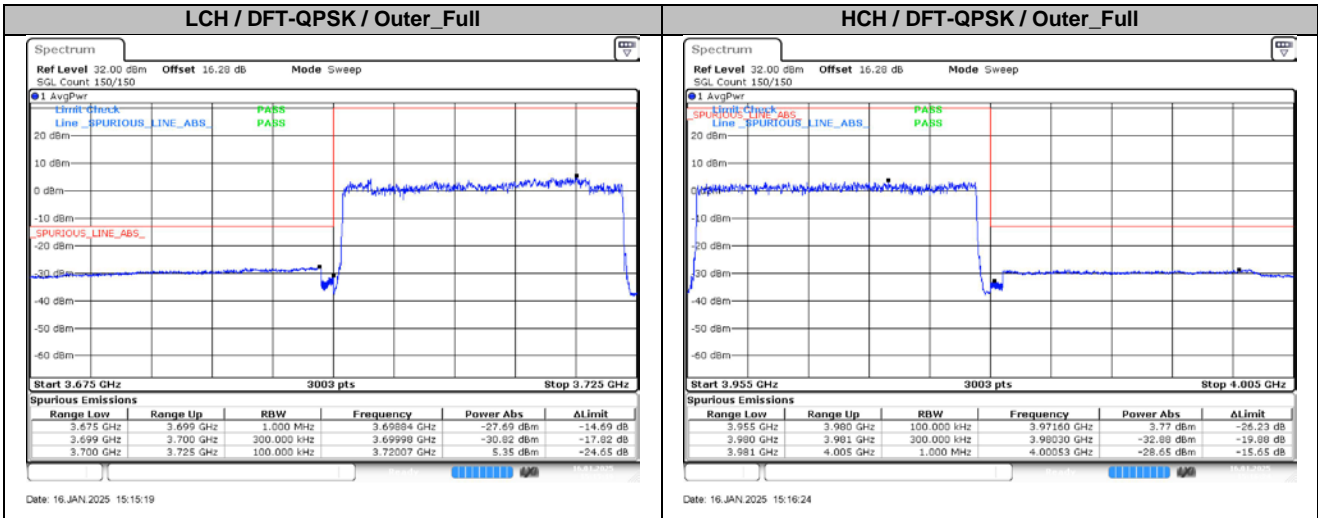


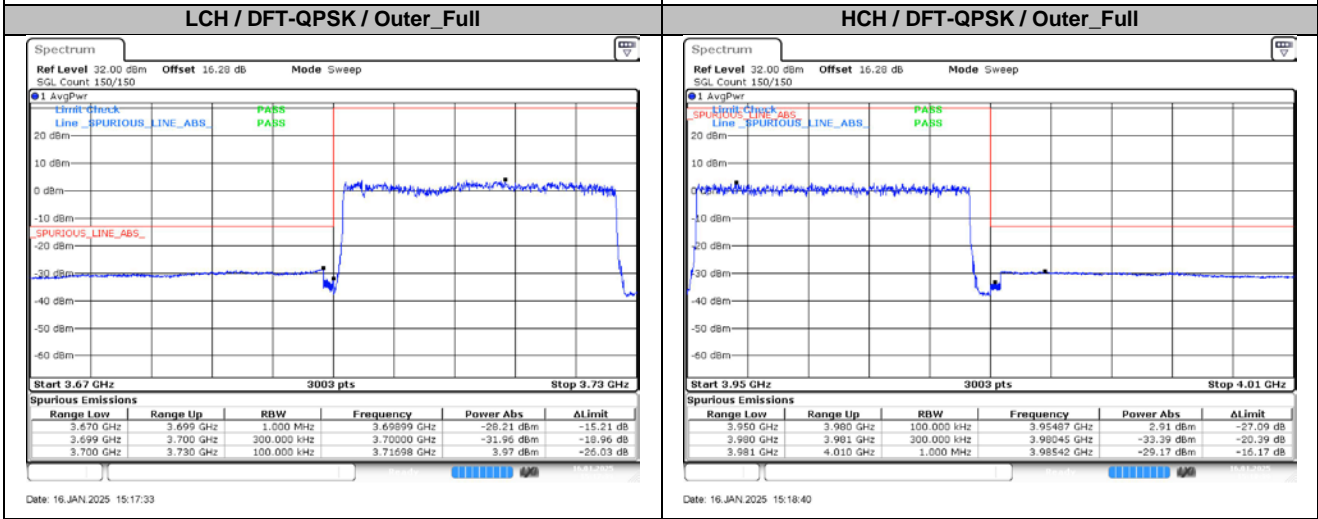
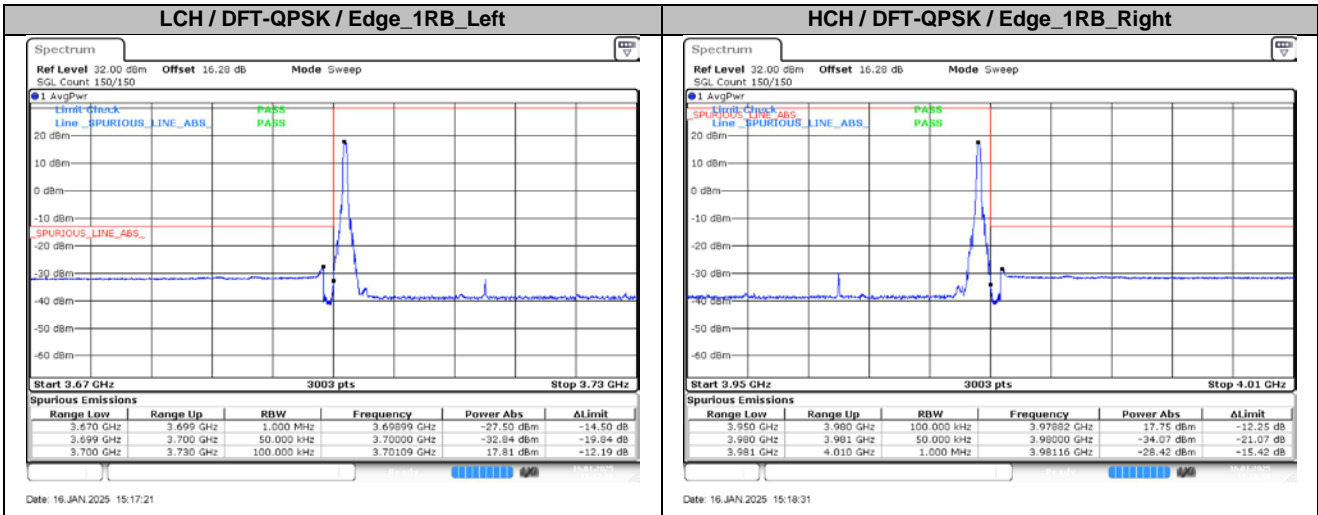
Date: 16, JAN 2025 15:15:08

HCH / DFT-QPSK / Edge_1RB_Right

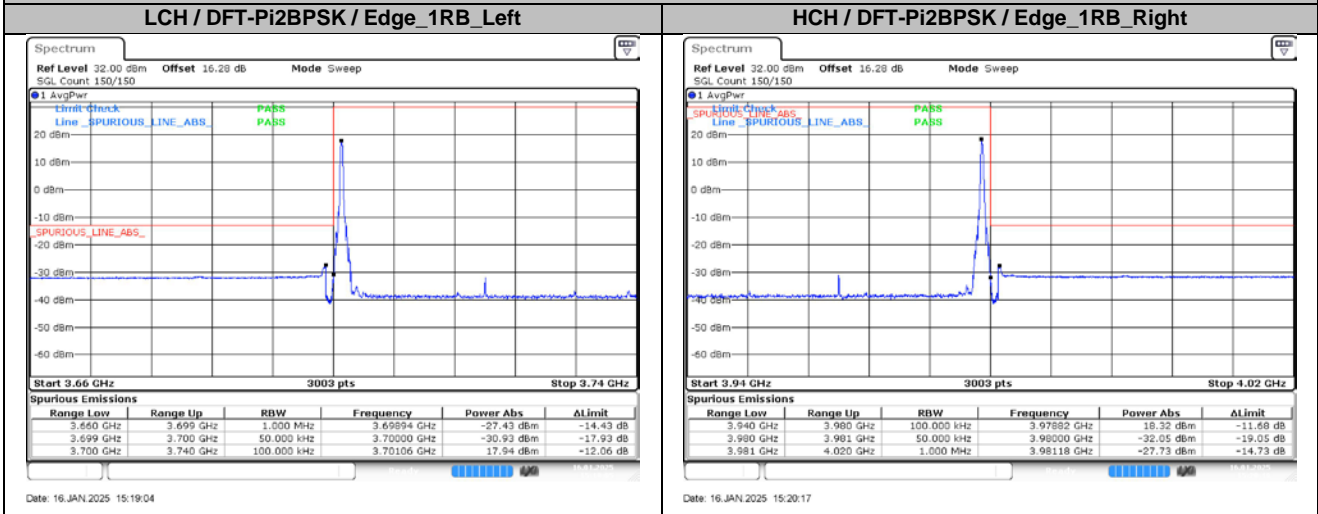


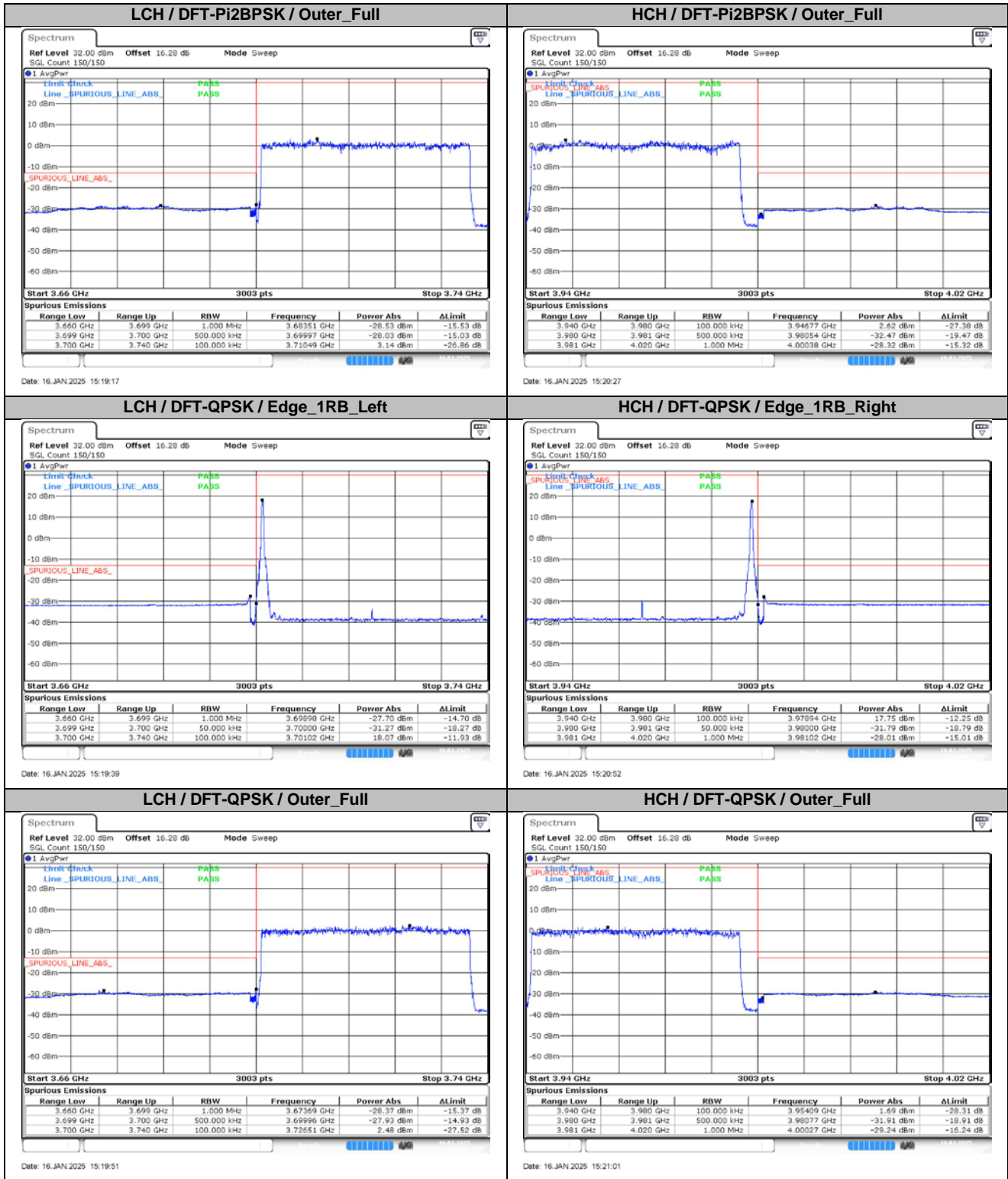
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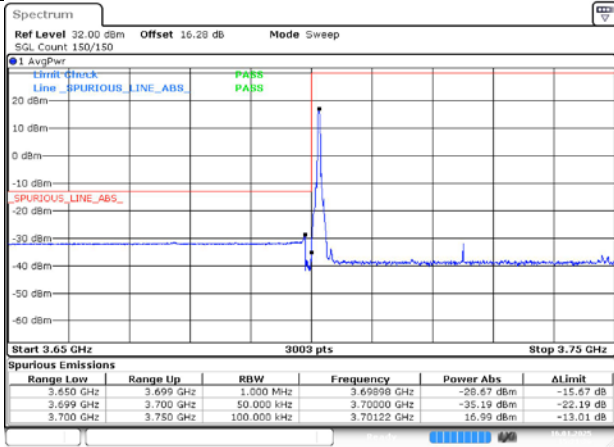
n77A / 30kHz / 40MHz





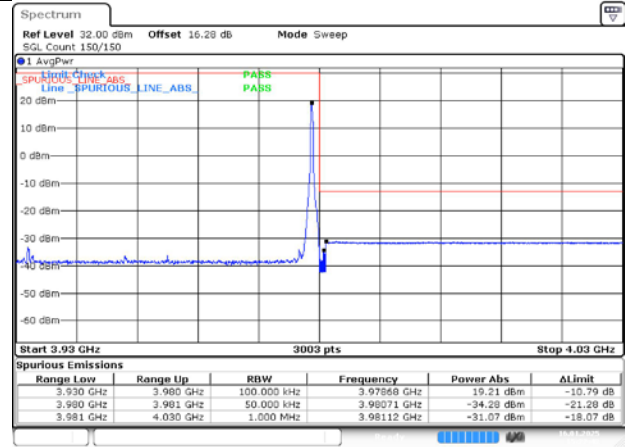
n77A / 30KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



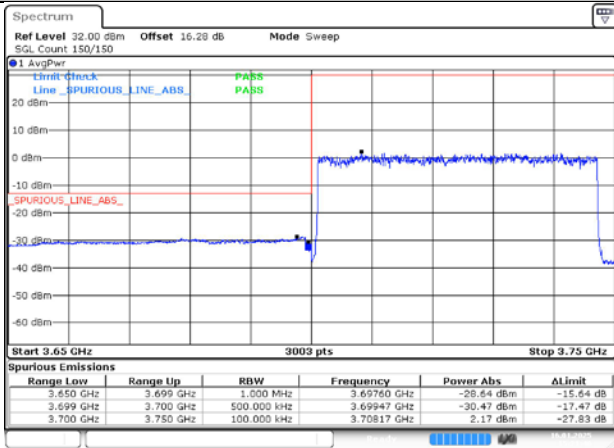
Date: 16 JAN 2025 15:21:27

HCH / DFT-Pi2BPSK / Edge_1RB_Right



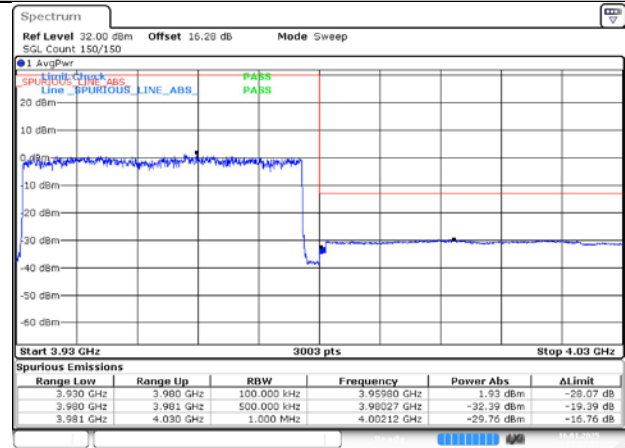
Date: 16 JAN 2025 15:22:41

LCH / DFT-Pi2BPSK / Outer_Full



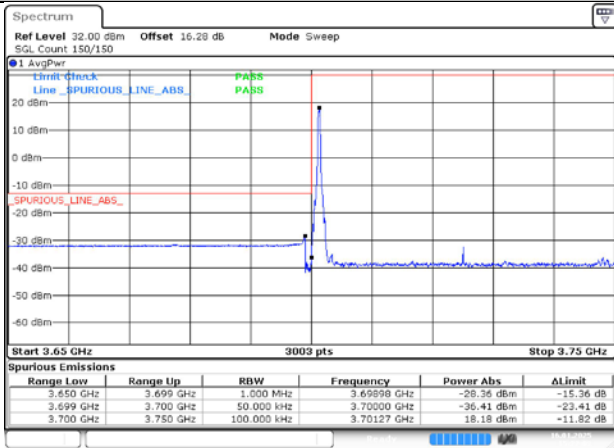
Date: 16 JAN 2025 15:21:39

HCH / DFT-Pi2BPSK / Outer_Full



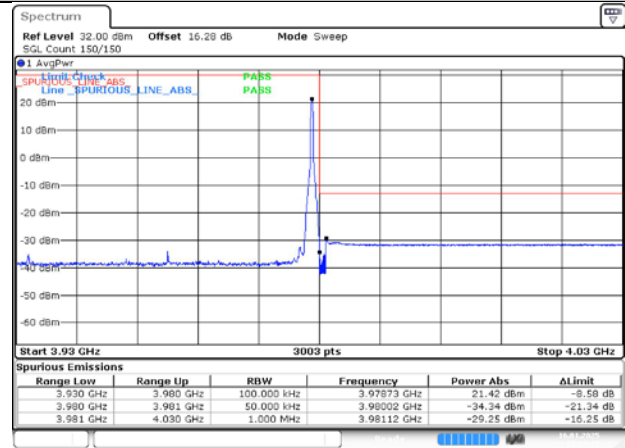
Date: 16 JAN 2025 15:22:52

LCH / DFT-QPSK / Edge_1RB_Left



Date: 16 JAN 2025 15:22:02

HCH / DFT-QPSK / Edge_1RB_Right



Date: 16 JAN 2025 15:23:17

