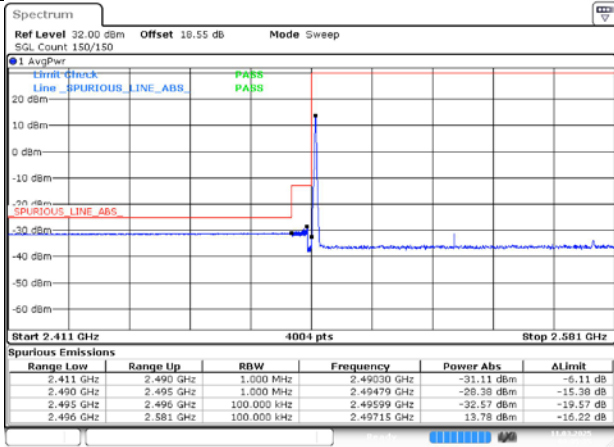


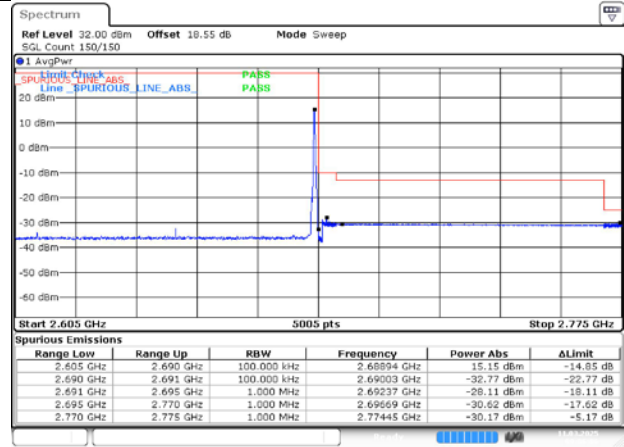
n41A / 30kHz / 80MHz

LCH / CP-QPSK / Edge_1RB_Left



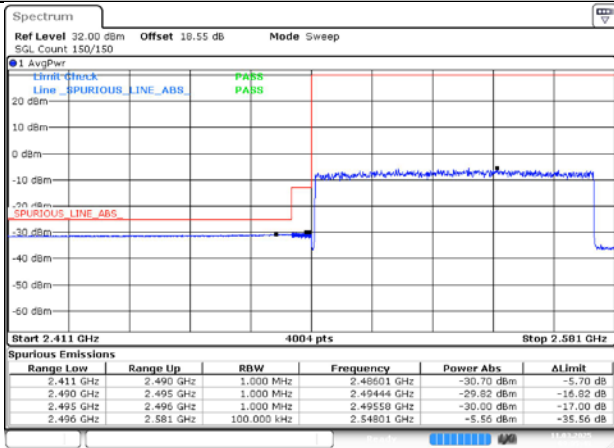
Date: 11.MAR.2025 09:05:28

HCH / CP-QPSK / Edge_1RB_Right



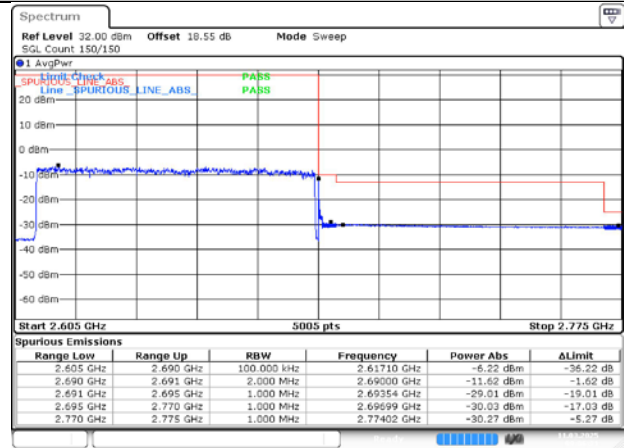
Date: 11.MAR.2025 09:06:07

LCH / CP-QPSK / Outer_Full



Date: 11.MAR.2025 09:05:43

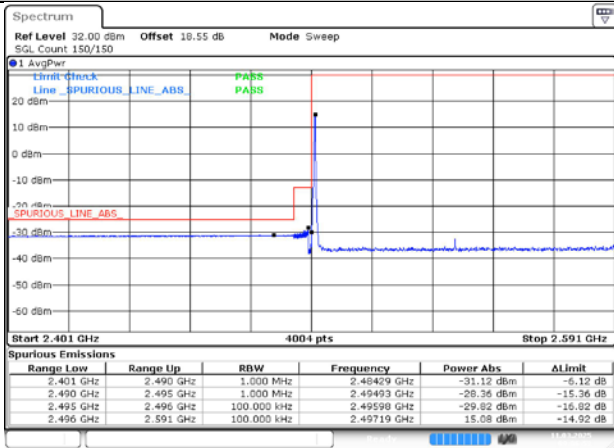
HCH / CP-QPSK / Outer_Full



Date: 11.MAR.2025 09:06:21

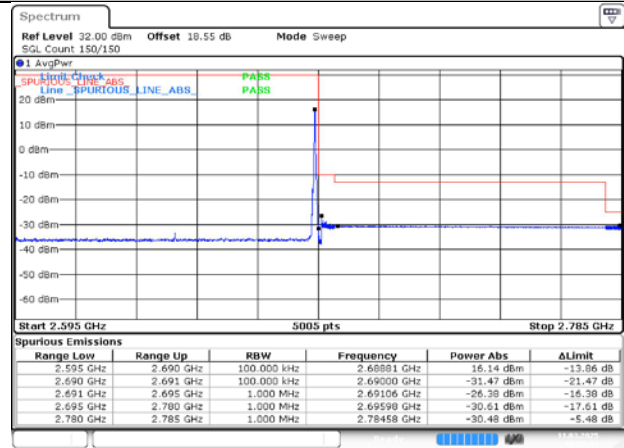
n41A / 30kHz / 90MHz

LCH / CP-QPSK / Edge_1RB_Left

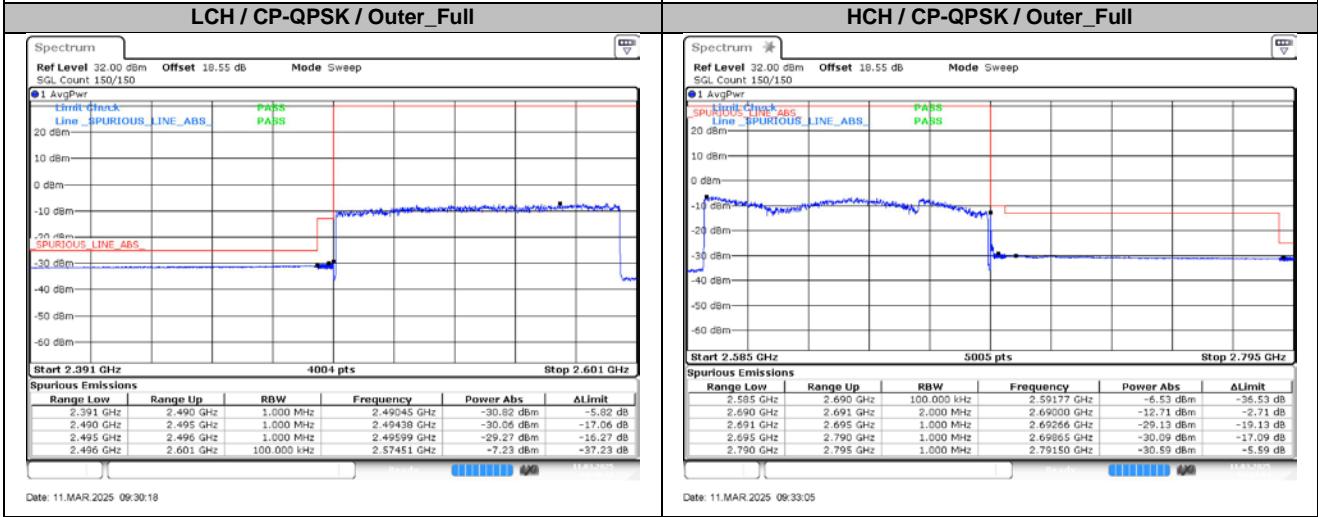
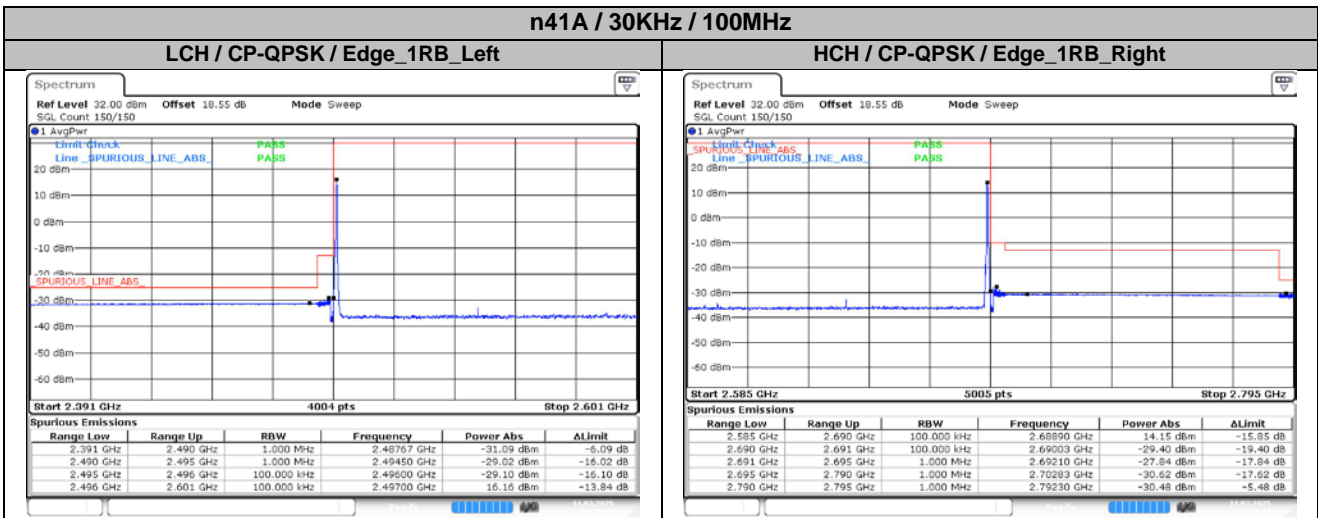
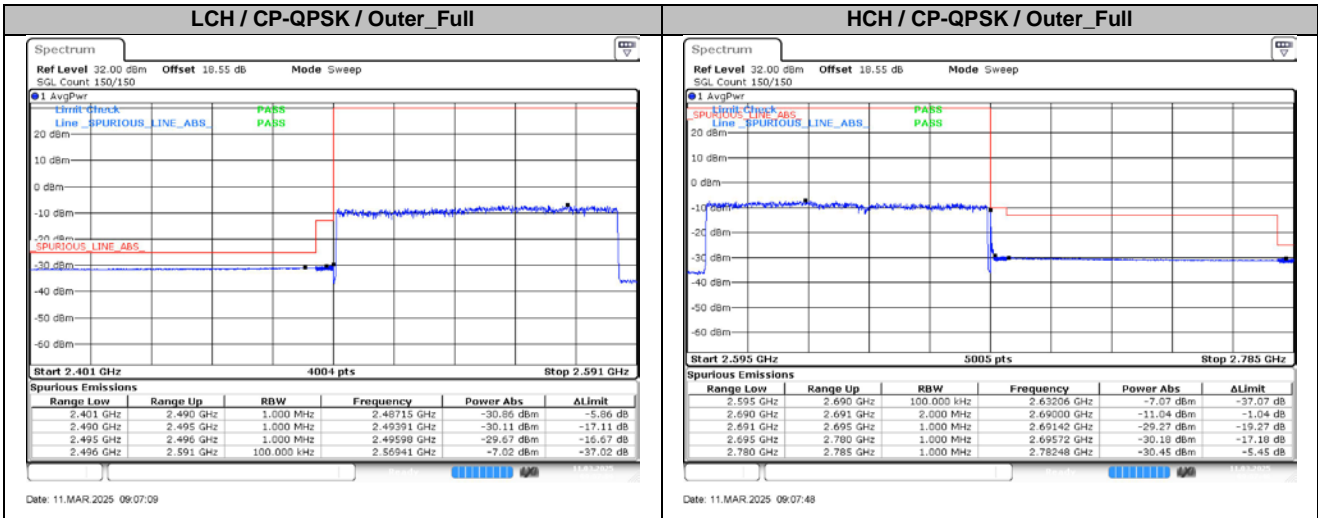


Date: 11.MAR.2025 09:06:52

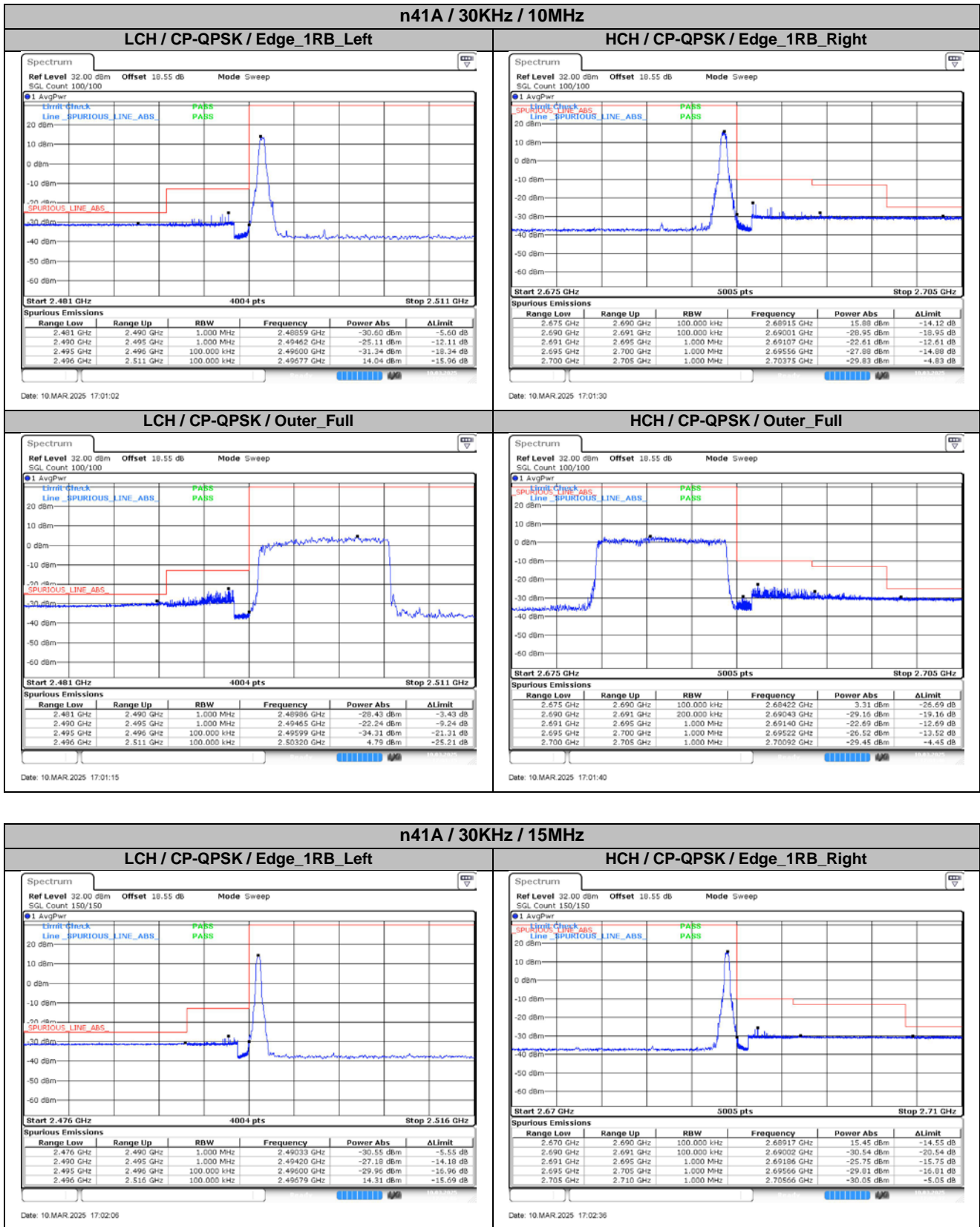
HCH / CP-QPSK / Edge_1RB_Right

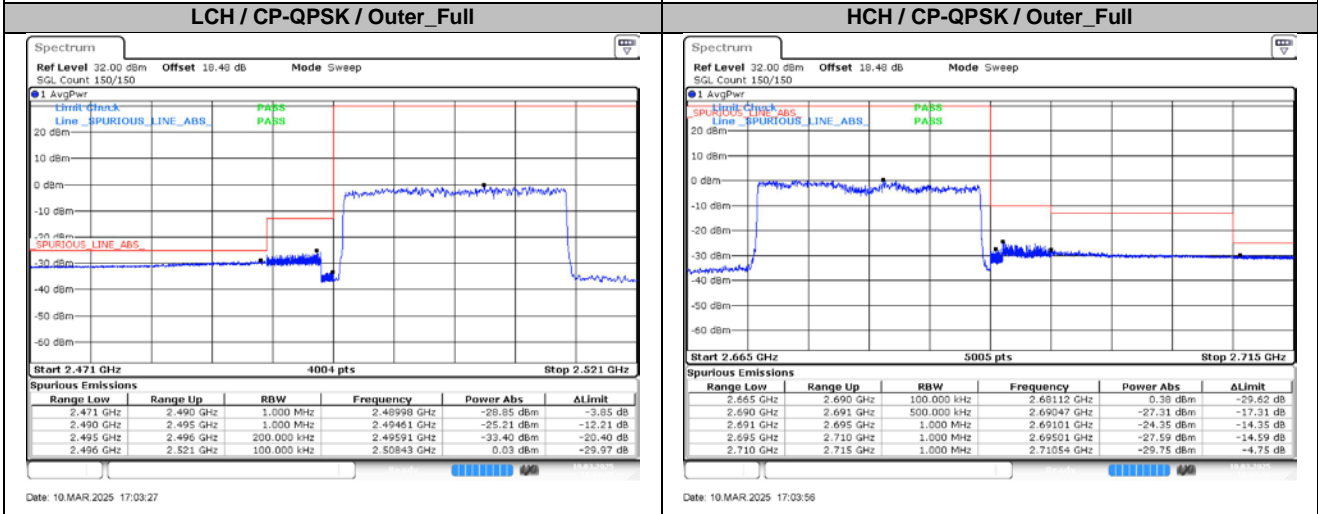
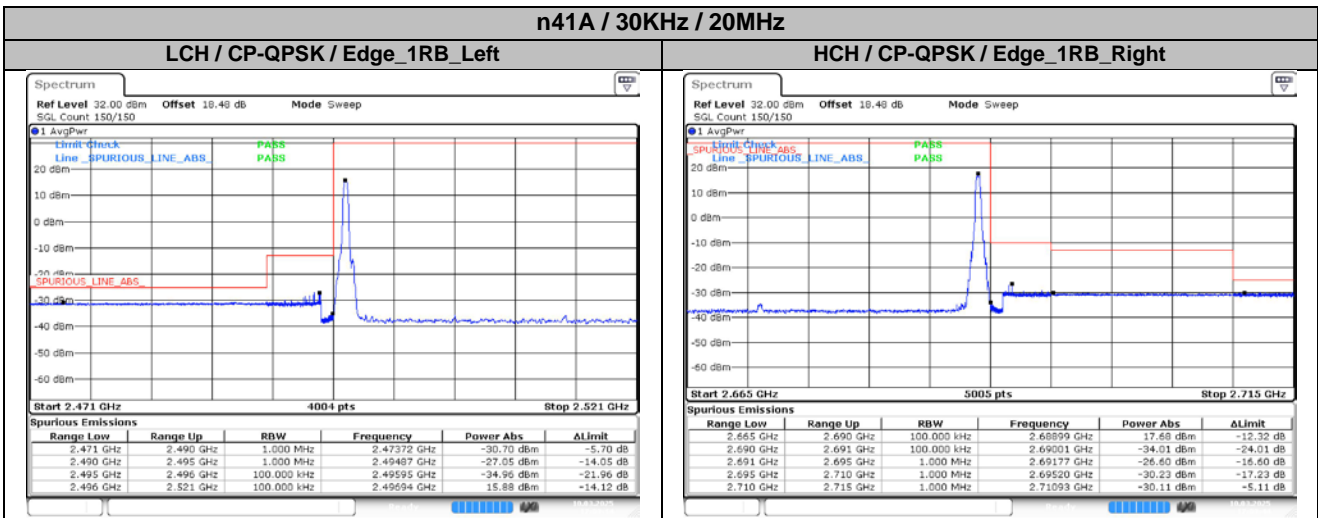
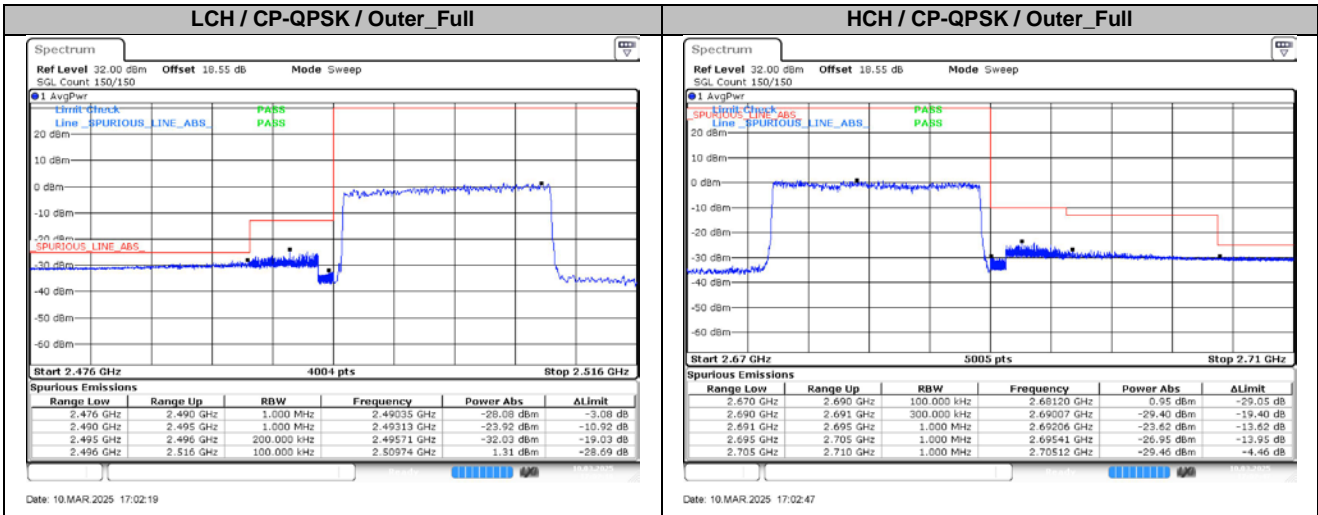


Date: 11.MAR.2025 09:07:33



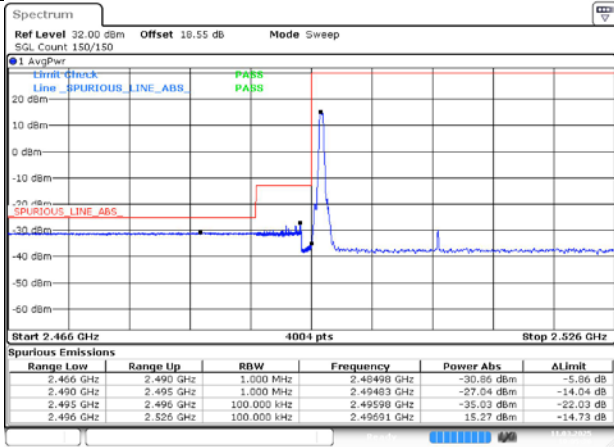
5.2. Test Plots for Ant2





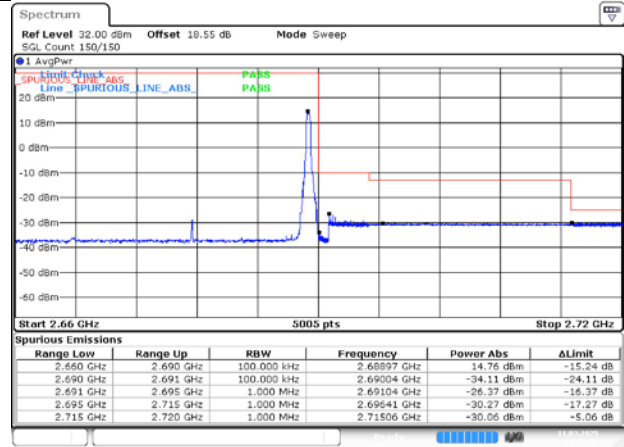
n41A / 30KHz / 25MHz

LCH / CP-QPSK / Edge_1RB_Left



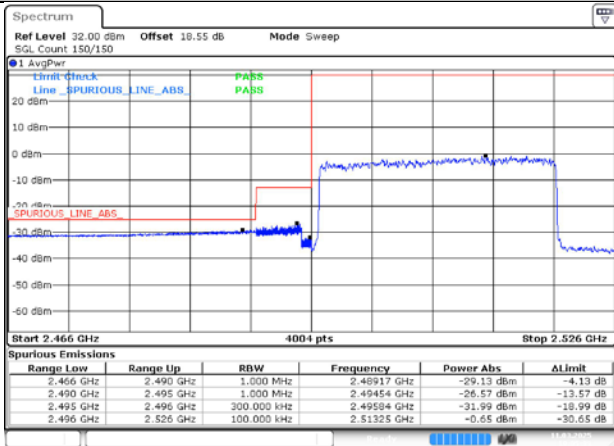
Date: 11.MAR.2025 10:05:21

HCH / CP-QPSK / Edge_1RB_Right



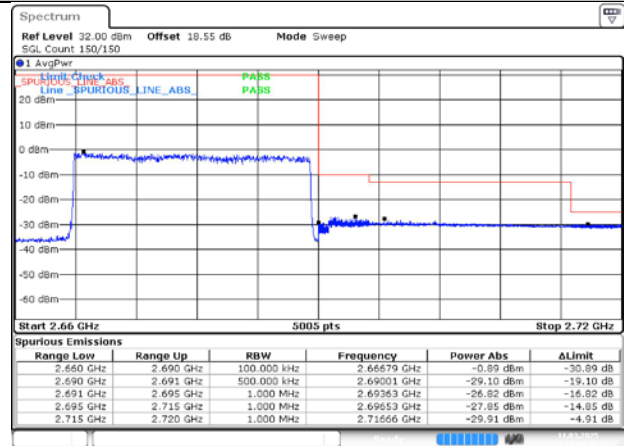
Date: 11.MAR.2025 10:05:51

LCH / CP-QPSK / Outer_Full



Date: 11.MAR.2025 10:05:33

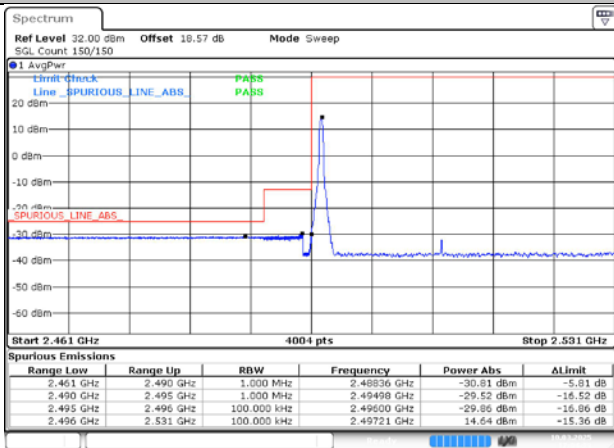
HCH / CP-QPSK / Outer_Full



Date: 11.MAR.2025 10:06:02

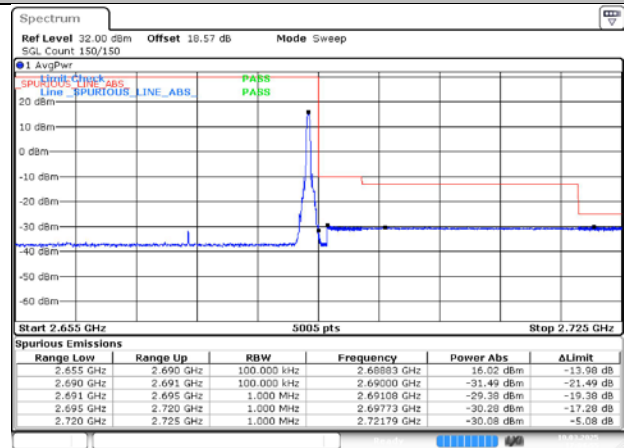
n41A / 30KHz / 30MHz

LCH / CP-QPSK / Edge_1RB_Left

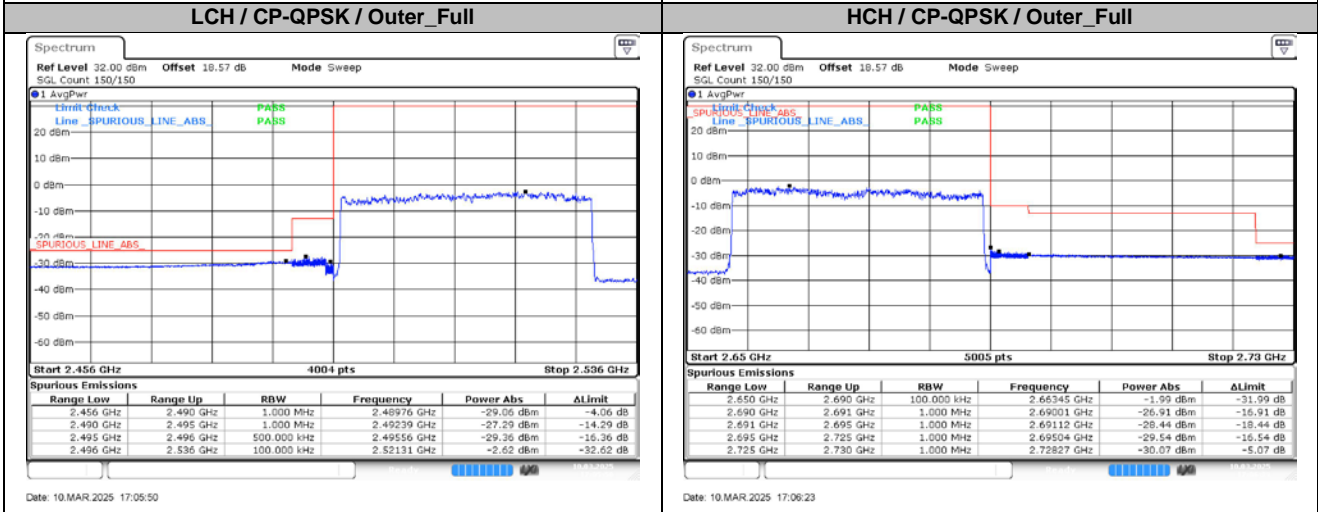
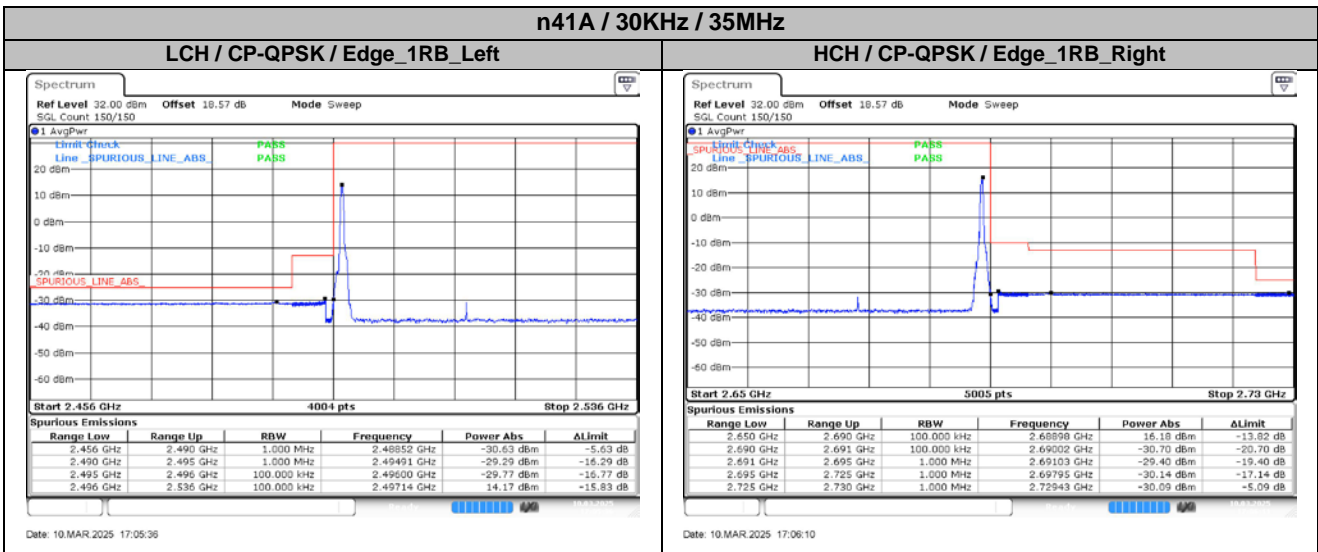
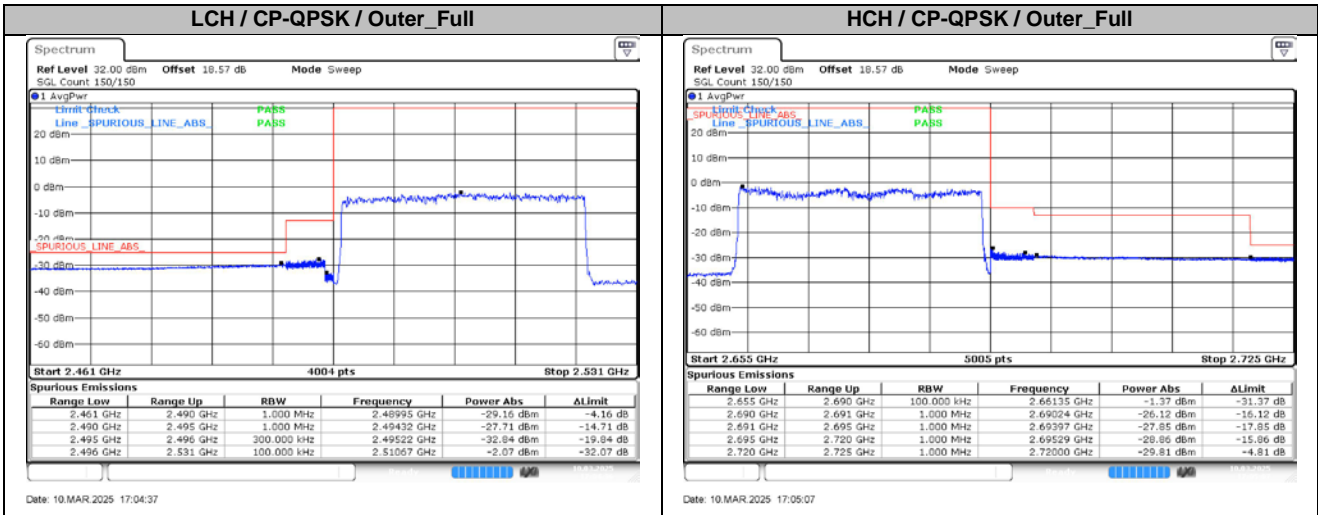


Date: 10.MAR.2025 17:04:23

HCH / CP-QPSK / Edge_1RB_Right

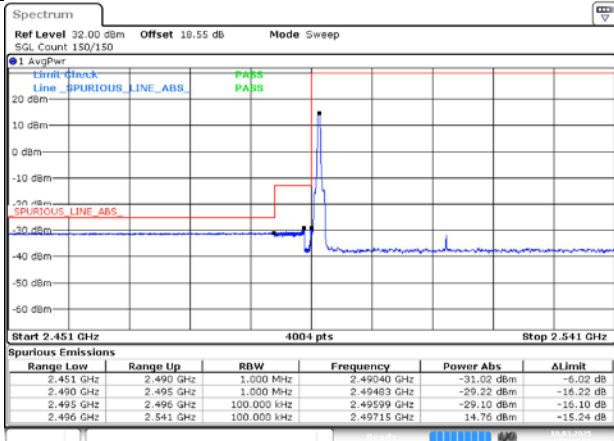


Date: 10.MAR.2025 17:04:55



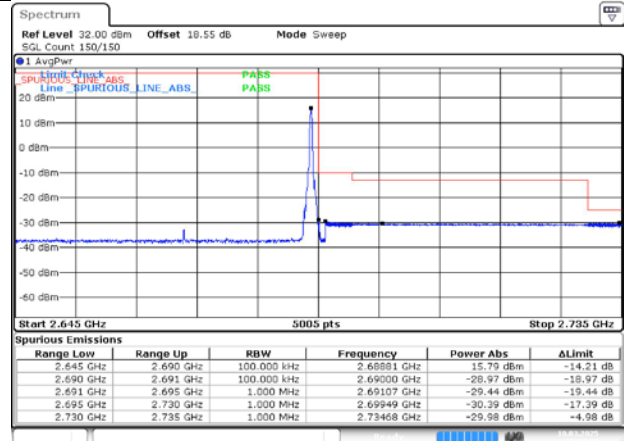
n41A / 30kHz / 40MHz

LCH / CP-QPSK / Edge_1RB_Left



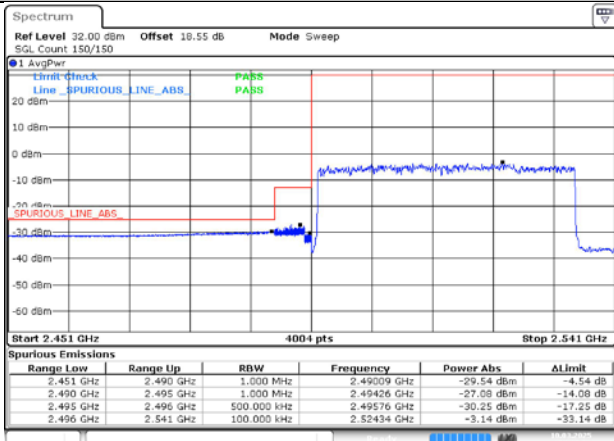
Date: 10.MAR.2025 17:06:52

HCH / CP-QPSK / Edge_1RB_Right



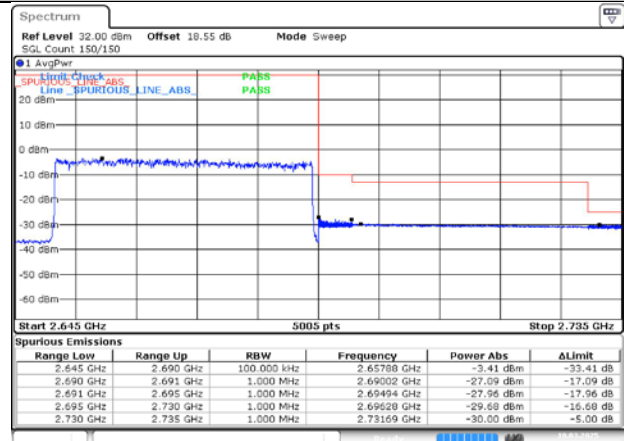
Date: 10.MAR.2025 17:07:27

LCH / CP-QPSK / Outer_Full



Date: 10.MAR.2025 17:07:06

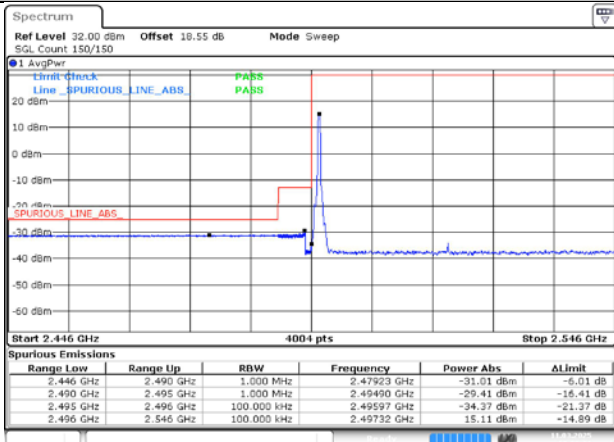
HCH / CP-QPSK / Outer_Full



Date: 10.MAR.2025 17:07:40

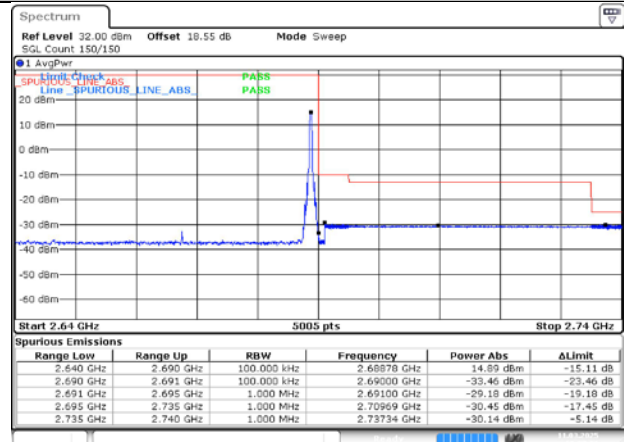
n41A / 30kHz / 45MHz

LCH / CP-QPSK / Edge_1RB_Left

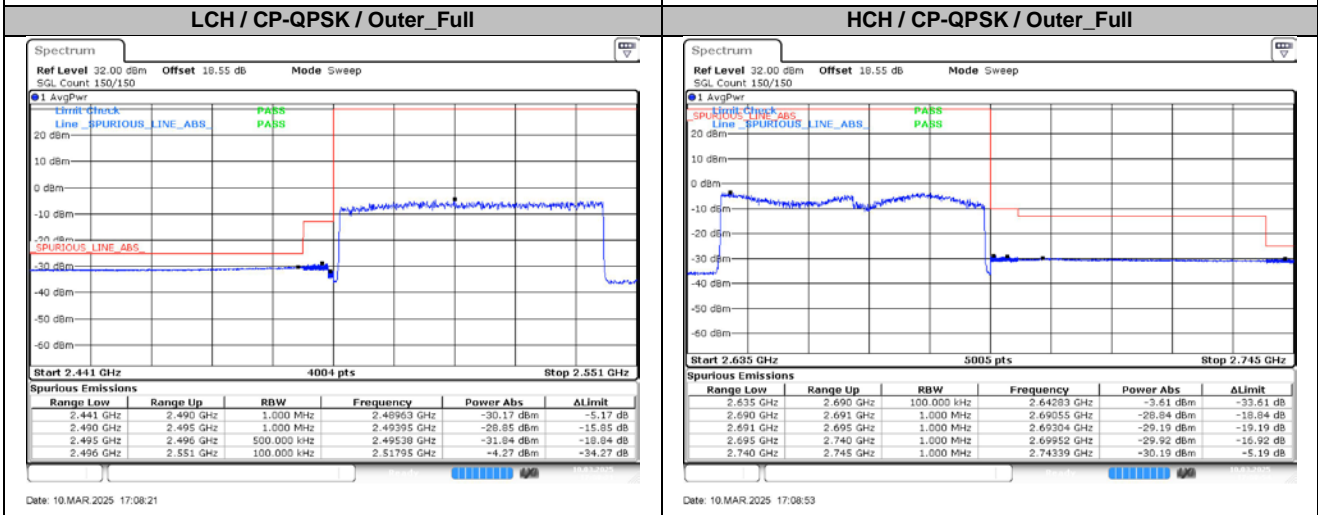
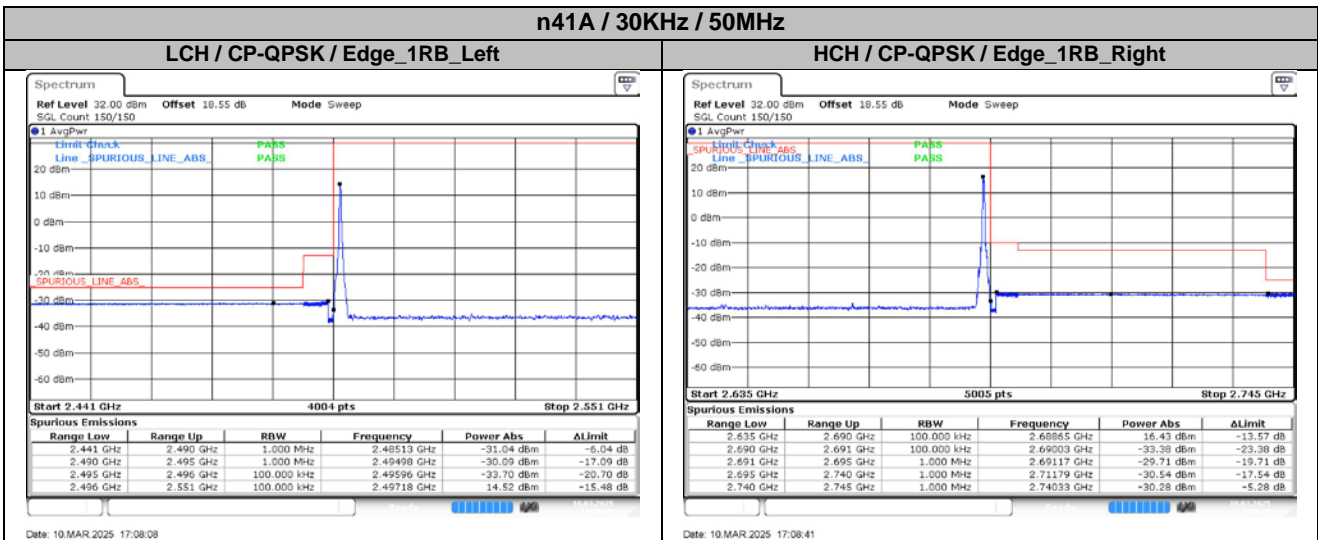
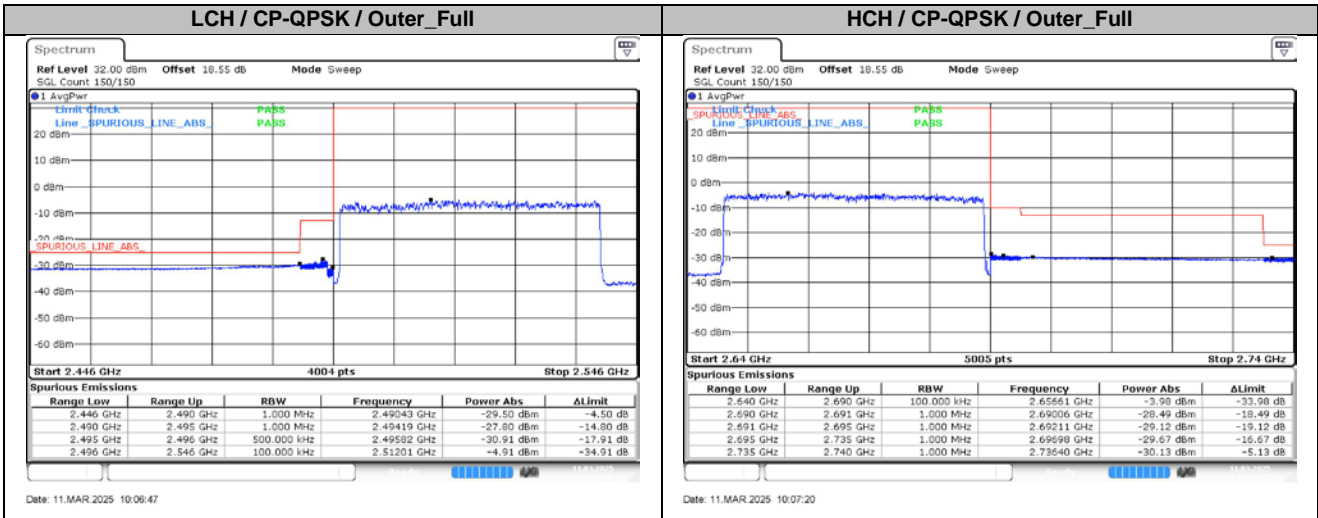


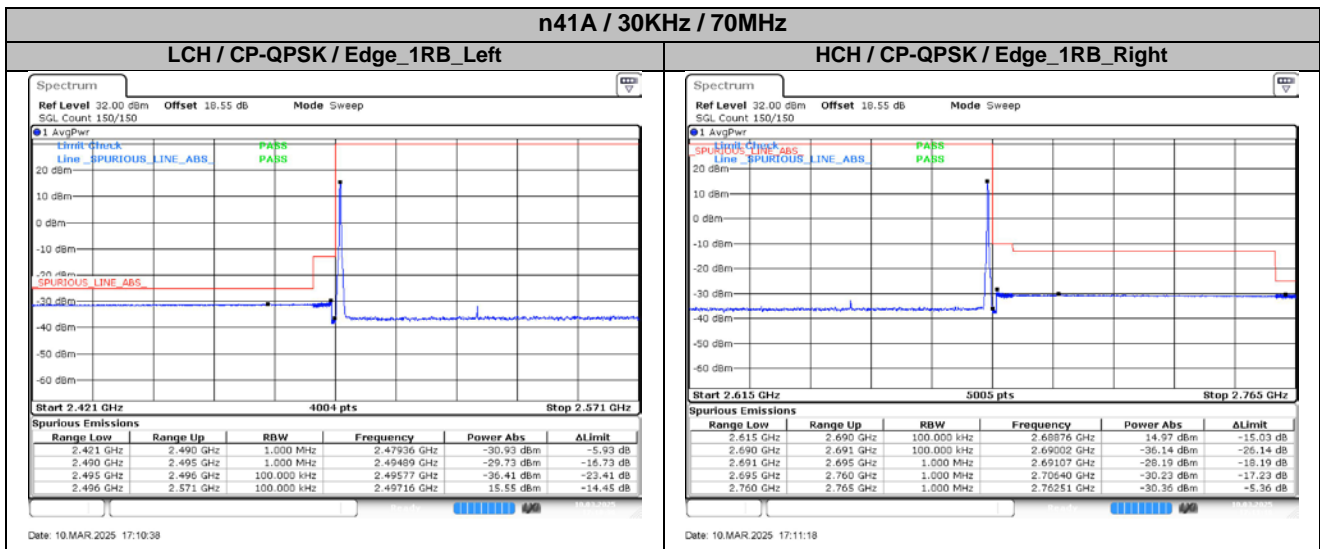
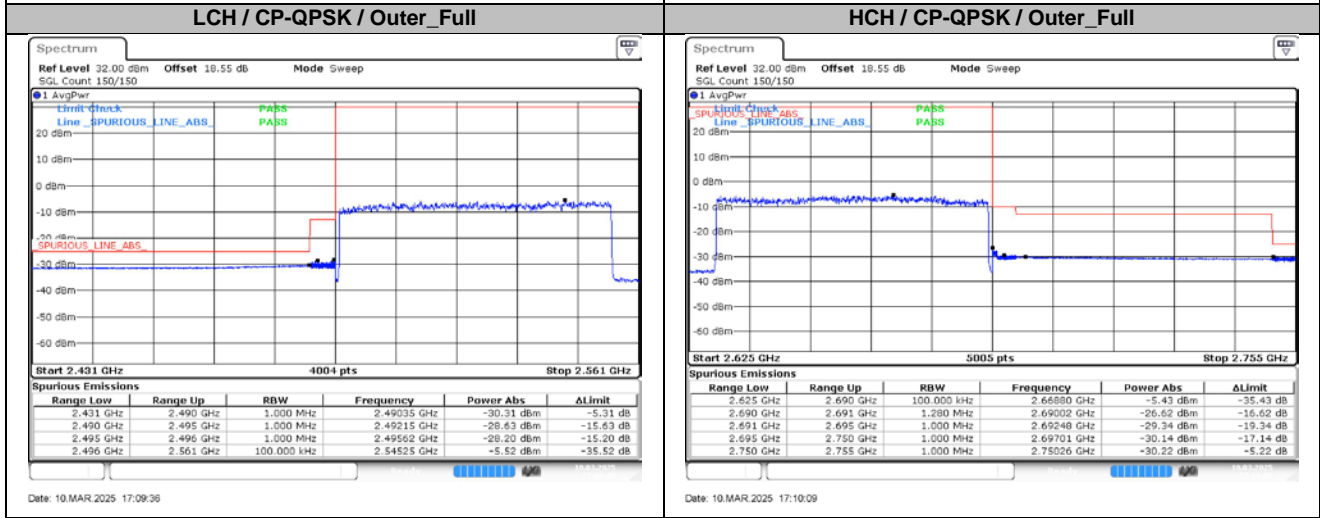
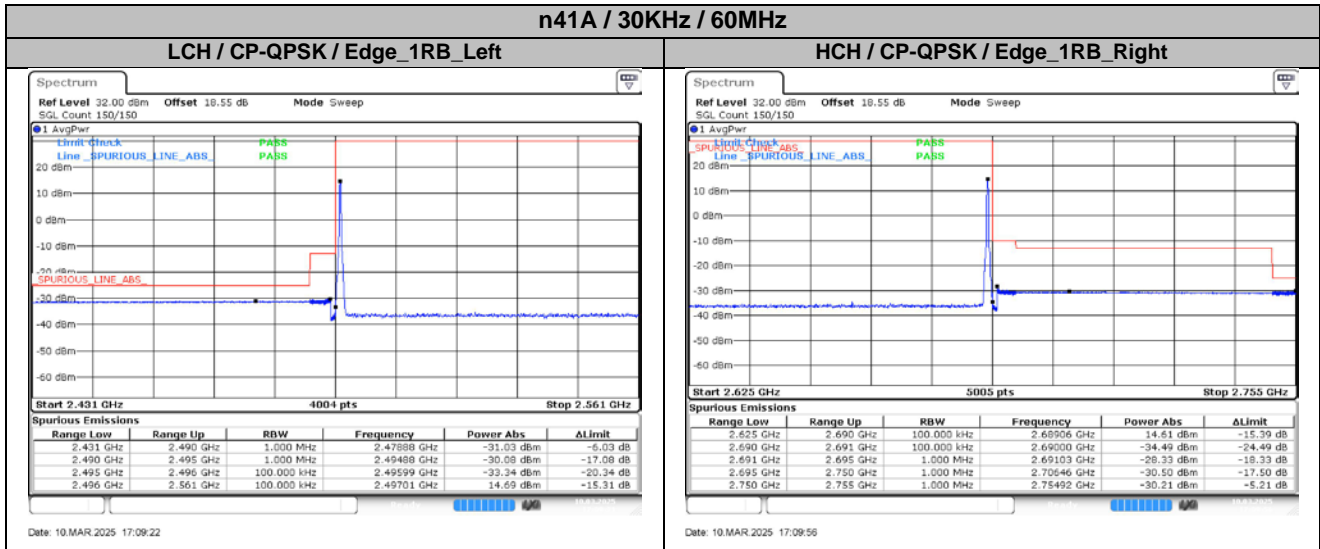
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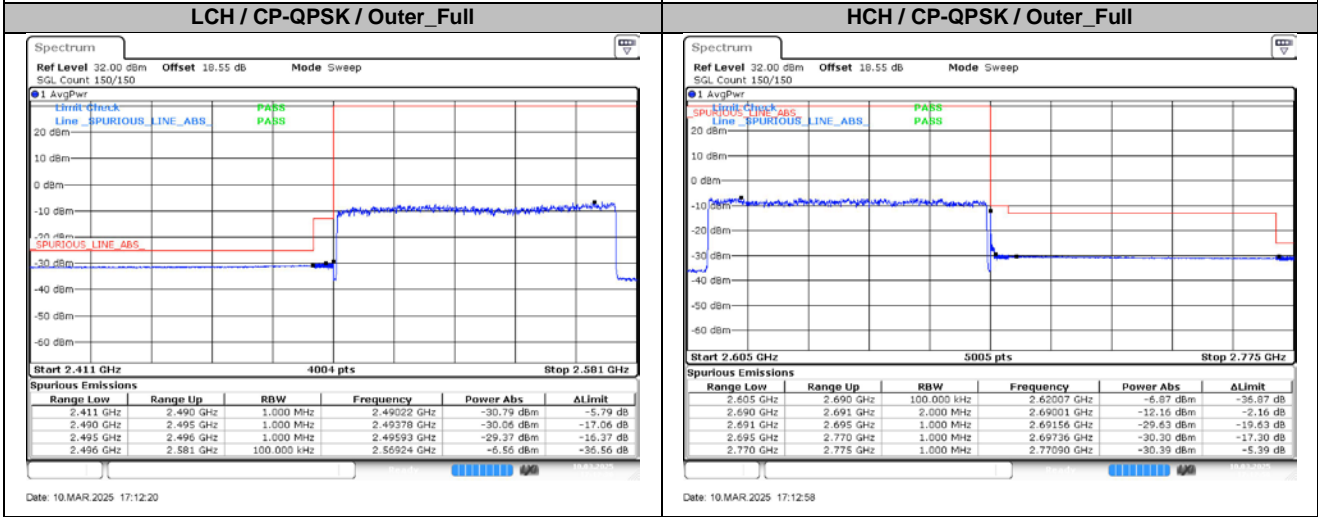
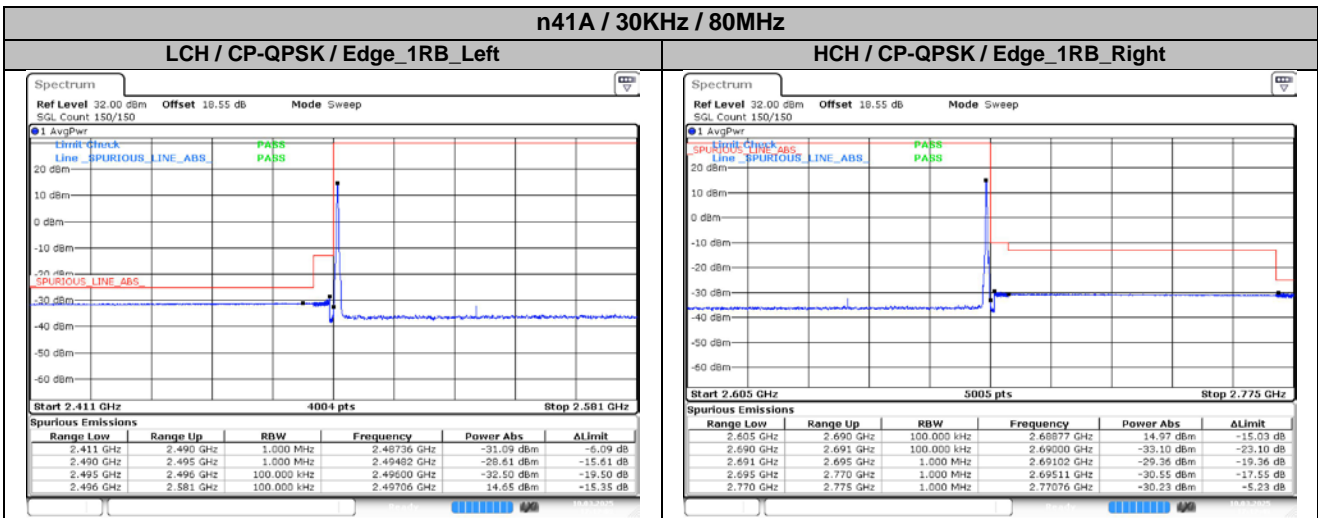
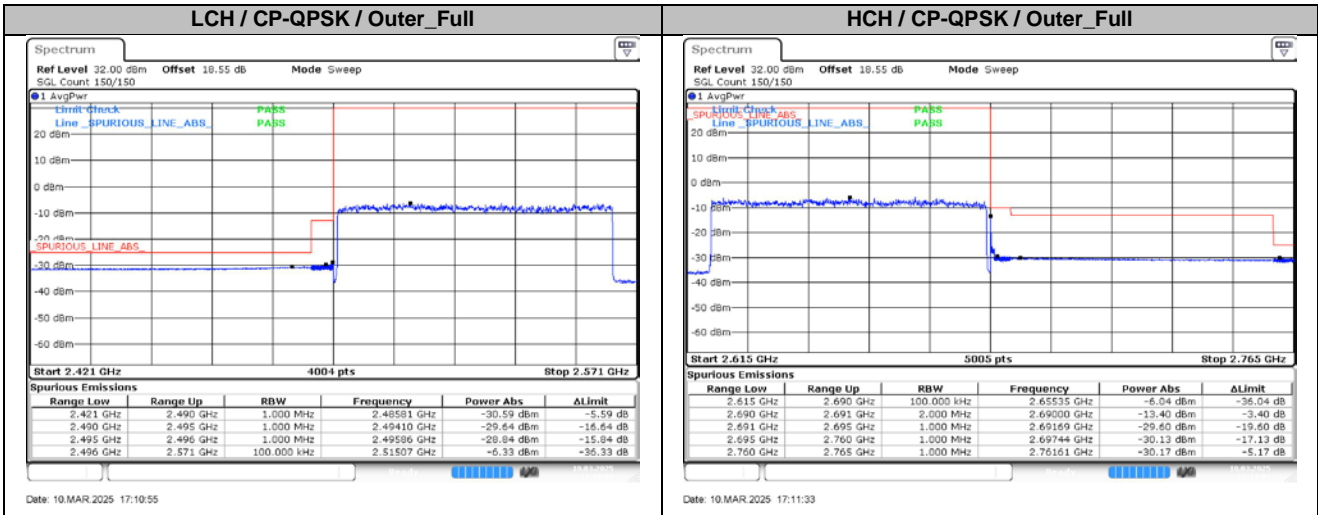
HCH / CP-QPSK / Edge_1RB_Right

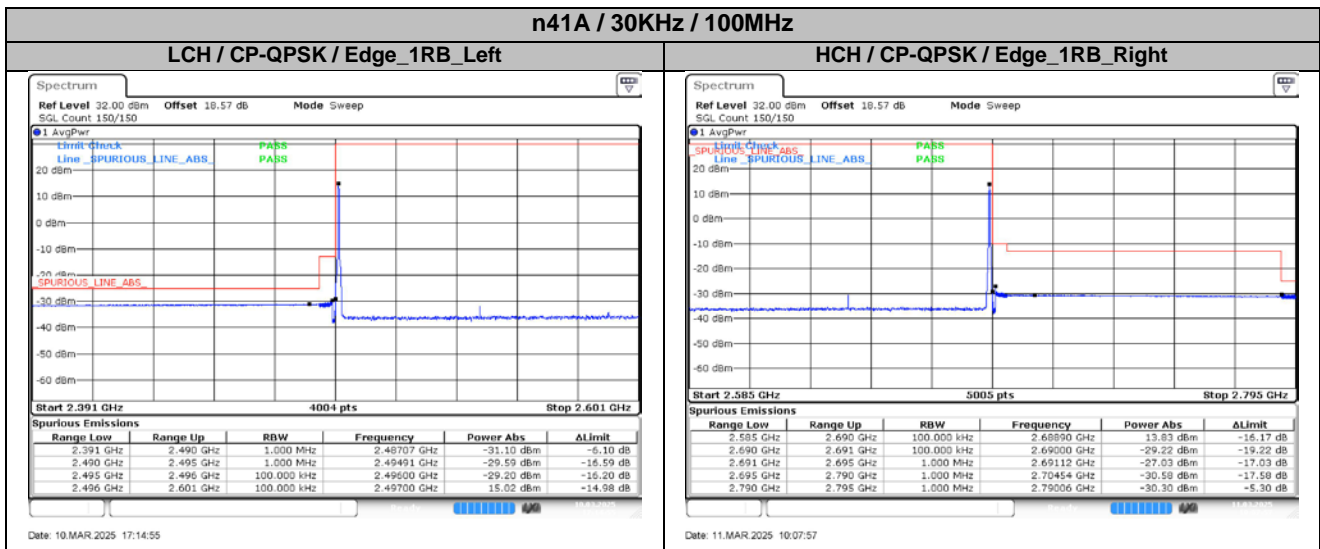
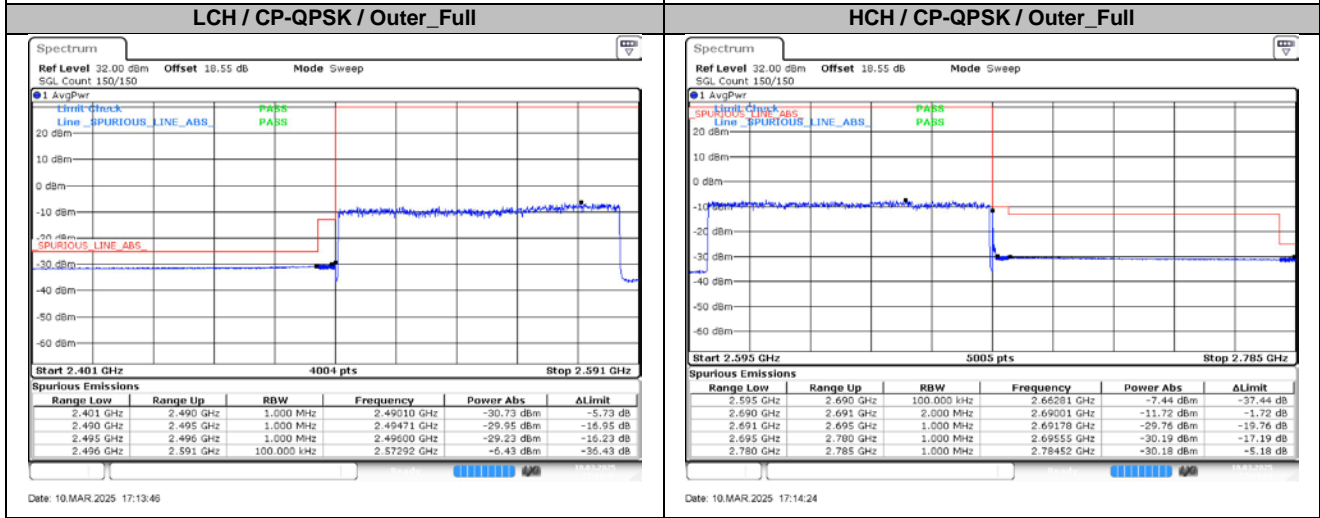
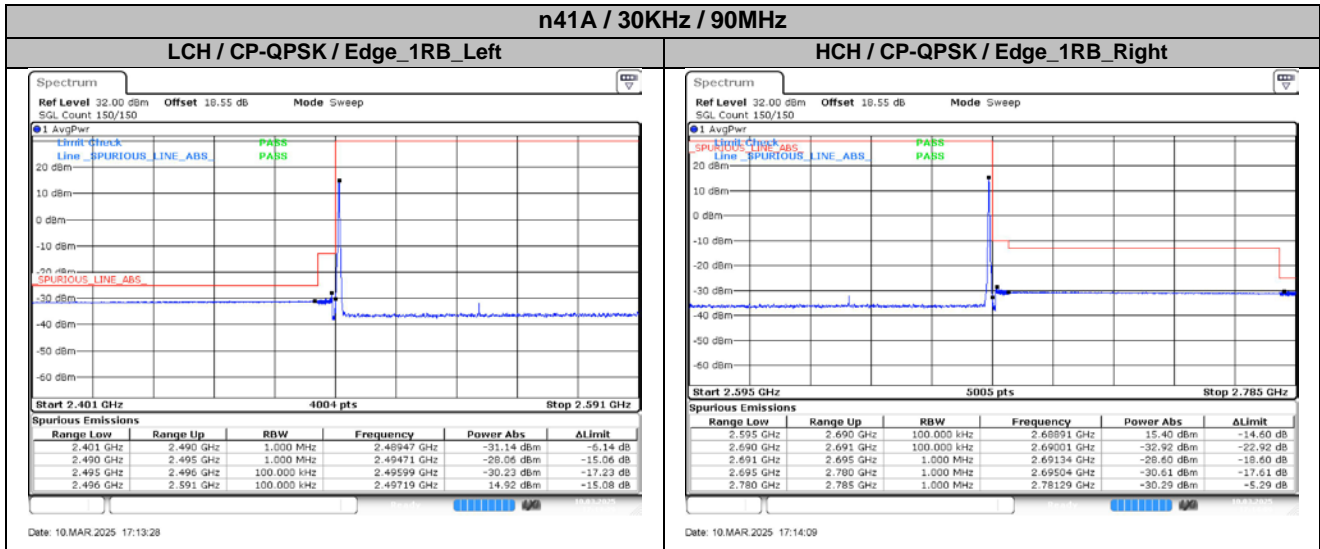


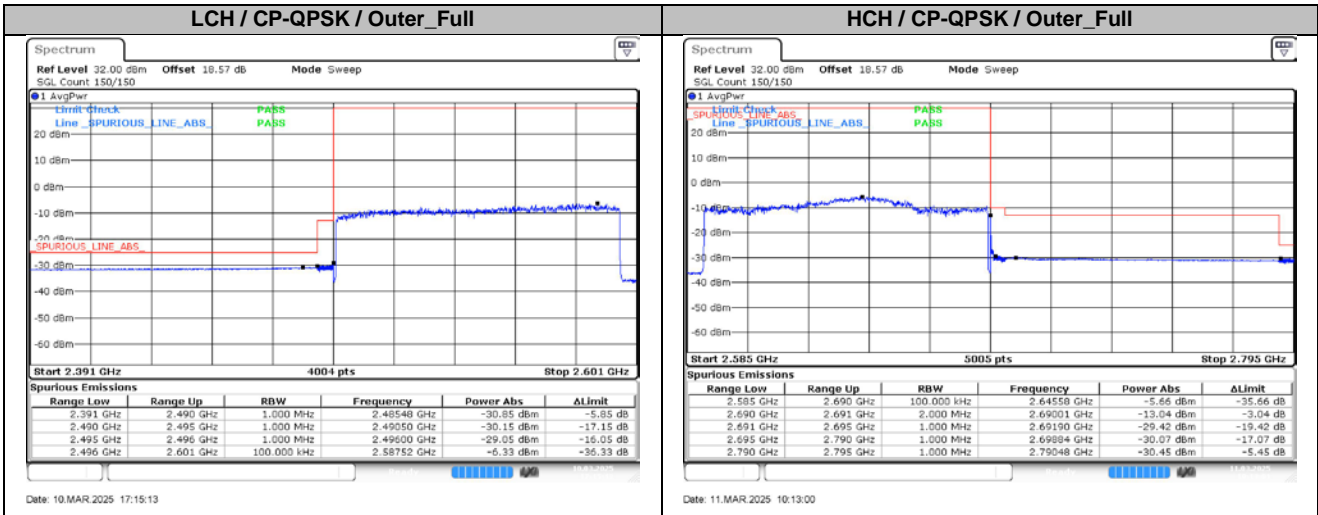
Date: 11.MAR.2025 10:07:07











6. Conducted Spurious Emission

6.1. Test Plots

