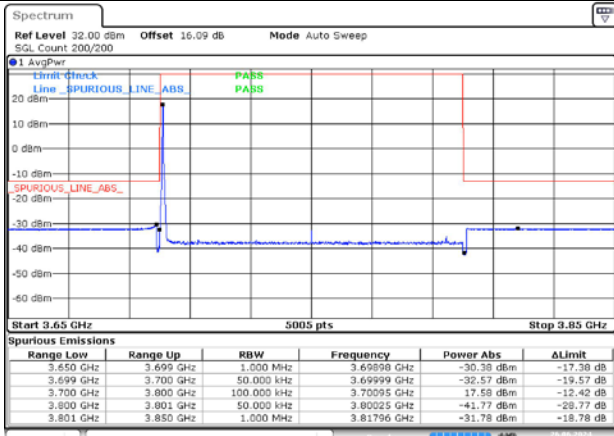


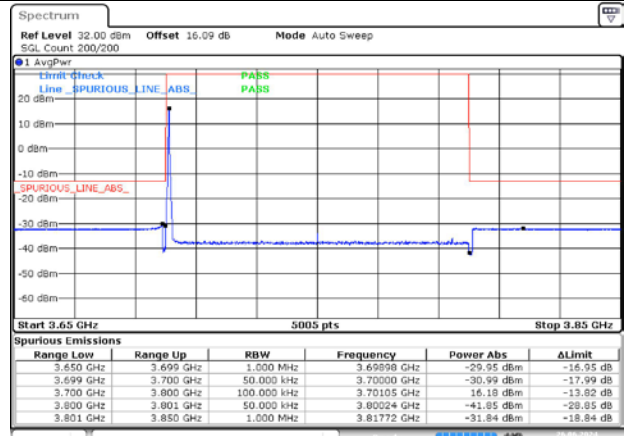
DC_41A_n78A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Edge_1RB_Left



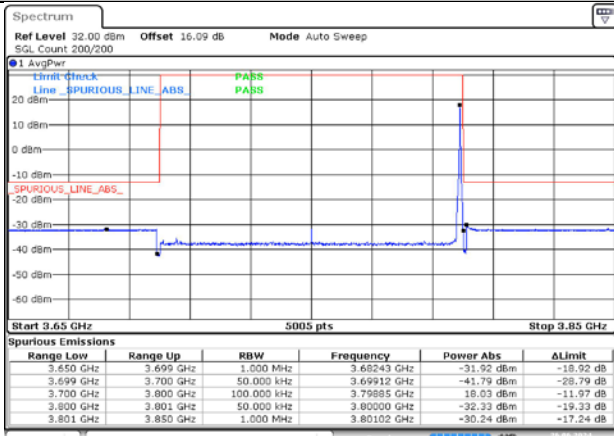
Date: 26 JUN 2024 01:03:29

MCH / DFT-QPSK / Edge_1RB_Left



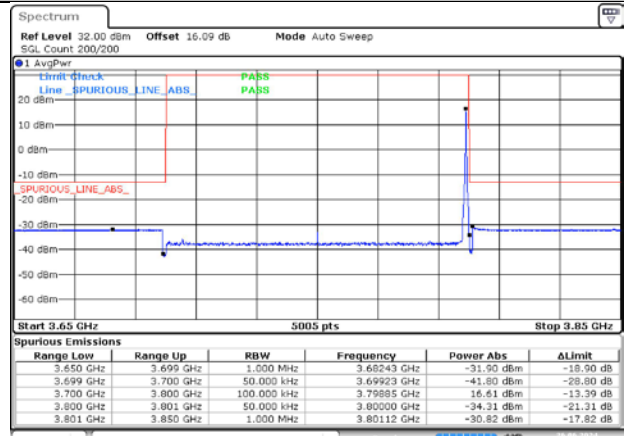
Date: 26 JUN 2024 01:05:31

MCH / DFT-Pi2BPSK / Edge_1RB_Right



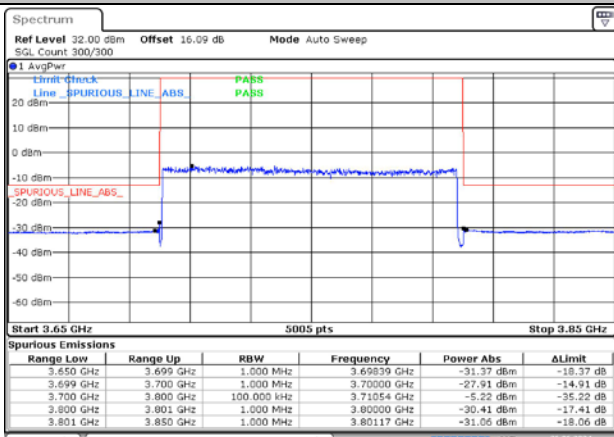
Date: 26 JUN 2024 01:04:19

MCH / DFT-QPSK / Edge_1RB_Right



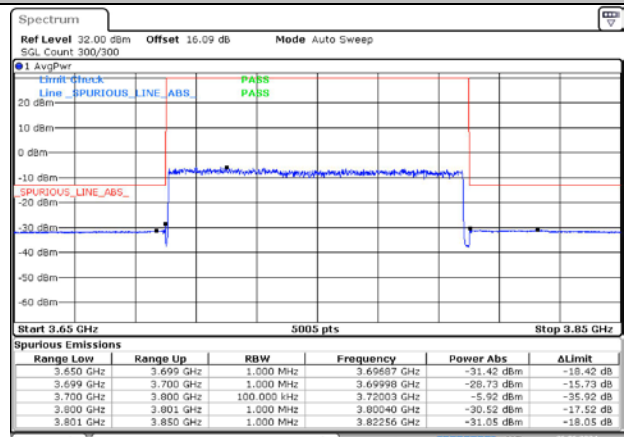
Date: 26 JUN 2024 01:06:20

MCH / DFT-Pi2BPSK / Outer_Full



Date: 26 JUN 2024 01:04:40

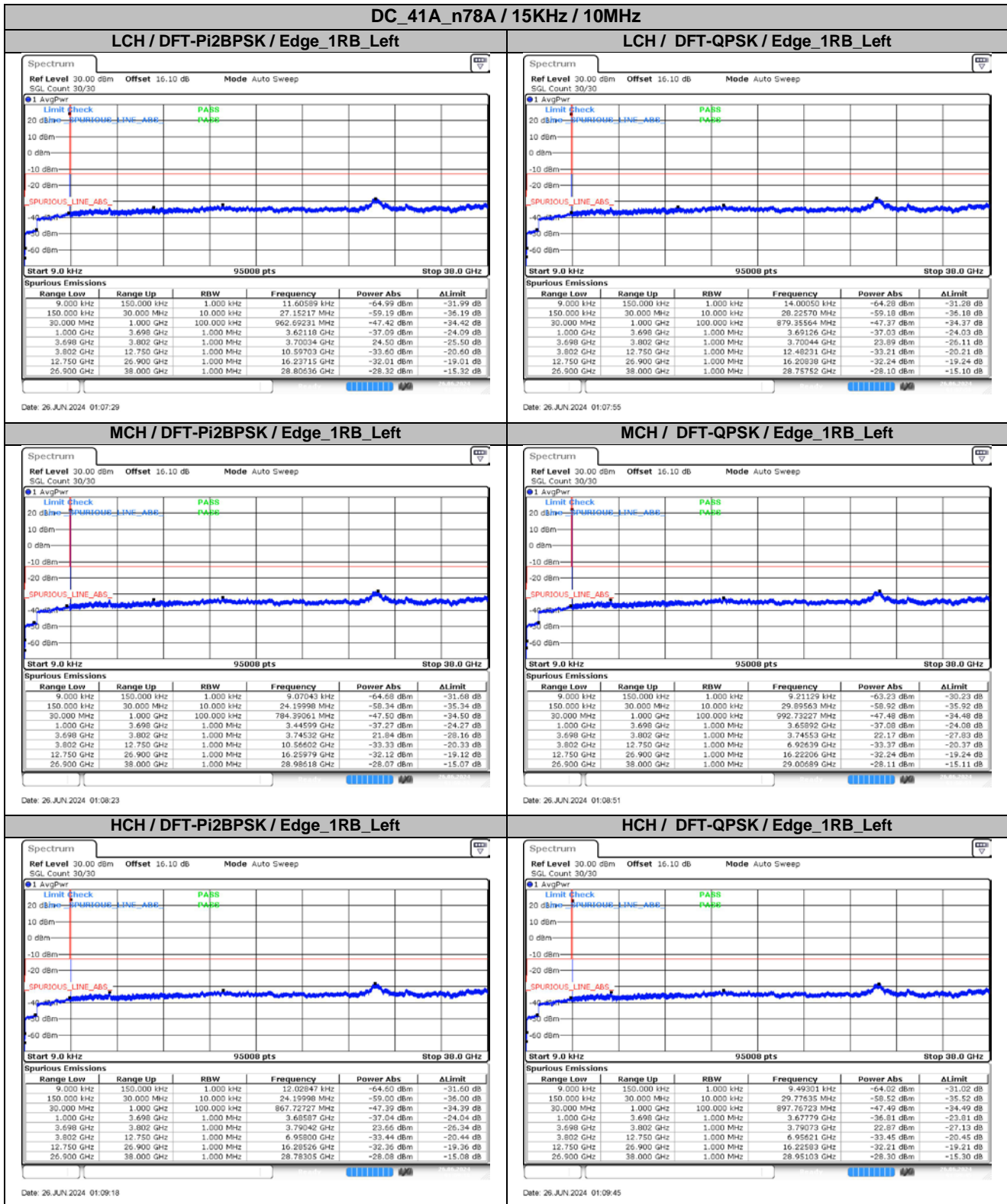
MCH / DFT-QPSK / Outer_Full



Date: 26 JUN 2024 01:06:41

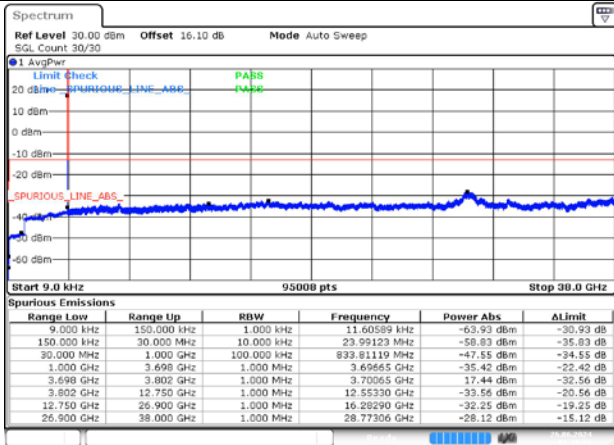
6. Conducted Spurious Emission

6.1. Test Plots



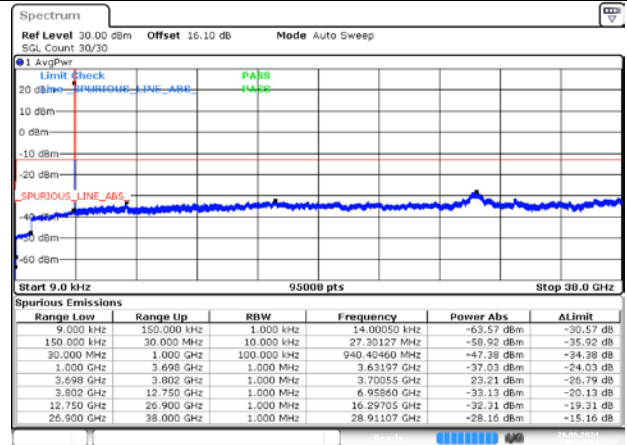
DC_41A_n78A / 15KHz / 15MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



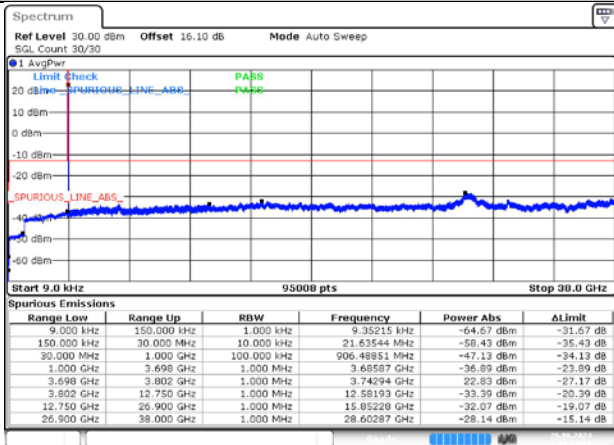
Date: 26 JUN 2024 01:10:28

LCH / DFT-QPSK / Edge_1RB_Left



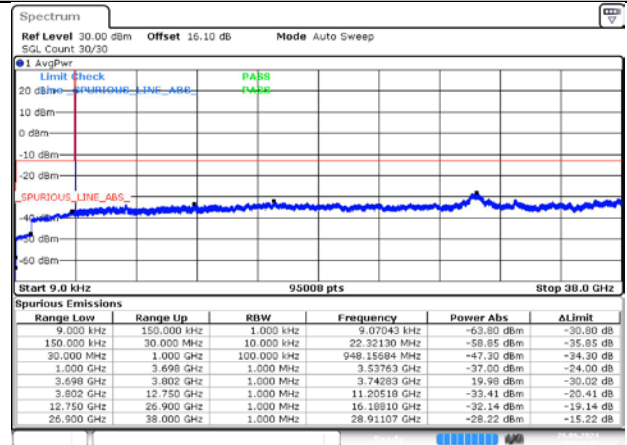
Date: 26 JUN 2024 01:10:53

MCH / DFT-Pi2BPSK / Edge_1RB_Left



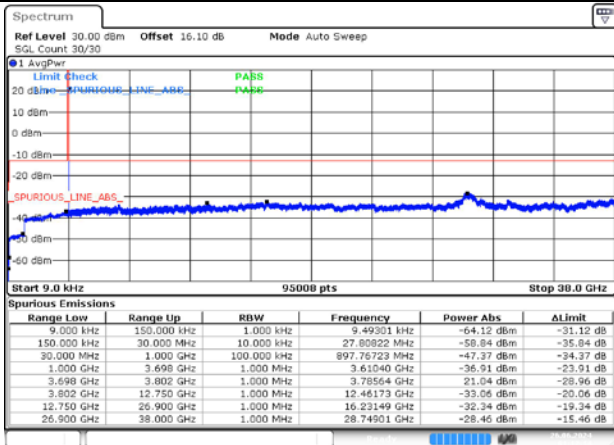
Date: 26 JUN 2024 01:11:20

MCH / DFT-QPSK / Edge_1RB_Left



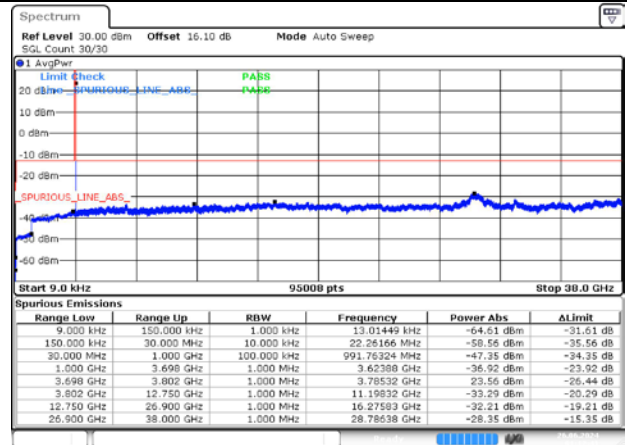
Date: 26 JUN 2024 01:11:47

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:12:14

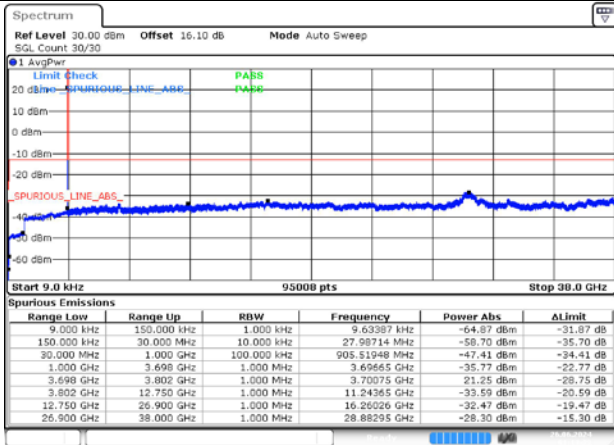
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:12:41

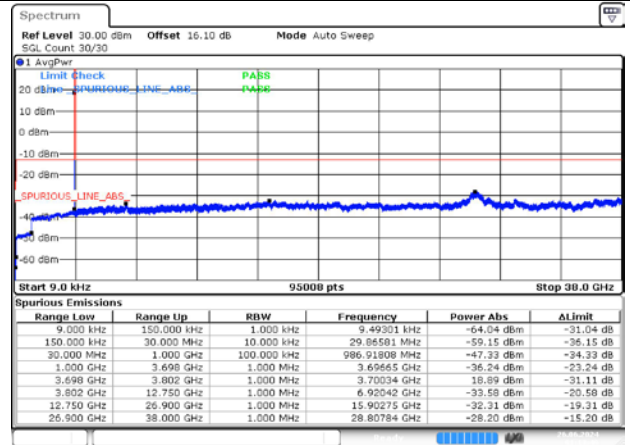
DC_41A_n78A / 15KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



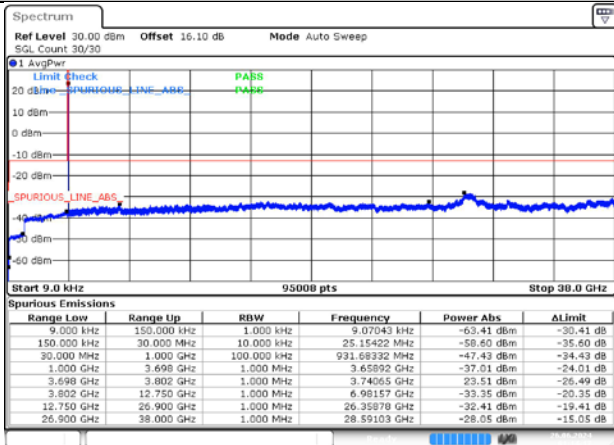
Date: 26 JUN 2024 01:13:22

LCH / DFT-QPSK / Edge_1RB_Left



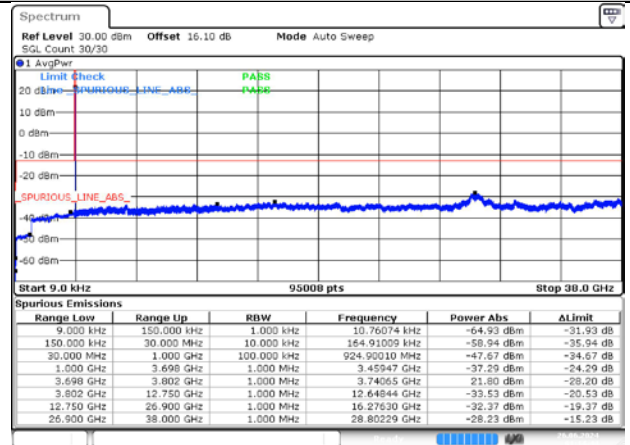
Date: 26 JUN 2024 01:13:49

MCH / DFT-Pi2BPSK / Edge_1RB_Left



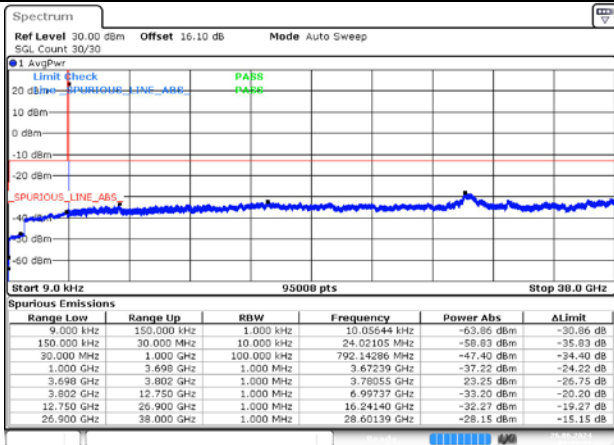
Date: 26 JUN 2024 01:14:17

MCH / DFT-QPSK / Edge_1RB_Left



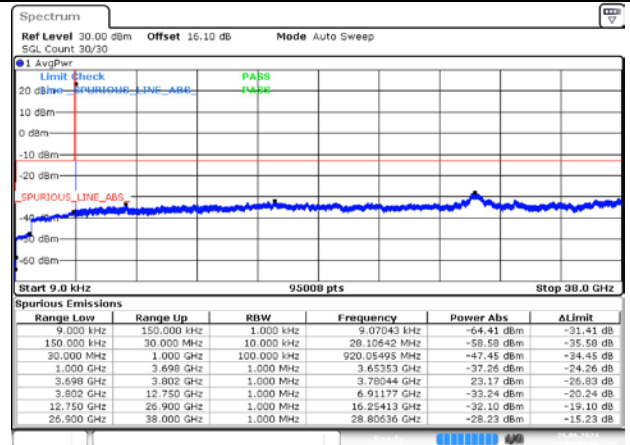
Date: 26 JUN 2024 01:14:44

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:15:11

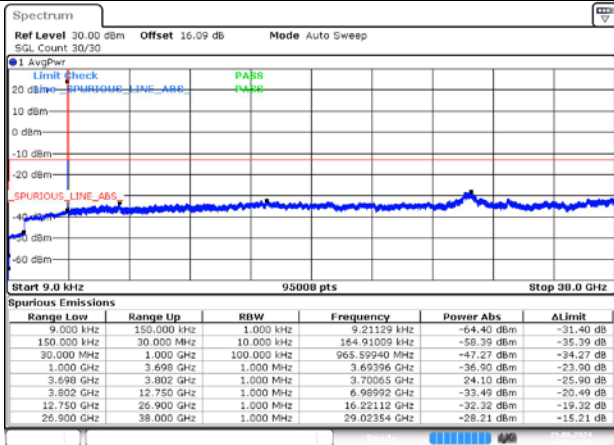
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:15:38

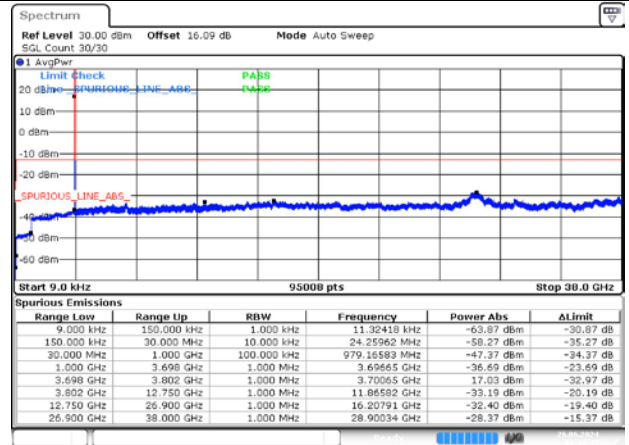
DC_41A_n78A / 15KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



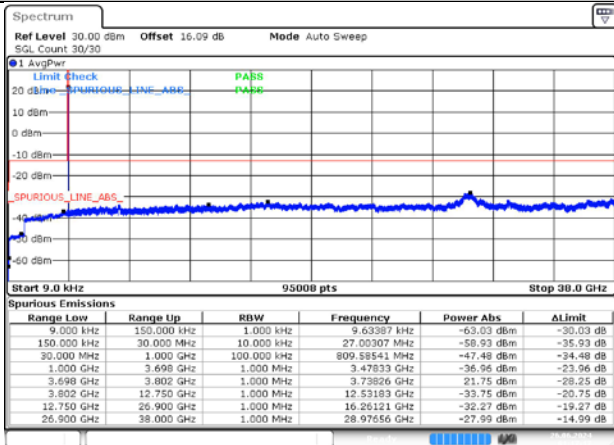
Date: 26 JUN 2024 01:16:20

LCH / DFT-QPSK / Edge_1RB_Left



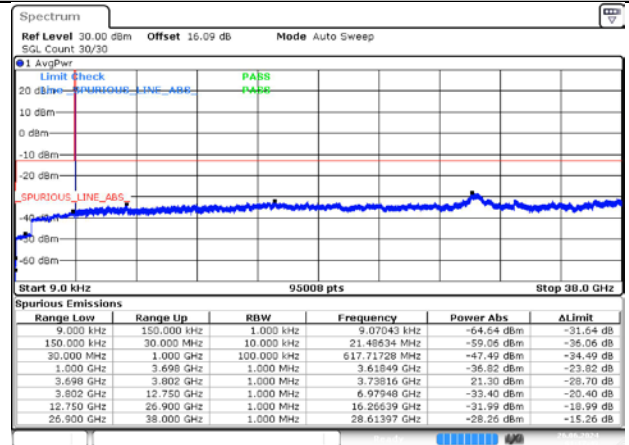
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MCH / DFT-Pi2BPSK / Edge_1RB_Left



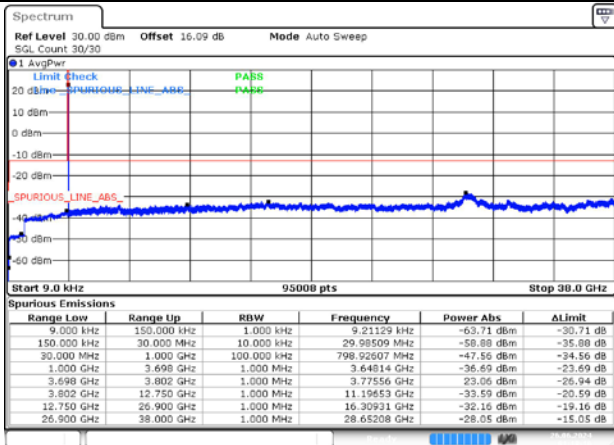
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MCH / DFT-QPSK / Edge_1RB_Left



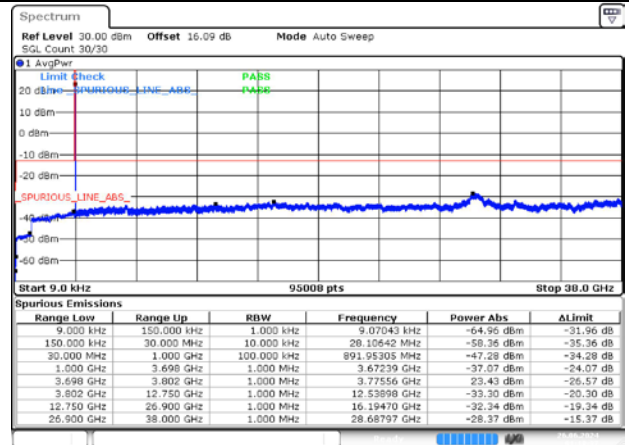
Date: 26 JUN 2024 01:17:42

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:18:10

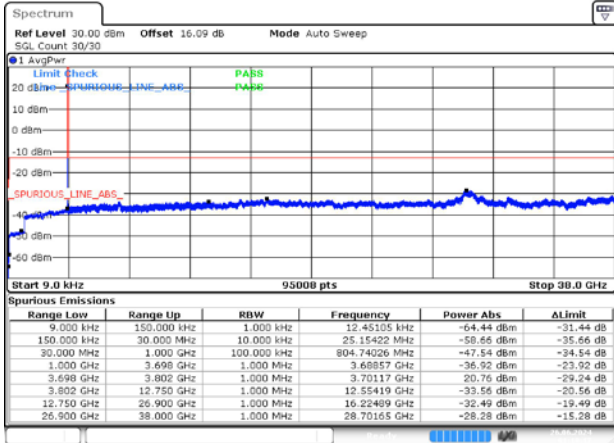
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:18:36

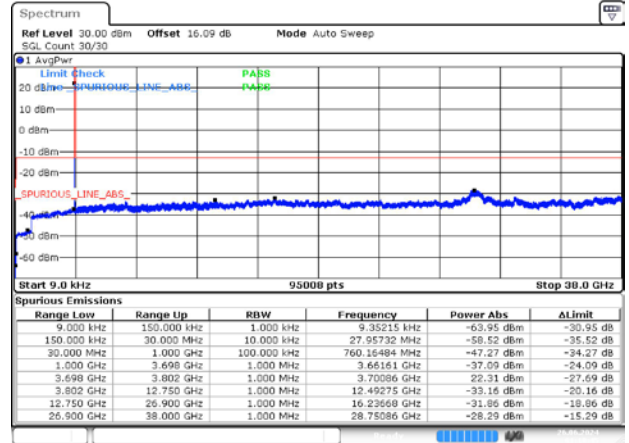
DC_41A_n78A / 15KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



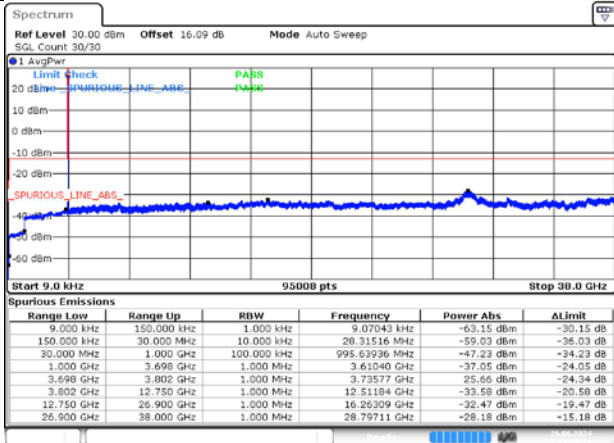
Date: 26 JUN 2024 01:19:17

LCH / DFT-QPSK / Edge_1RB_Left



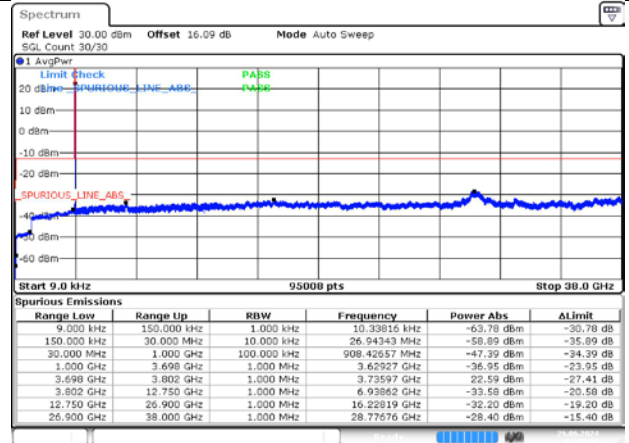
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MCH / DFT-Pi2BPSK / Edge_1RB_Left



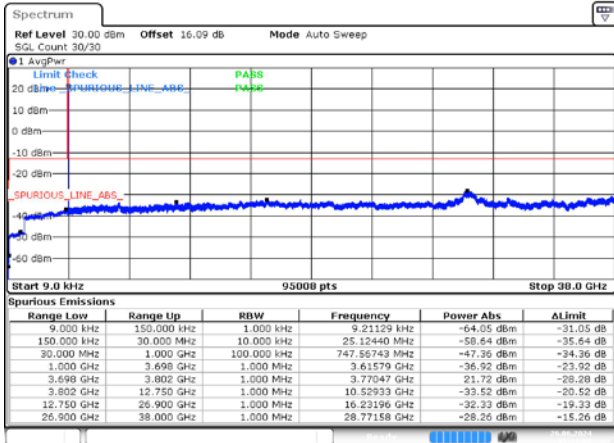
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MCH / DFT-QPSK / Edge_1RB_Left



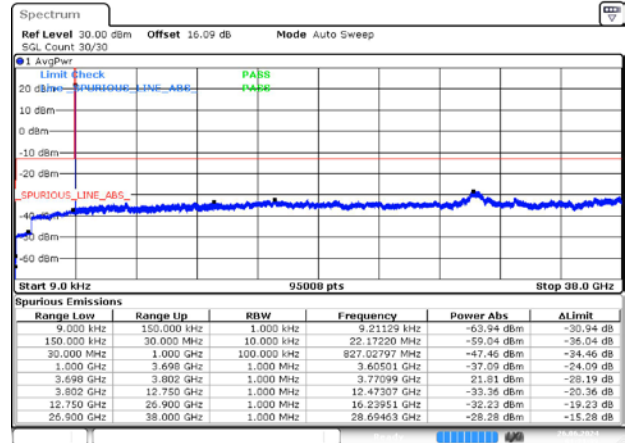
Date: 26 JUN 2024 01:20:38

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:21:06

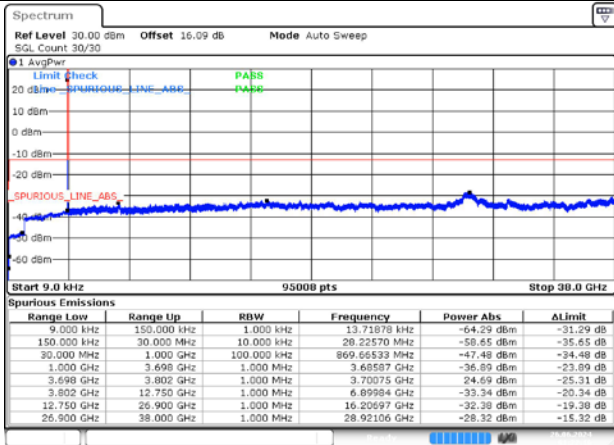
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:21:33

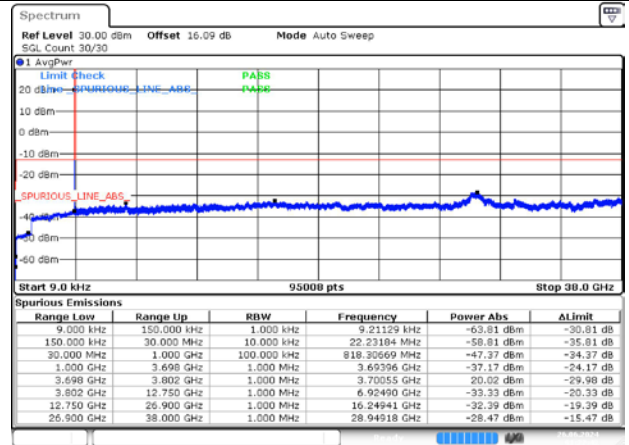
DC_41A_n78A / 15KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



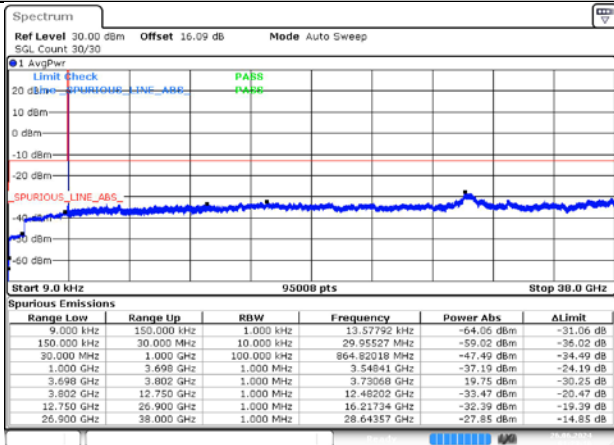
Date: 26 JUN 2024 01:22:15

LCH / DFT-QPSK / Edge_1RB_Left



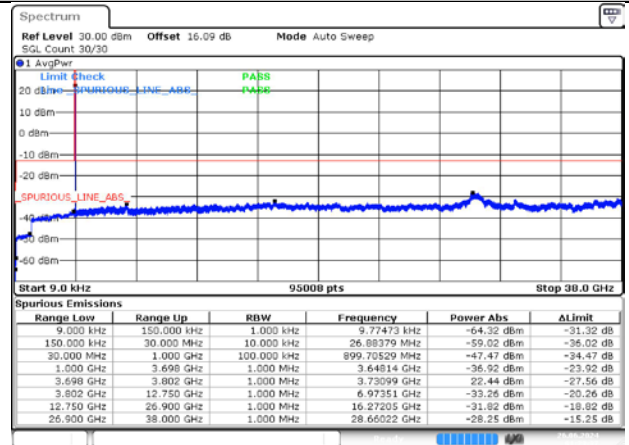
Date: 26 JUN 2024 01:22:42

MCH / DFT-Pi2BPSK / Edge_1RB_Left



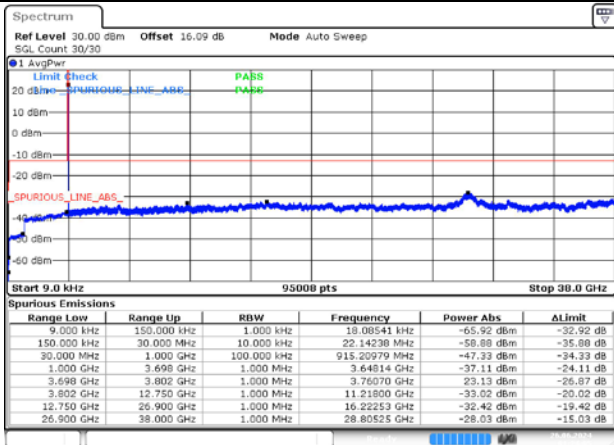
Date: 26 JUN 2024 01:23:10

MCH / DFT-QPSK / Edge_1RB_Left



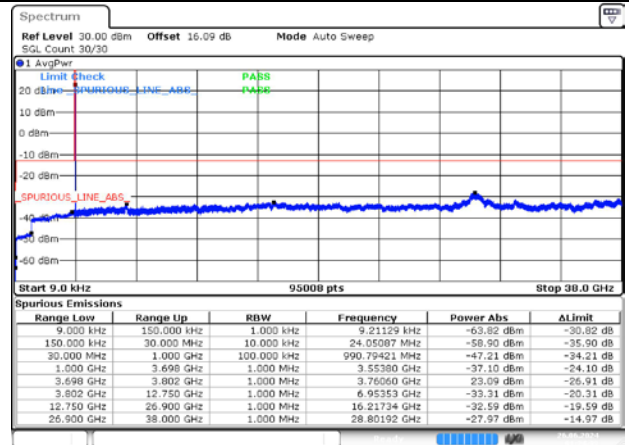
Date: 26 JUN 2024 01:23:36

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:24:05

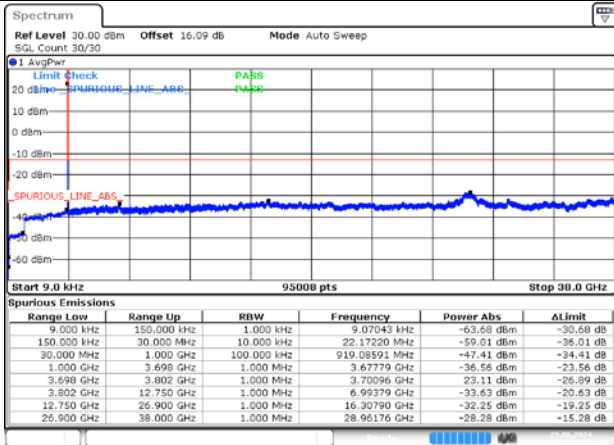
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:24:31

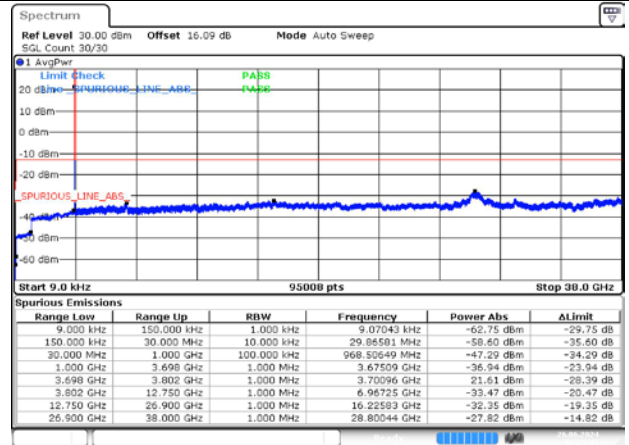
DC_41A_n78A / 15KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



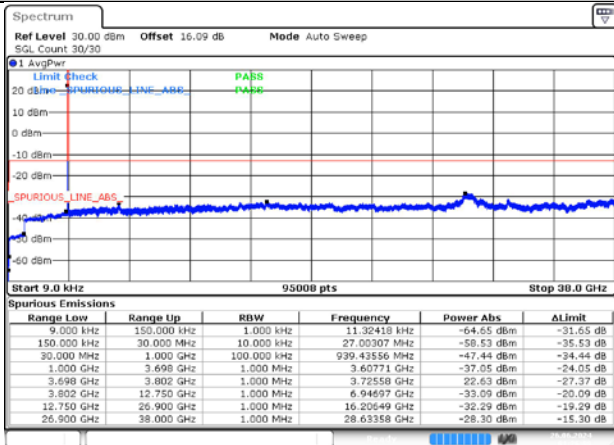
Date: 26 JUN 2024 01:25:13

LCH / DFT-QPSK / Edge_1RB_Left



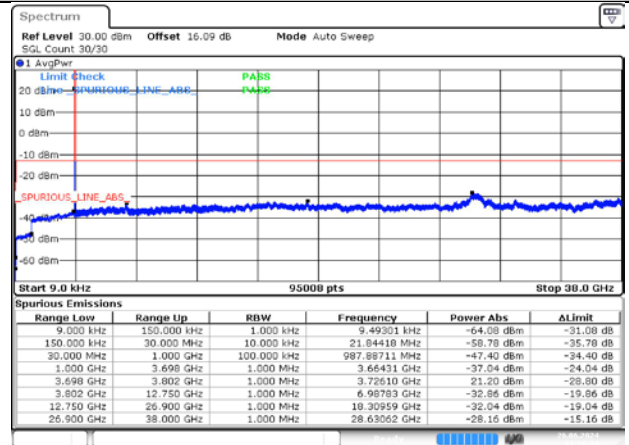
Date: 26 JUN 2024 01:25:40

MCH / DFT-Pi2BPSK / Edge_1RB_Left



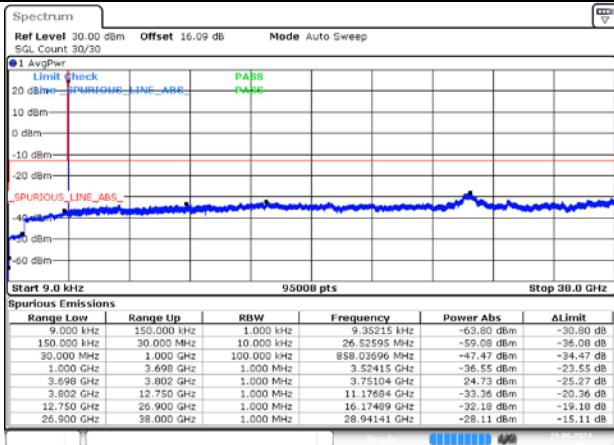
Date: 26 JUN 2024 01:26:07

MCH / DFT-QPSK / Edge_1RB_Left



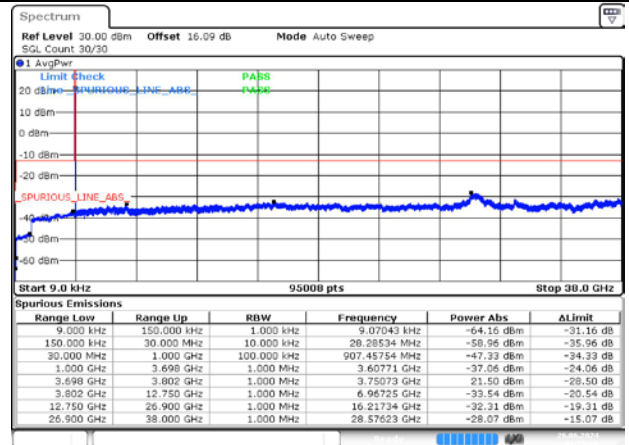
Date: 26 JUN 2024 01:26:34

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:27:01

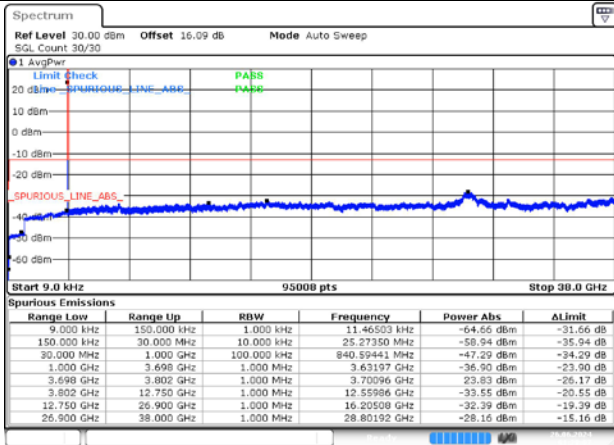
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:27:28

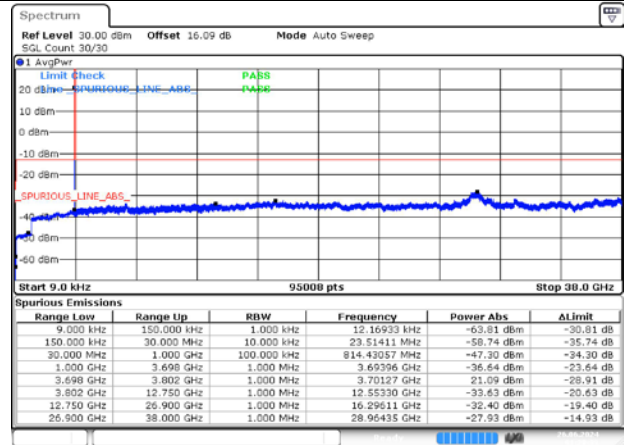
DC_41A_n78A / 30KHz / 10MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



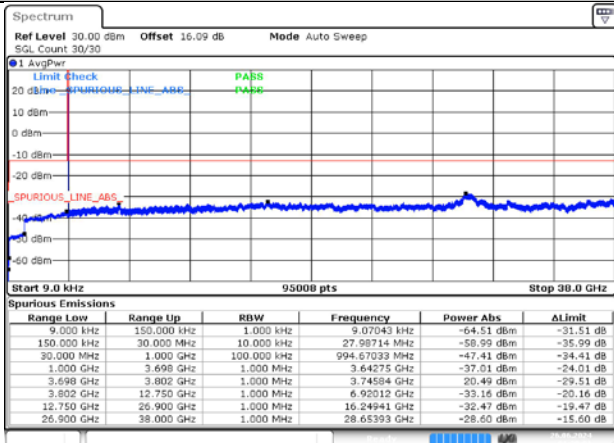
Date: 26 JUN 2024 01:28:09

LCH / DFT-QPSK / Edge_1RB_Left



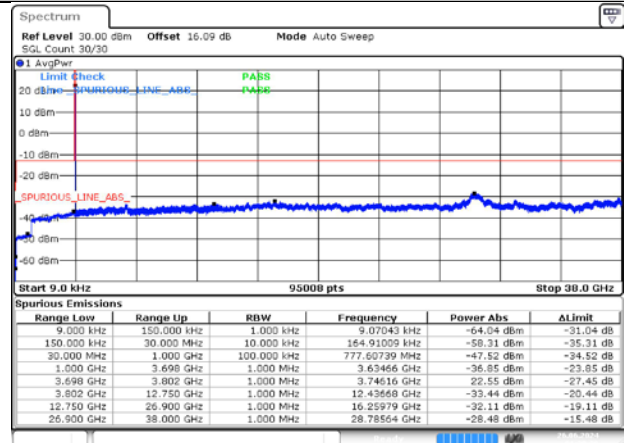
Date: 26 JUN 2024 01:28:33

MCH / DFT-Pi2BPSK / Edge_1RB_Left



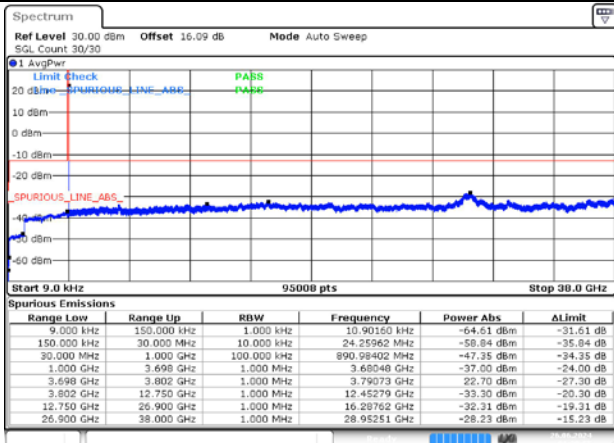
Date: 26 JUN 2024 01:29:00

MCH / DFT-QPSK / Edge_1RB_Left



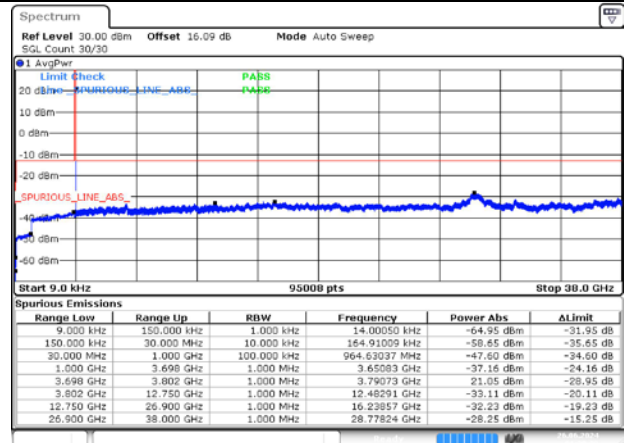
Date: 26 JUN 2024 01:29:27

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:29:55

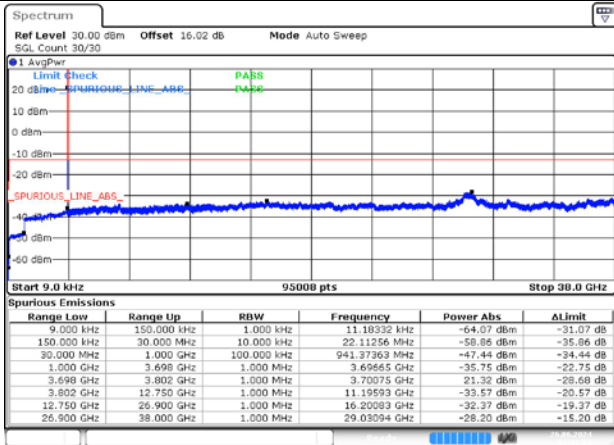
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:30:23

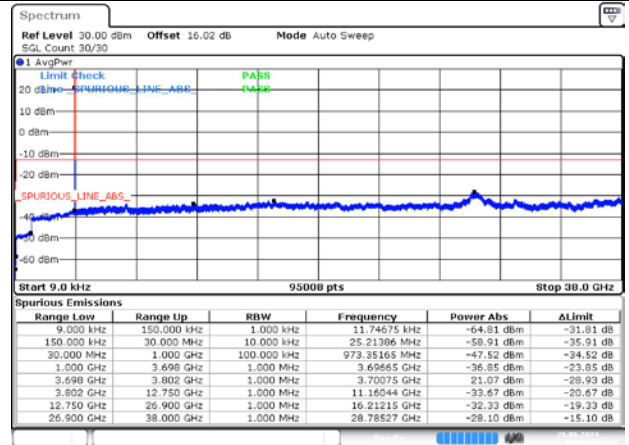
DC_41A_n78A / 30KHz / 15MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



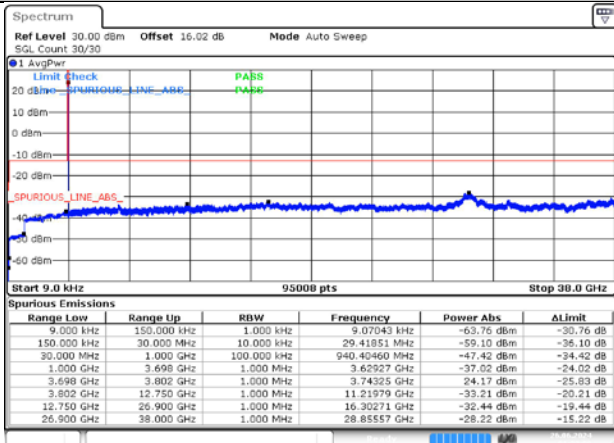
Date: 26 JUN 2024 01:31:04

LCH / DFT-QPSK / Edge_1RB_Left



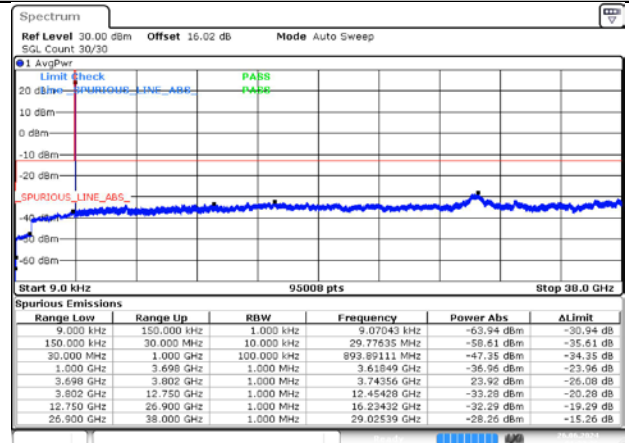
Date: 26 JUN 2024 01:31:31

MCH / DFT-Pi2BPSK / Edge_1RB_Left



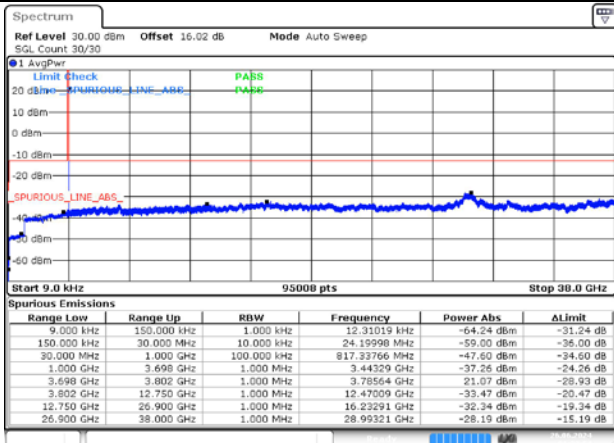
Date: 26 JUN 2024 01:31:59

MCH / DFT-QPSK / Edge_1RB_Left



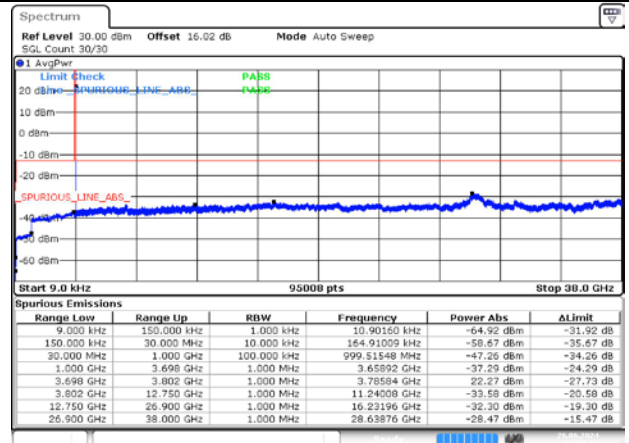
Date: 26 JUN 2024 01:32:25

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:32:53

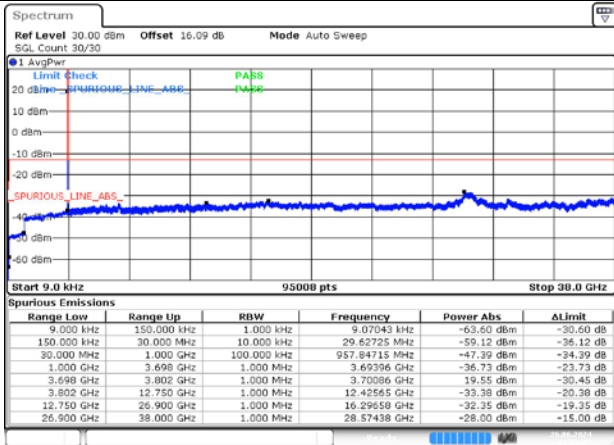
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:33:19

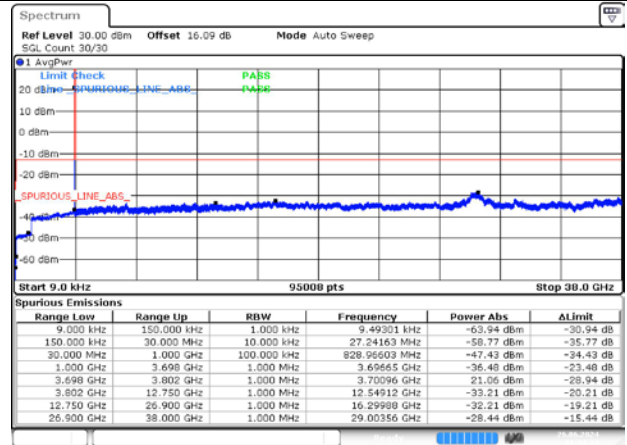
DC_41A_n78A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



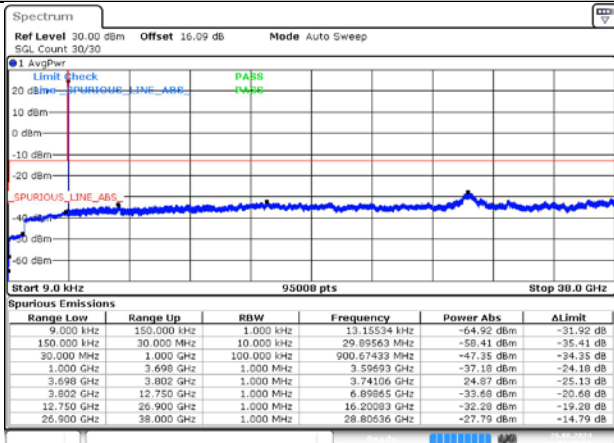
Date: 26 JUN 2024 01:34:04

LCH / DFT-QPSK / Edge_1RB_Left



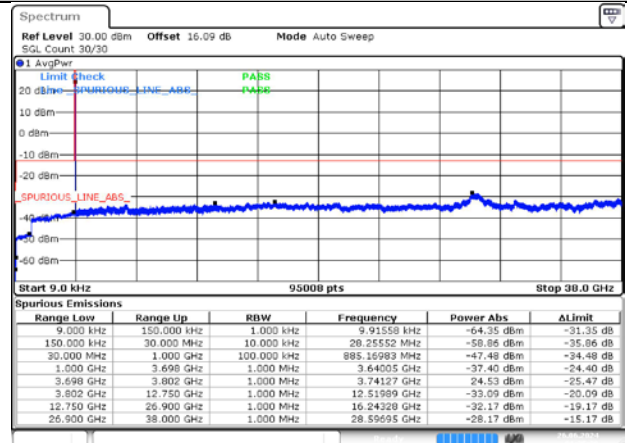
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MCH / DFT-Pi2BPSK / Edge_1RB_Left



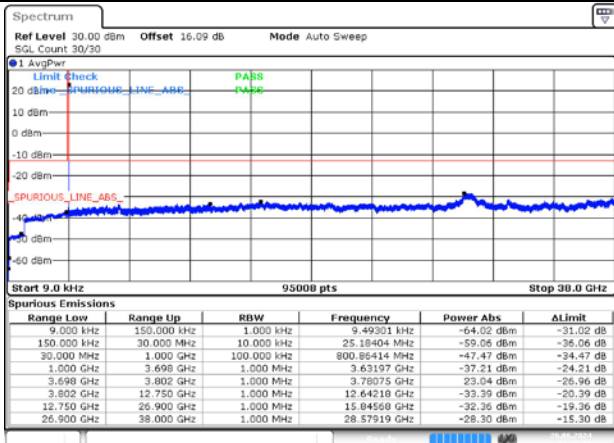
Date: 26 JUN 2024 01:35:01

MCH / DFT-QPSK / Edge_1RB_Left



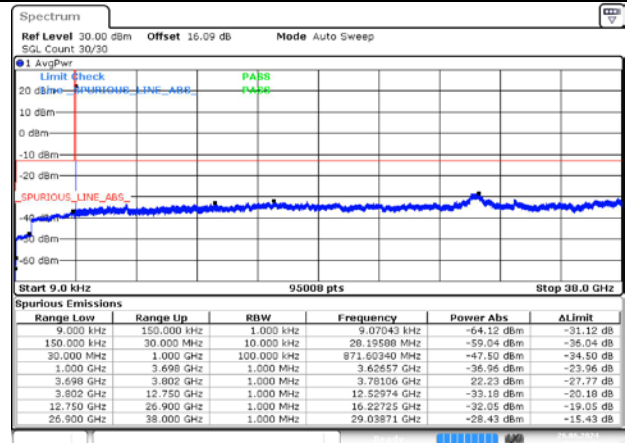
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HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:35:55

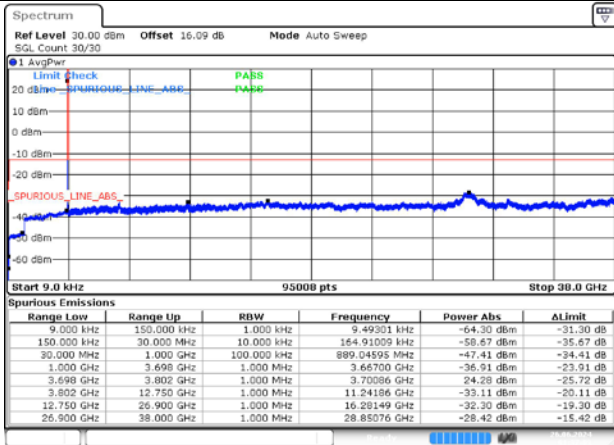
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:36:22

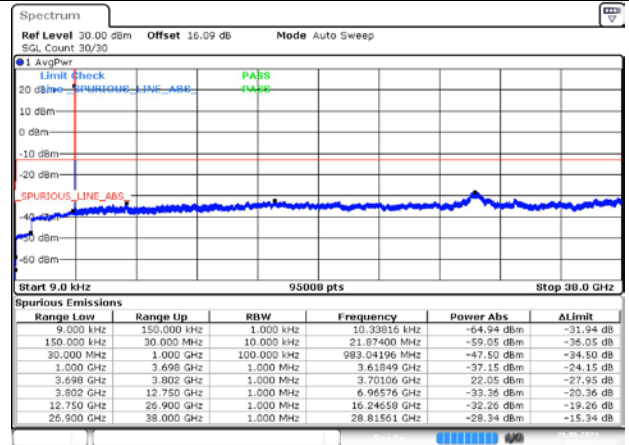
DC_41A_n78A / 30KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



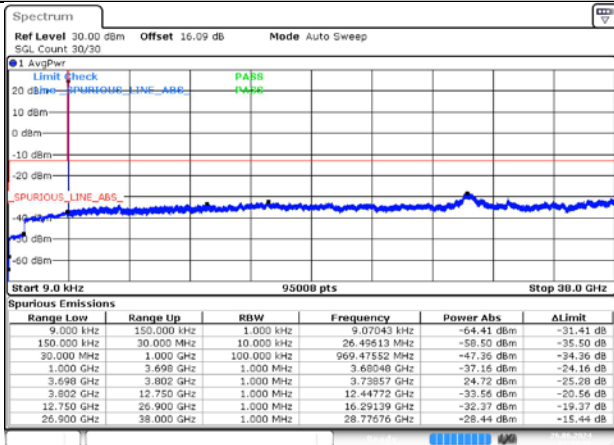
Date: 26 JUN 2024 01:37:03

LCH / DFT-QPSK / Edge_1RB_Left



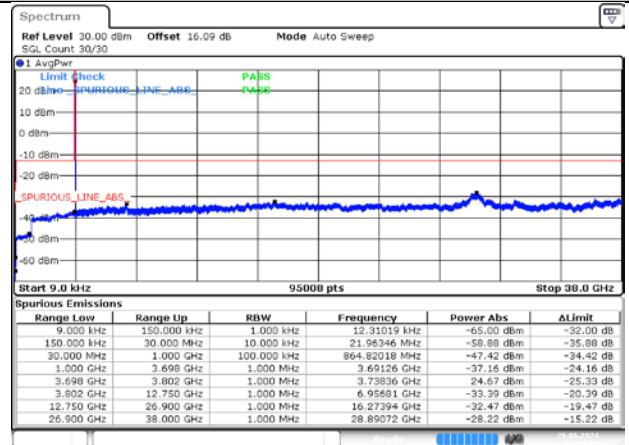
Date: 26 JUN 2024 01:37:29

MCH / DFT-Pi2BPSK / Edge_1RB_Left



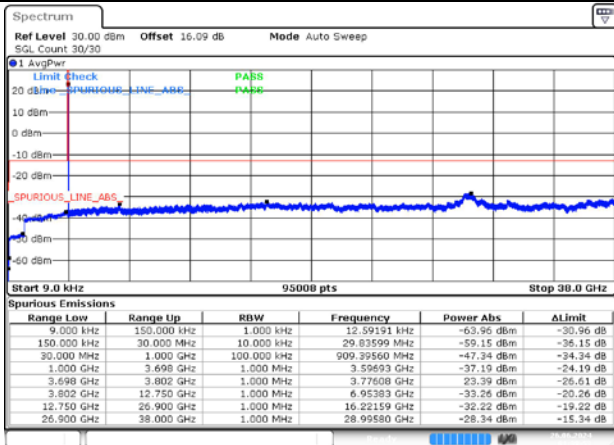
Date: 26 JUN 2024 01:37:57

MCH / DFT-QPSK / Edge_1RB_Left



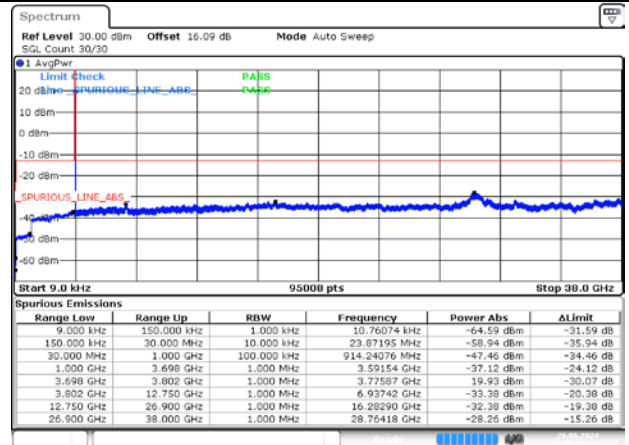
Date: 26 JUN 2024 01:38:25

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:38:52

HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 01:39:19