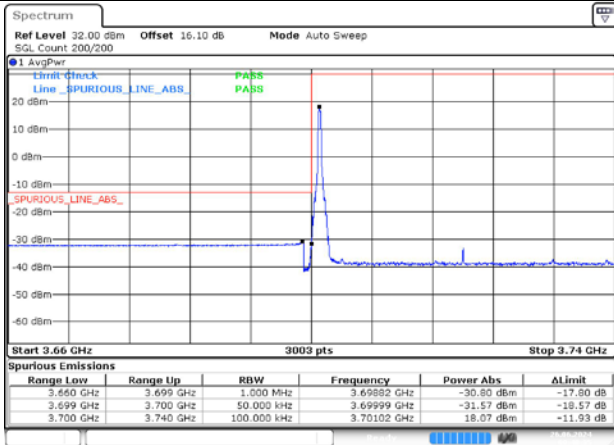


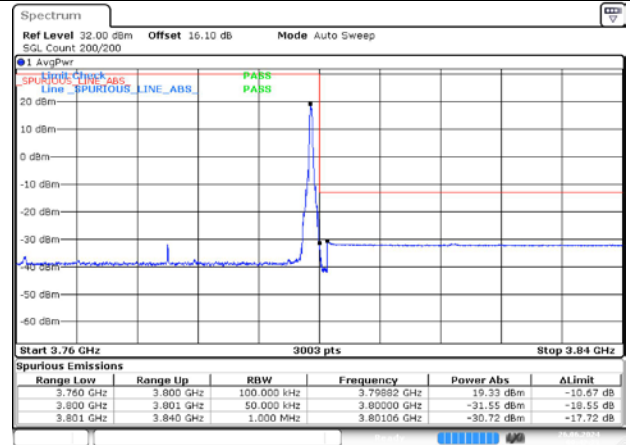
DC\_41A\_n78A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



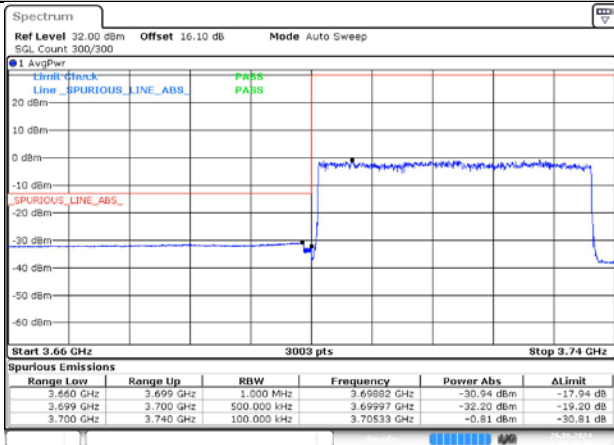
Date: 26 JUN 2024 00:49:35

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



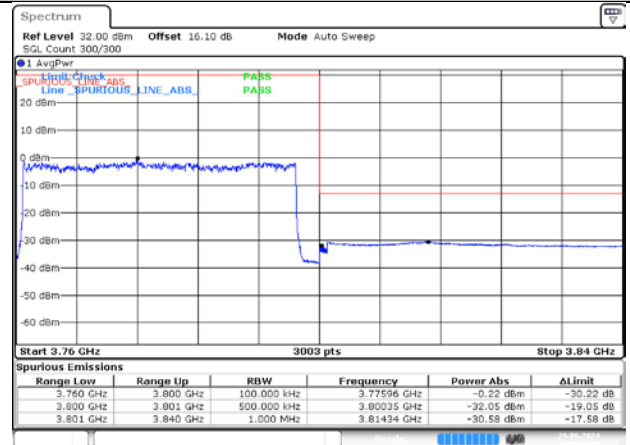
Date: 26 JUN 2024 00:42:22

LCH / DFT-Pi2BPSK / Outer\_Full



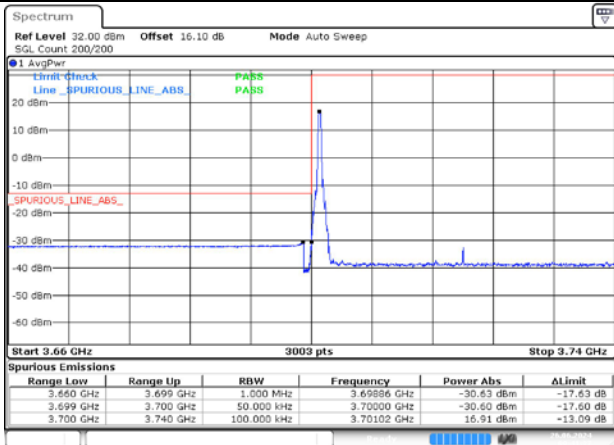
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HCH / DFT-Pi2BPSK / Outer\_Full



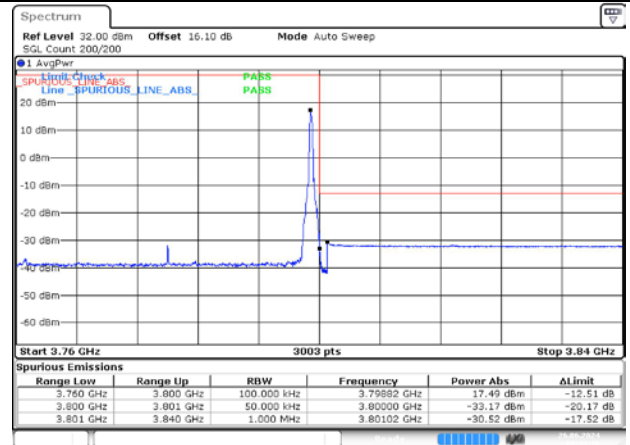
Date: 26 JUN 2024 00:42:41

LCH / DFT-QPSK / Edge\_1RB\_Left



Date: 26 JUN 2024 00:41:27

HCH / DFT-QPSK / Edge\_1RB\_Right



Date: 26 JUN 2024 00:43:17

