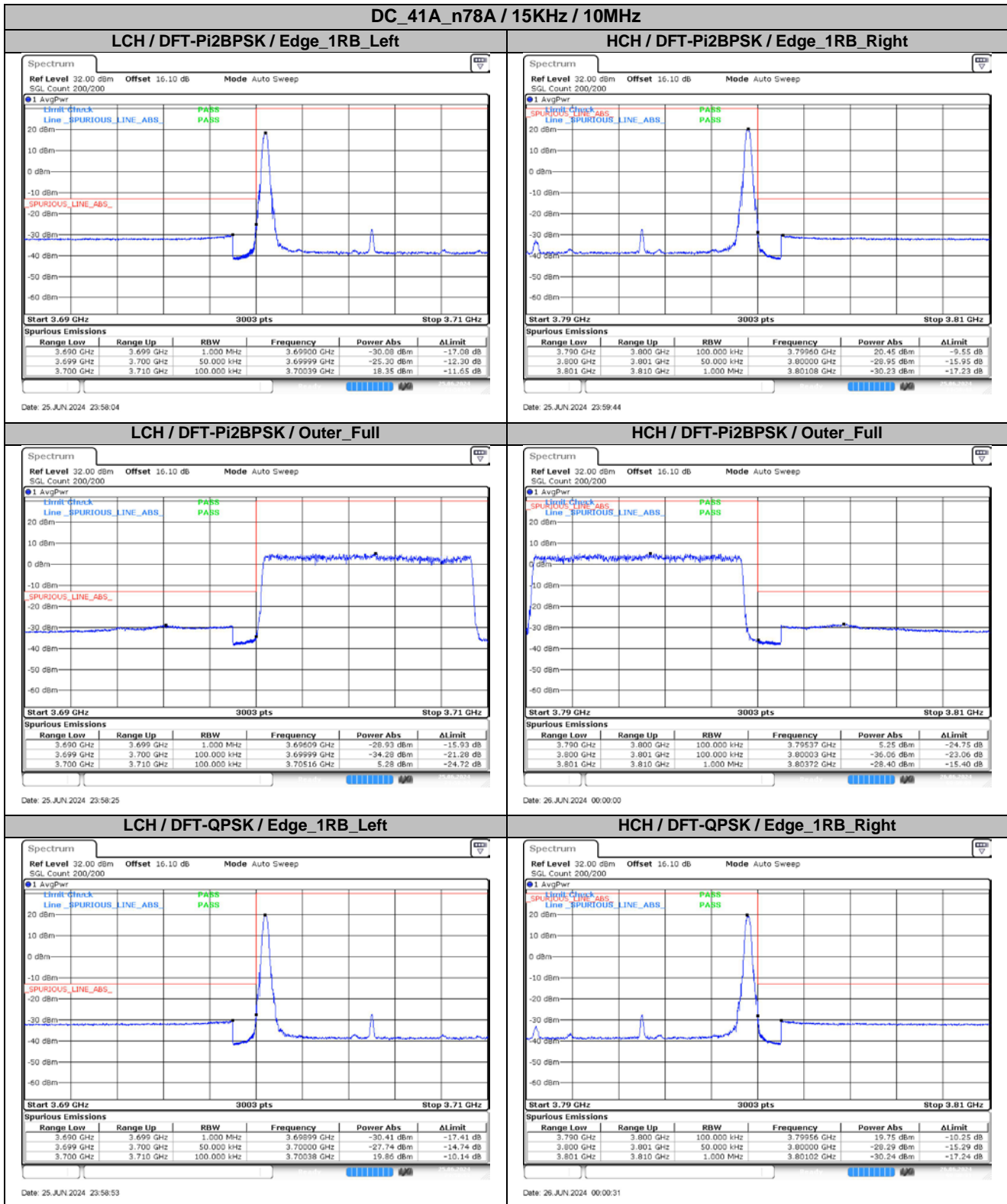
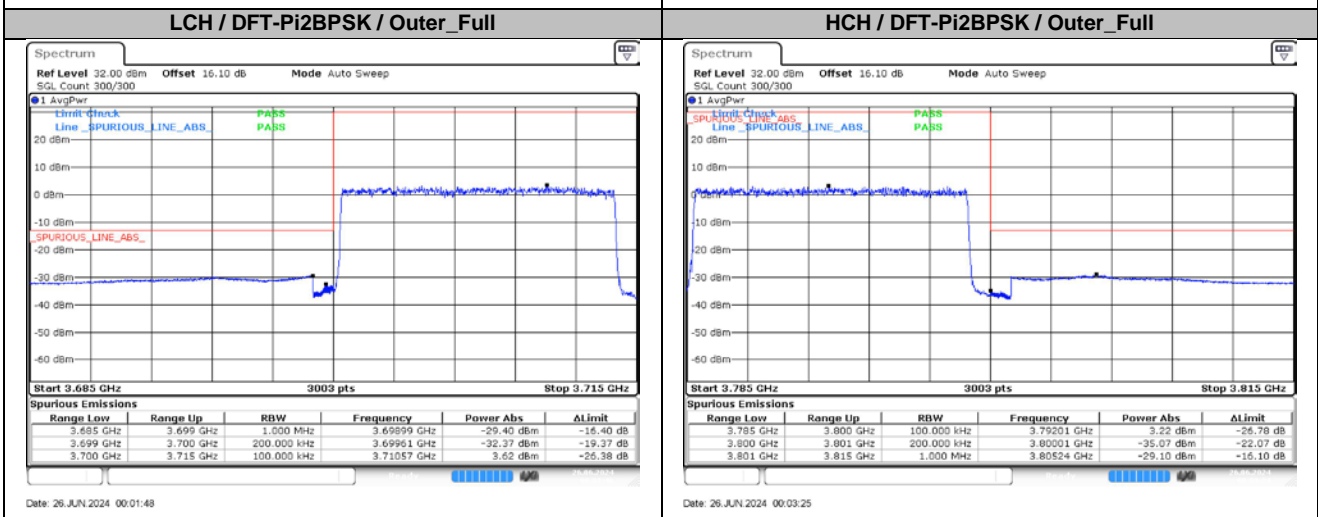
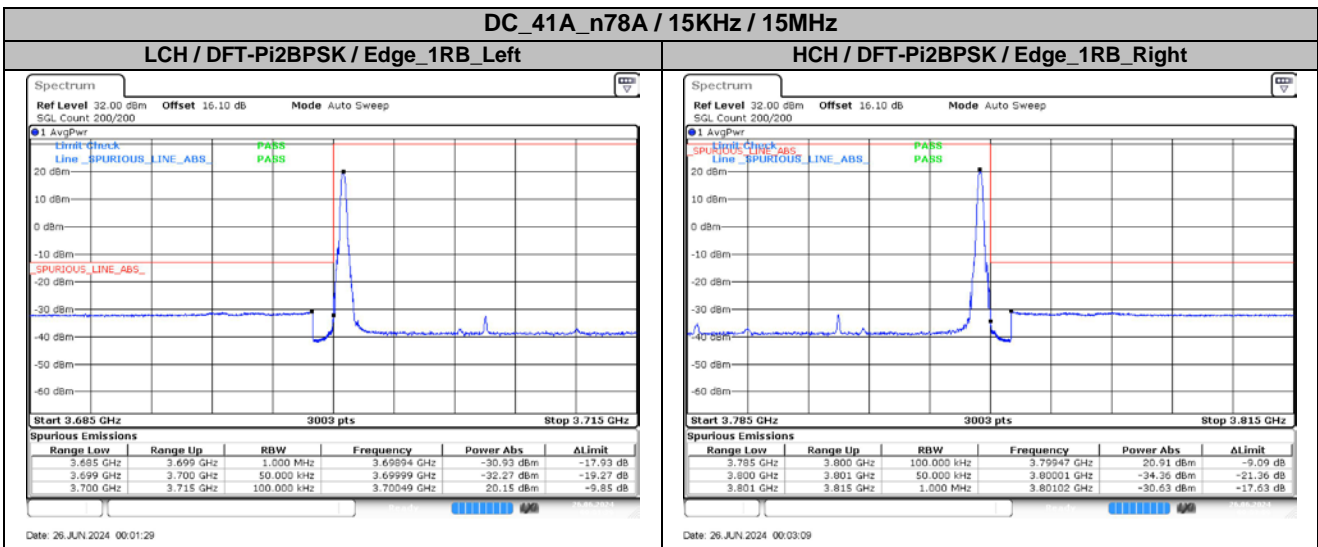
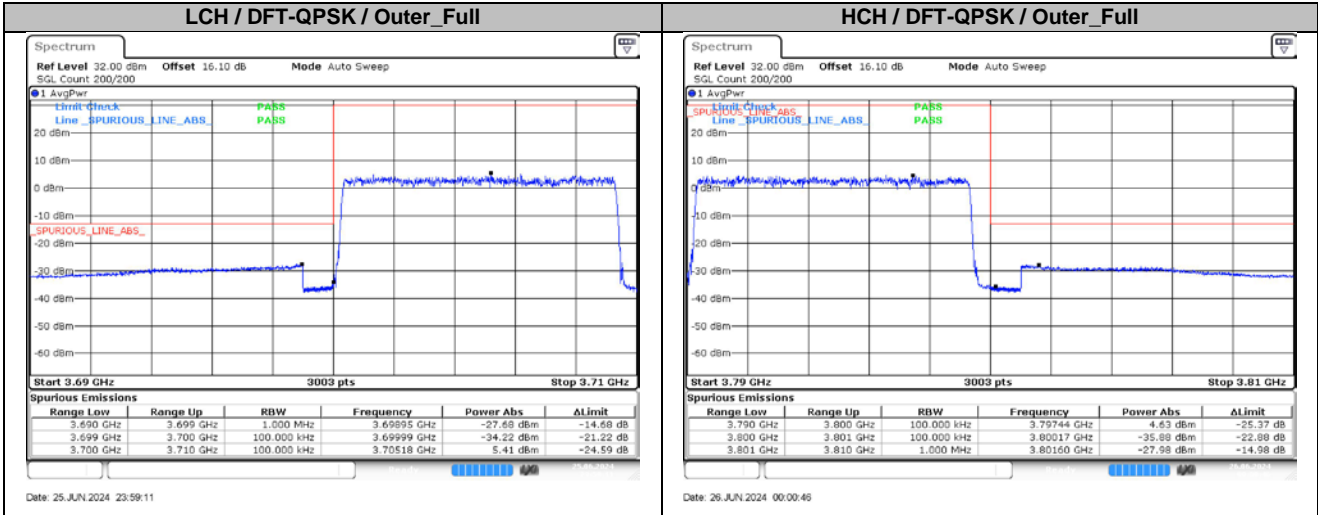
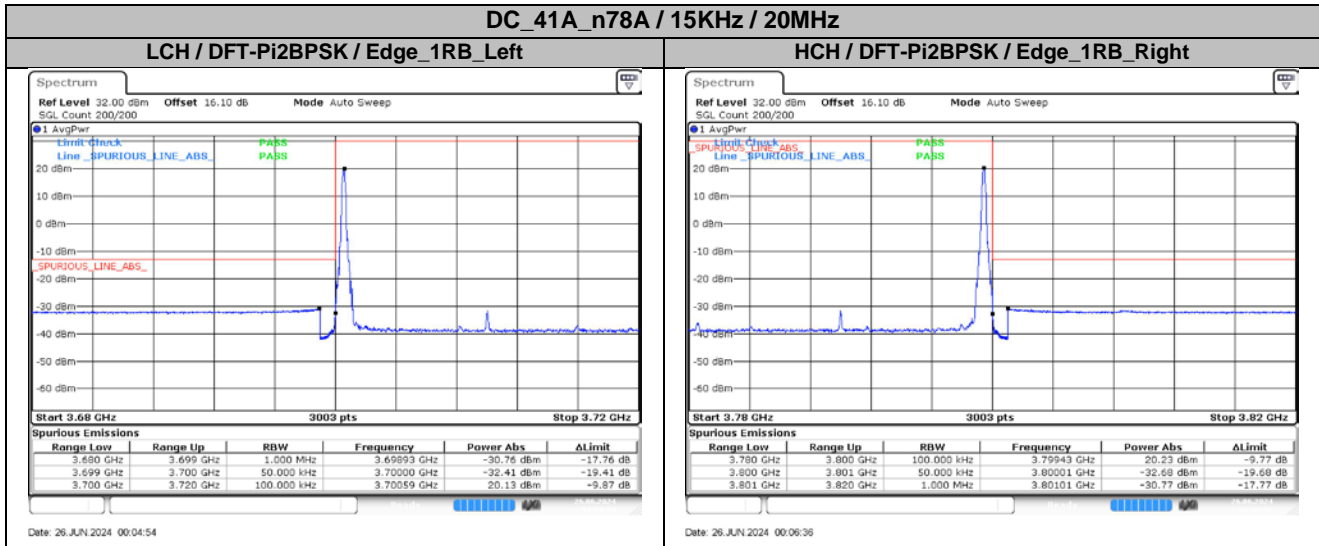
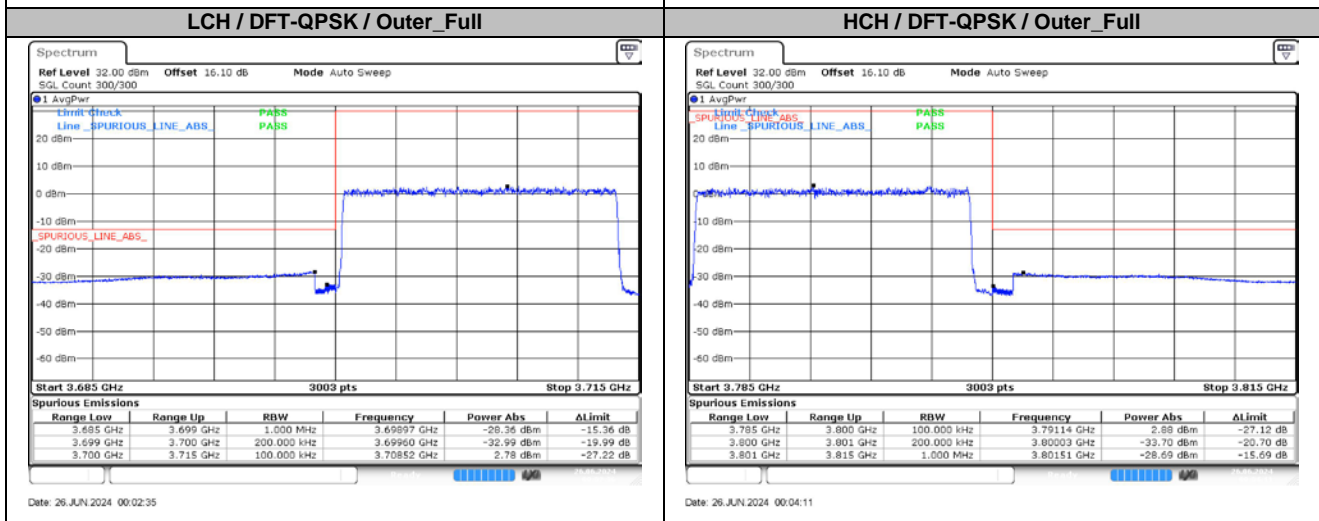
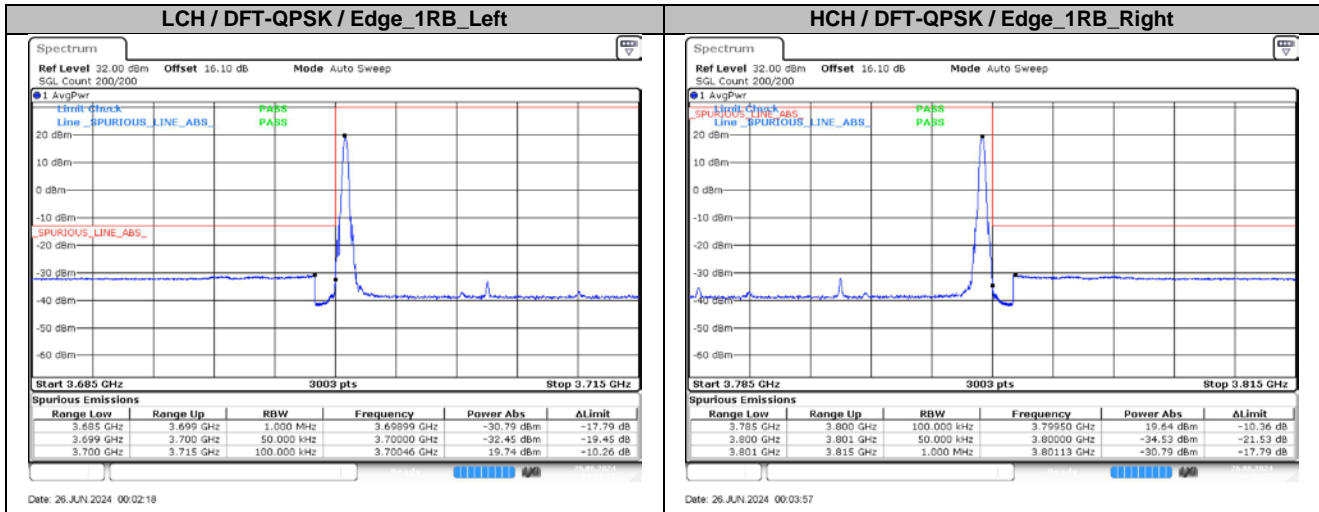


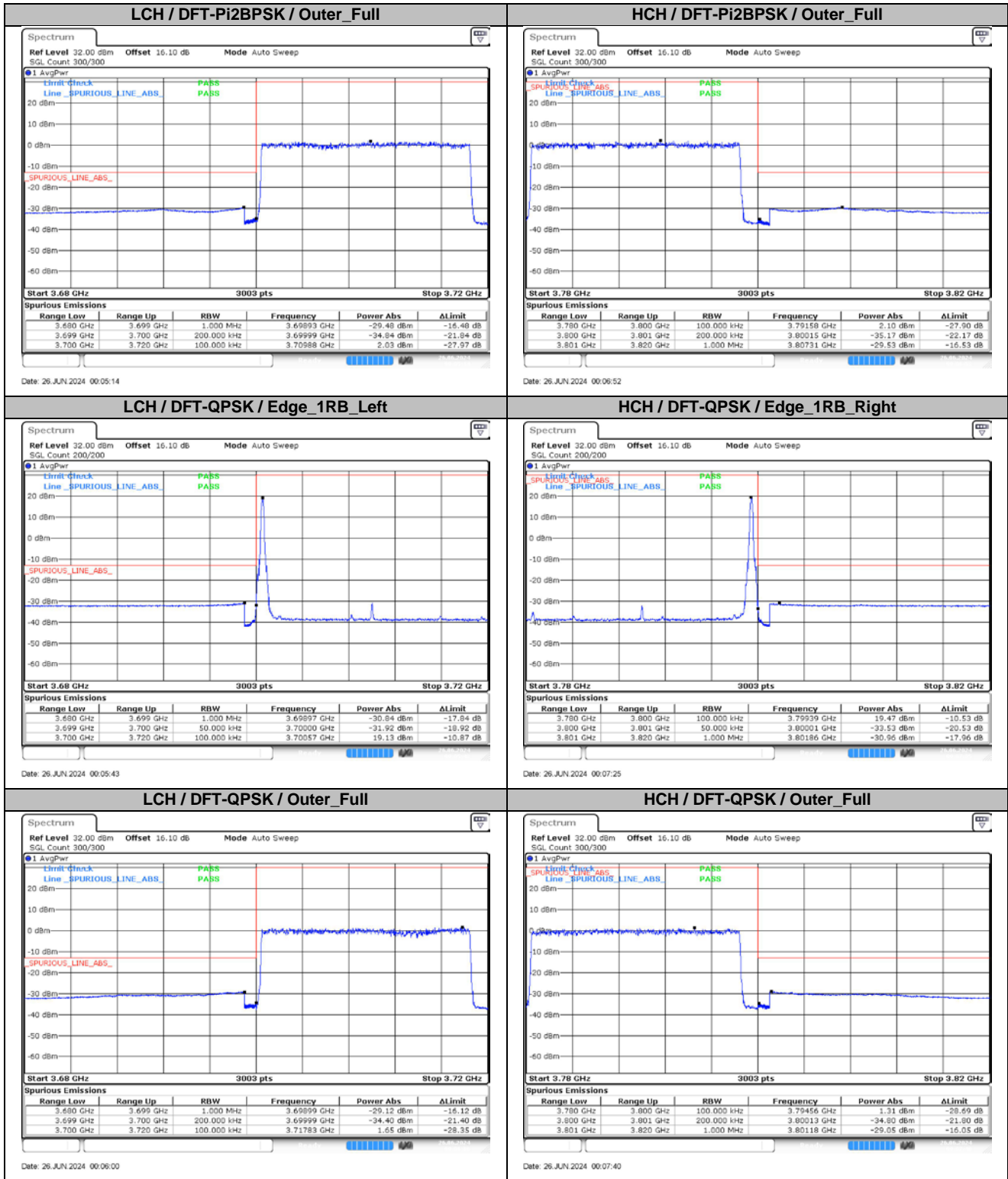
5. Conducted Band Edges

5.1. Test Plots



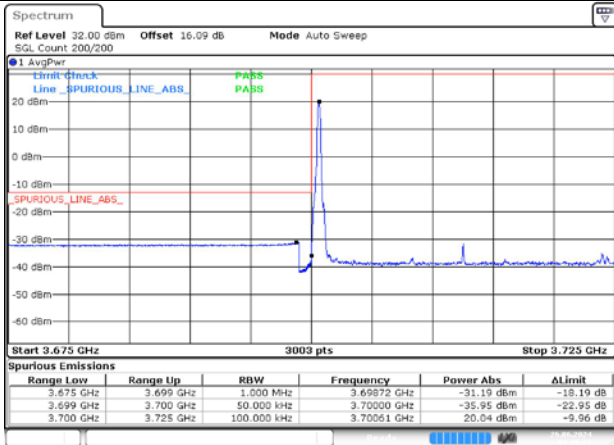






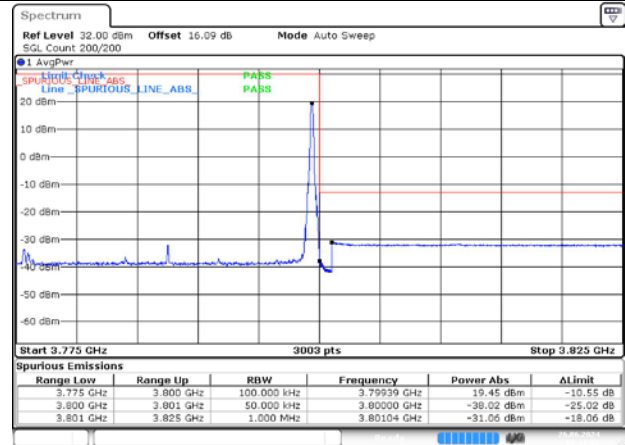
DC_41A_n78A / 15KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



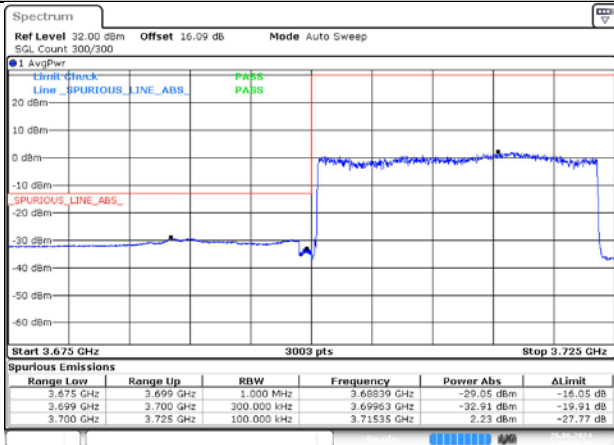
Date: 26 JUN 2024 00:08:23

HCH / DFT-Pi2BPSK / Edge_1RB_Right



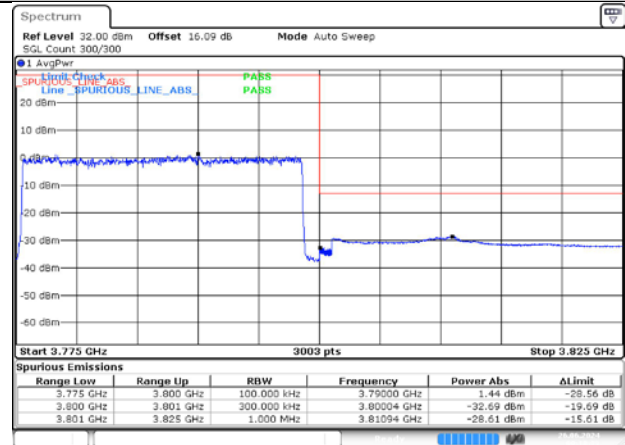
Date: 26 JUN 2024 00:10:07

LCH / DFT-Pi2BPSK / Outer_Full



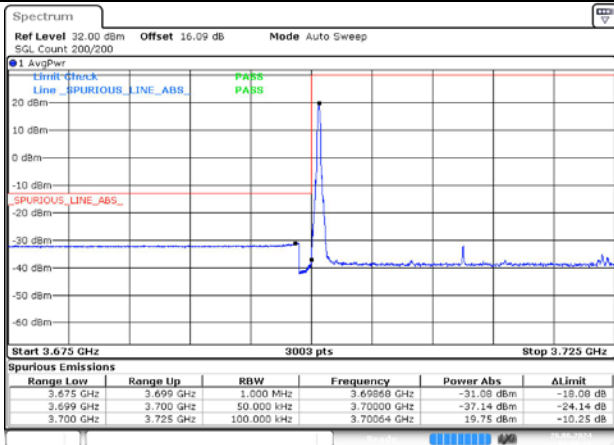
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HCH / DFT-Pi2BPSK / Outer_Full



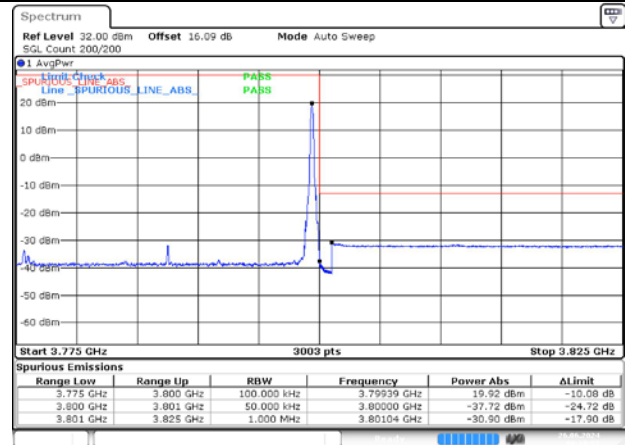
Date: 26 JUN 2024 00:10:23

LCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 00:09:13

HCH / DFT-QPSK / Edge_1RB_Right



Date: 26 JUN 2024 00:10:56

