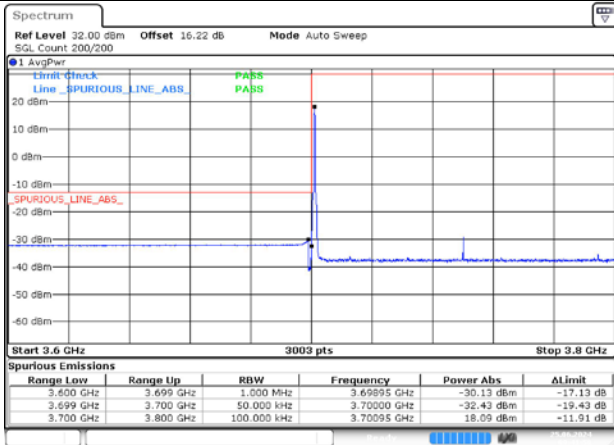


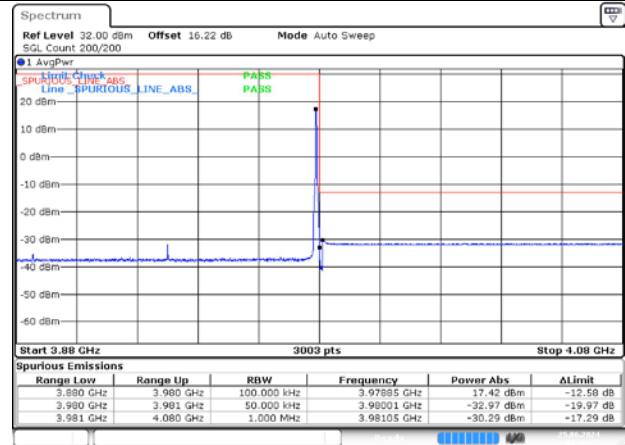
DC_41A_n77A / 30KHz / 100MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



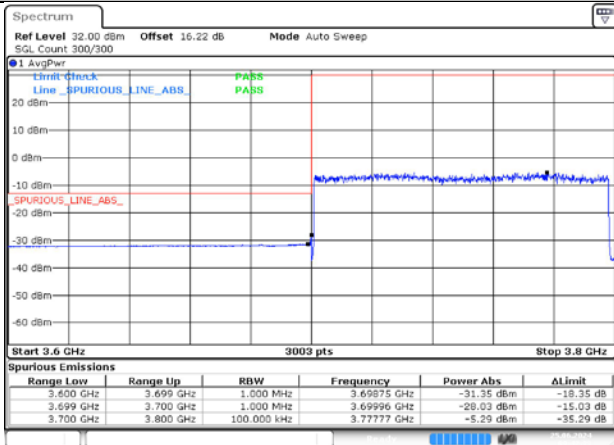
Date: 25 JUN 2024 09:41:59

HCH / DFT-Pi2BPSK / Edge_1RB_Right



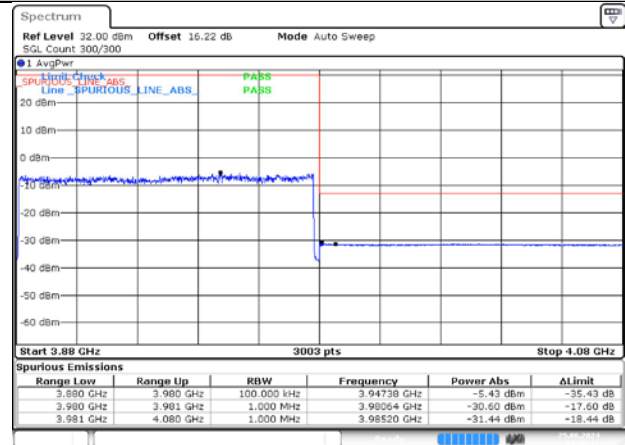
Date: 25 JUN 2024 09:43:53

LCH / DFT-Pi2BPSK / Outer_Full



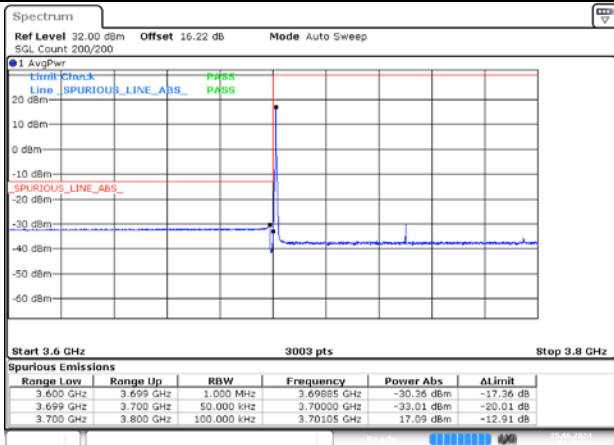
Date: 25 JUN 2024 09:42:22

HCH / DFT-Pi2BPSK / Outer_Full



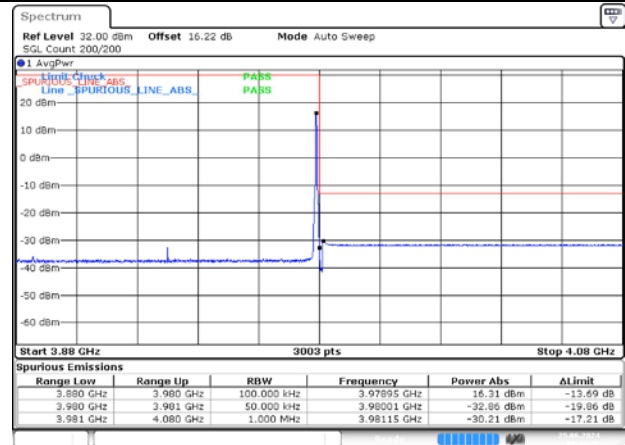
Date: 25 JUN 2024 09:44:13

LCH / DFT-QPSK / Edge_1RB_Left

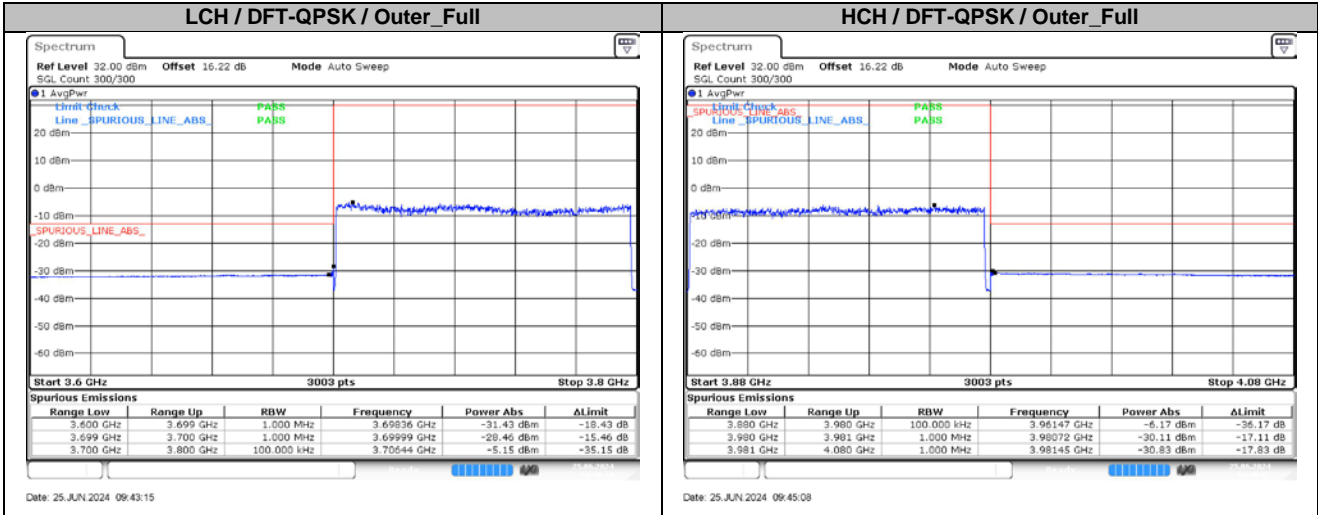


Date: 25 JUN 2024 09:42:54

HCH / DFT-QPSK / Edge_1RB_Right

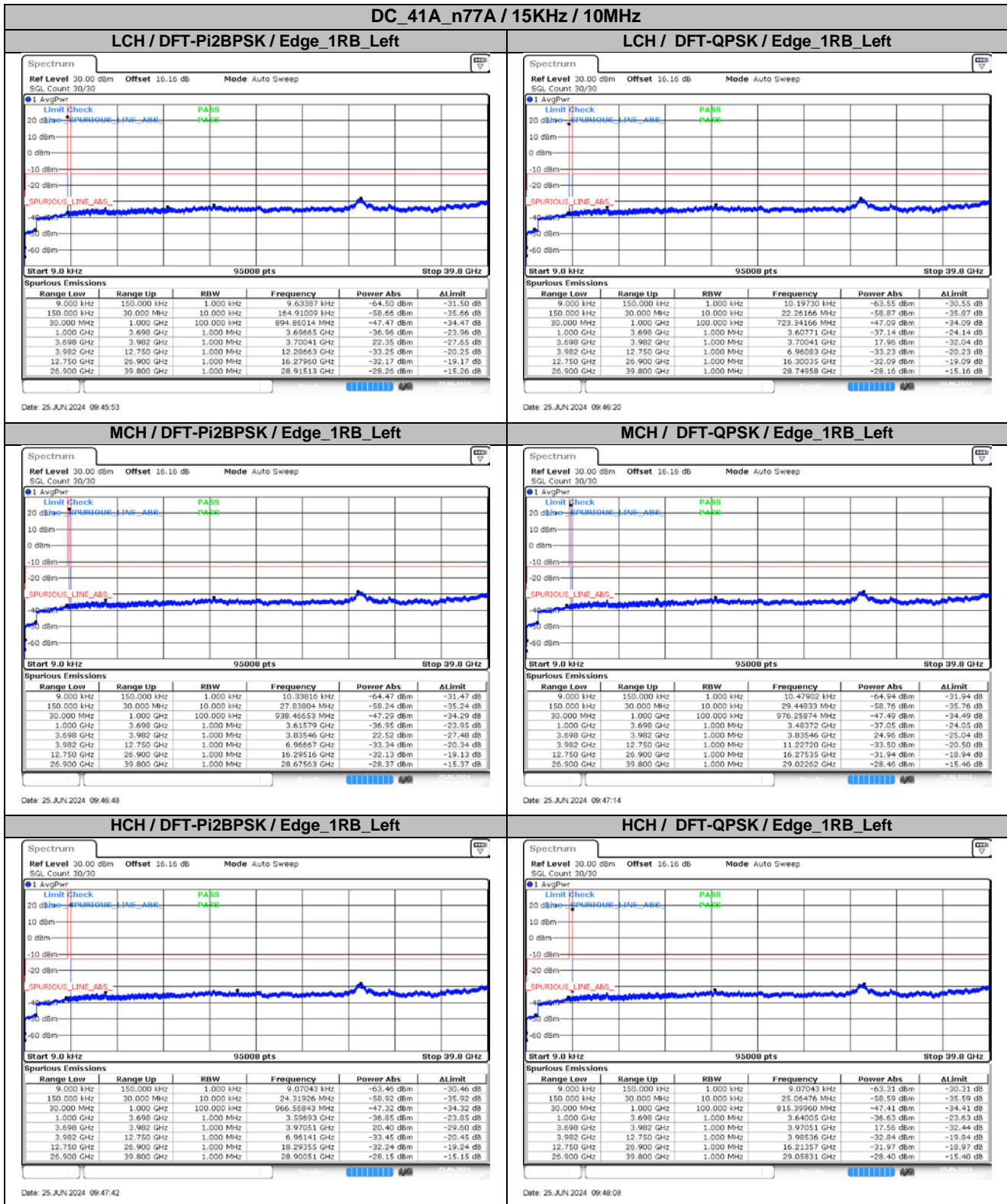


Date: 25 JUN 2024 09:44:49



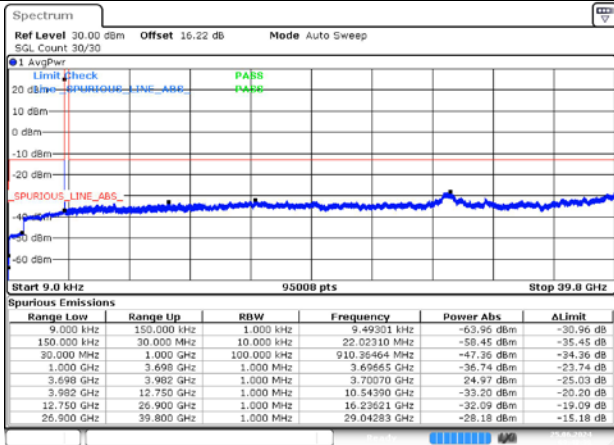
6. Conducted Spurious Emission

6.1. Test Plots



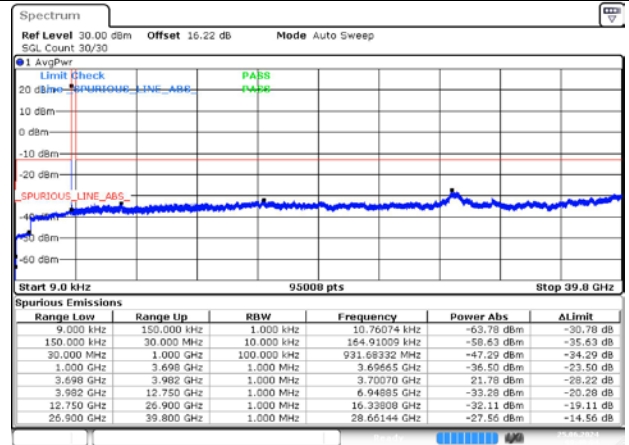
DC_41A_n77A / 15KHz / 15MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



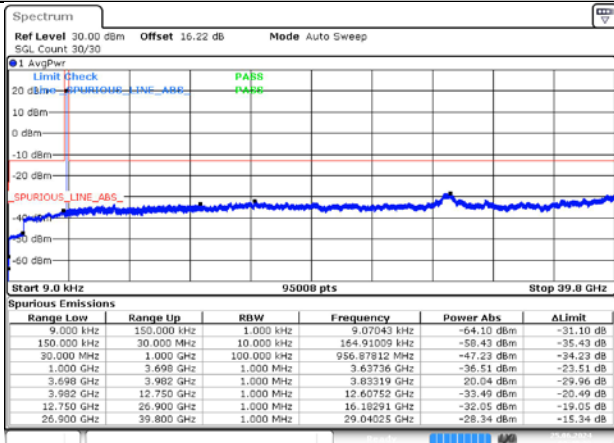
Date: 25 JUN 2024 09:48:38

LCH / DFT-QPSK / Edge_1RB_Left



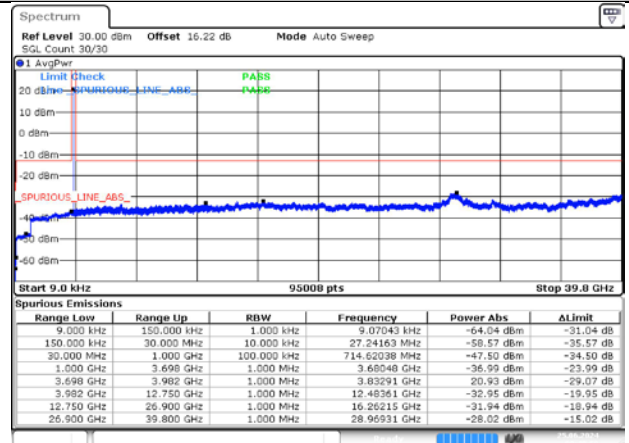
Date: 25 JUN 2024 09:49:05

MCH / DFT-Pi2BPSK / Edge_1RB_Left



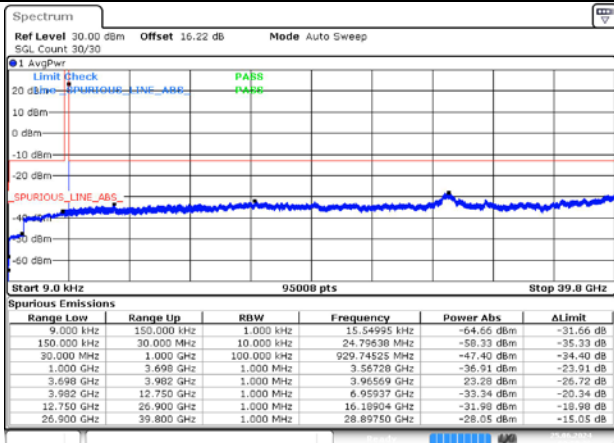
Date: 25 JUN 2024 09:49:32

MCH / DFT-QPSK / Edge_1RB_Left



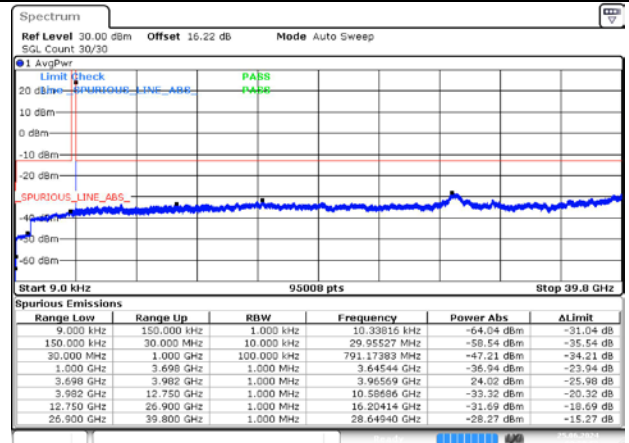
Date: 25 JUN 2024 09:49:59

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 25 JUN 2024 09:50:28

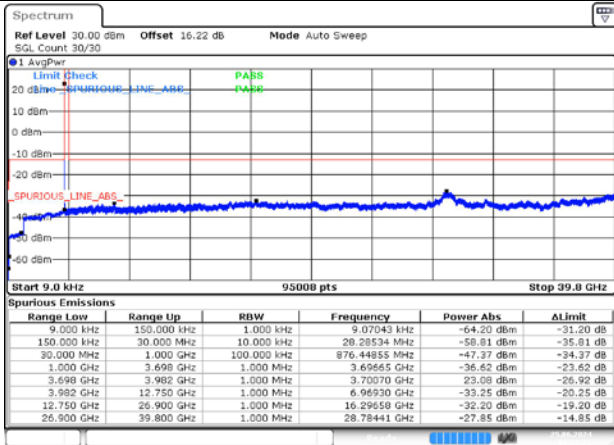
HCH / DFT-QPSK / Edge_1RB_Left



Date: 25 JUN 2024 09:50:53

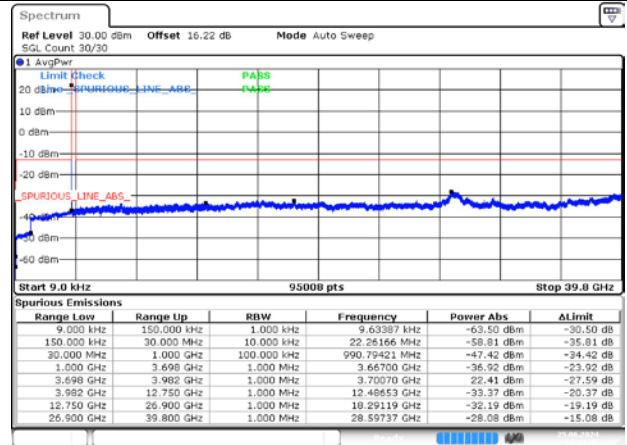
DC_41A_n77A / 15KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



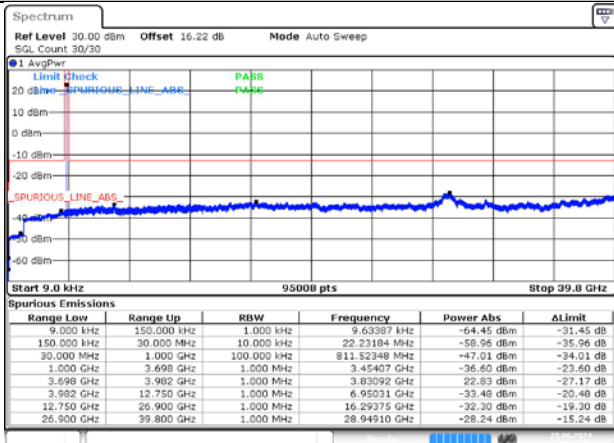
Date: 25 JUN 2024 09:51:23

LCH / DFT-QPSK / Edge_1RB_Left



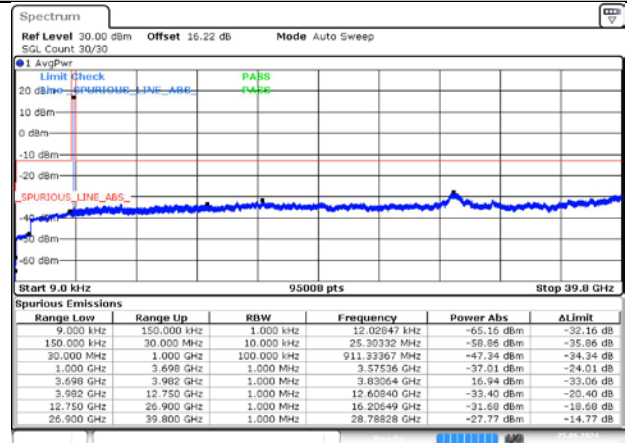
Date: 25 JUN 2024 09:51:50

MCH / DFT-Pi2BPSK / Edge_1RB_Left



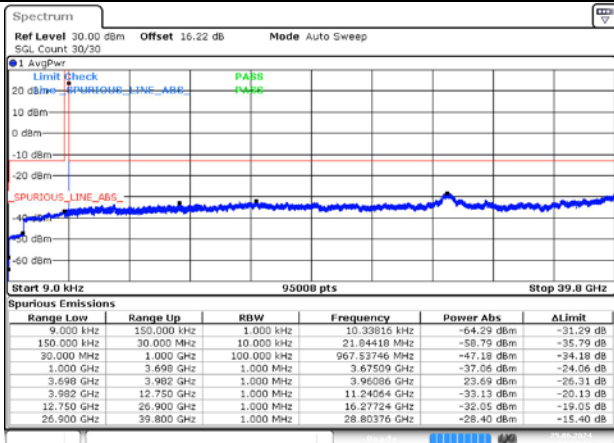
Date: 25 JUN 2024 09:52:17

MCH / DFT-QPSK / Edge_1RB_Left



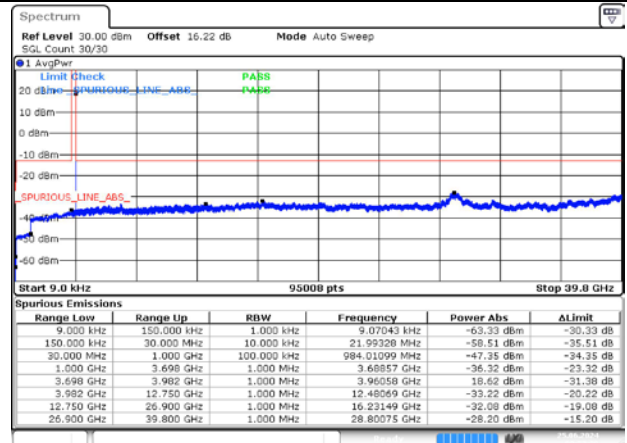
Date: 25 JUN 2024 09:52:44

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 25 JUN 2024 09:53:12

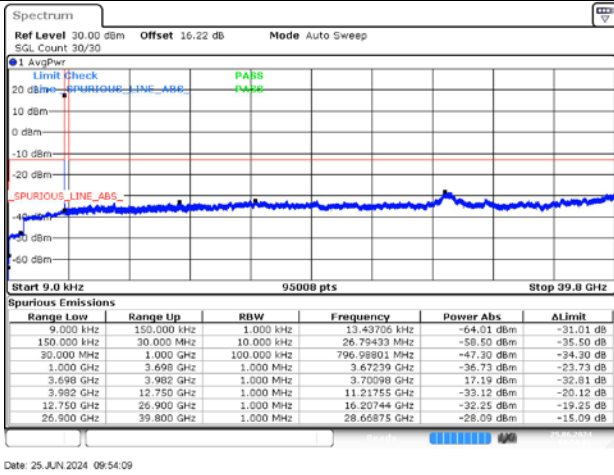
HCH / DFT-QPSK / Edge_1RB_Left



Date: 25 JUN 2024 09:53:39

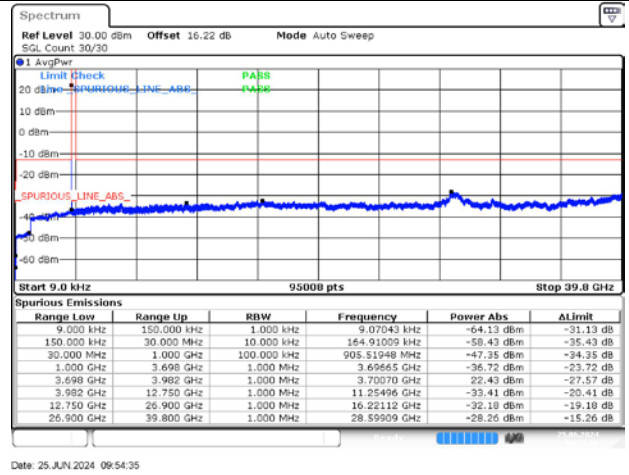
DC_41A_n77A / 15KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



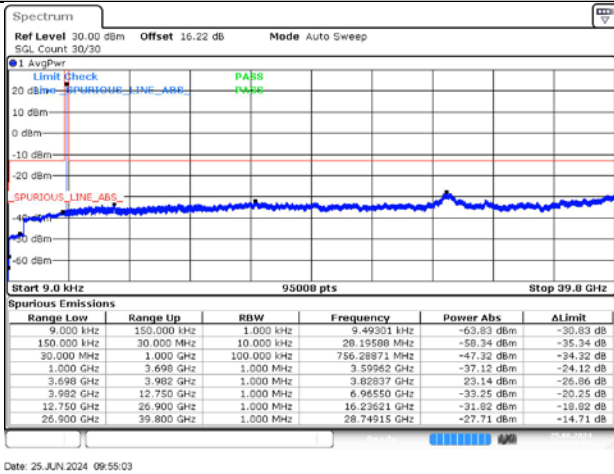
Date: 25 JUN 2024 09:54:09

LCH / DFT-QPSK / Edge_1RB_Left



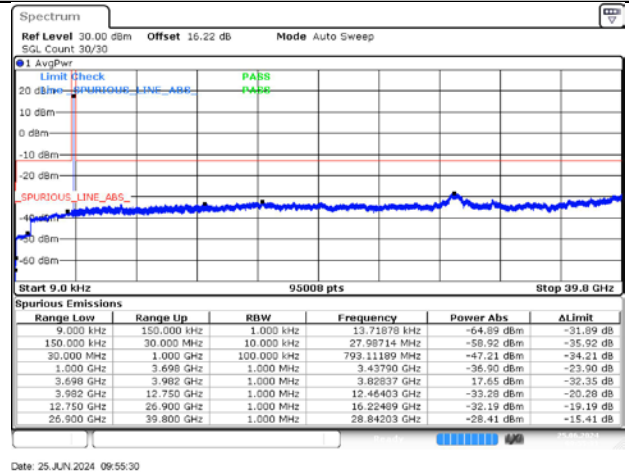
Date: 25 JUN 2024 09:54:35

MCH / DFT-Pi2BPSK / Edge_1RB_Left



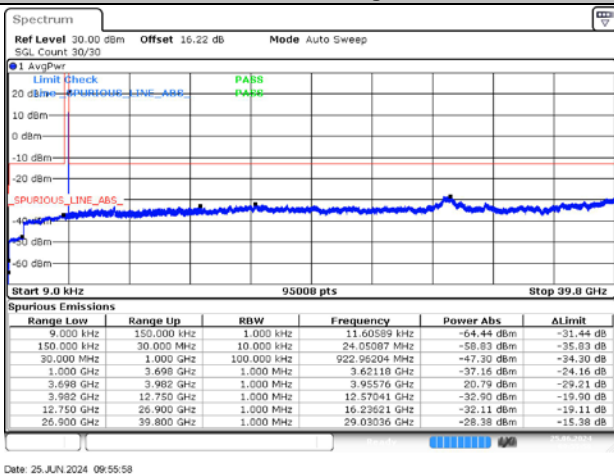
Date: 25 JUN 2024 09:55:03

MCH / DFT-QPSK / Edge_1RB_Left



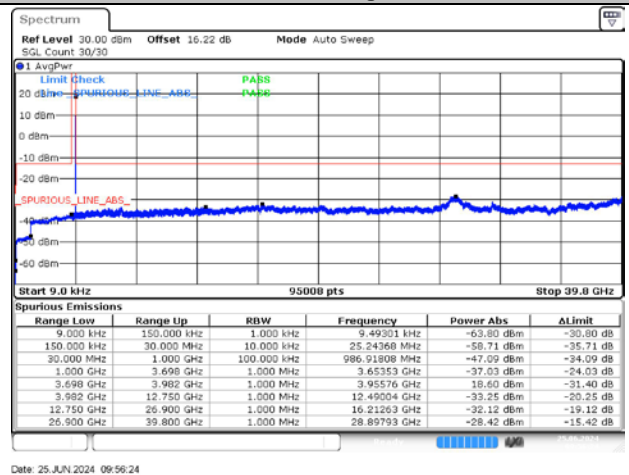
Date: 25 JUN 2024 09:55:30

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 25 JUN 2024 09:55:58

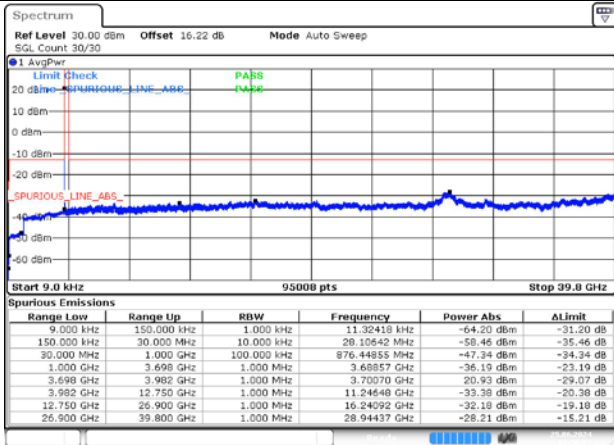
HCH / DFT-QPSK / Edge_1RB_Left



Date: 25 JUN 2024 09:56:24

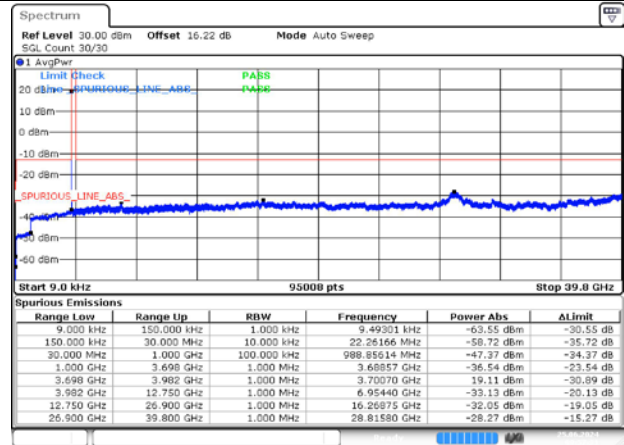
DC_41A_n77A / 15KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



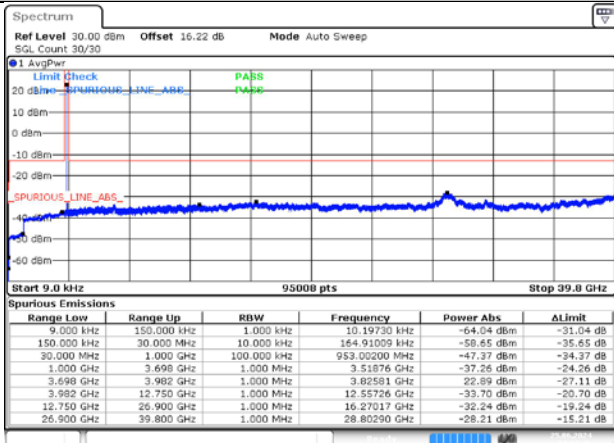
Date: 25 JUN 2024 09:56:54

LCH / DFT-QPSK / Edge_1RB_Left



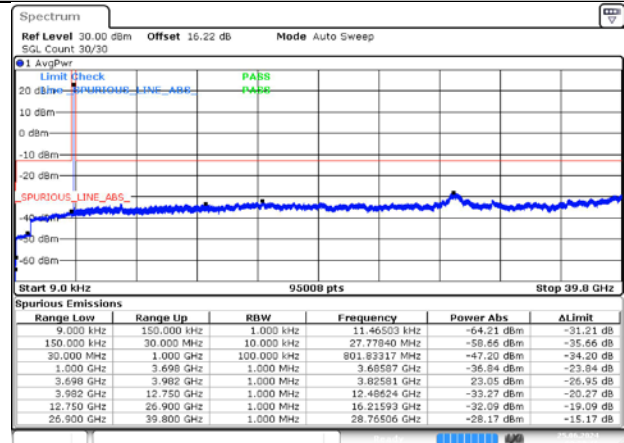
Date: 25 JUN 2024 09:57:21

MCH / DFT-Pi2BPSK / Edge_1RB_Left



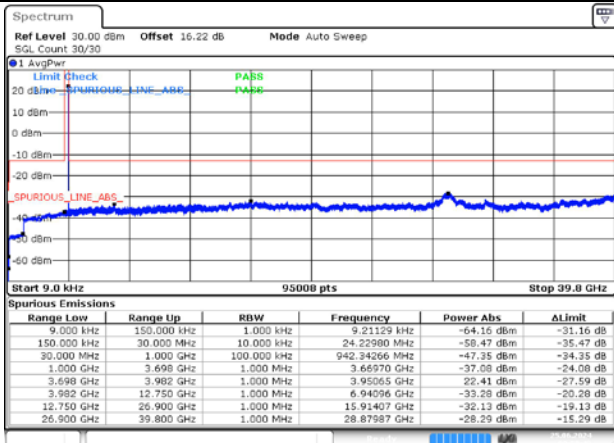
Date: 25 JUN 2024 09:57:48

MCH / DFT-QPSK / Edge_1RB_Left



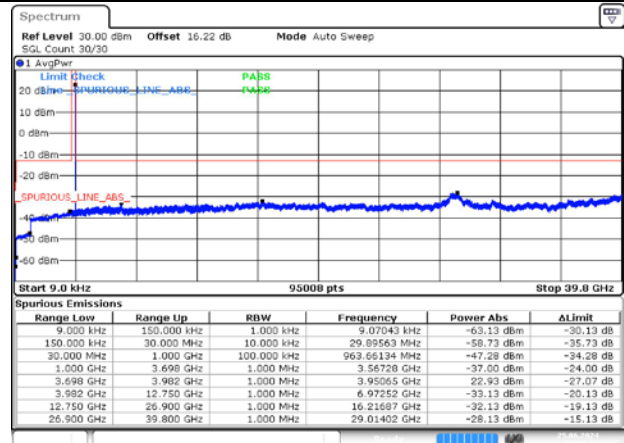
Date: 25 JUN 2024 09:58:15

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 25 JUN 2024 09:58:43

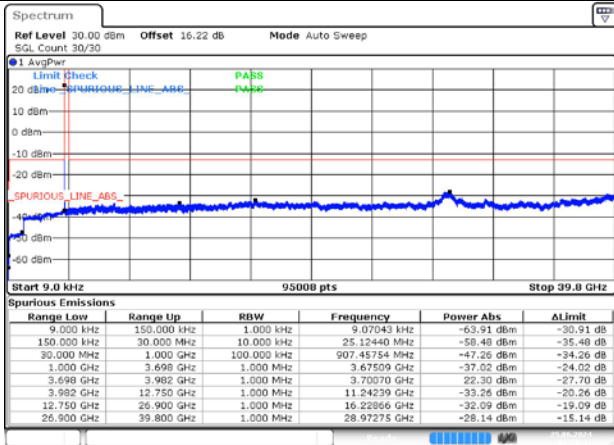
HCH / DFT-QPSK / Edge_1RB_Left



Date: 25 JUN 2024 09:59:10

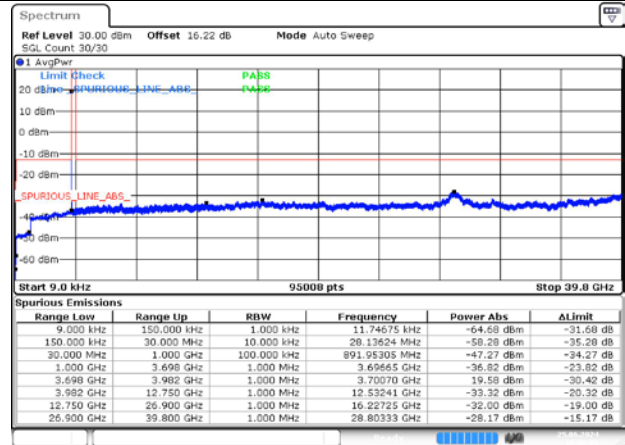
DC_41A_n77A / 15KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



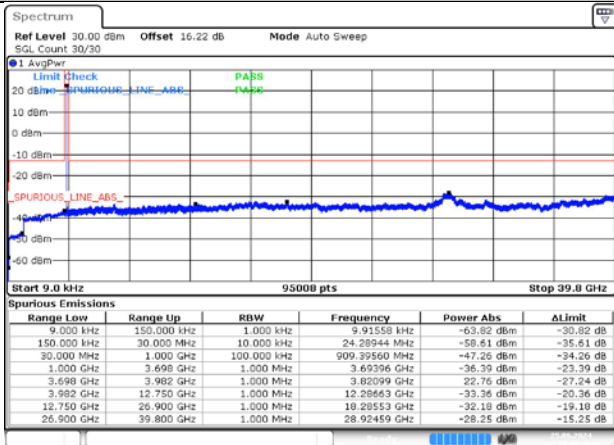
Date: 25 JUN 2024 09:59:40

LCH / DFT-QPSK / Edge_1RB_Left



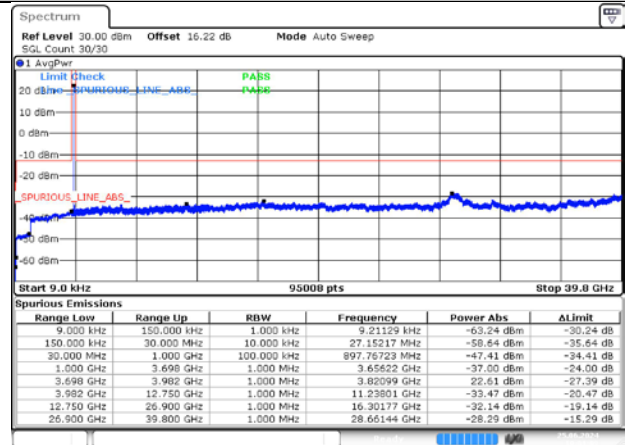
Date: 25 JUN 2024 10:00:07

MCH / DFT-Pi2BPSK / Edge_1RB_Left



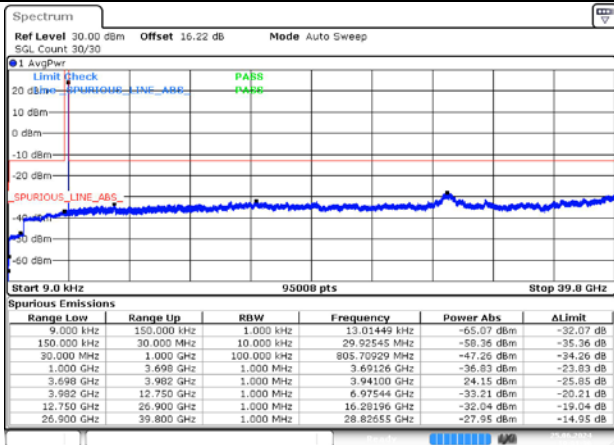
Date: 25 JUN 2024 10:00:35

MCH / DFT-QPSK / Edge_1RB_Left



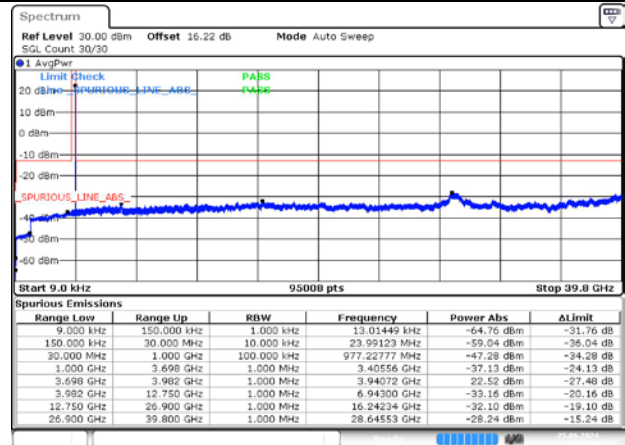
Date: 25 JUN 2024 10:01:02

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 25 JUN 2024 10:01:30

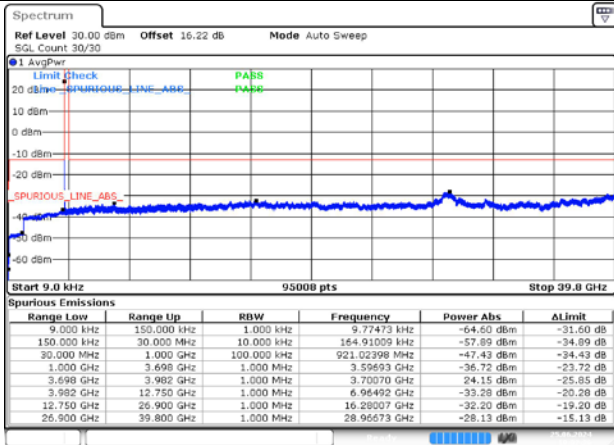
HCH / DFT-QPSK / Edge_1RB_Left



Date: 25 JUN 2024 10:01:56

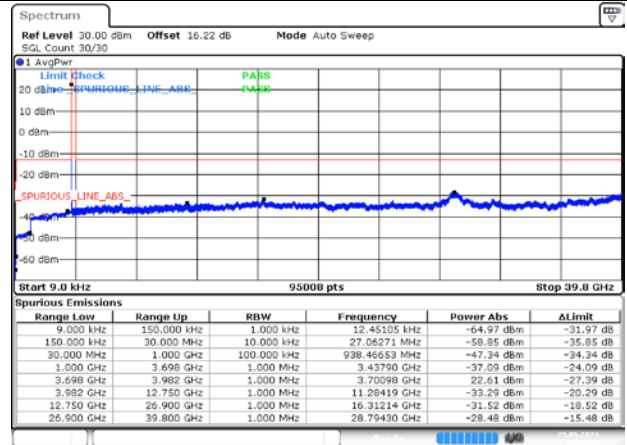
DC_41A_n77A / 15KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



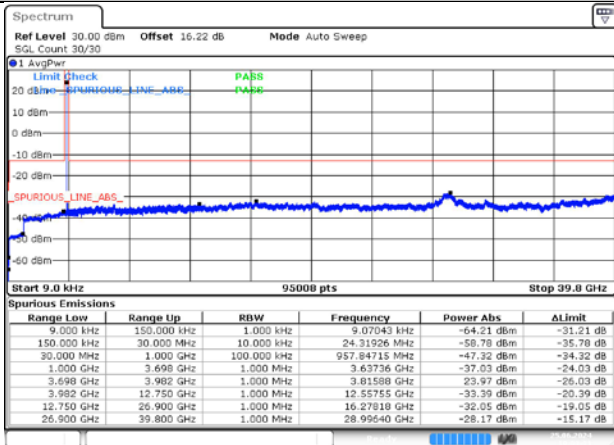
Date: 25 JUN 2024 10:02:28

LCH / DFT-QPSK / Edge_1RB_Left



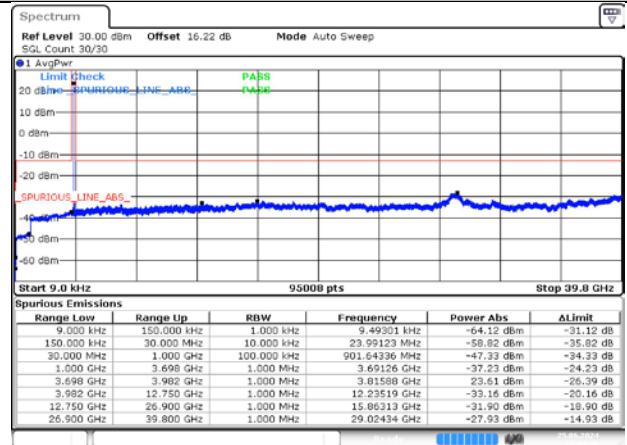
Date: 25 JUN 2024 10:02:53

MCH / DFT-Pi2BPSK / Edge_1RB_Left



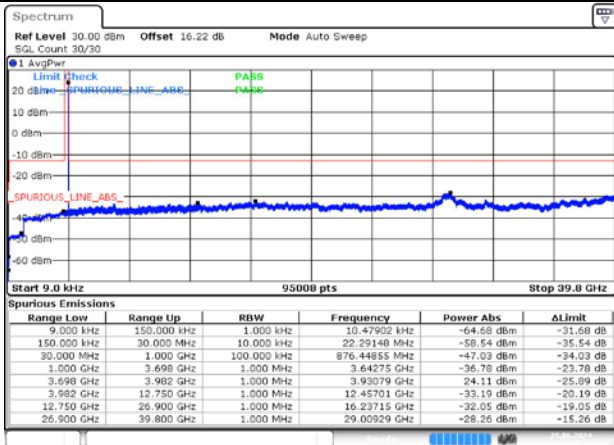
Date: 25 JUN 2024 10:03:21

MCH / DFT-QPSK / Edge_1RB_Left



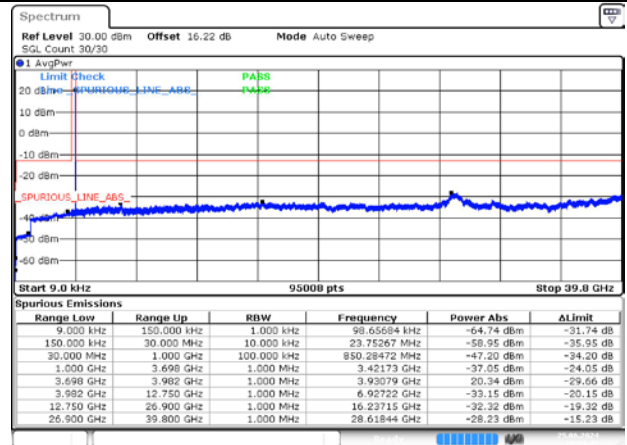
Date: 25 JUN 2024 10:03:48

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 25 JUN 2024 10:04:16

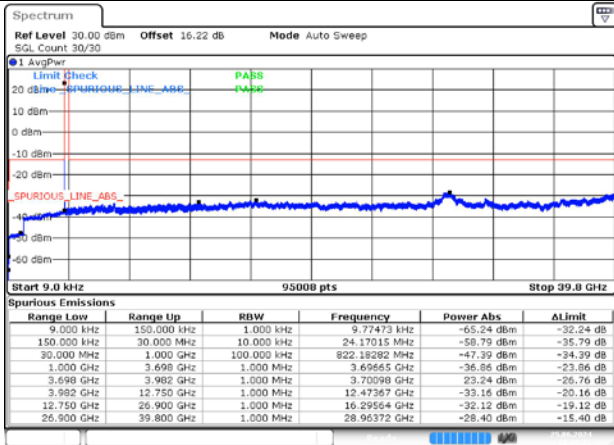
HCH / DFT-QPSK / Edge_1RB_Left



Date: 25 JUN 2024 10:04:42

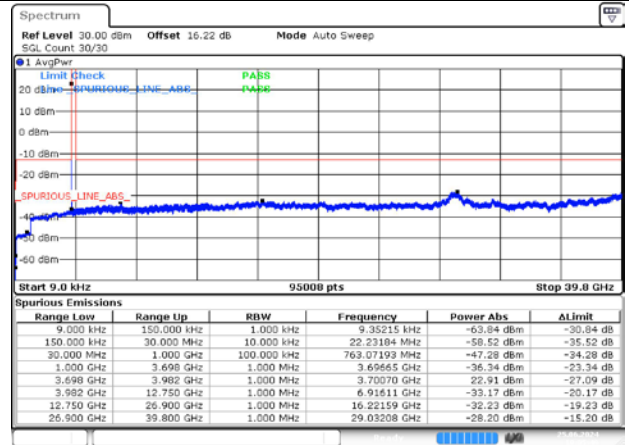
DC_41A_n77A / 30KHz / 10MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



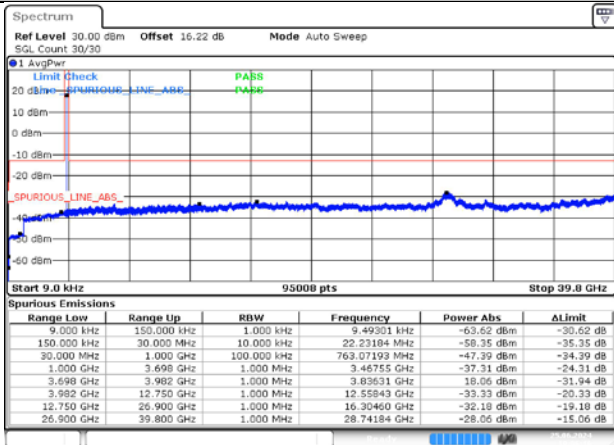
Date: 25 JUN 2024 10:05:22

LCH / DFT-QPSK / Edge_1RB_Left



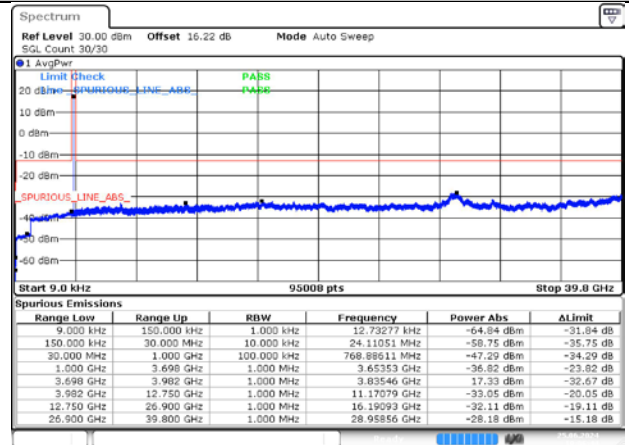
Date: 25 JUN 2024 10:05:49

MCH / DFT-Pi2BPSK / Edge_1RB_Left



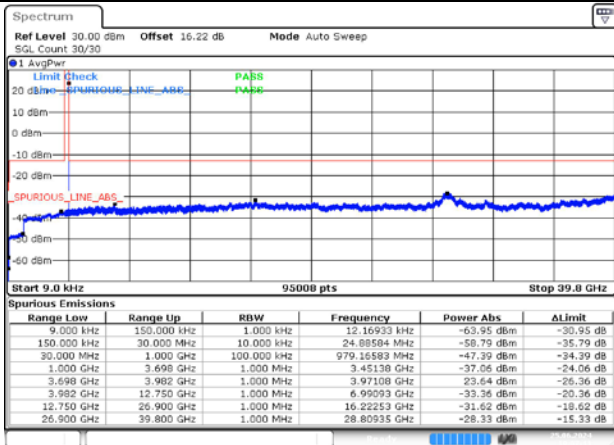
Date: 25 JUN 2024 10:06:17

MCH / DFT-QPSK / Edge_1RB_Left



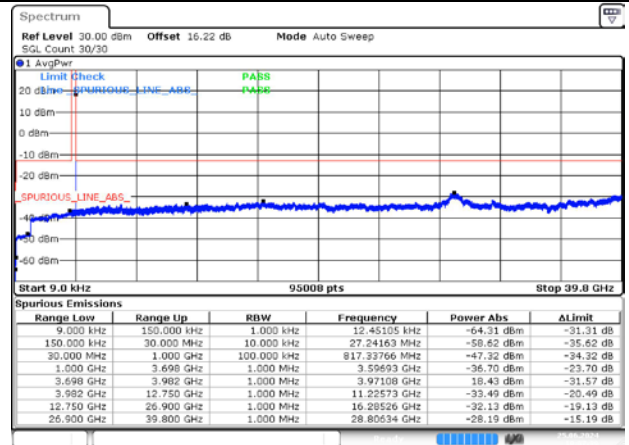
Date: 25 JUN 2024 10:06:43

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 25 JUN 2024 10:07:11

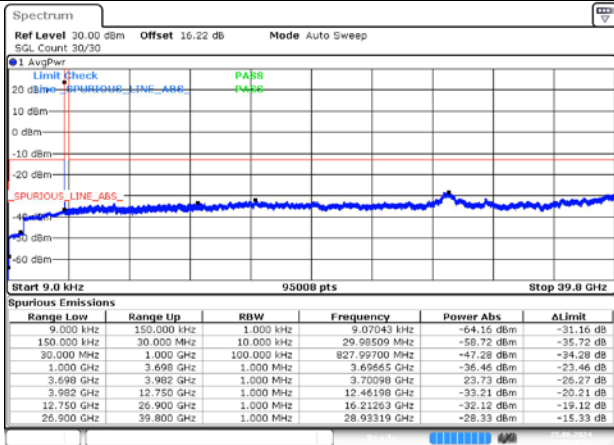
HCH / DFT-QPSK / Edge_1RB_Left



Date: 25 JUN 2024 10:07:38

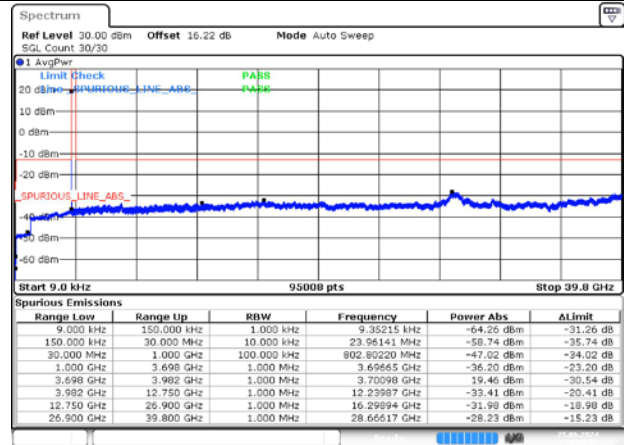
DC_41A_n77A / 30KHz / 15MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



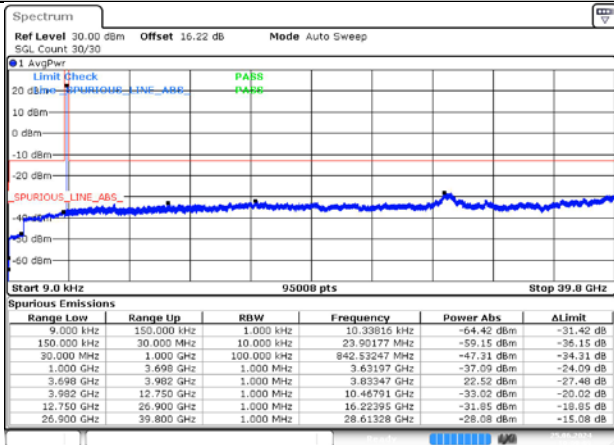
Date: 25 JUN 2024 10:08:08

LCH / DFT-QPSK / Edge_1RB_Left



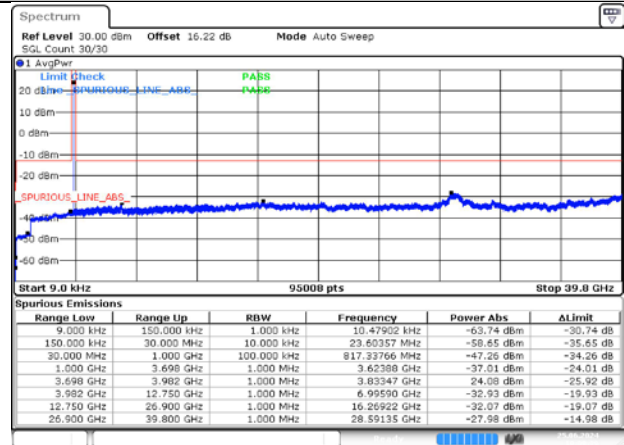
Date: 25 JUN 2024 10:08:35

MCH / DFT-Pi2BPSK / Edge_1RB_Left



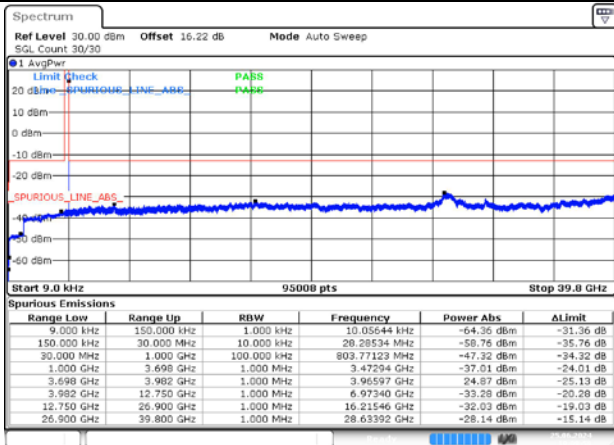
Date: 25 JUN 2024 10:09:02

MCH / DFT-QPSK / Edge_1RB_Left



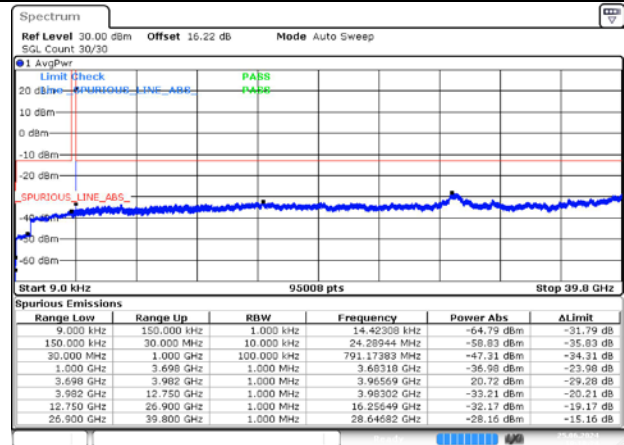
Date: 25 JUN 2024 10:09:29

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 25 JUN 2024 10:09:57

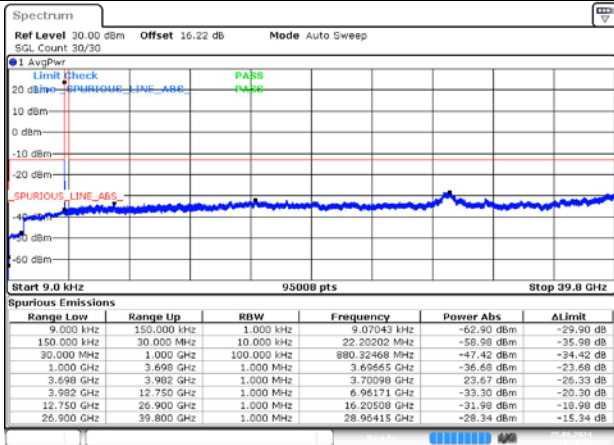
HCH / DFT-QPSK / Edge_1RB_Left



Date: 25 JUN 2024 10:10:24

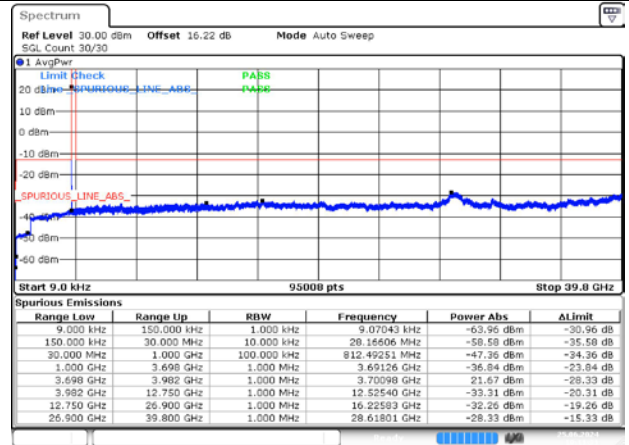
DC_41A_n77A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



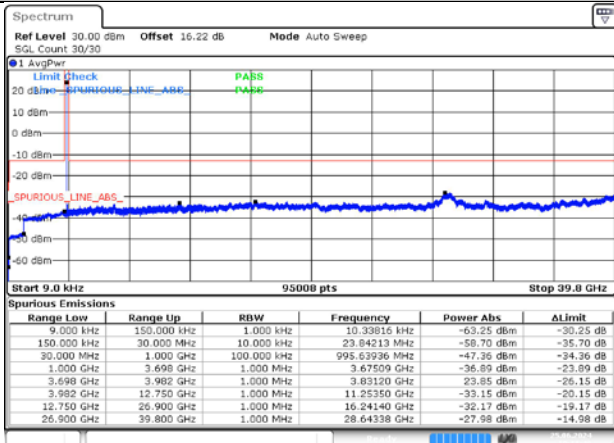
Date: 25 JUN 2024 10:10:53

LCH / DFT-QPSK / Edge_1RB_Left



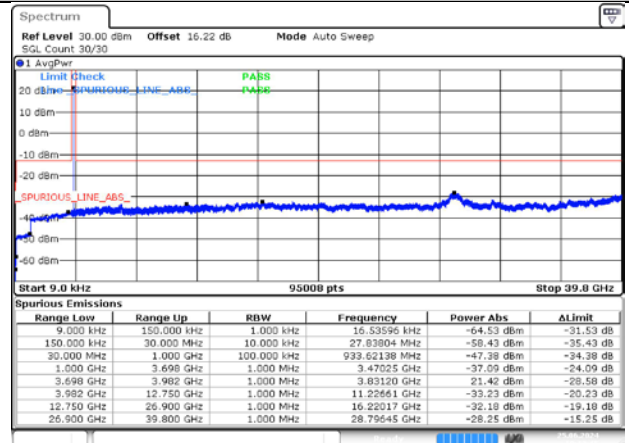
Date: 25 JUN 2024 10:11:20

MCH / DFT-Pi2BPSK / Edge_1RB_Left



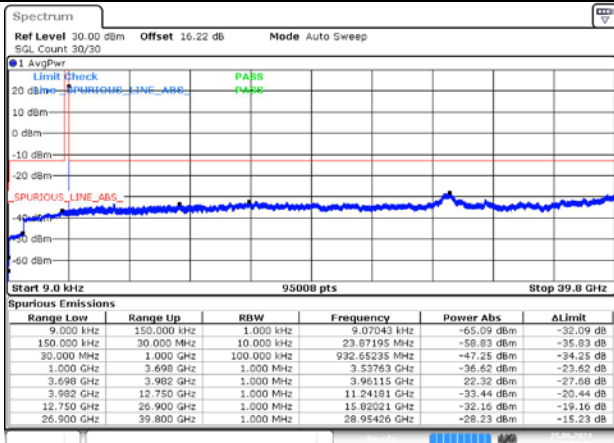
Date: 25 JUN 2024 10:11:48

MCH / DFT-QPSK / Edge_1RB_Left



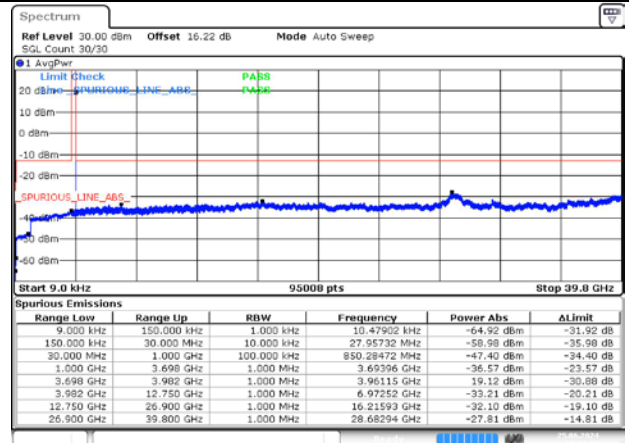
Date: 25 JUN 2024 10:12:15

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 25 JUN 2024 10:12:43

HCH / DFT-QPSK / Edge_1RB_Left



Date: 25 JUN 2024 10:13:10