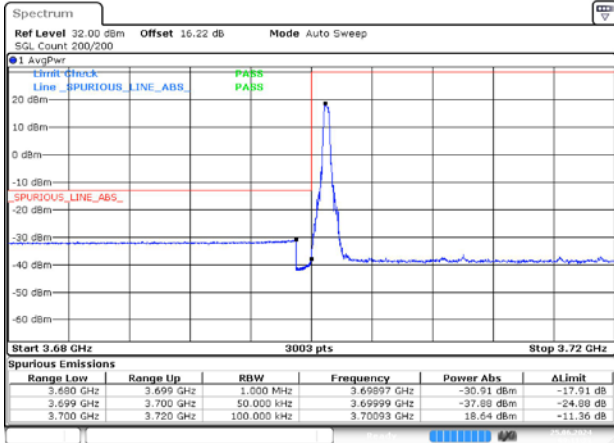


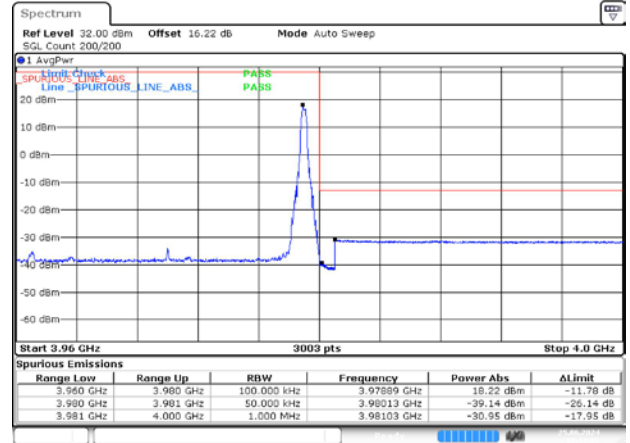
DC_41A_n77A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



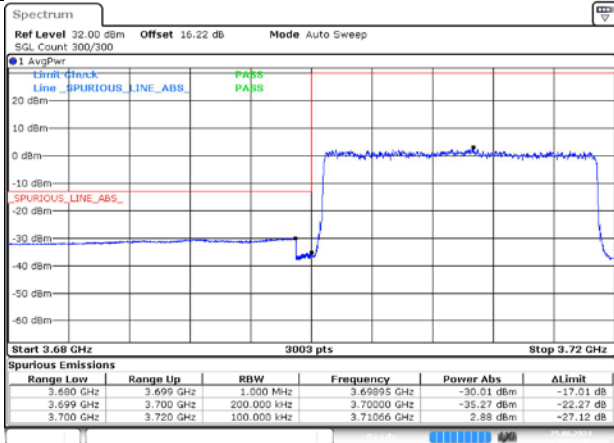
Date: 25 JUN 2024 09:11:23

HCH / DFT-Pi2BPSK / Edge_1RB_Right



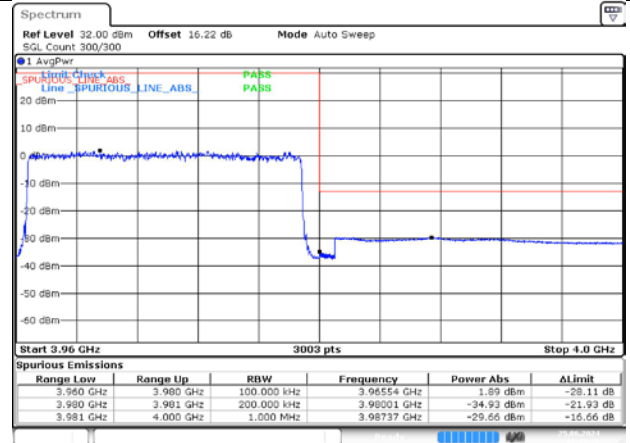
Date: 25 JUN 2024 09:13:02

LCH / DFT-Pi2BPSK / Outer_Full



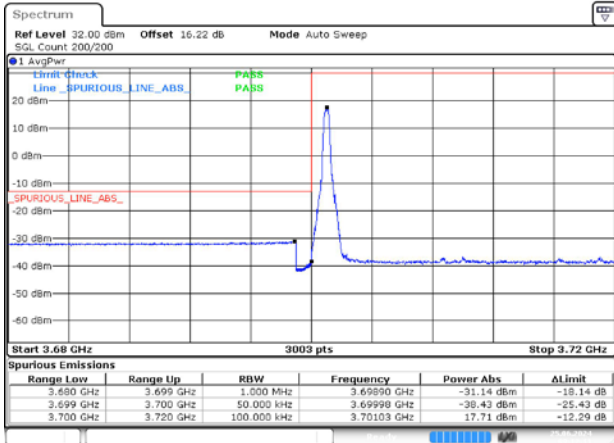
Date: 25 JUN 2024 09:11:42

HCH / DFT-Pi2BPSK / Outer_Full



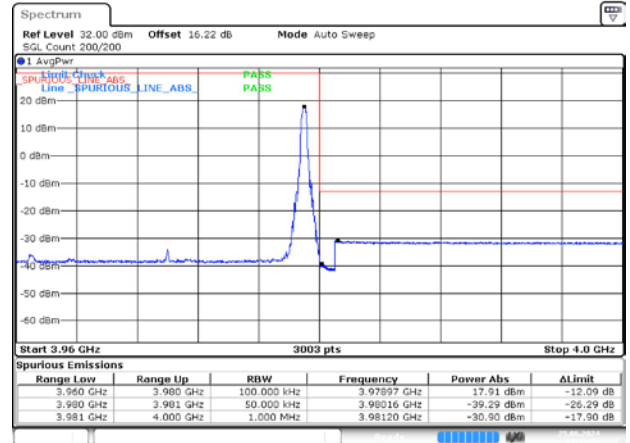
Date: 25 JUN 2024 09:13:17

LCH / DFT-QPSK / Edge_1RB_Left

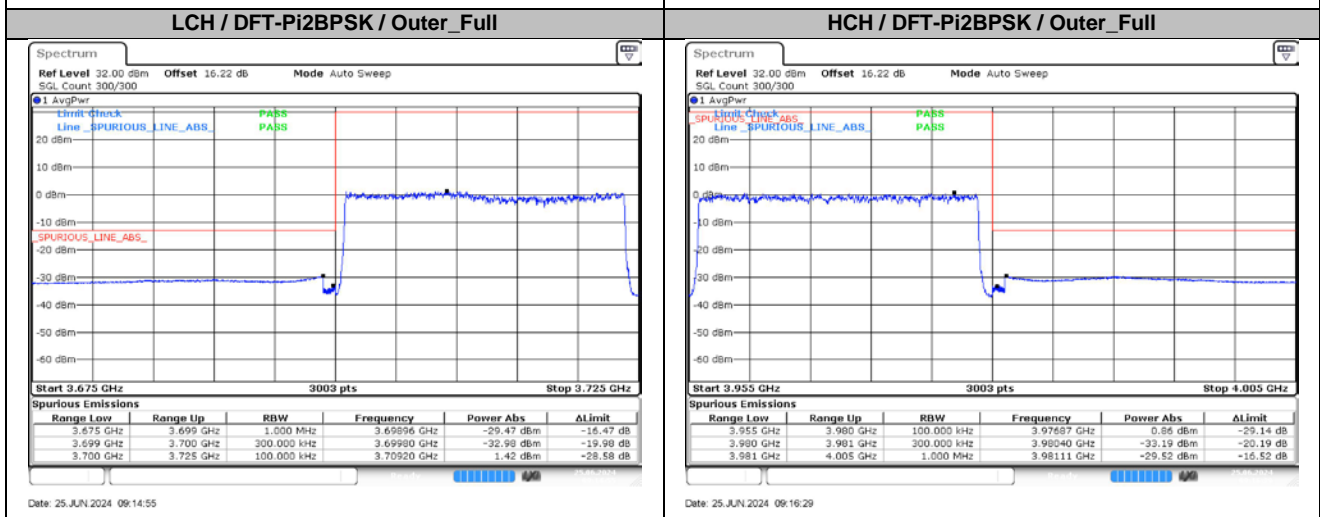
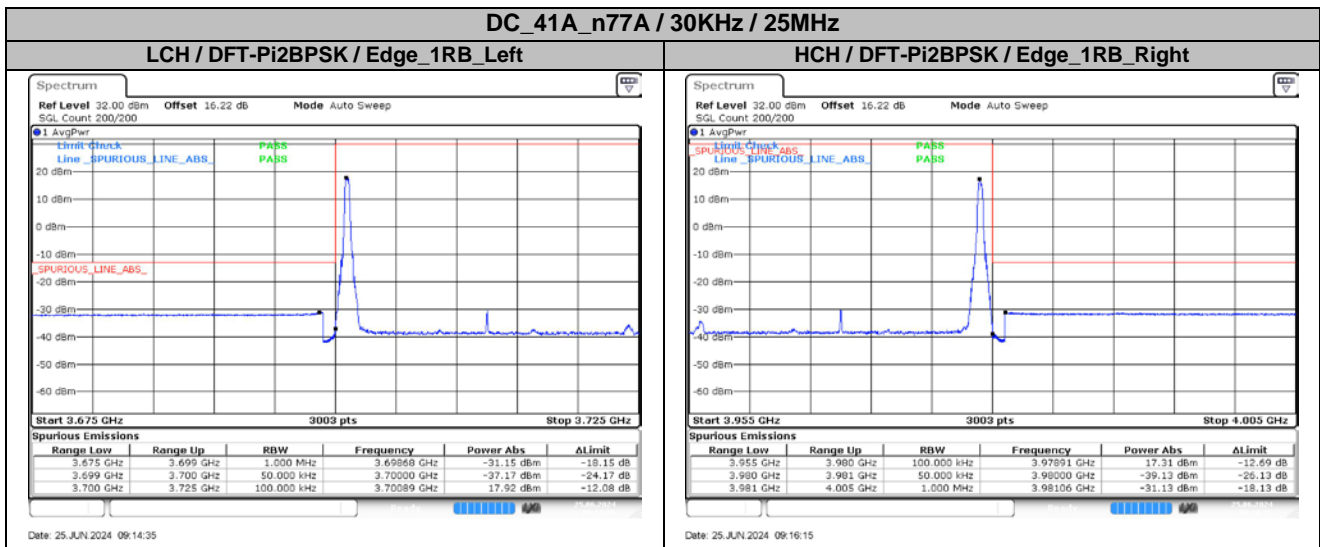
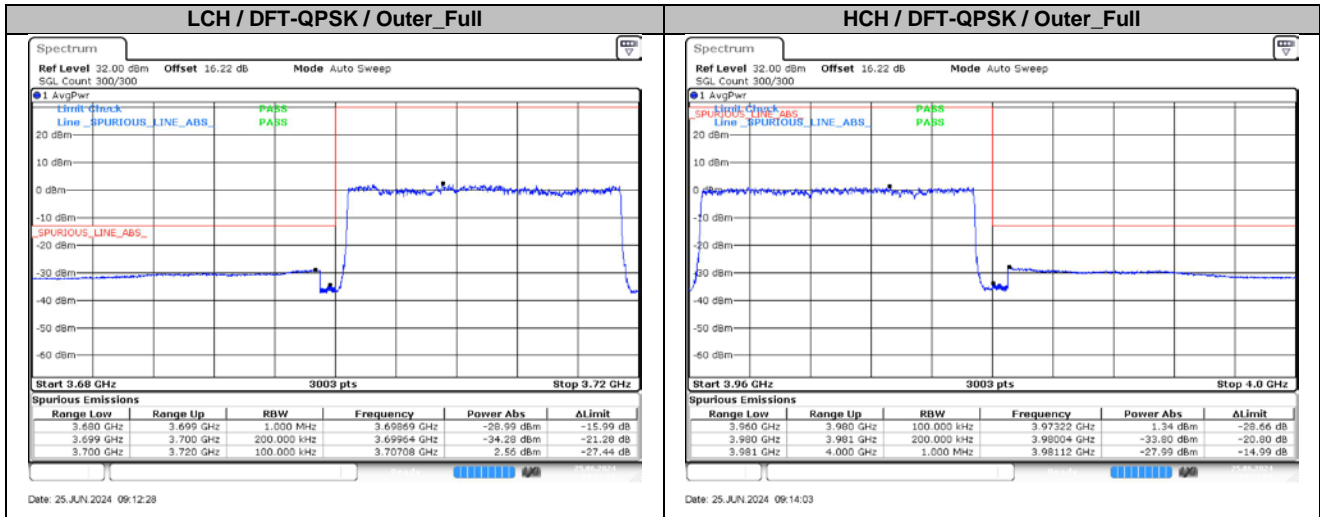


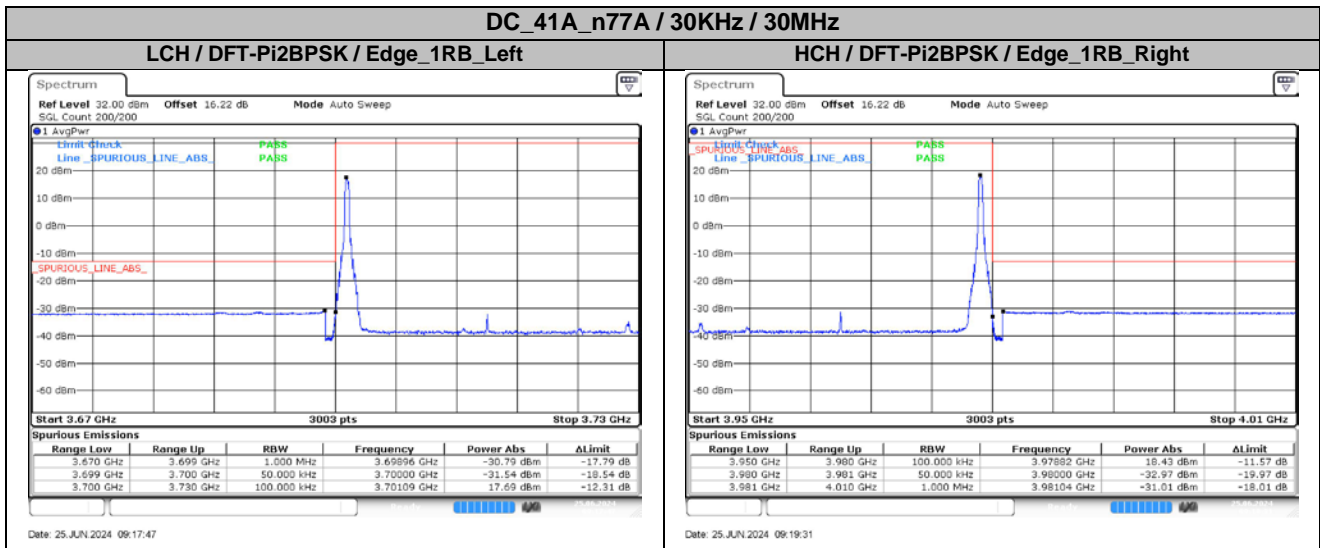
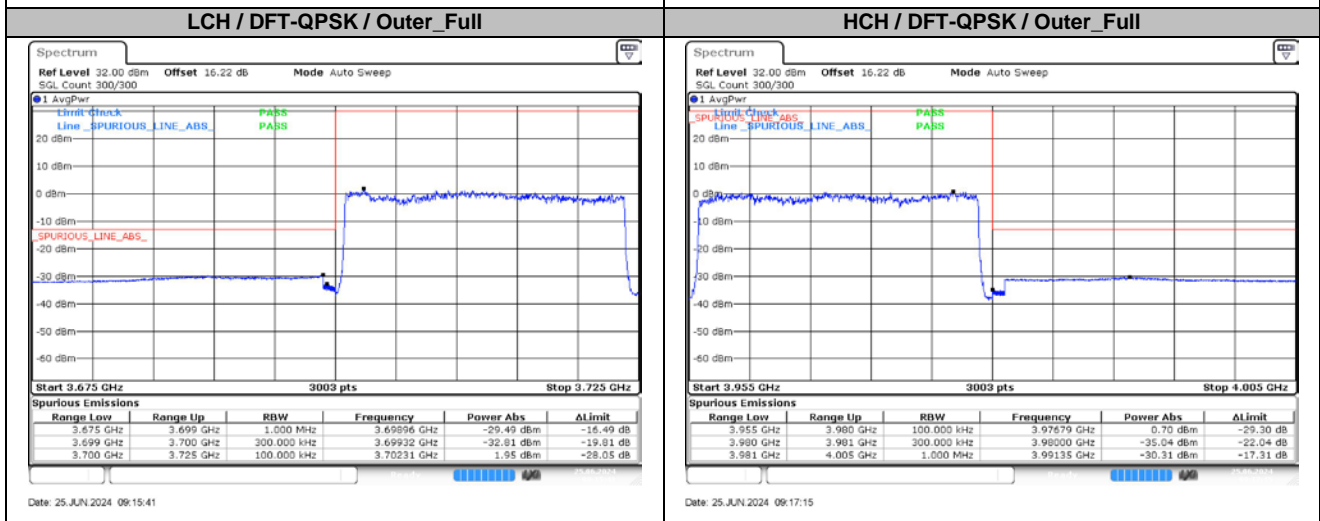
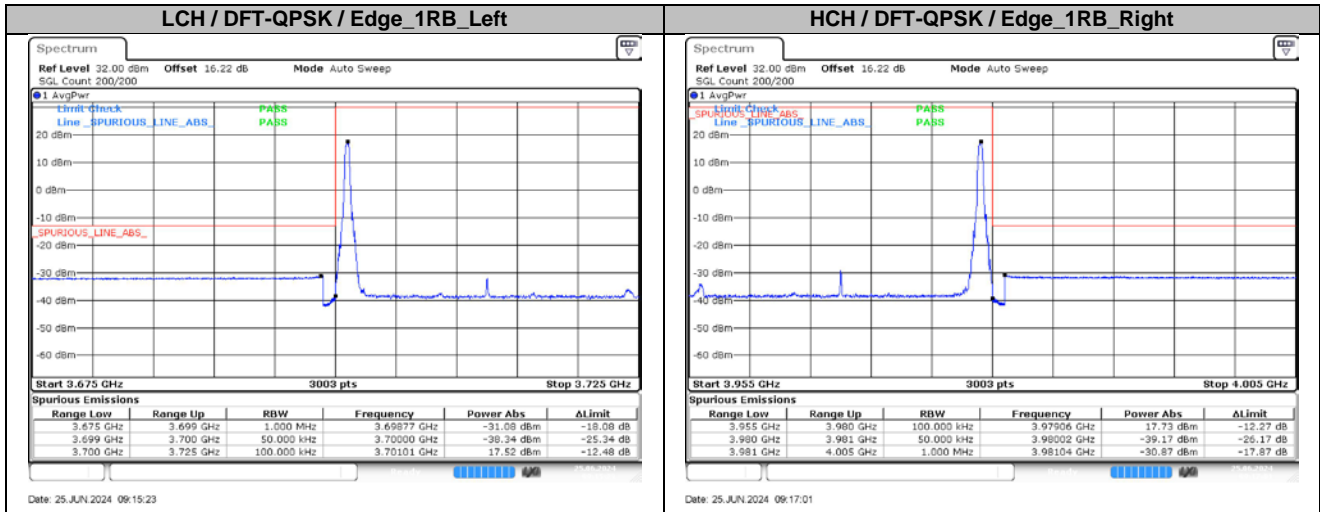
Date: 25 JUN 2024 09:12:11

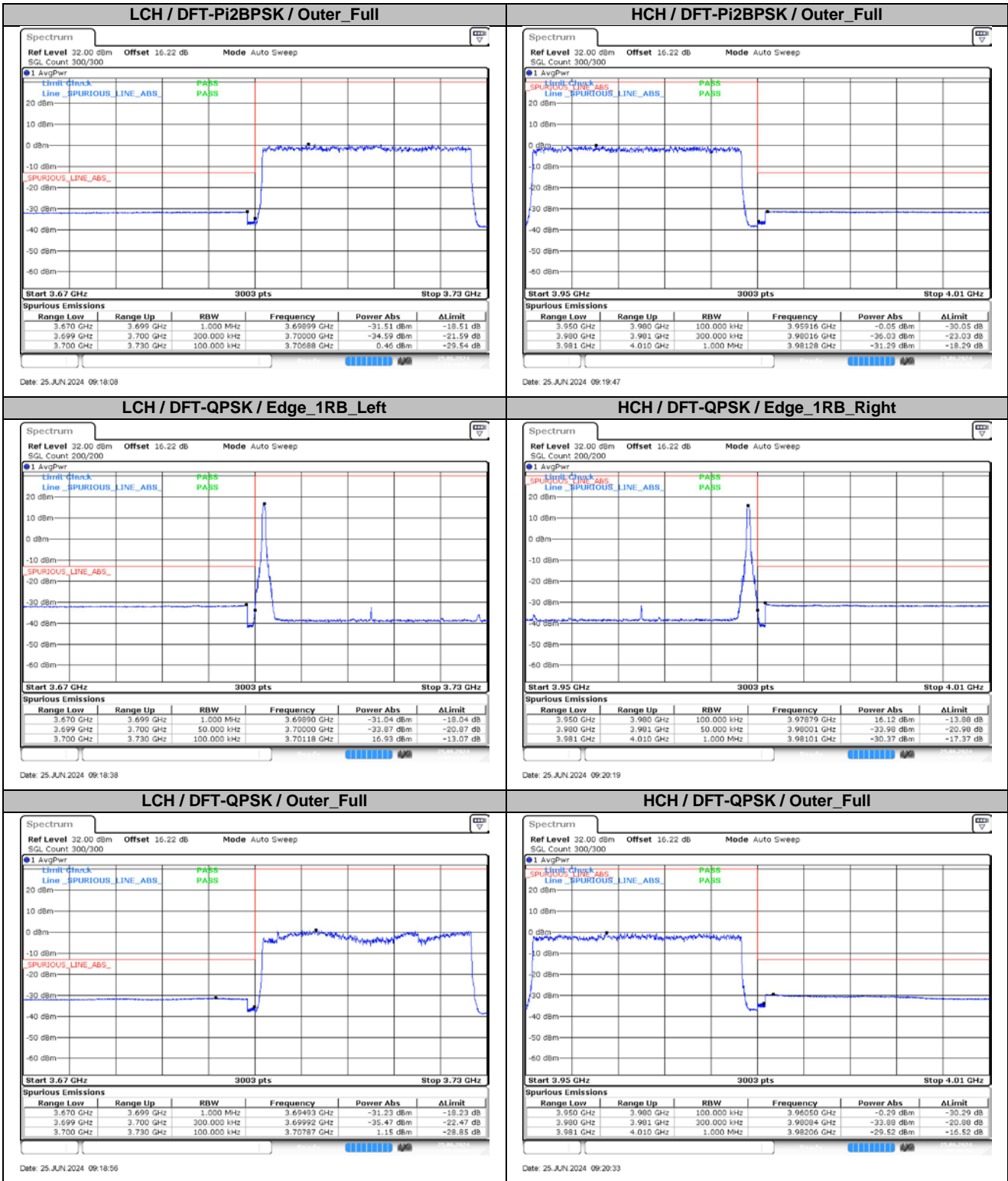
HCH / DFT-QPSK / Edge_1RB_Right



Date: 25 JUN 2024 09:13:49

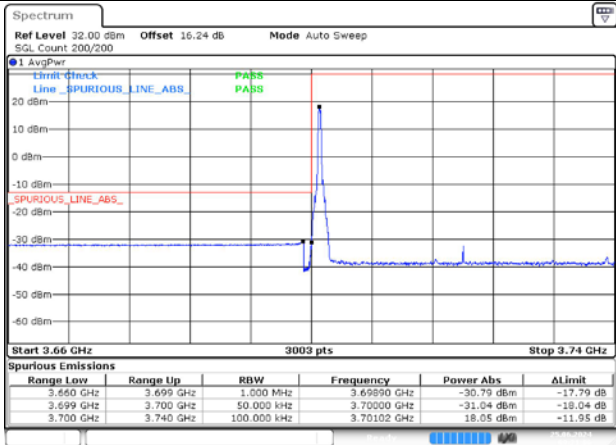






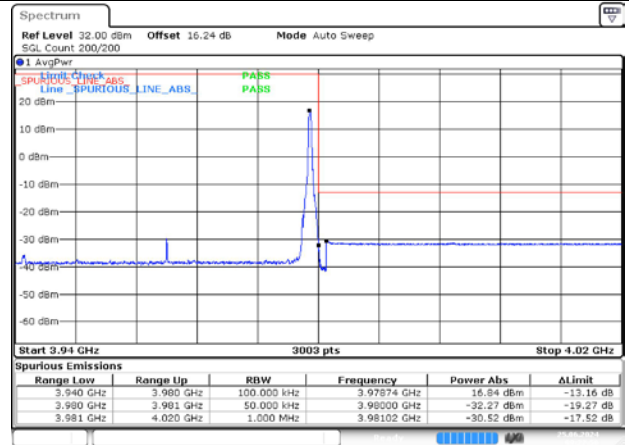
DC_41A_n77A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



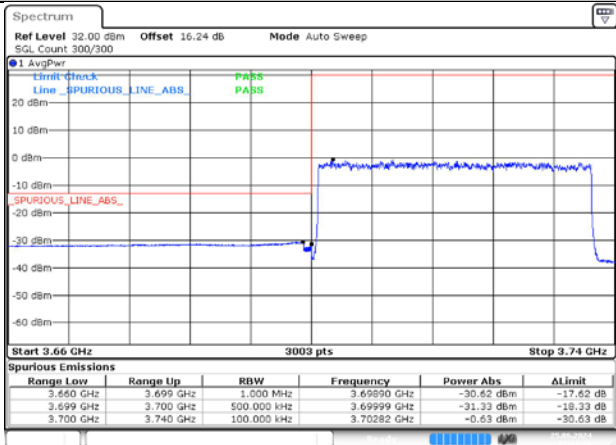
Date: 25 JUN 2024 09:21:06

HCH / DFT-Pi2BPSK / Edge_1RB_Right



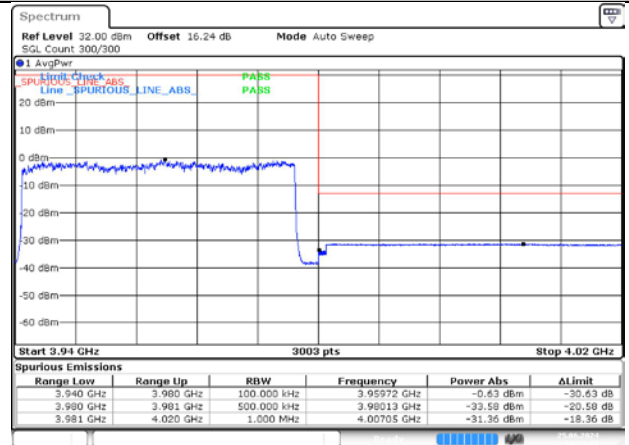
Date: 25 JUN 2024 09:22:50

LCH / DFT-Pi2BPSK / Outer_Full



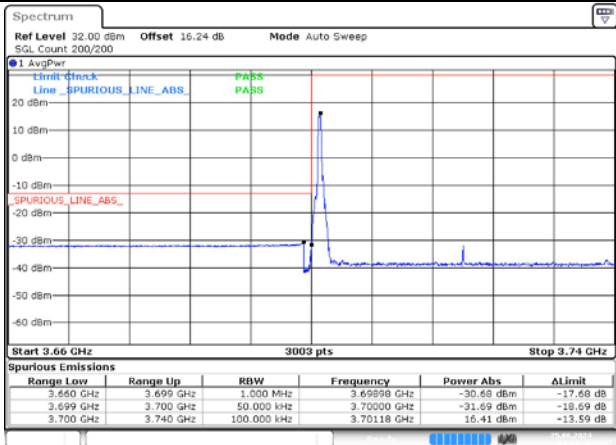
Date: 25 JUN 2024 09:21:26

HCH / DFT-Pi2BPSK / Outer_Full



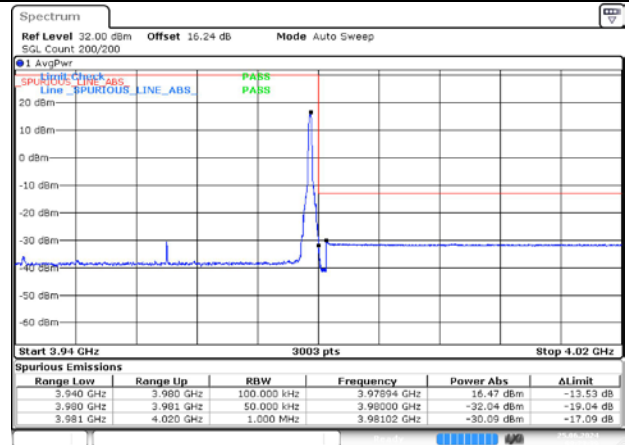
Date: 25 JUN 2024 09:23:06

LCH / DFT-QPSK / Edge_1RB_Left



Date: 25 JUN 2024 09:21:56

HCH / DFT-QPSK / Edge_1RB_Right



Date: 25 JUN 2024 09:23:40

