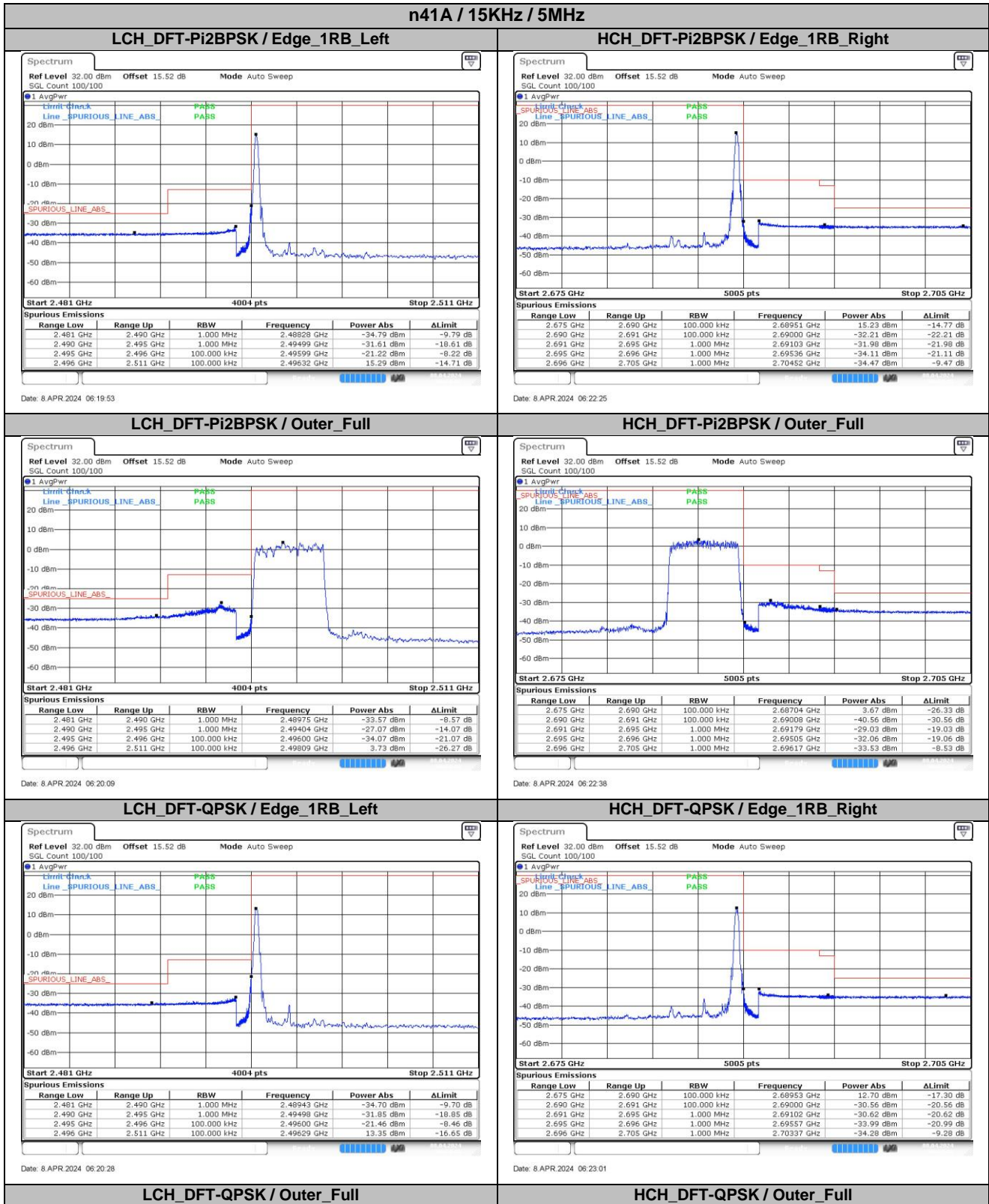
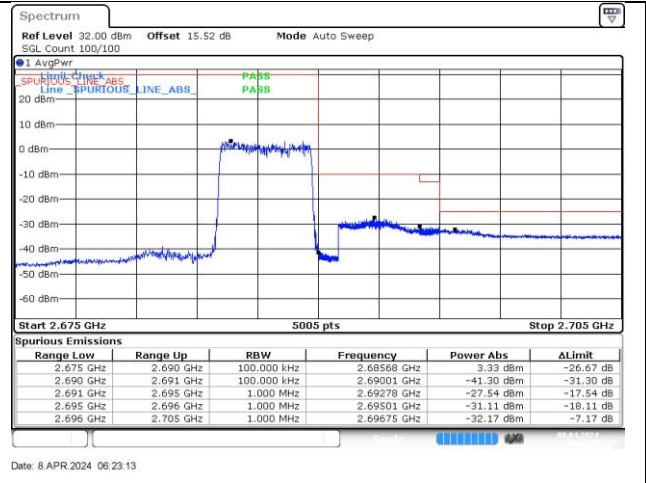
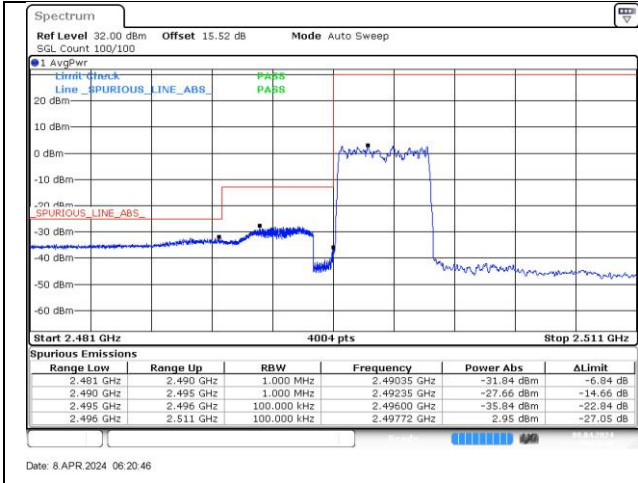


### 5. Conducted Band Edges

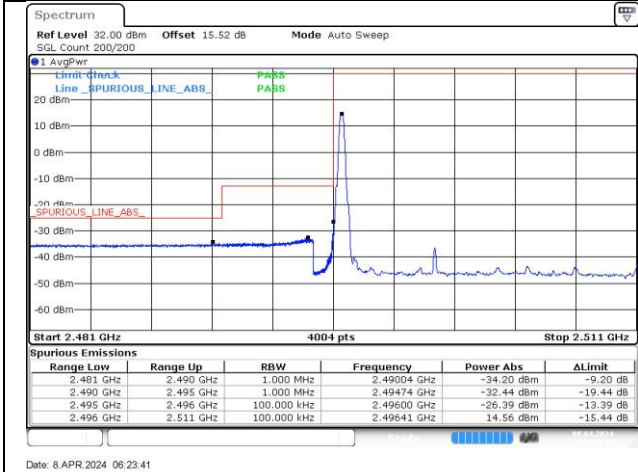
#### 5.1. Test Plots



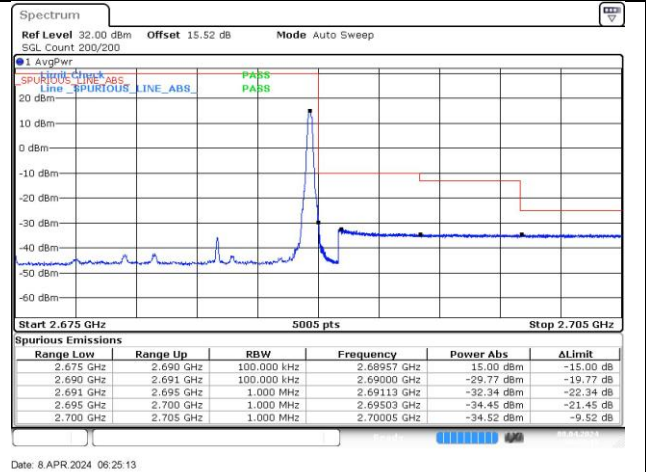


n41A / 15KHz / 10MHz

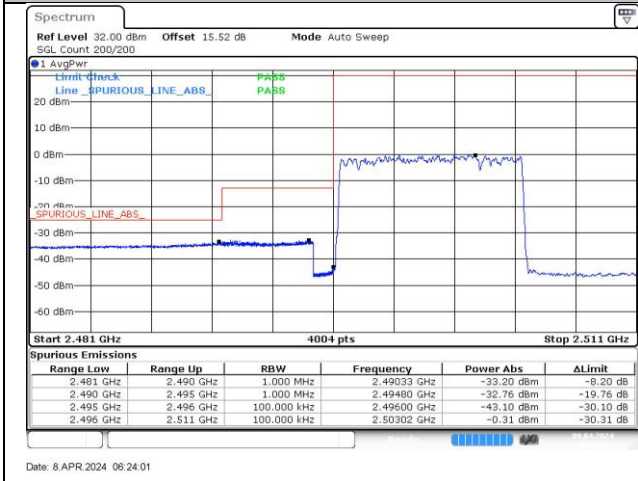
LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



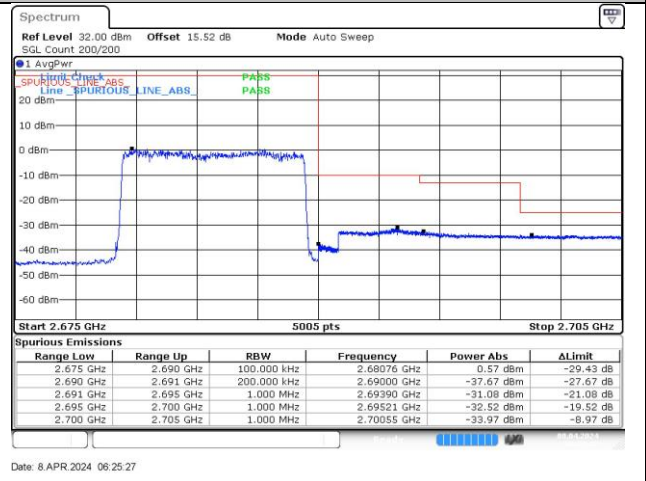
HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right

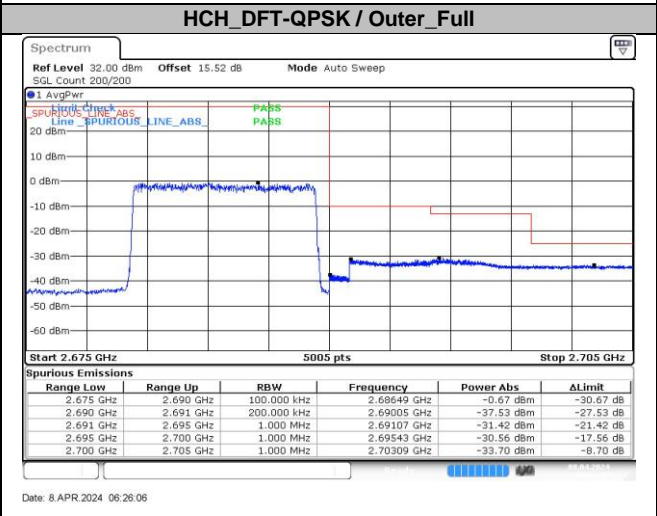
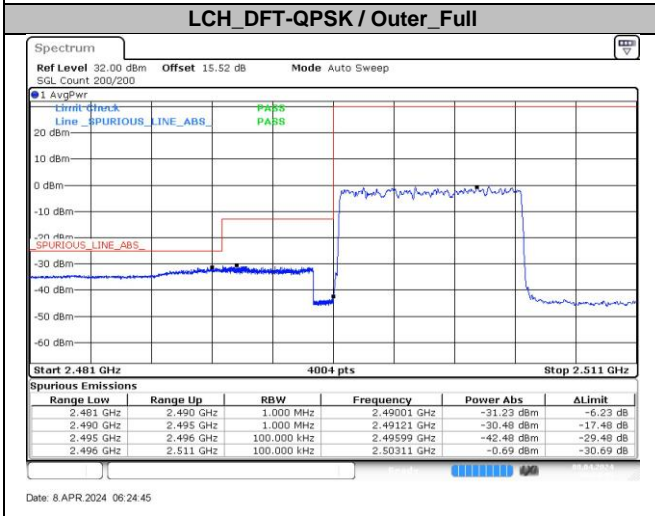
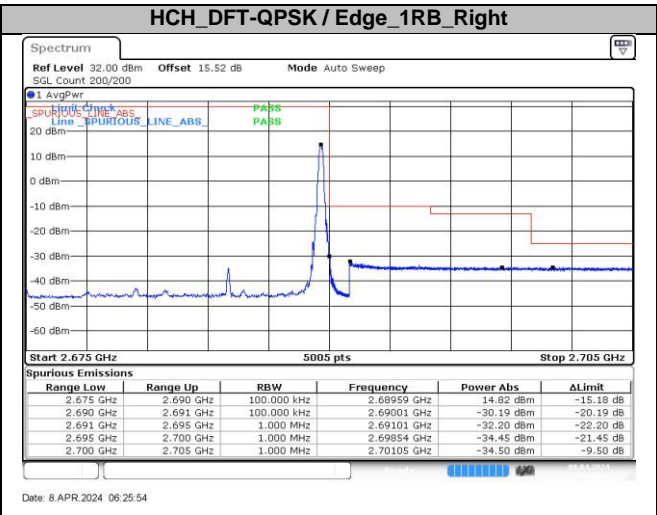
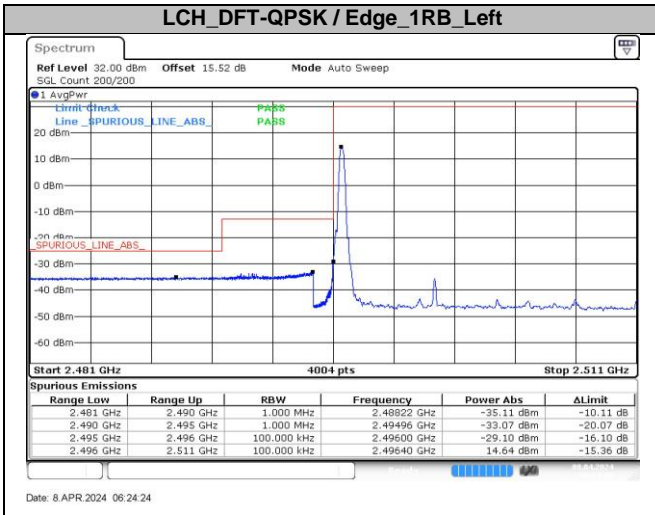


LCH\_DFT-Pi2BPSK / Outer\_Full

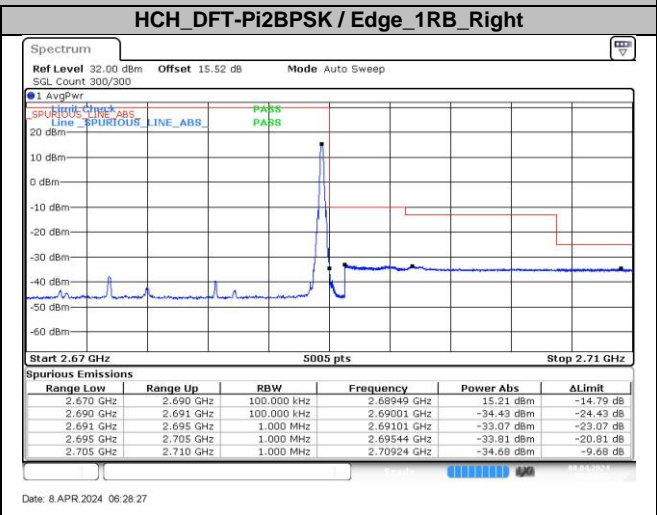
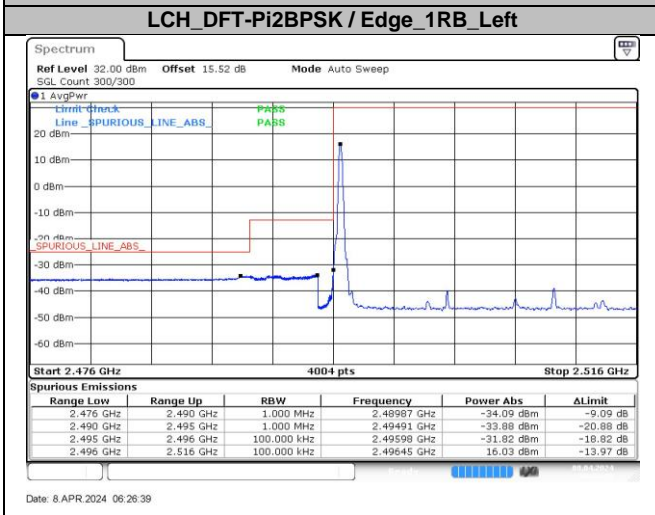


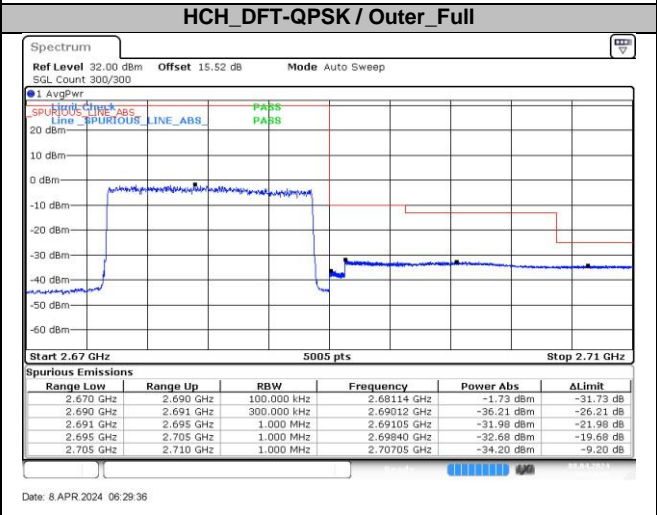
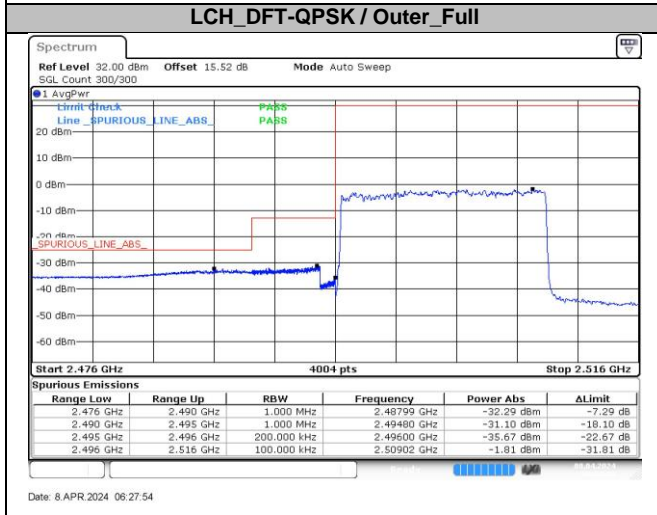
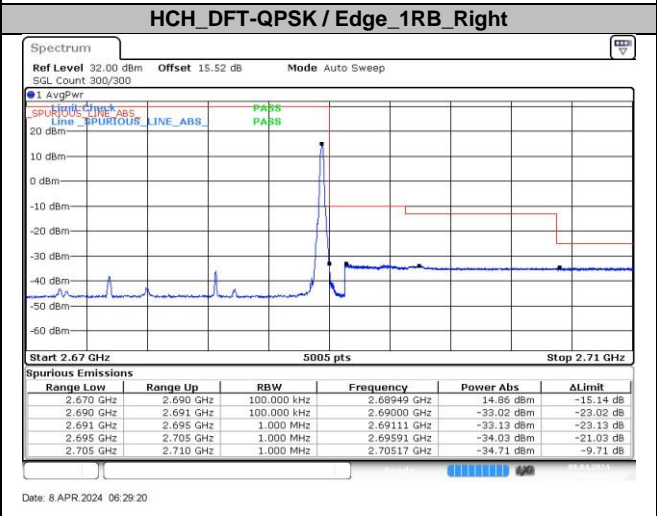
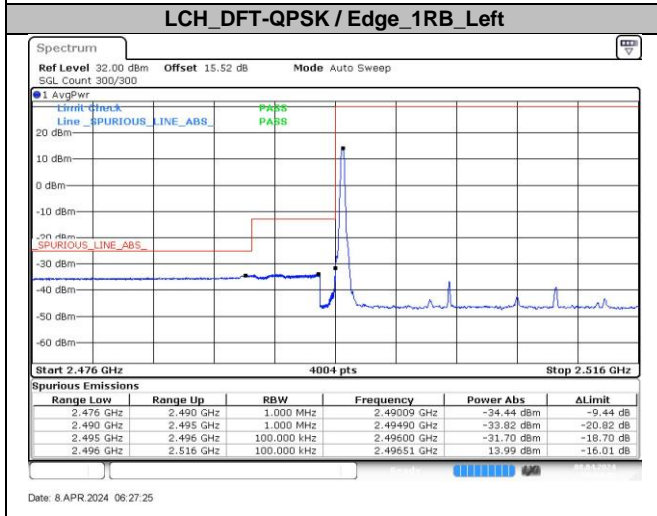
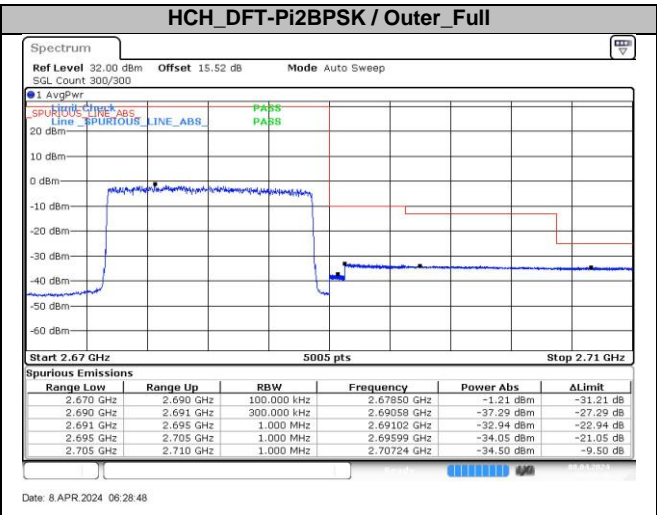
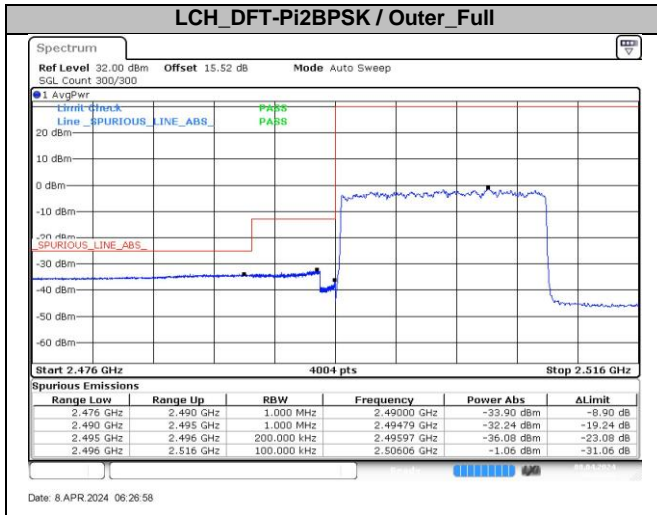
HCH\_DFT-Pi2BPSK / Outer\_Full





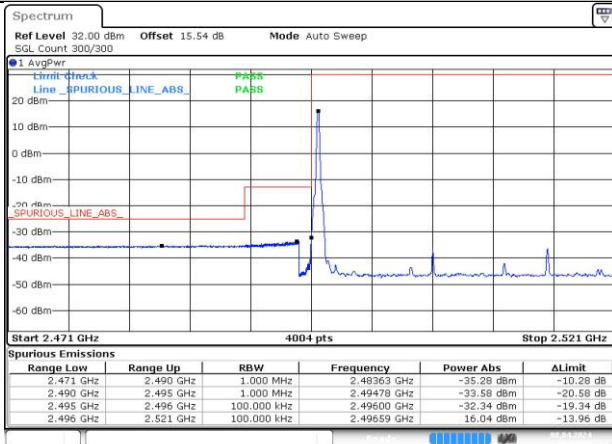
## n41A / 15KHz / 15MHz





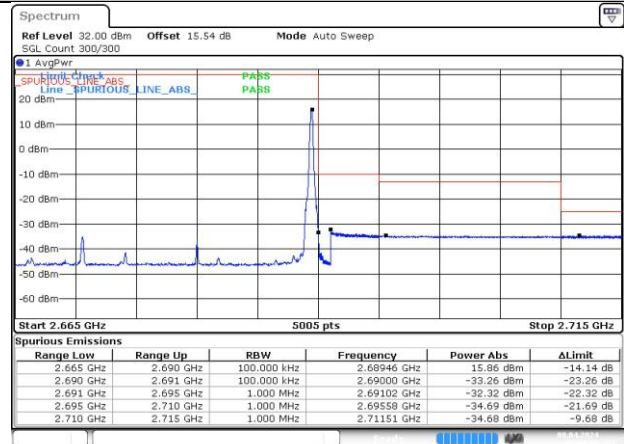
n41A / 15KHz / 20MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



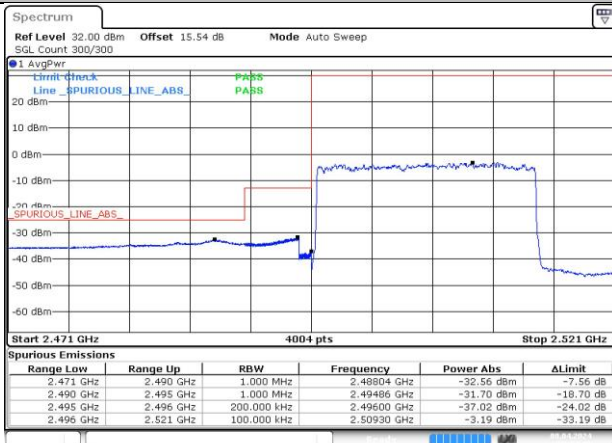
Date: 8 APR 2024 06:30:09

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



Date: 8 APR 2024 06:31:51

LCH\_DFT-Pi2BPSK / Outer\_Full



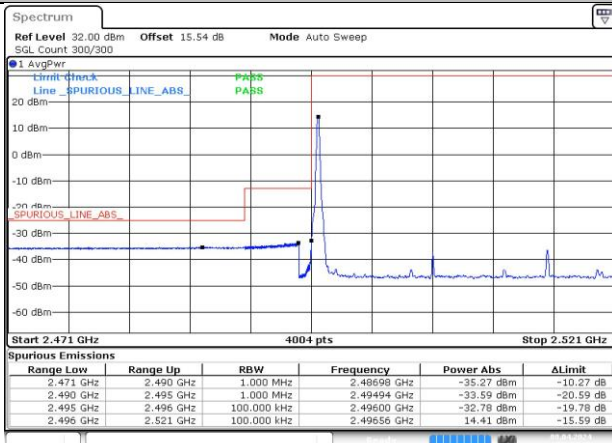
Date: 8 APR 2024 06:30:28

HCH\_DFT-Pi2BPSK / Outer\_Full



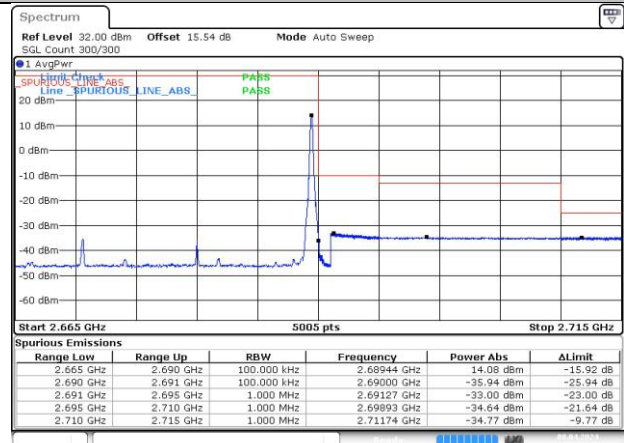
Date: 8 APR 2024 06:32:07

LCH\_DFT-QPSK / Edge\_1RB\_Left

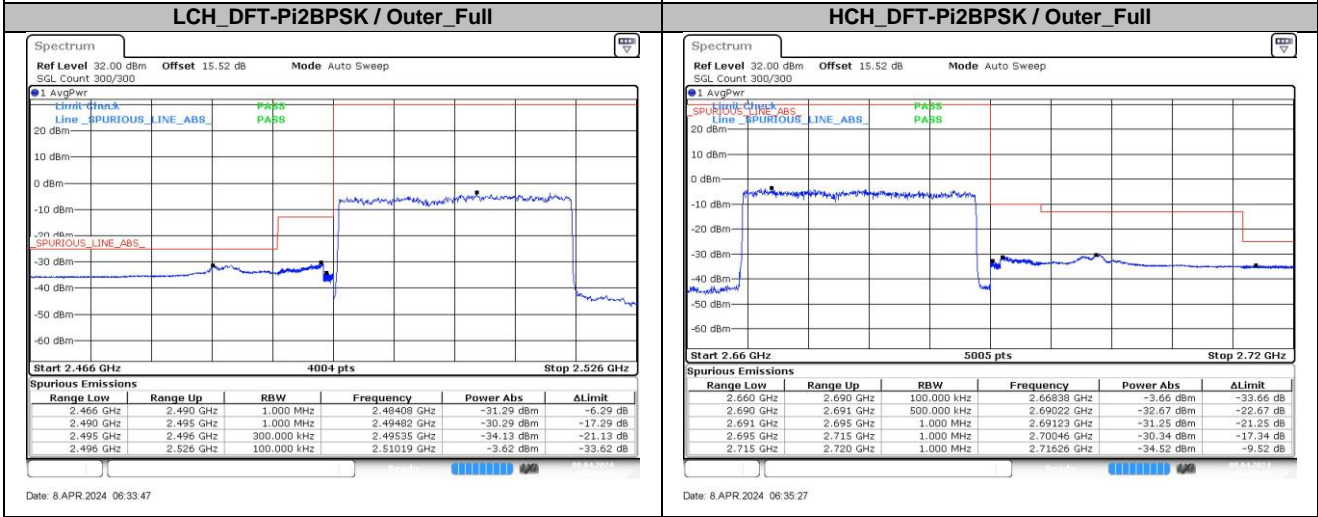
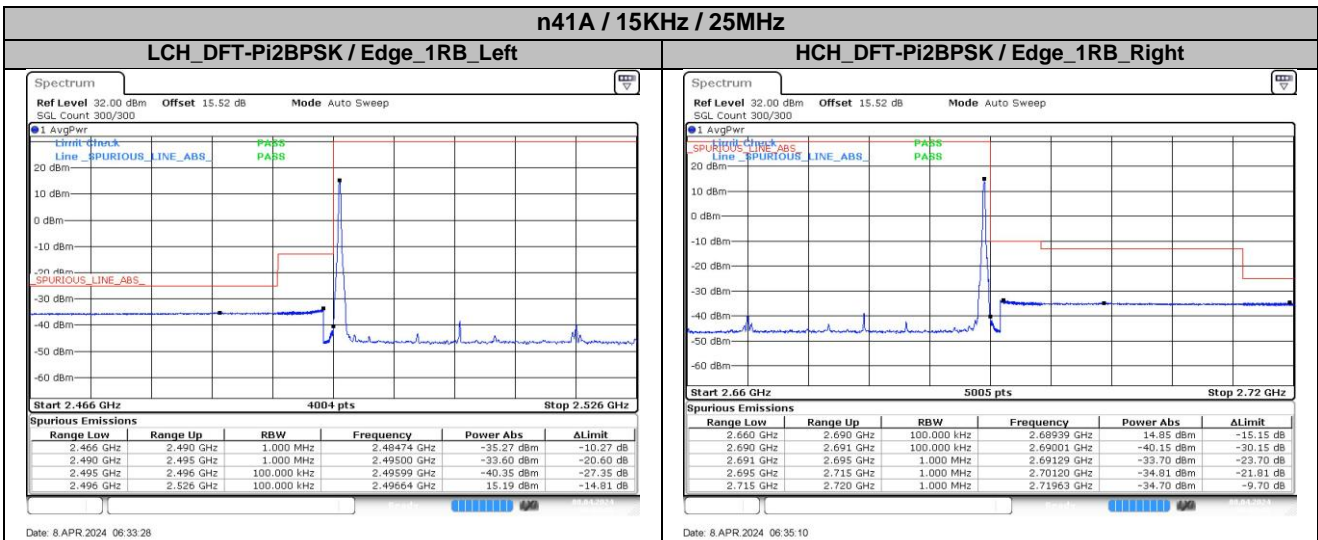
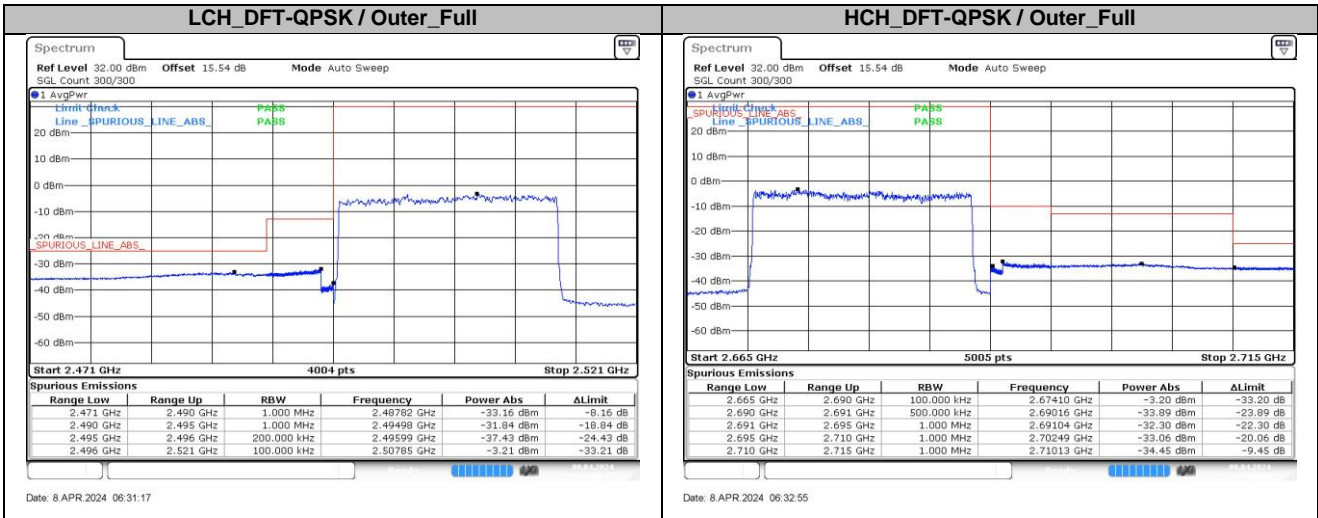


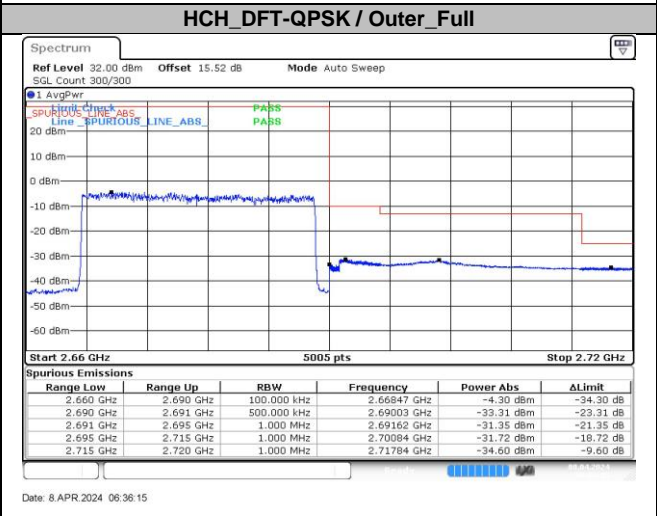
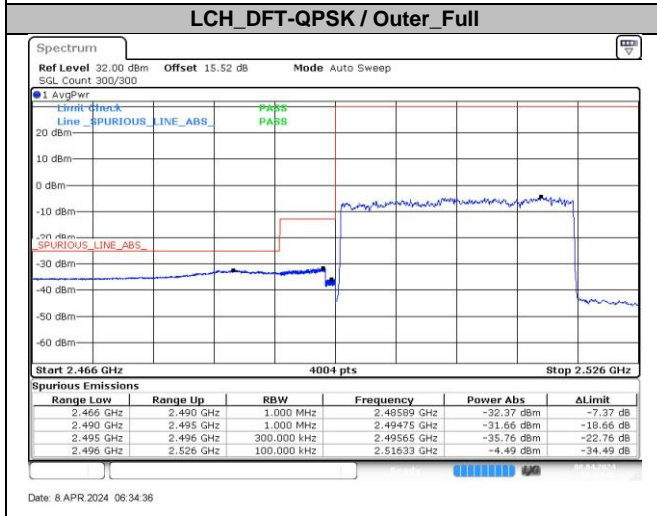
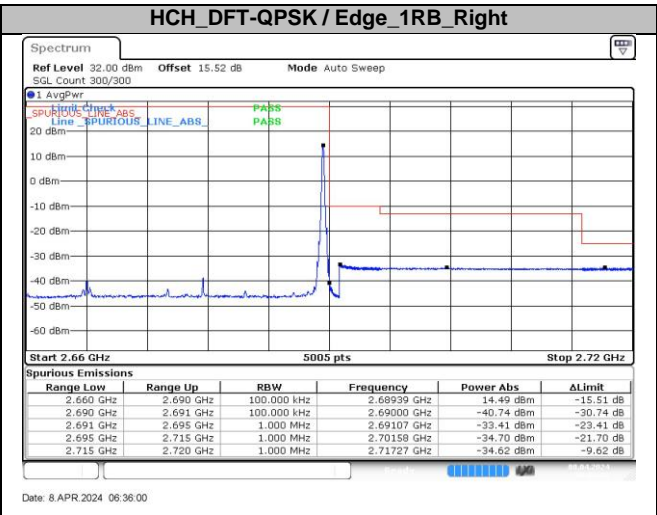
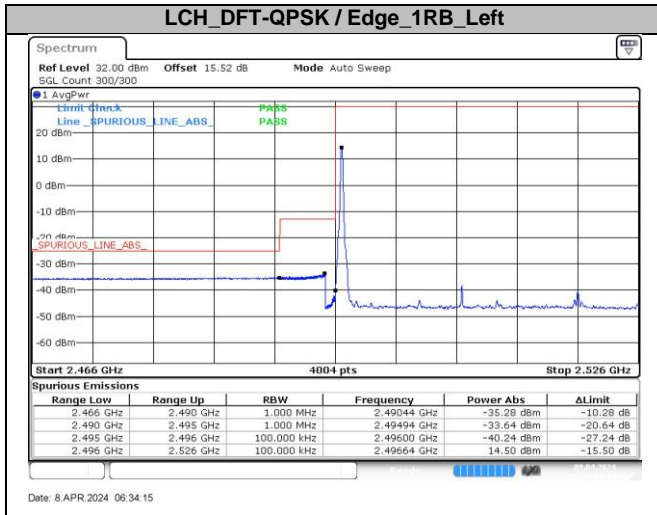
Date: 8 APR 2024 06:30:56

HCH\_DFT-QPSK / Edge\_1RB\_Right

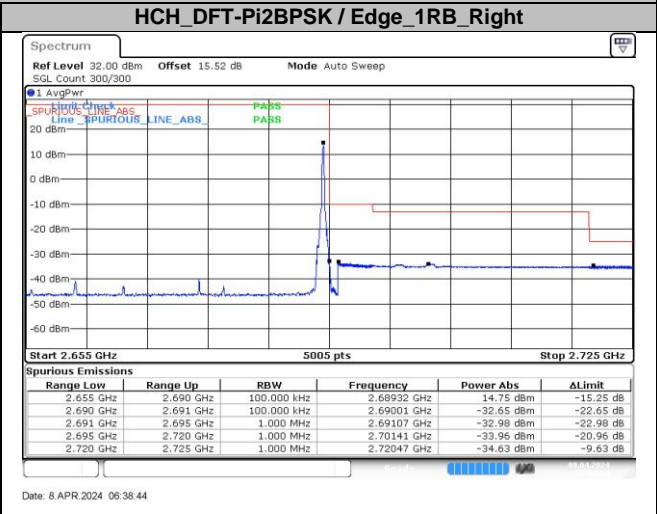
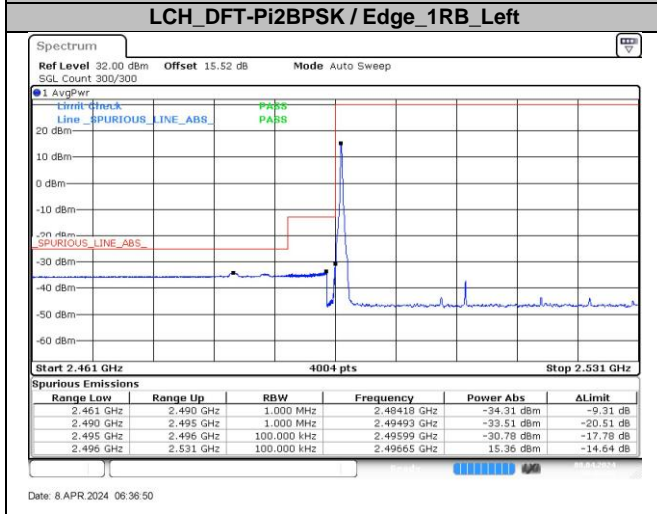


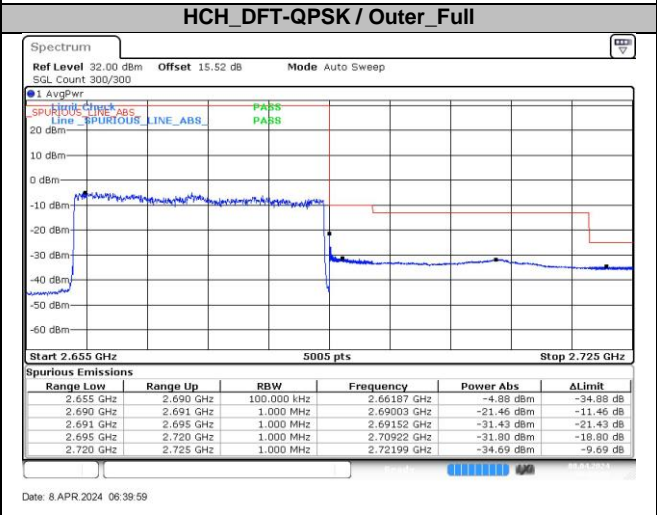
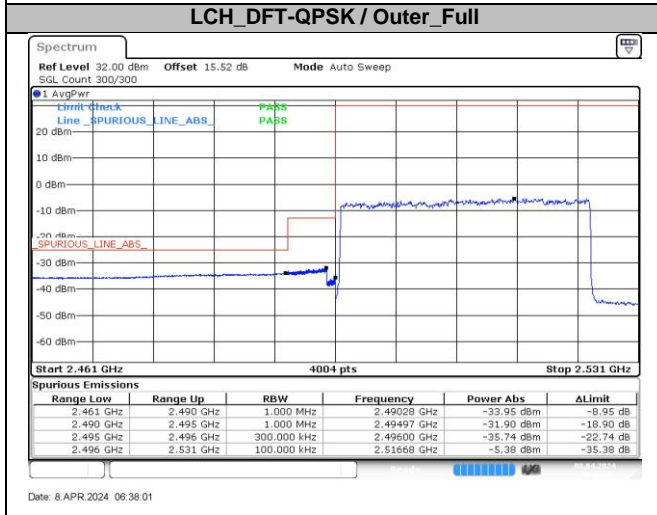
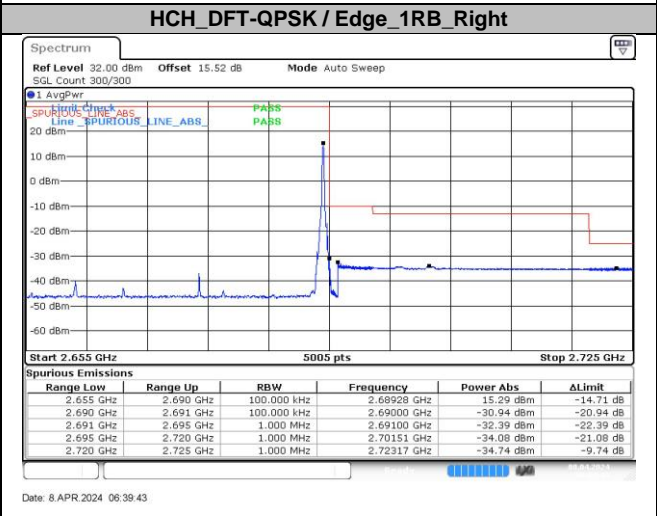
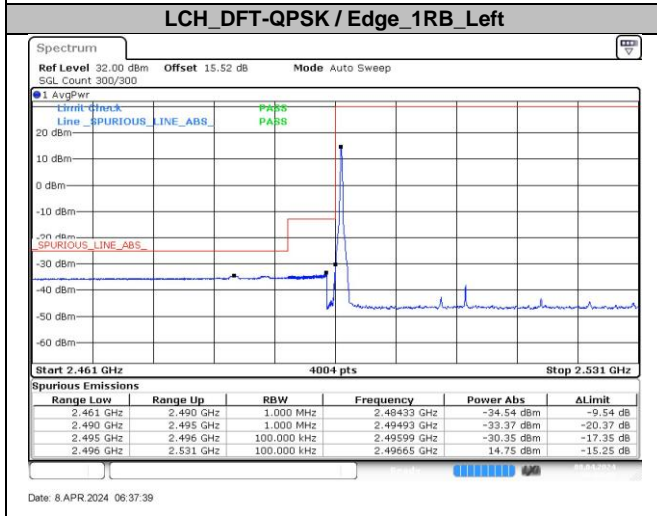
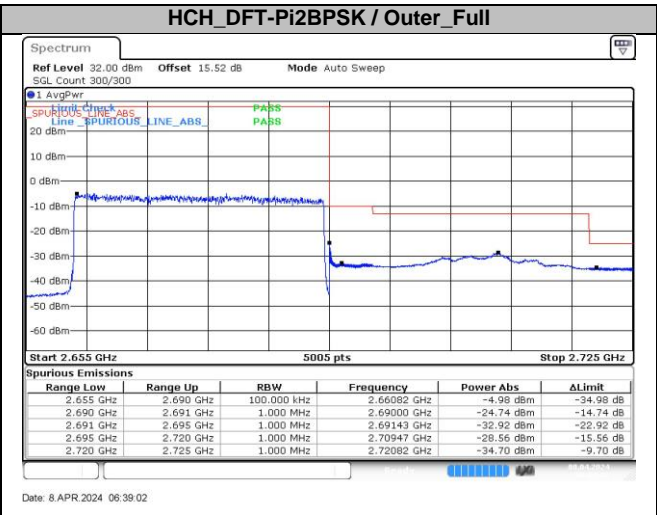
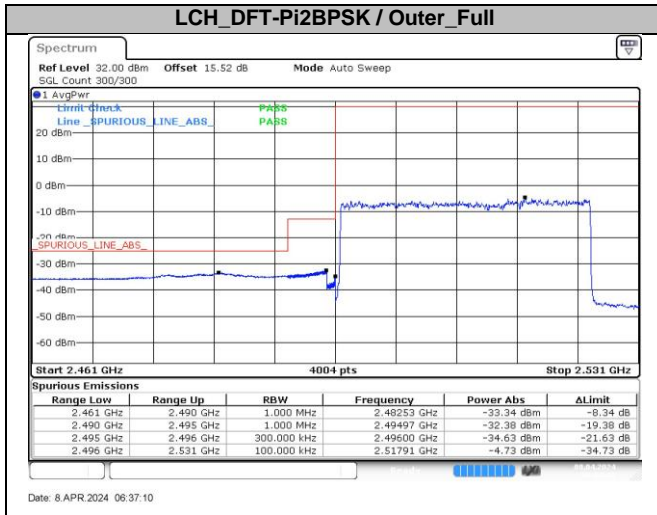
Date: 8 APR 2024 06:32:40





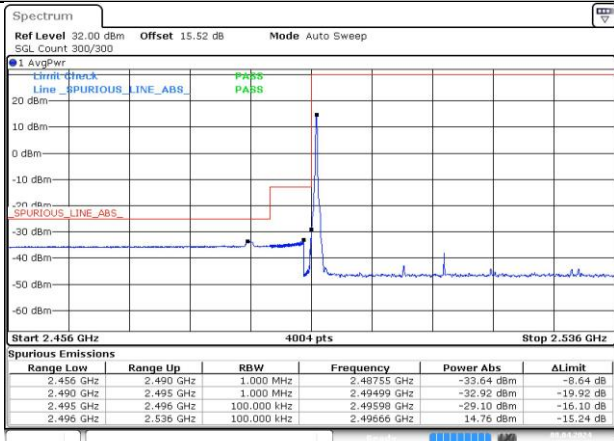
## n41A / 15KHz / 30MHz





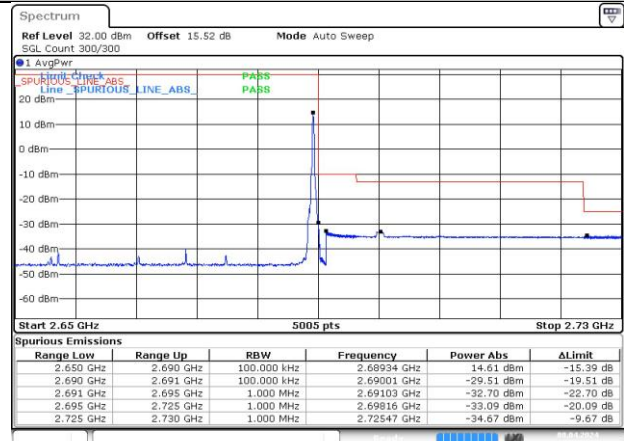
n41A / 15KHz / 35MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



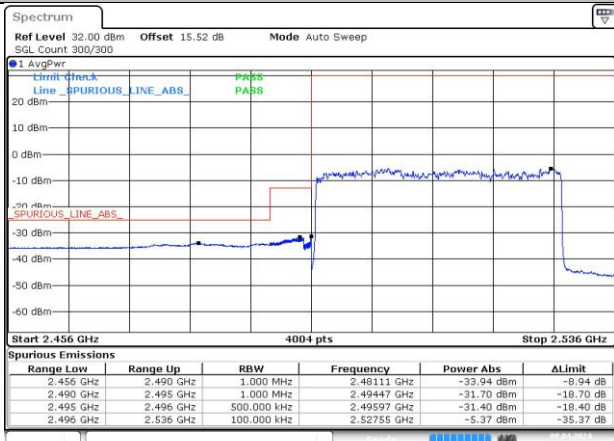
Date: 8 APR 2024 06:40:35

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



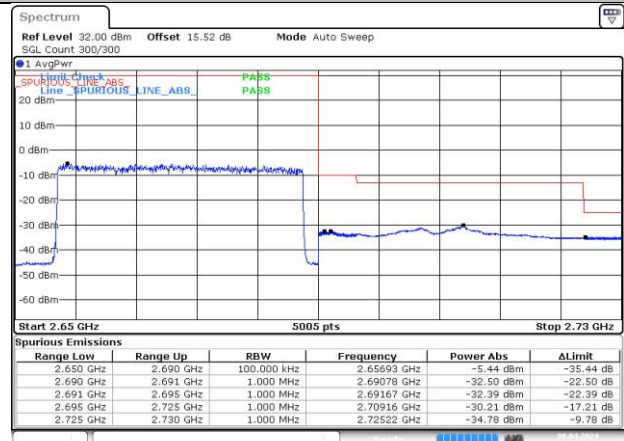
Date: 8 APR 2024 06:42:24

LCH\_DFT-Pi2BPSK / Outer\_Full



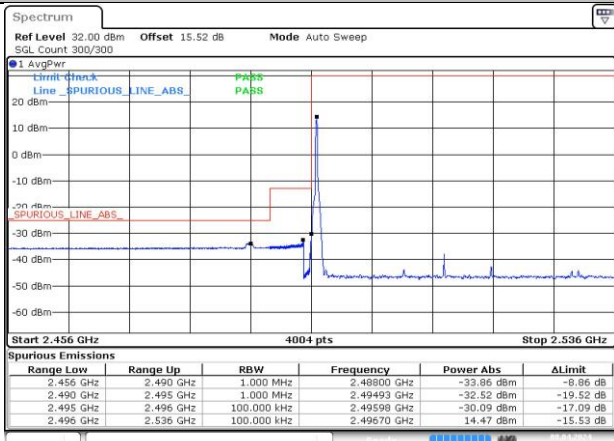
Date: 8 APR 2024 06:40:56

HCH\_DFT-Pi2BPSK / Outer\_Full



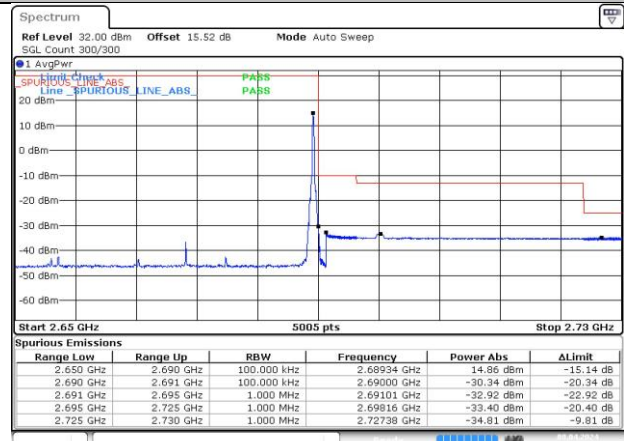
Date: 8 APR 2024 06:42:22

LCH\_DFT-QPSK / Edge\_1RB\_Left

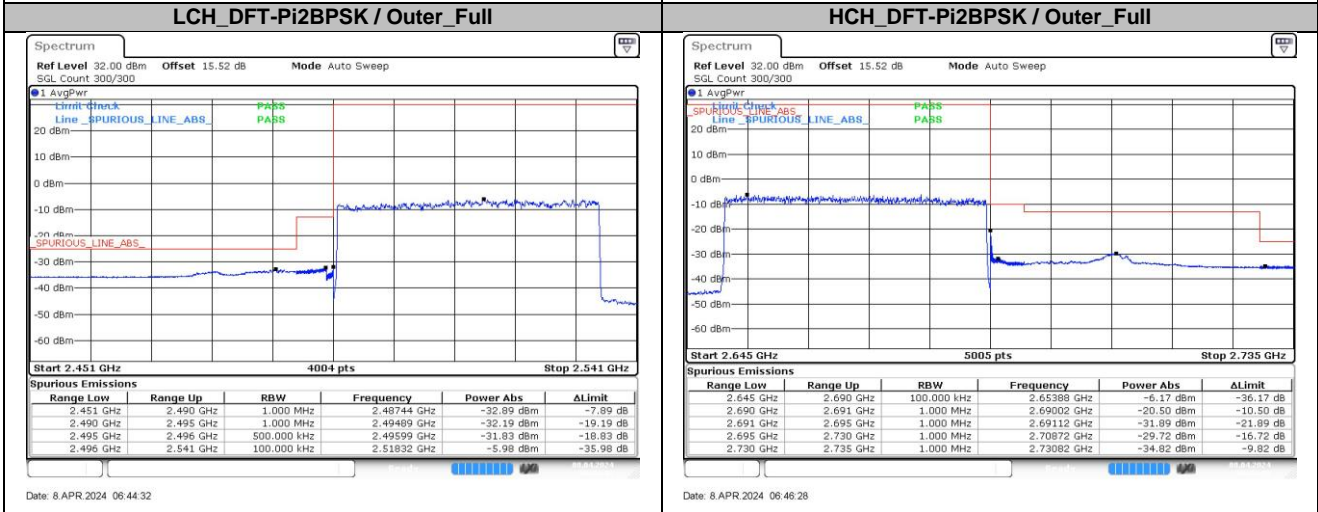
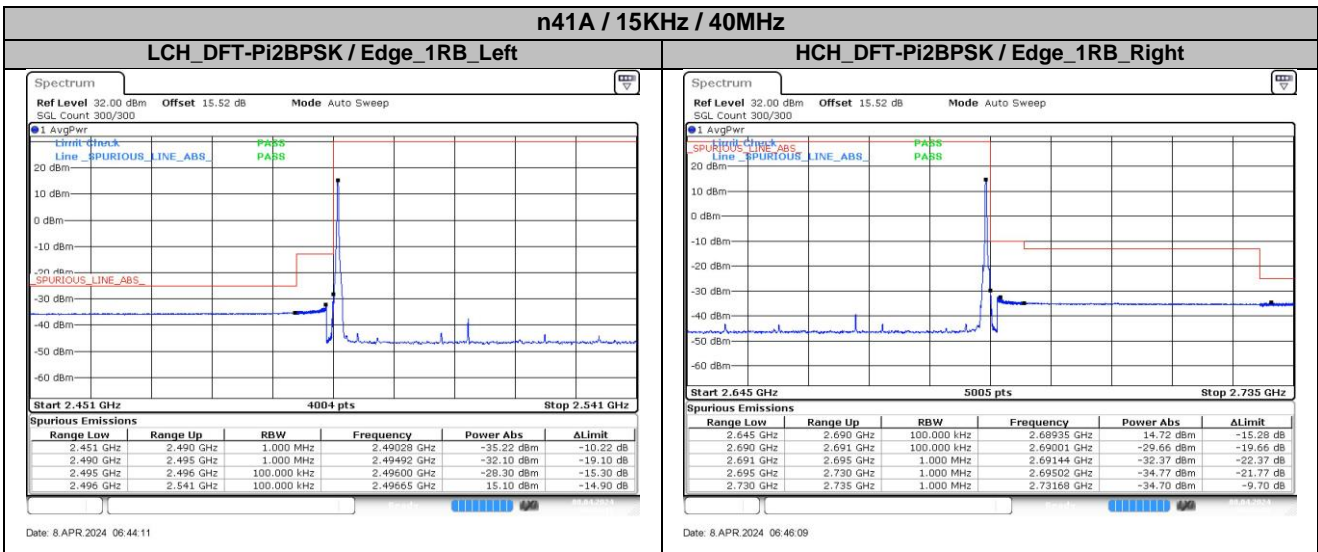
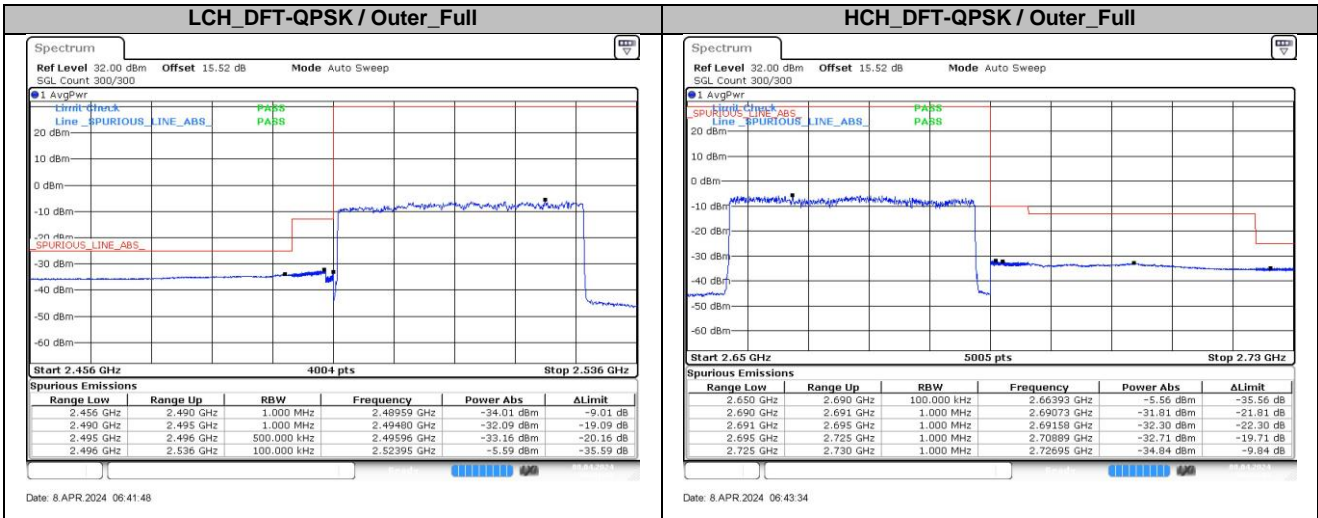


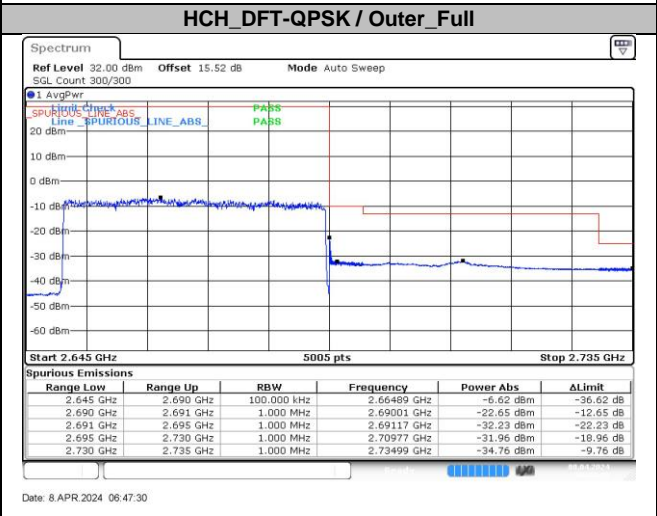
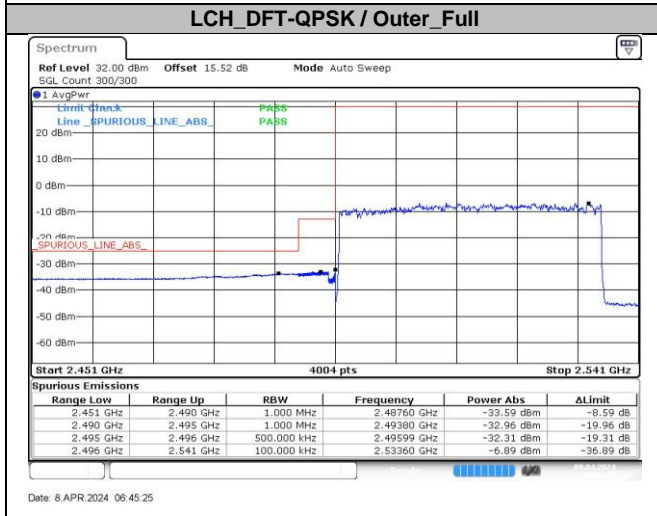
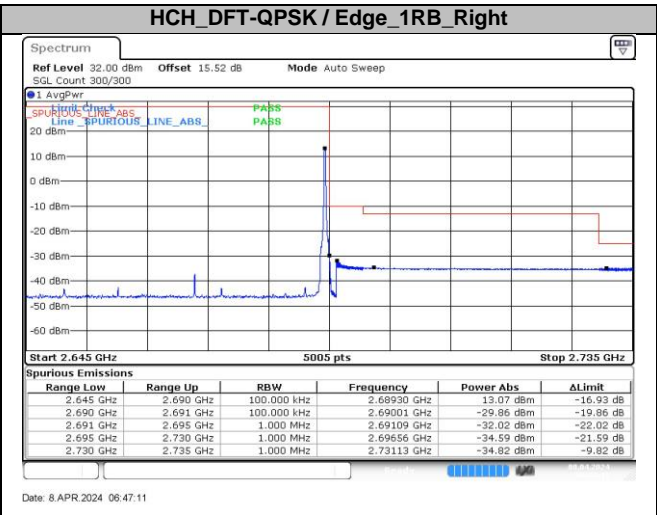
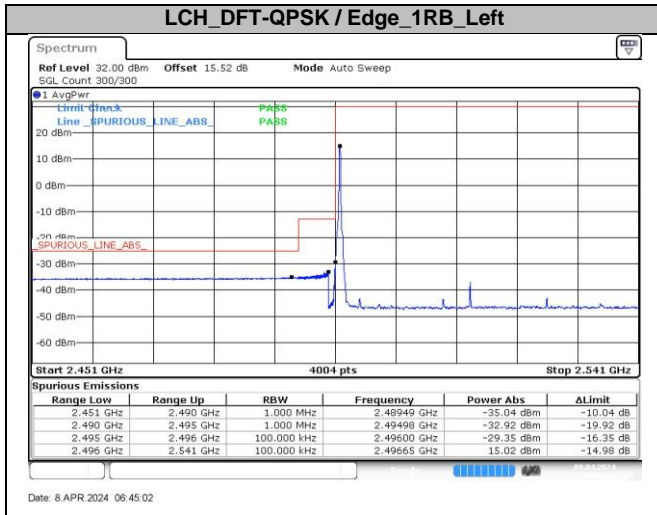
Date: 8 APR 2024 06:41:26

HCH\_DFT-QPSK / Edge\_1RB\_Right

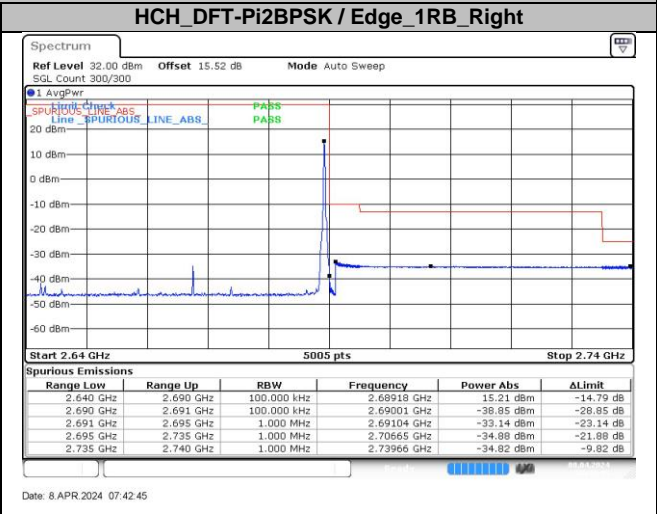
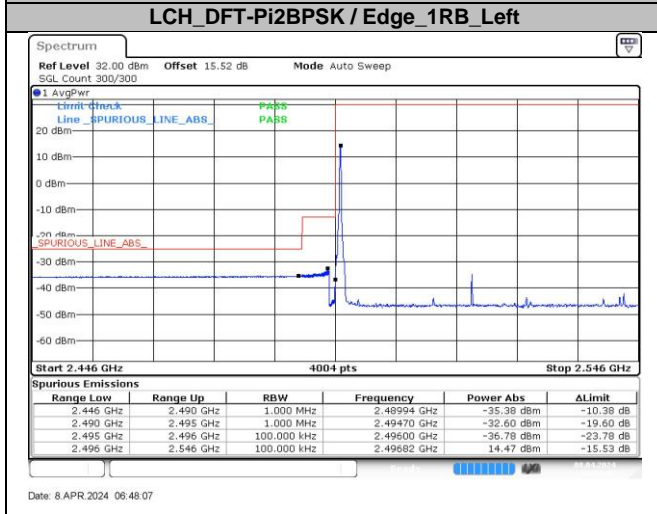


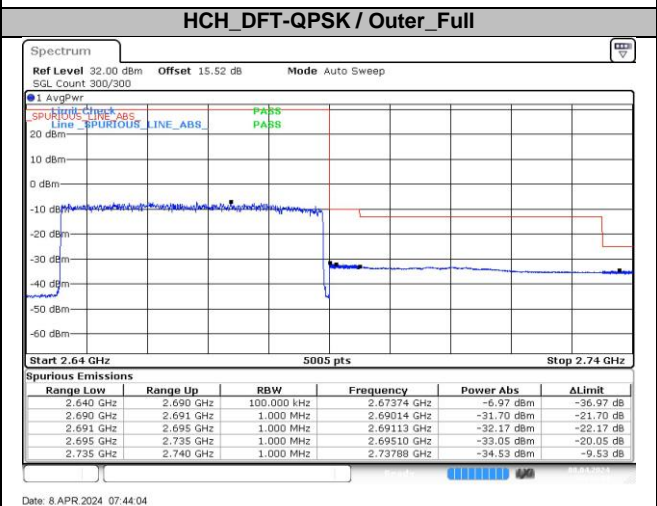
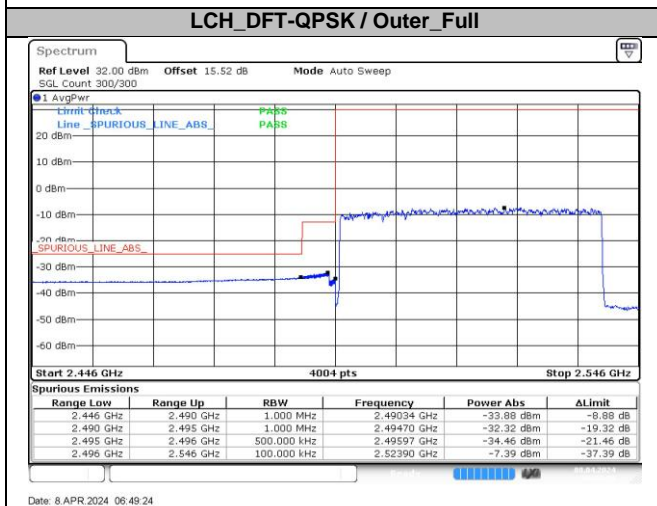
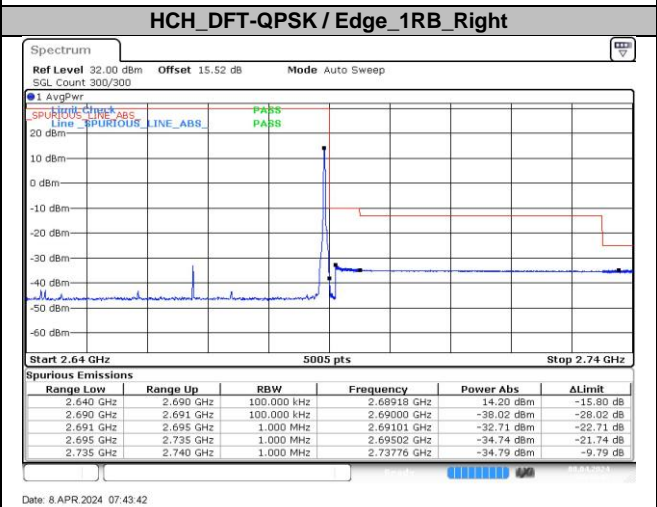
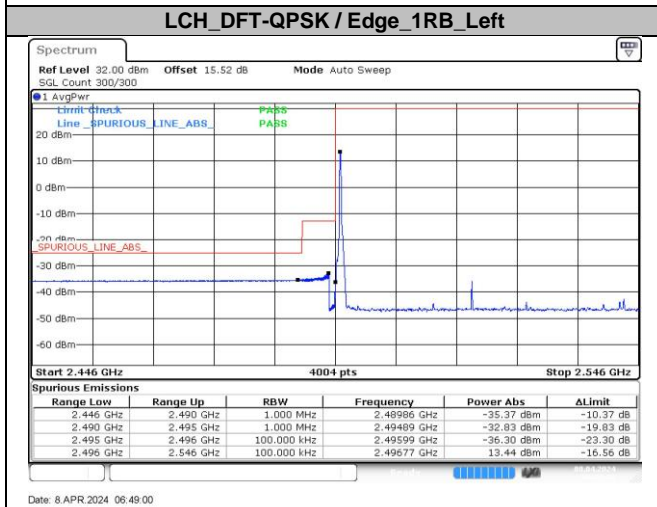
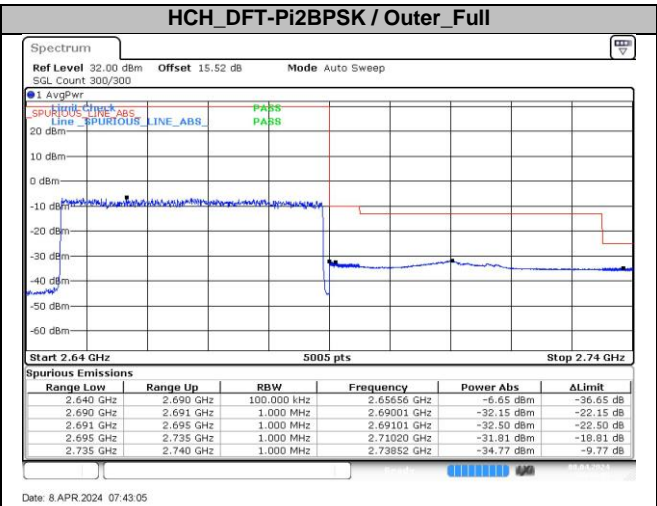
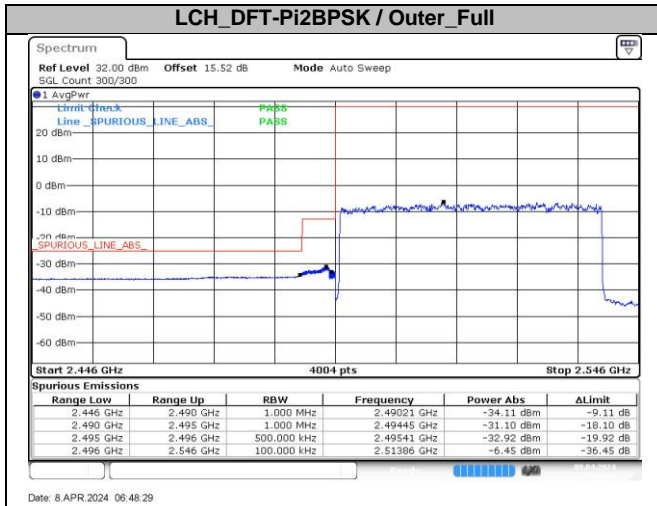
Date: 8 APR 2024 06:43:17





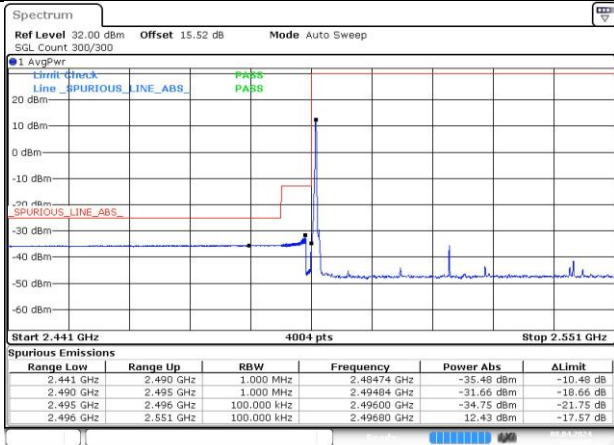
## n41A / 15KHz / 45MHz





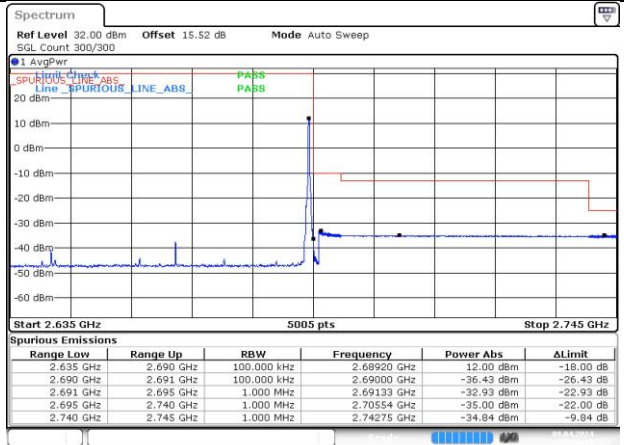
n41A / 15KHz / 50MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



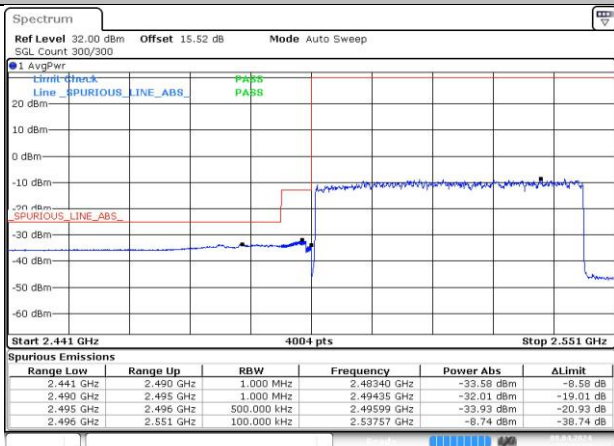
Date: 8 APR 2024 07:44:35

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



Date: 8 APR 2024 07:46:19

LCH\_DFT-Pi2BPSK / Outer\_Full



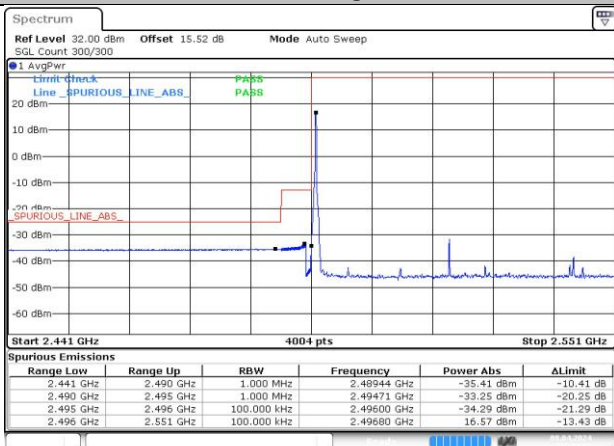
Date: 8 APR 2024 07:44:54

HCH\_DFT-Pi2BPSK / Outer\_Full



Date: 8 APR 2024 07:46:37

LCH\_DFT-QPSK / Edge\_1RB\_Left

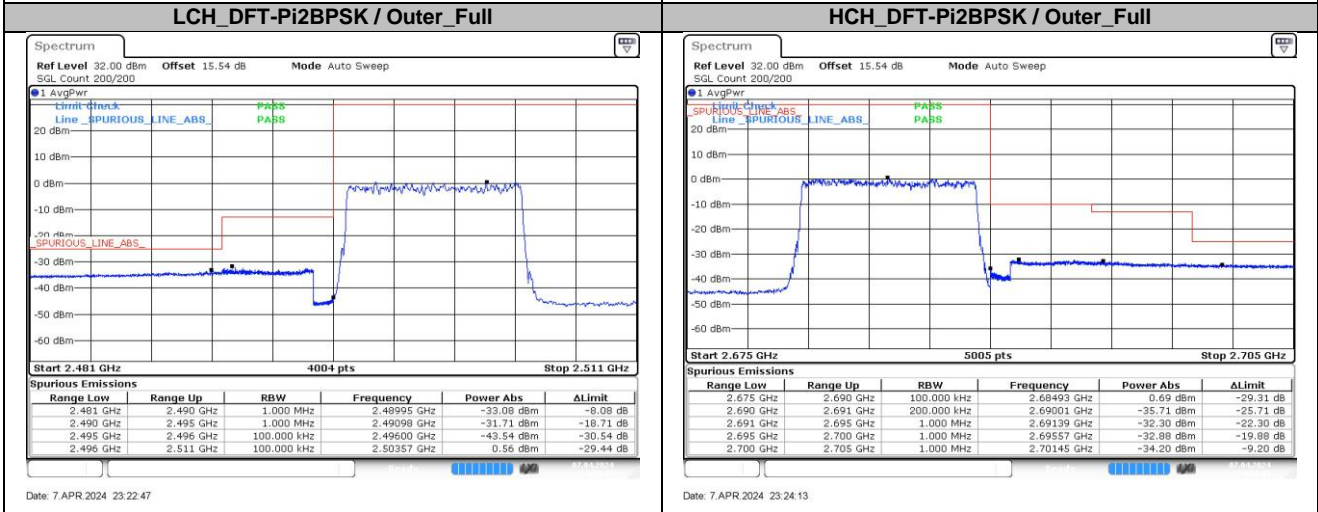
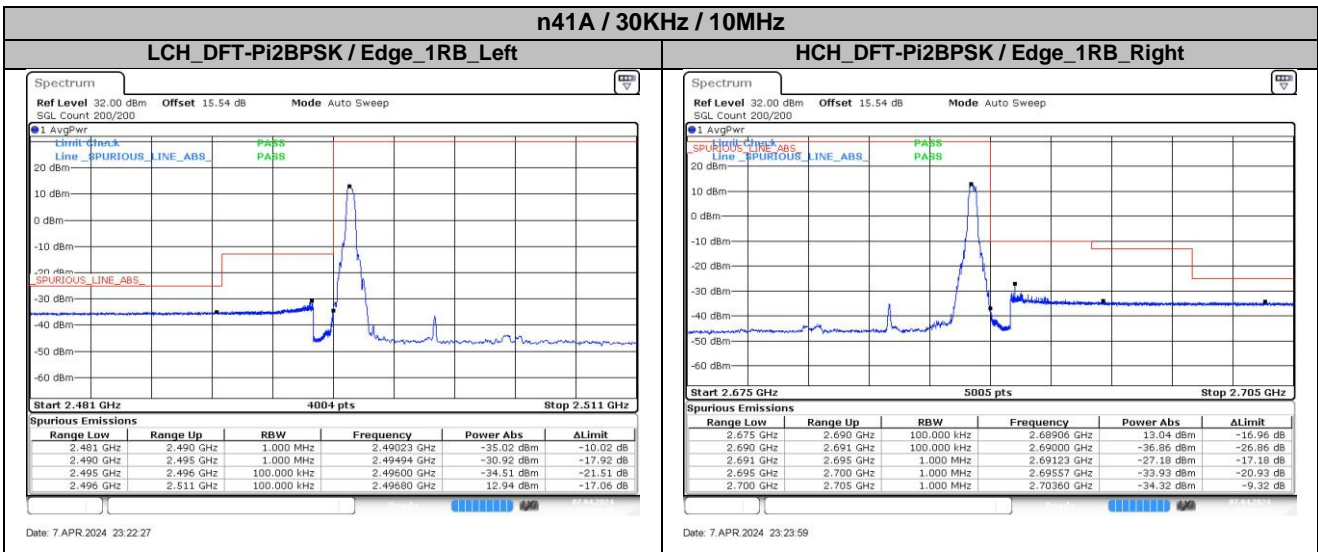
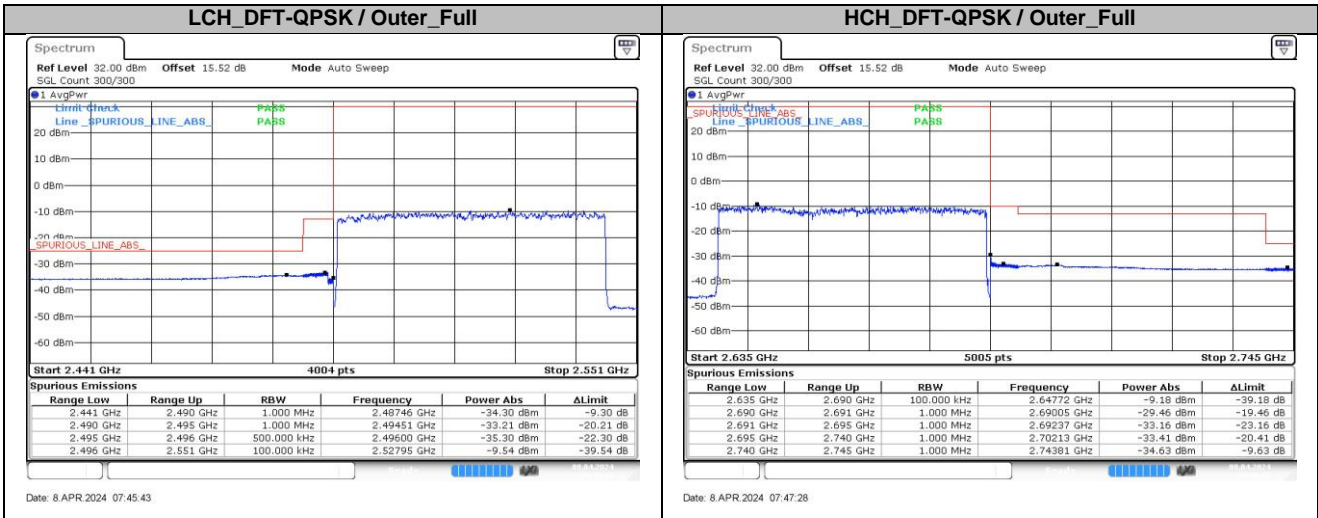


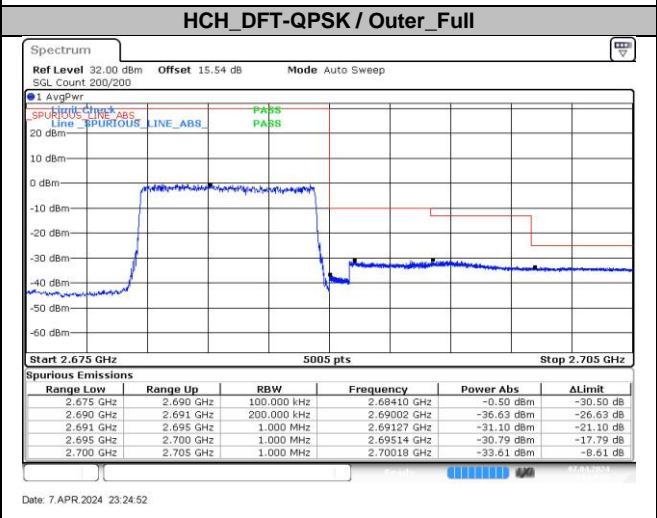
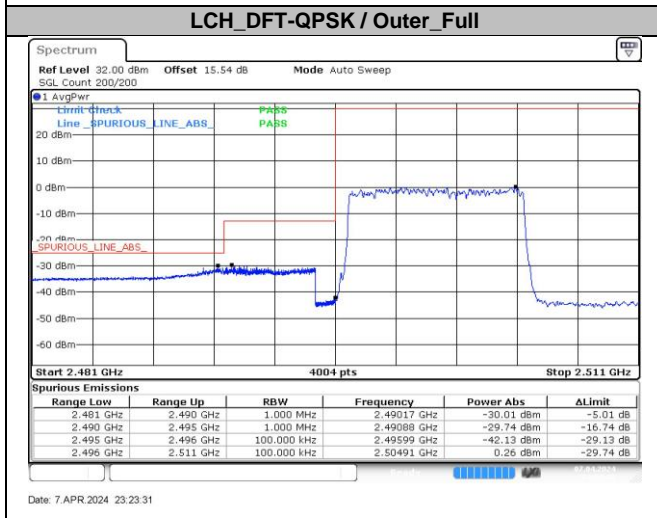
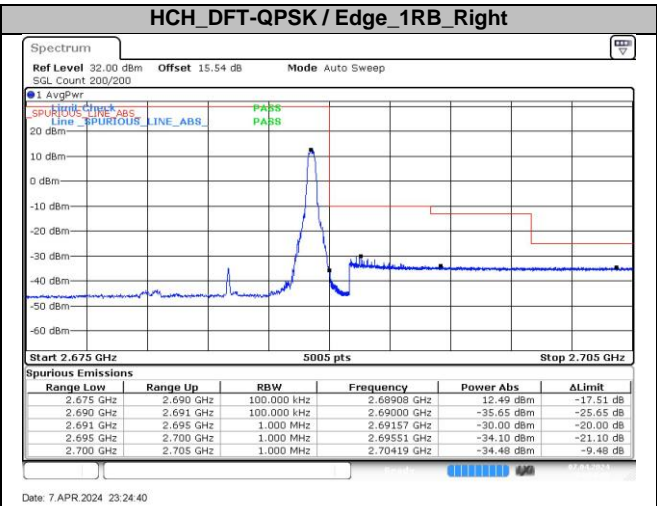
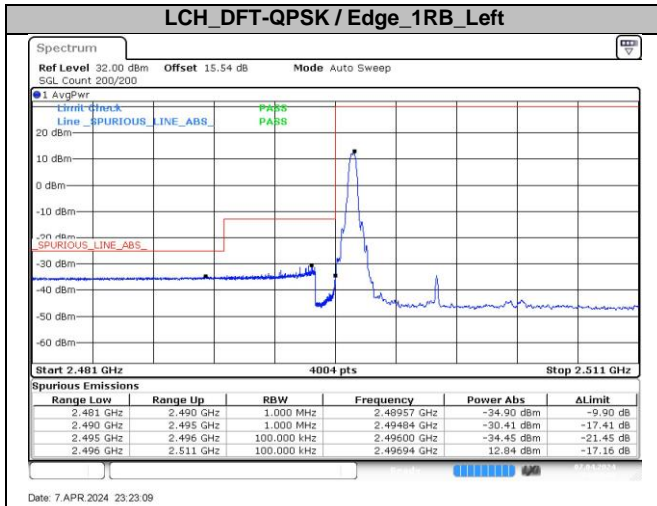
Date: 8 APR 2024 07:45:22

HCH\_DFT-QPSK / Edge\_1RB\_Right



Date: 8 APR 2024 07:47:11





## n41A / 30KHz / 15MHz

