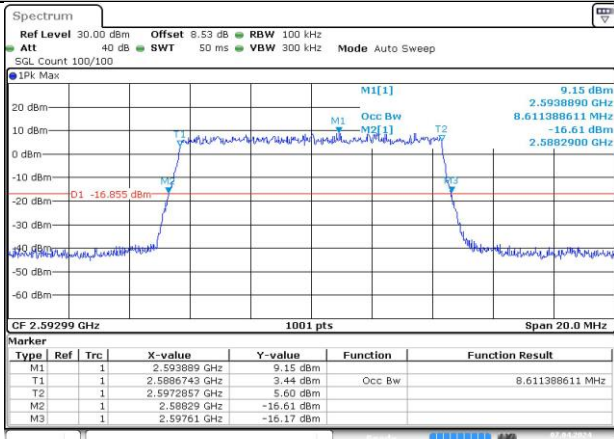


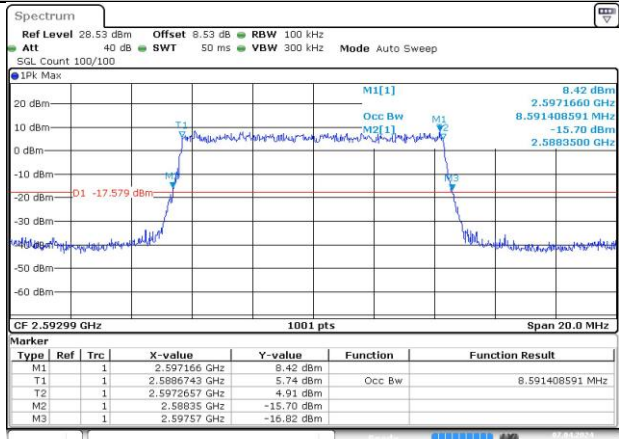
n41A / 30kHz / 10MHz

MCH_DFT-Pi2BPSK / Outer_Full



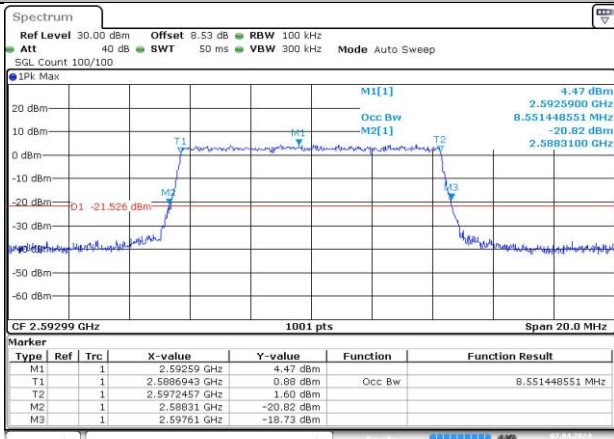
Date: 7 APR 2024 21:18:52

MCH_DFT-QPSK / Outer_Full



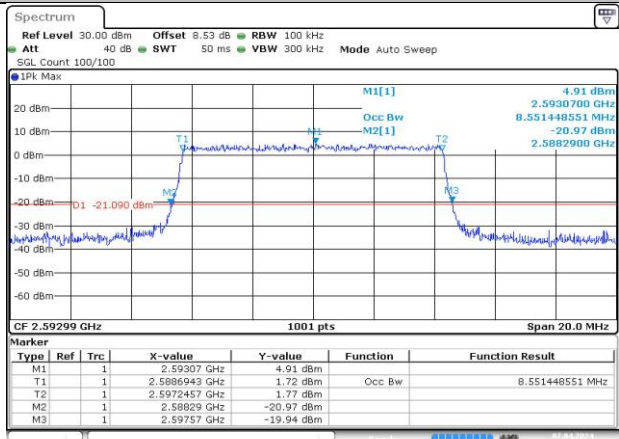
Date: 7 APR 2024 21:19:24

MCH_CP-QPSK / Outer_Full



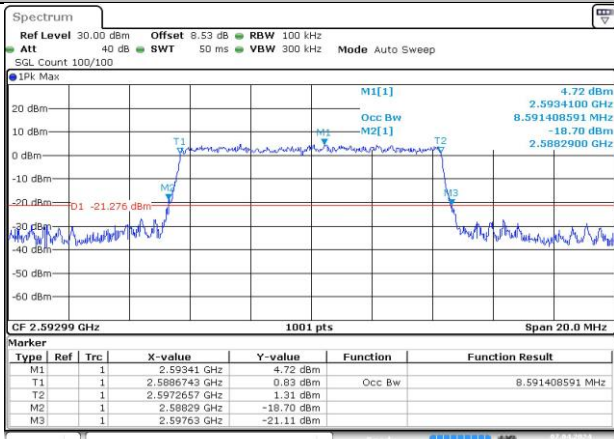
Date: 7 APR 2024 21:19:37

MCH_CP-16QAM / Outer_Full



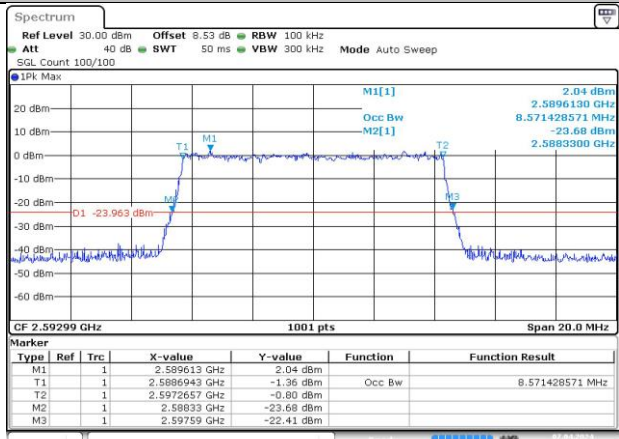
Date: 7 APR 2024 21:19:49

MCH_CP-64QAM / Outer_Full

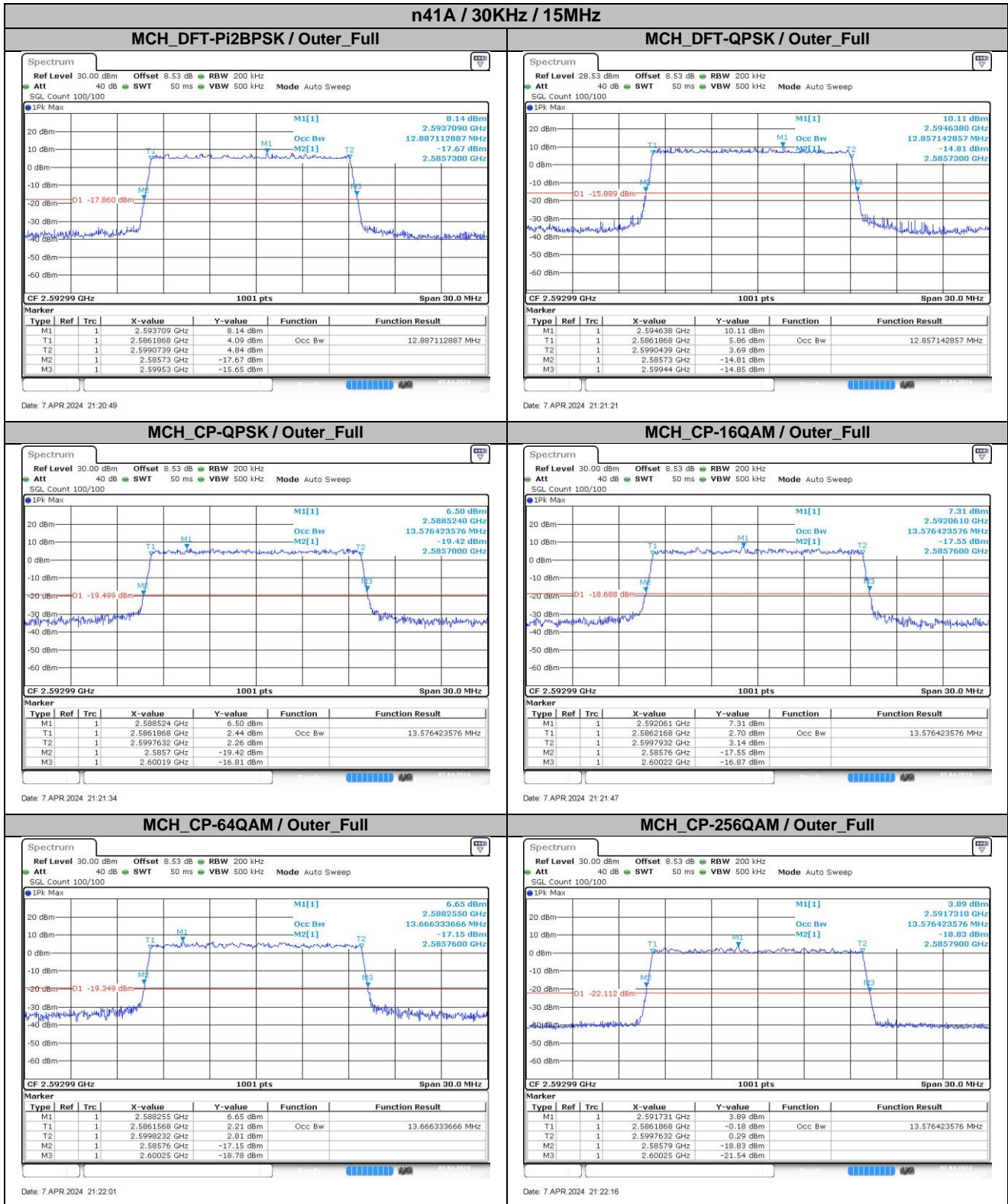


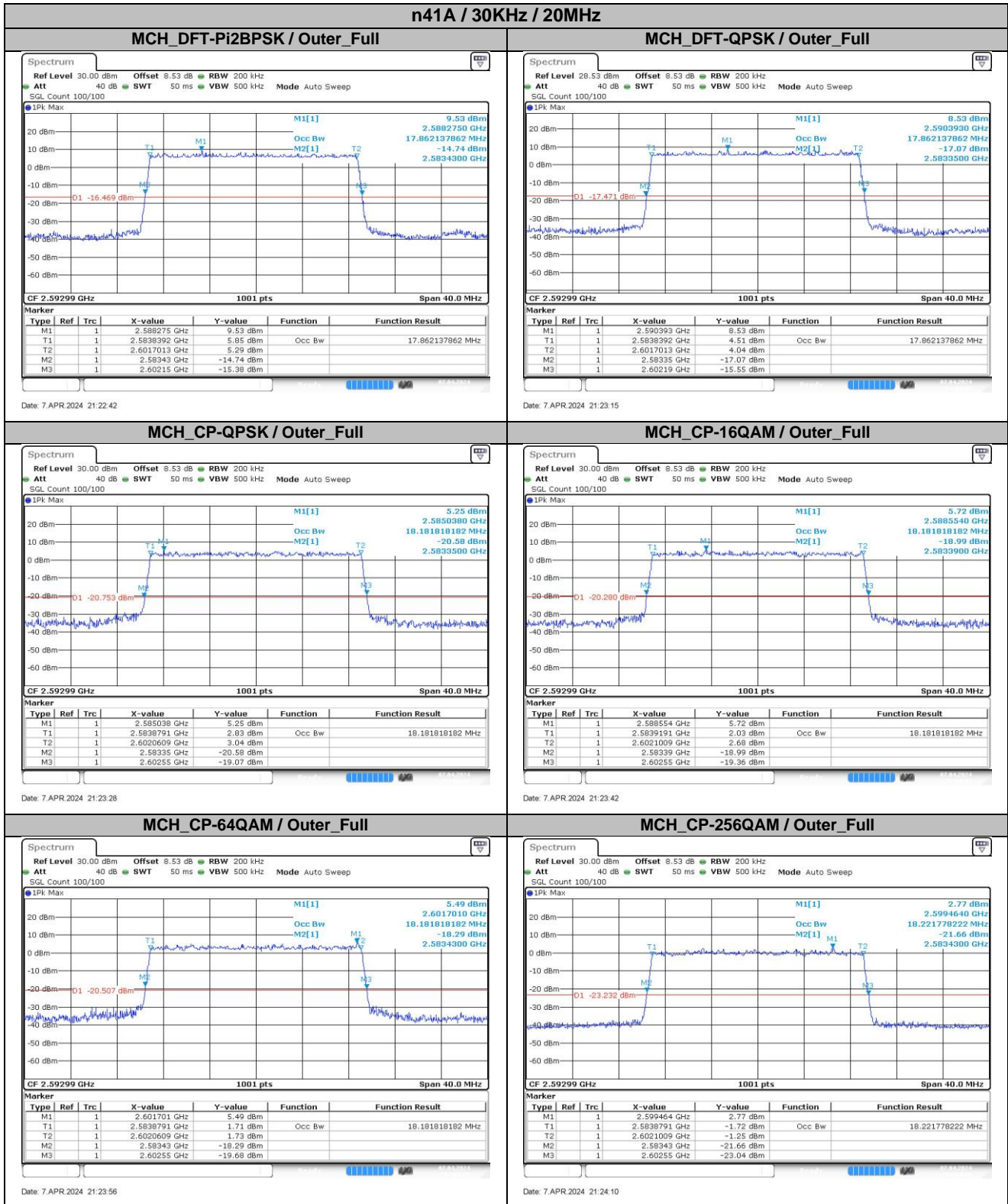
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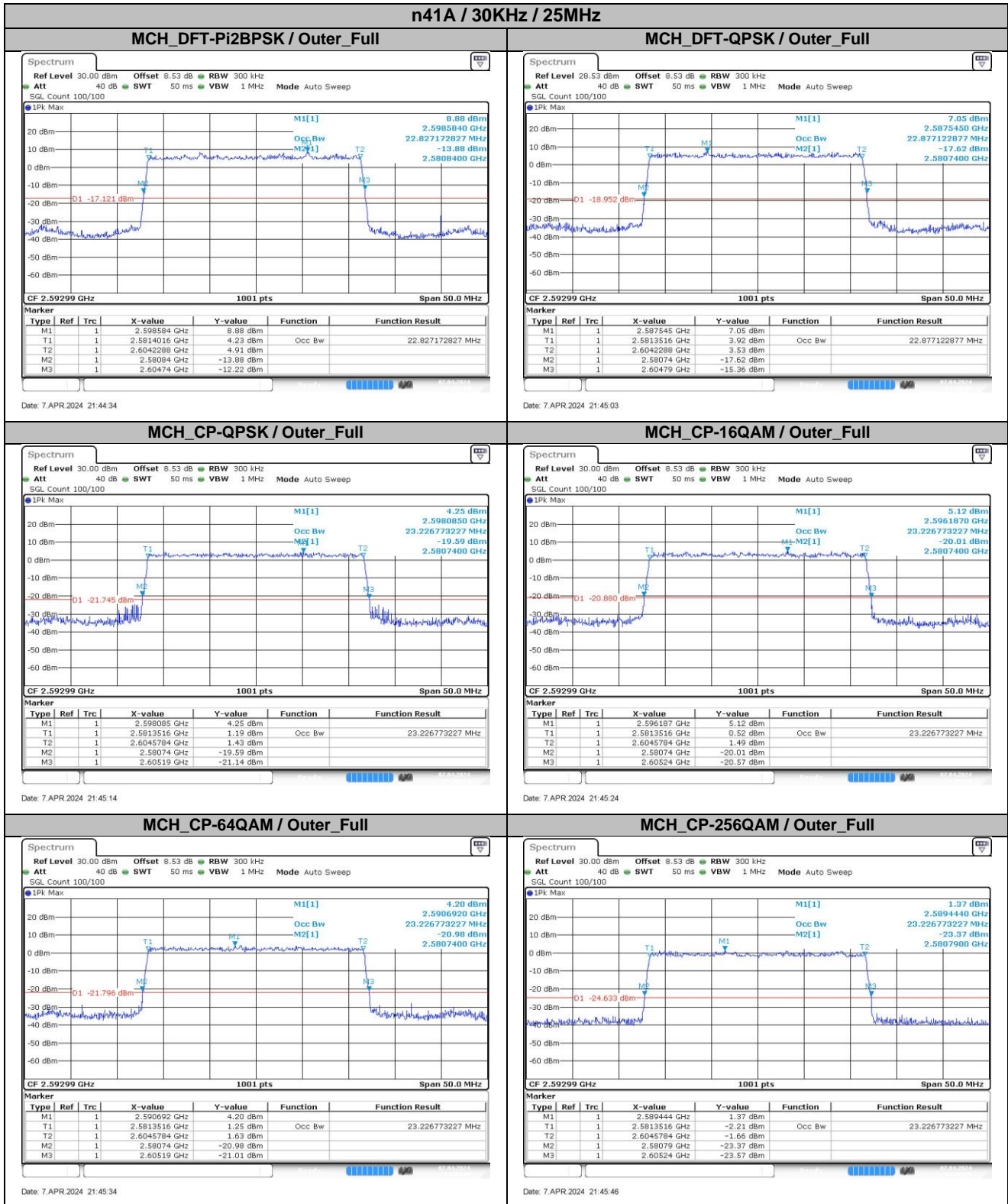
MCH_CP-256QAM / Outer_Full



Date: 7 APR 2024 21:20:16

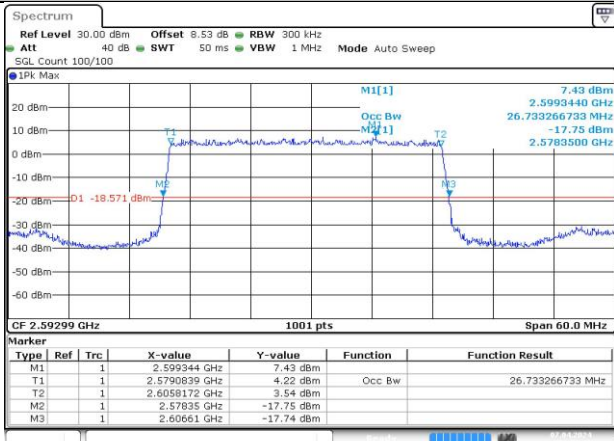




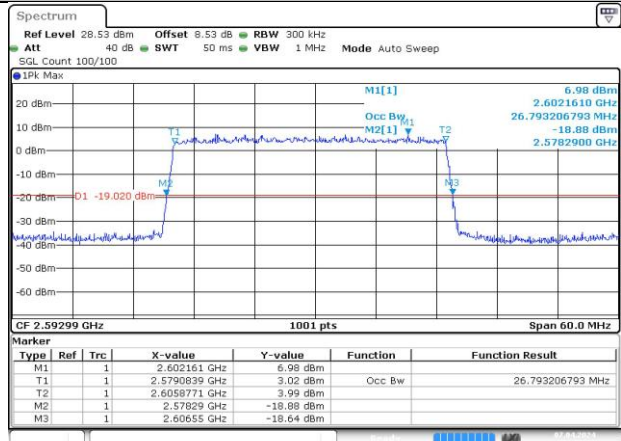


n41A / 30kHz / 30MHz

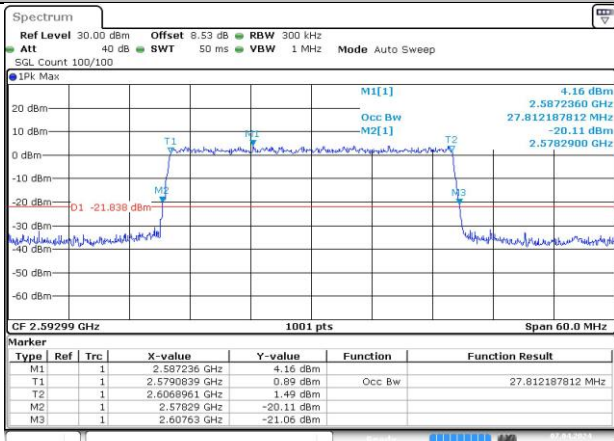
MCH_DFT-Pi2BPSK / Outer_Full



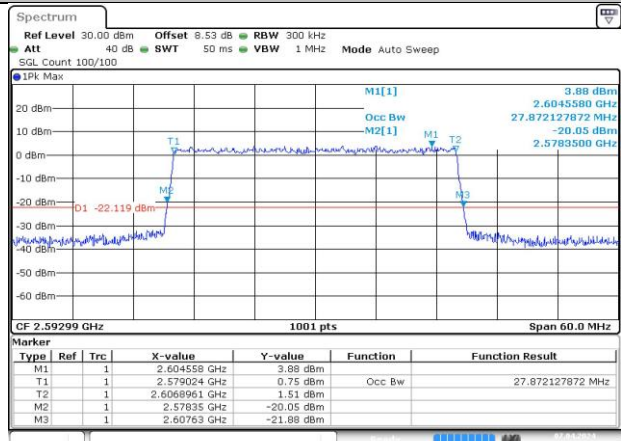
MCH_DFT-QPSK / Outer_Full



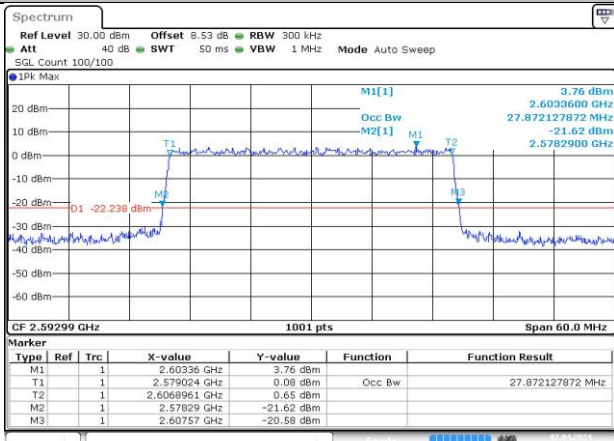
MCH_CP-QPSK / Outer_Full



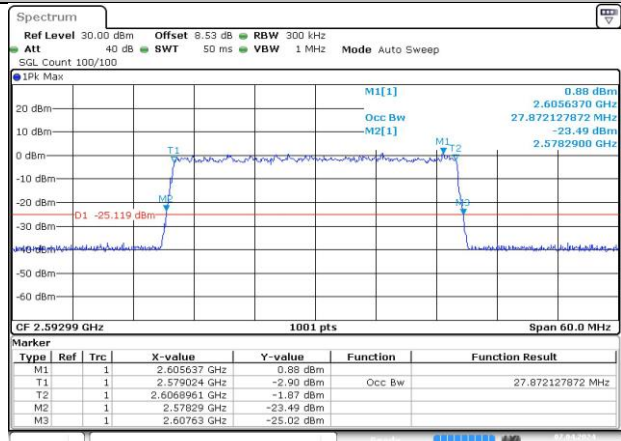
MCH_CP-16QAM / Outer_Full

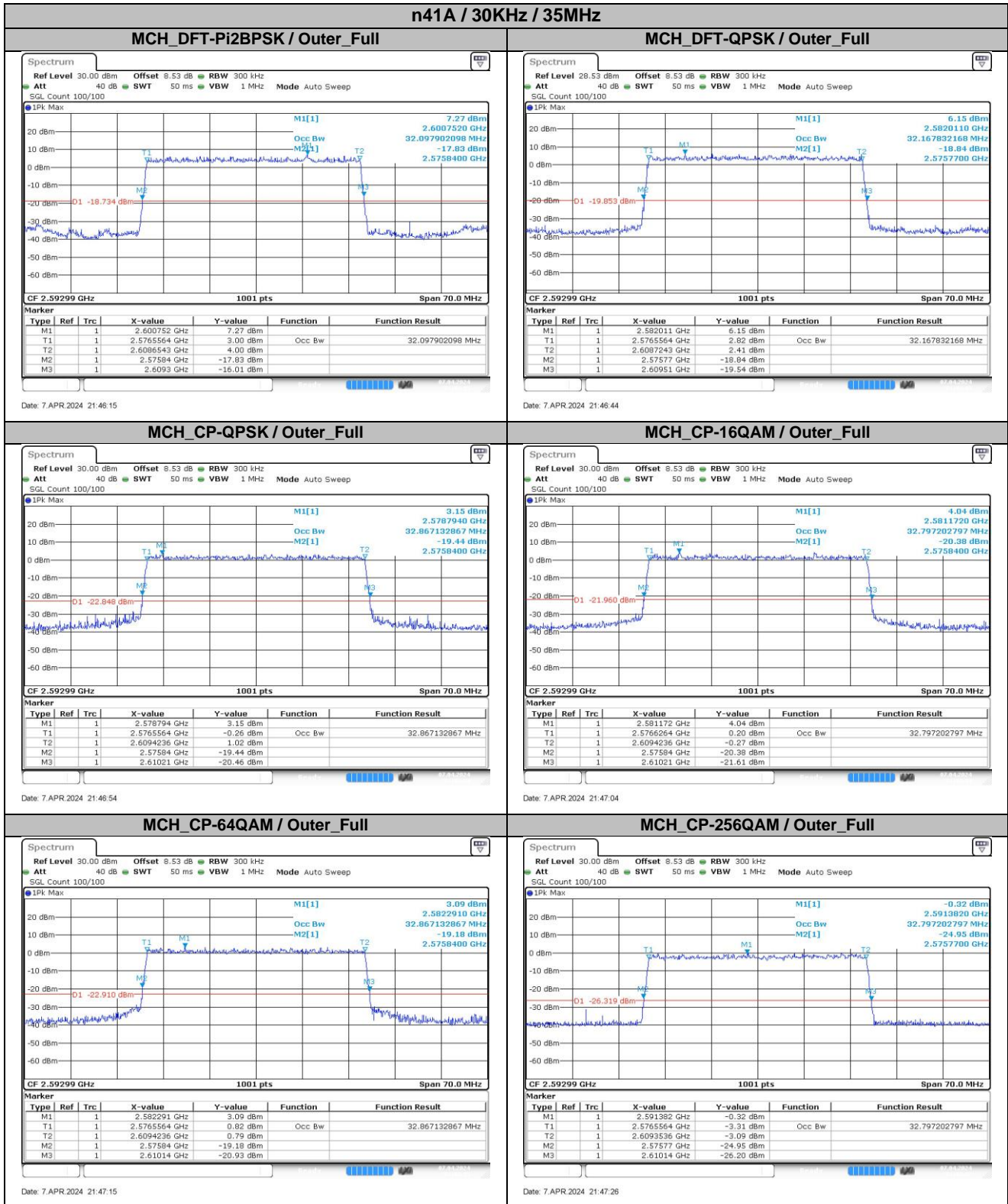


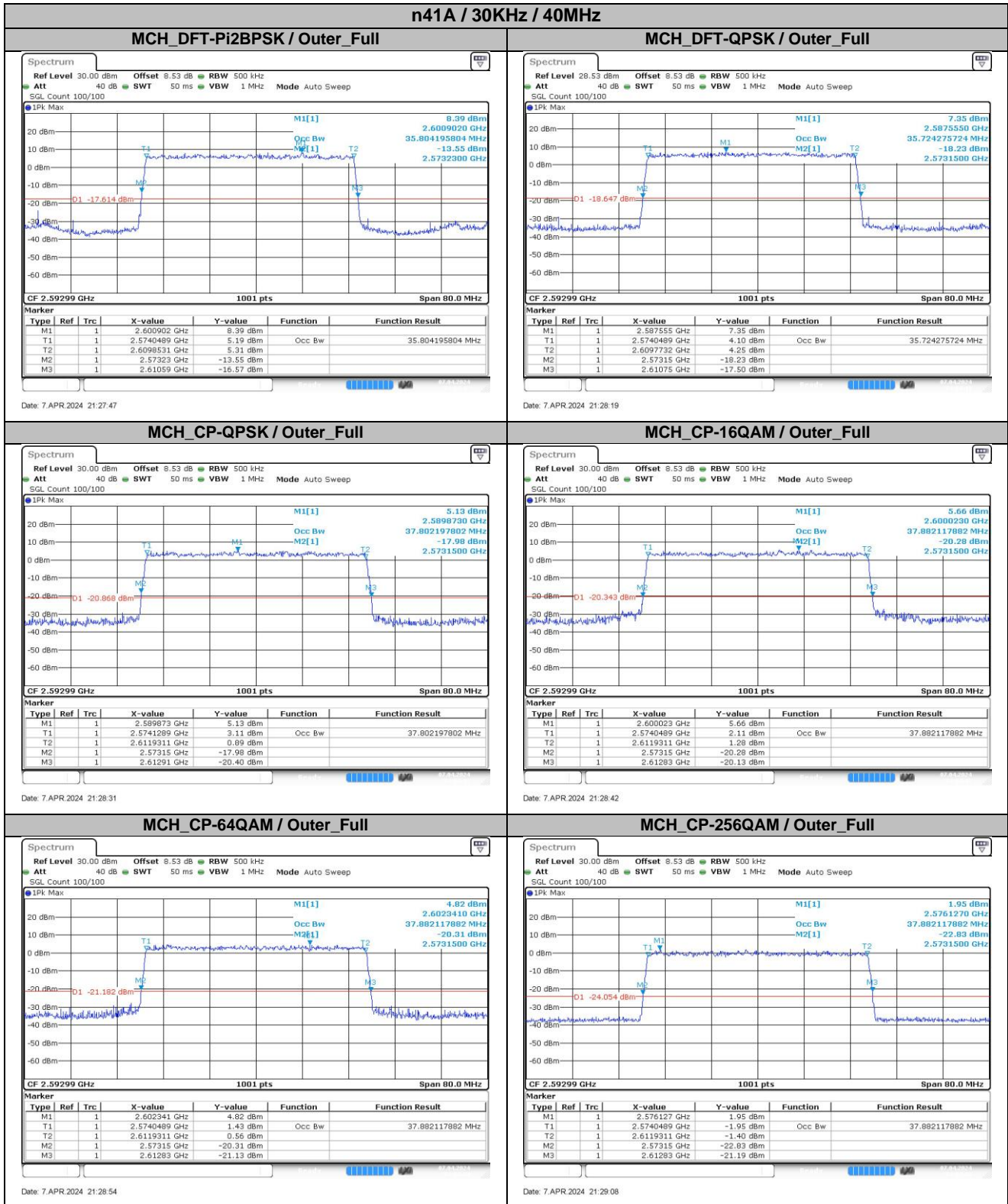
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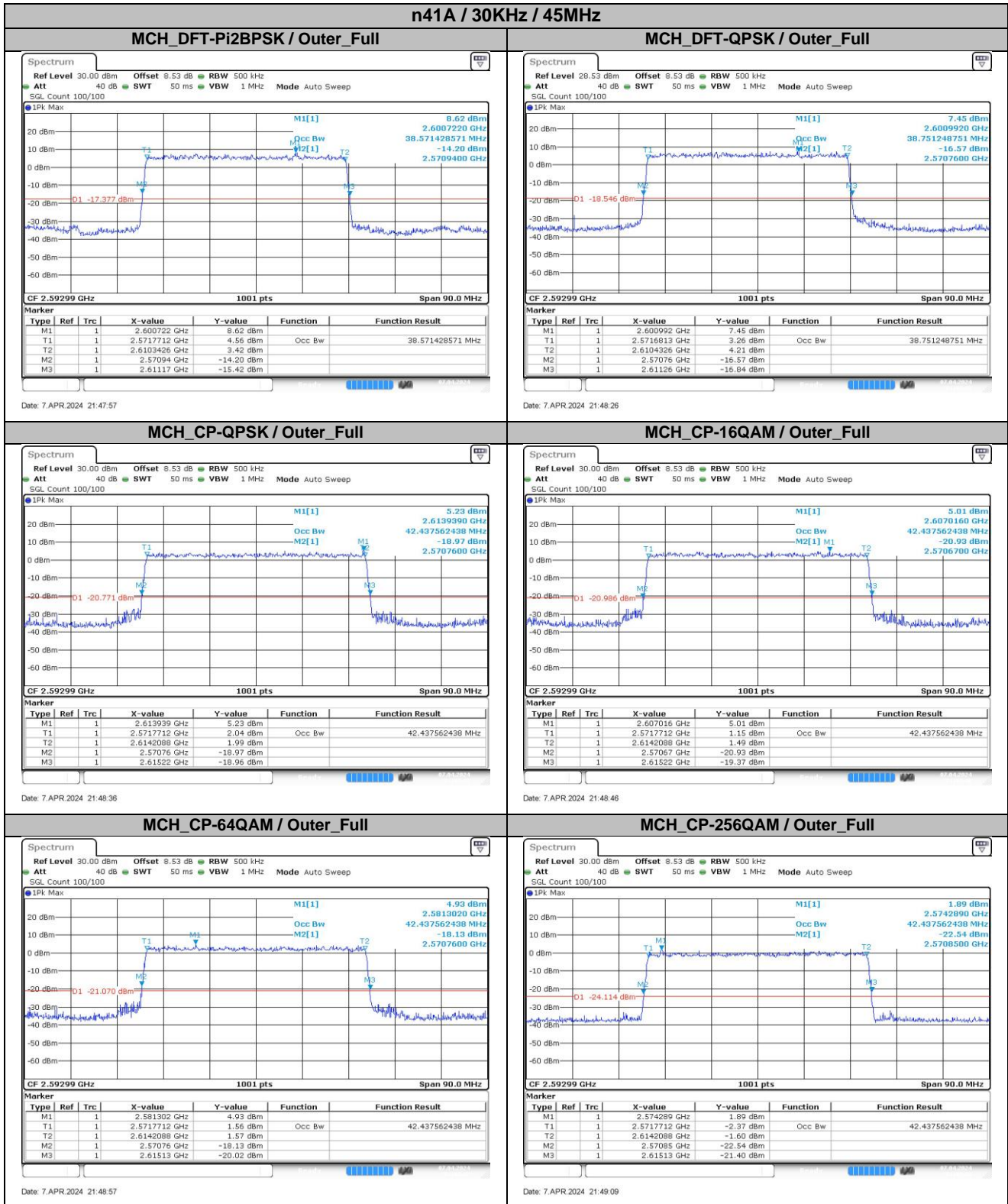


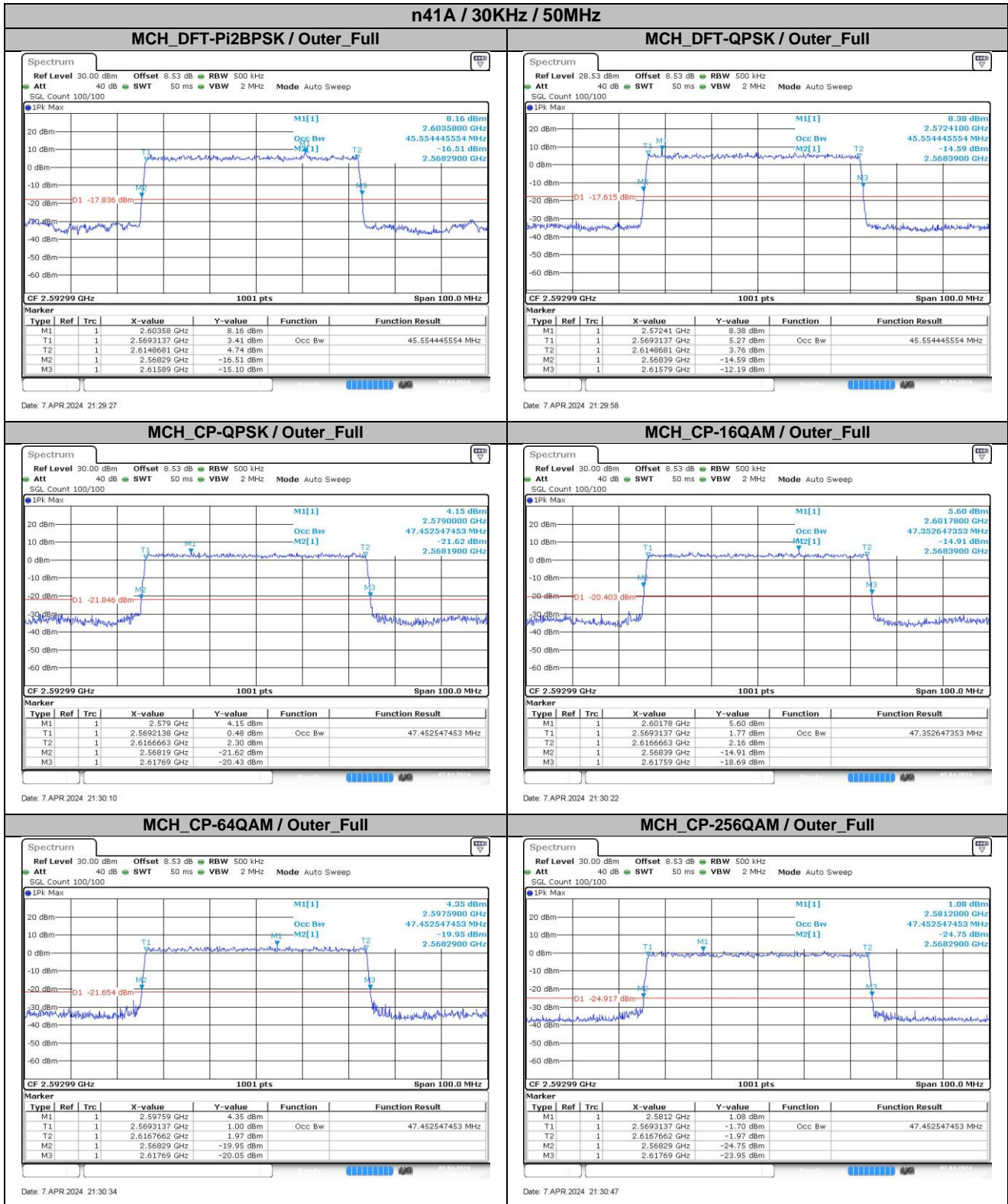
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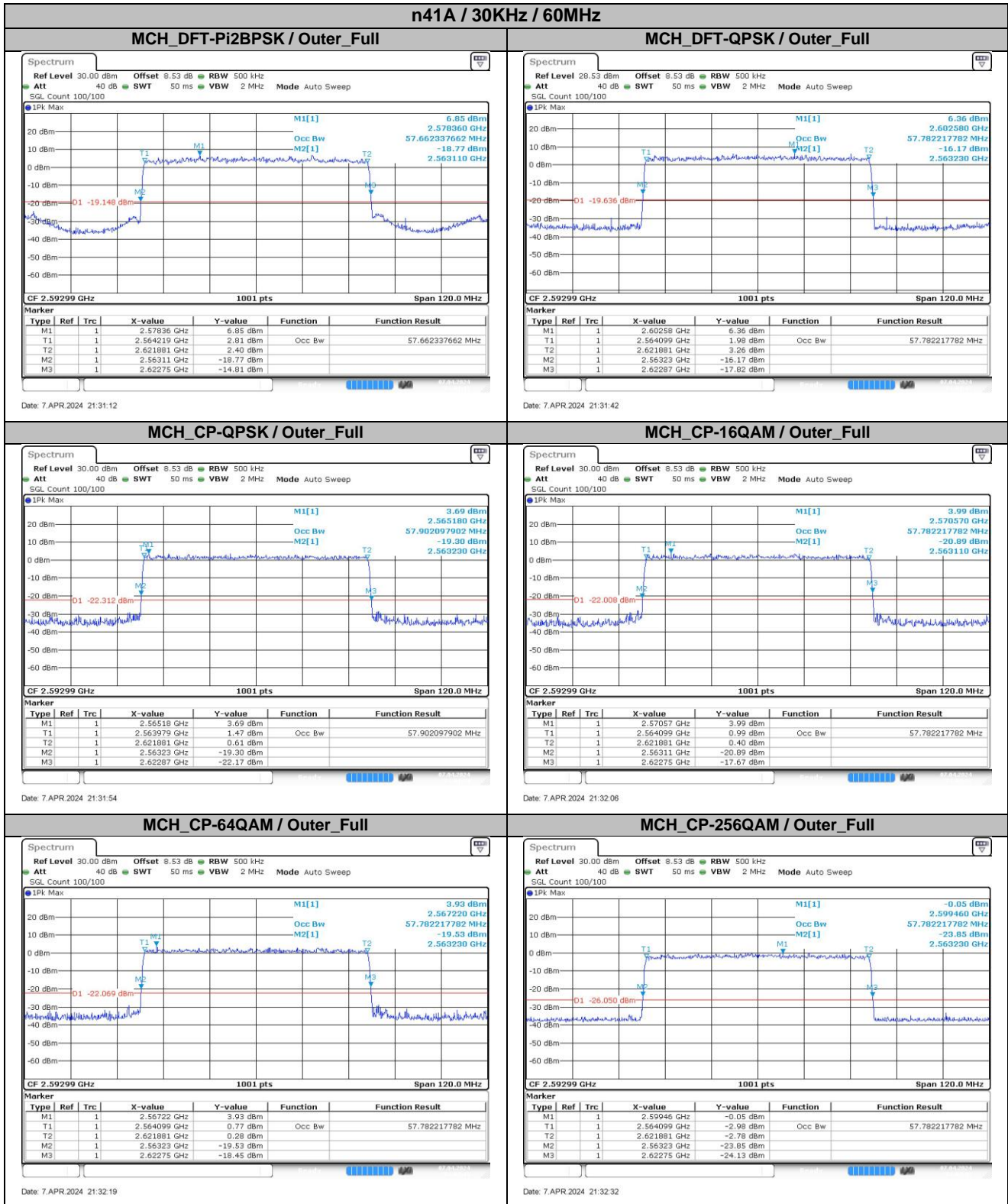


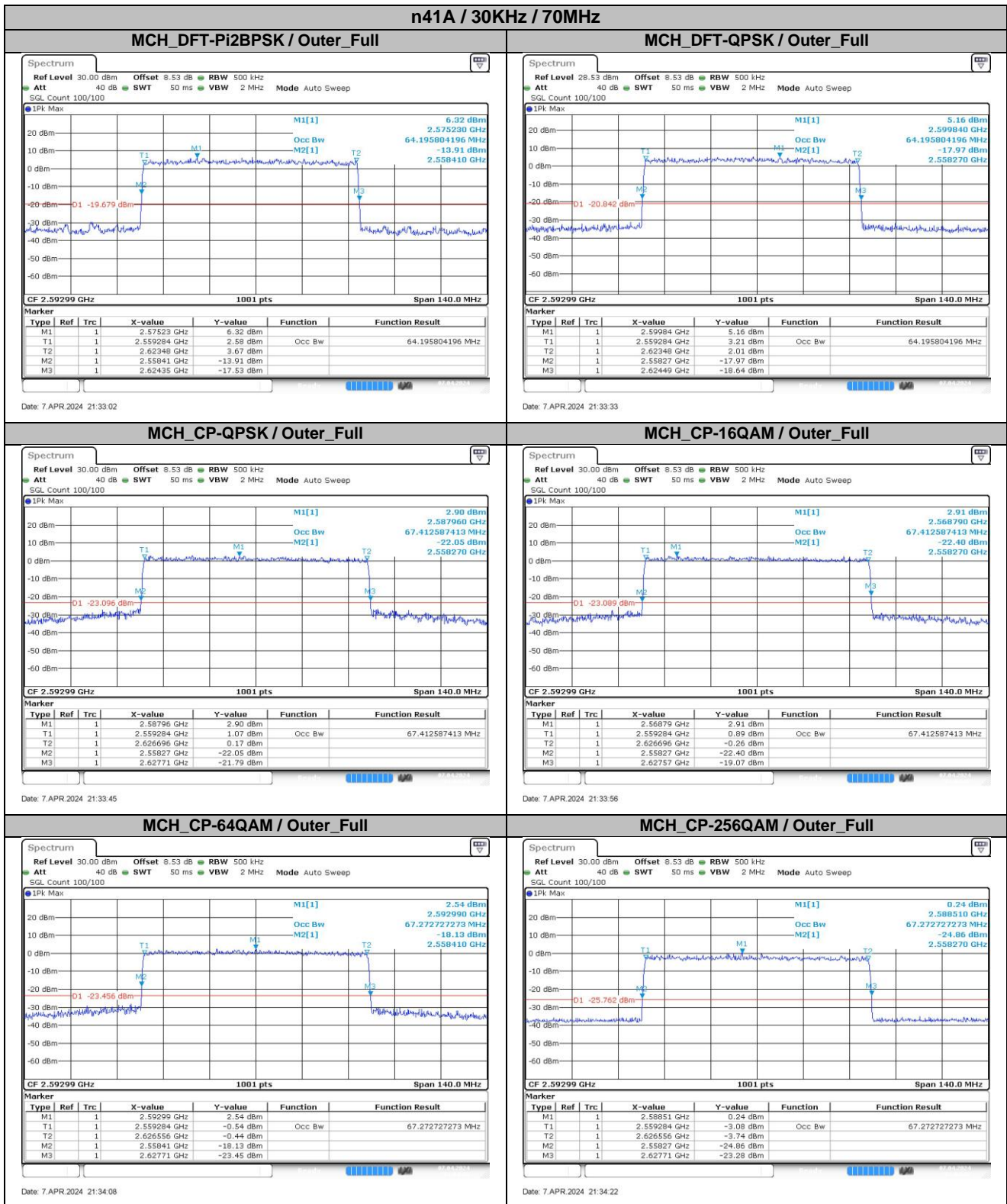






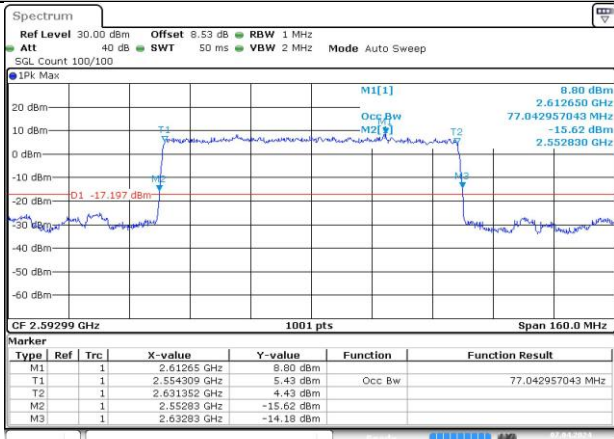






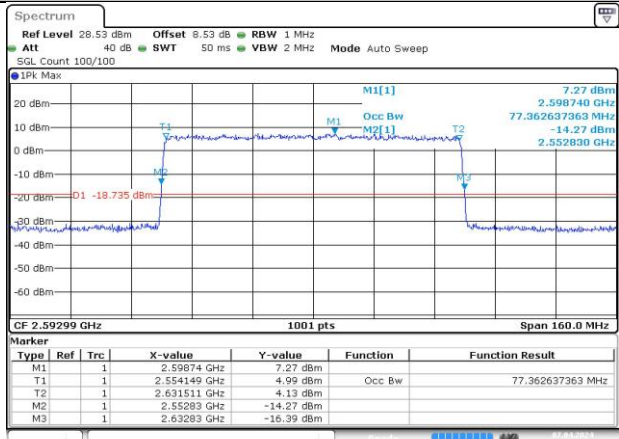
n41A / 30kHz / 80MHz

MCH_DFT-Pi2BPSK / Outer_Full



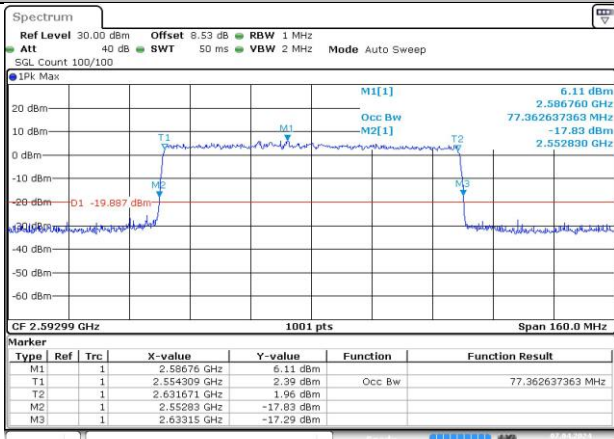
Date: 7 APR 2024 21:34:47

MCH_DFT-QPSK / Outer_Full



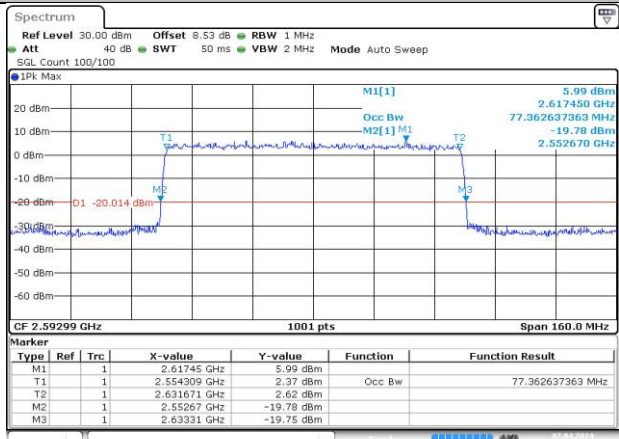
Date: 7 APR 2024 21:35:18

MCH_CP-QPSK / Outer_Full



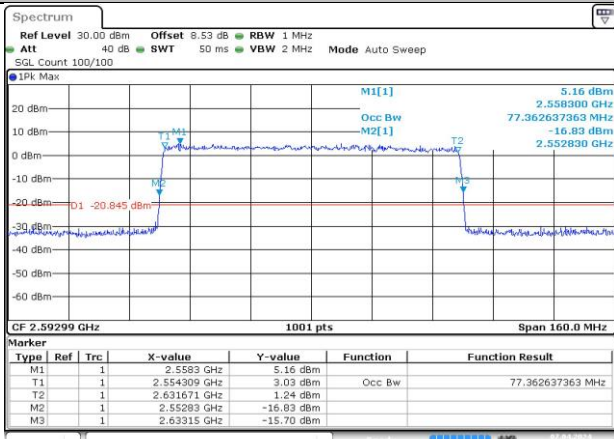
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MCH_CP-16QAM / Outer_Full



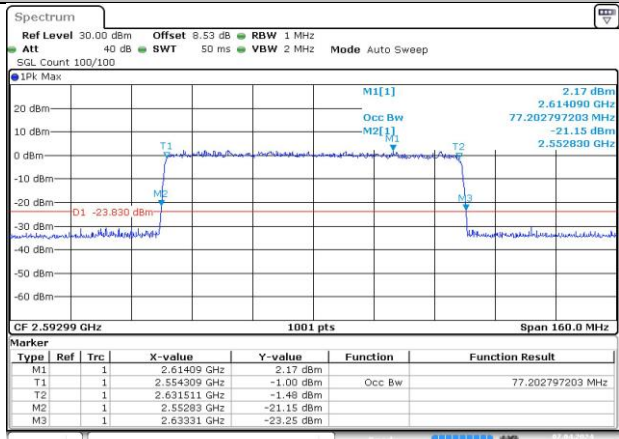
Date: 7 APR 2024 21:35:42

MCH_CP-64QAM / Outer_Full

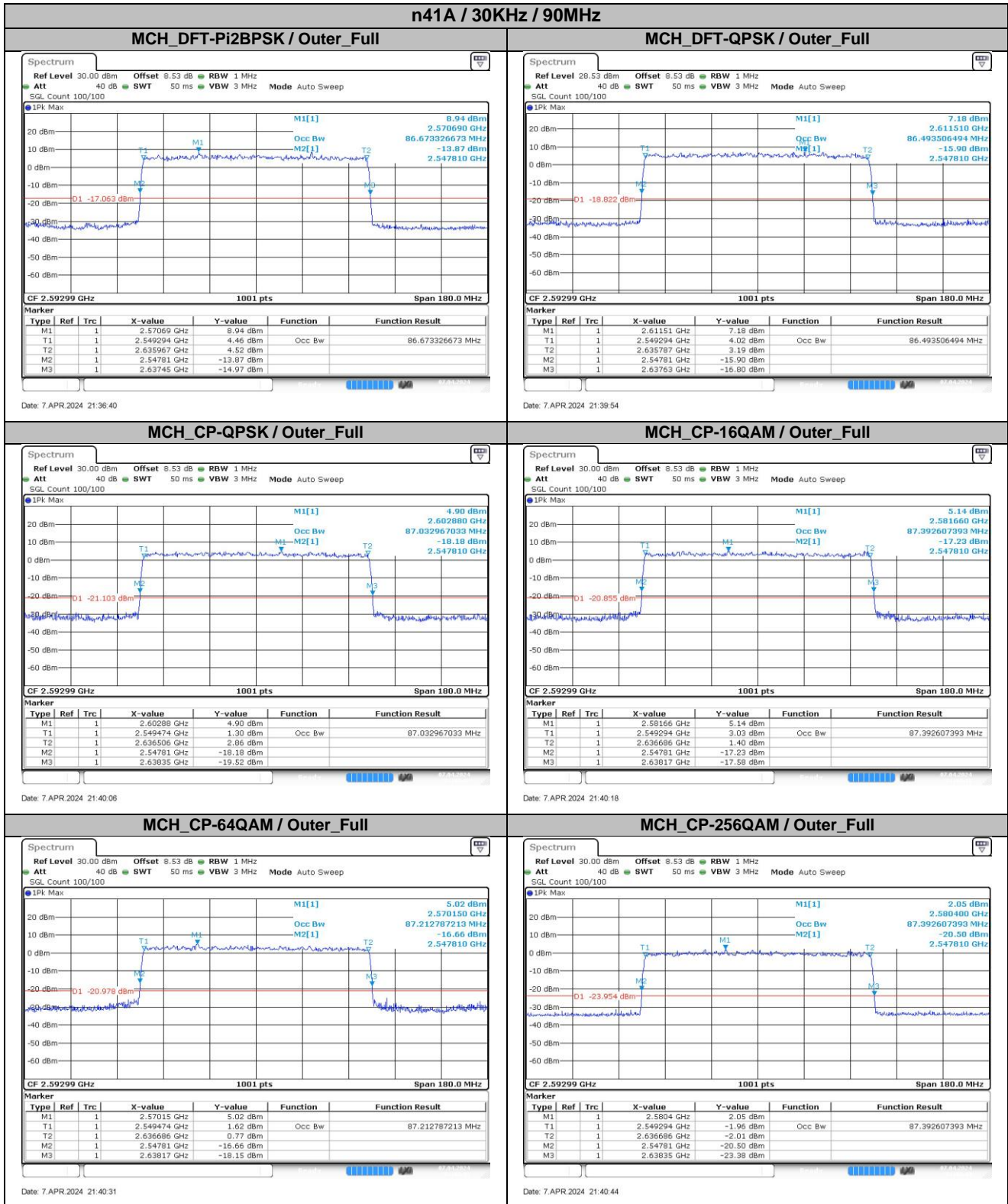


Date: 7 APR 2024 21:35:54

MCH_CP-256QAM / Outer_Full

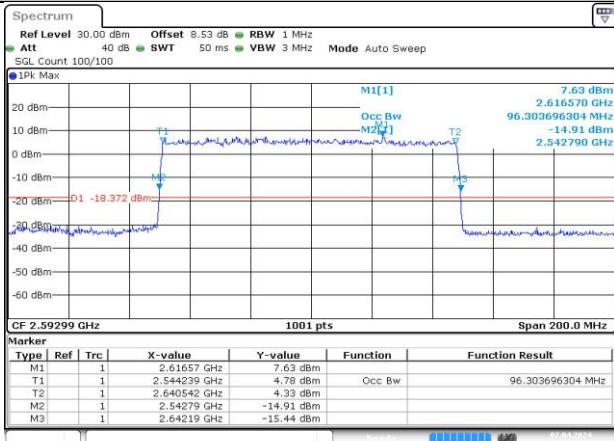


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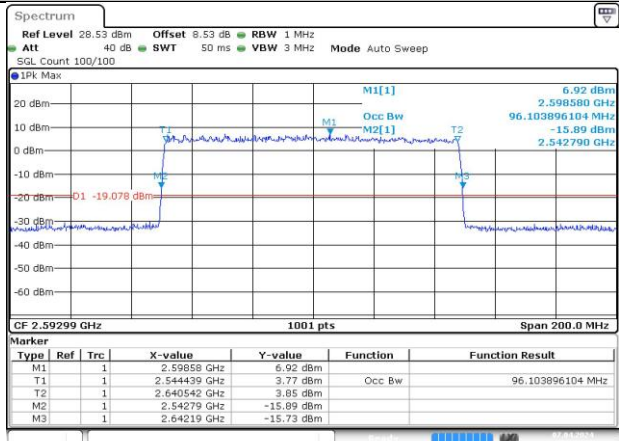


n41A / 30KHz / 100MHz

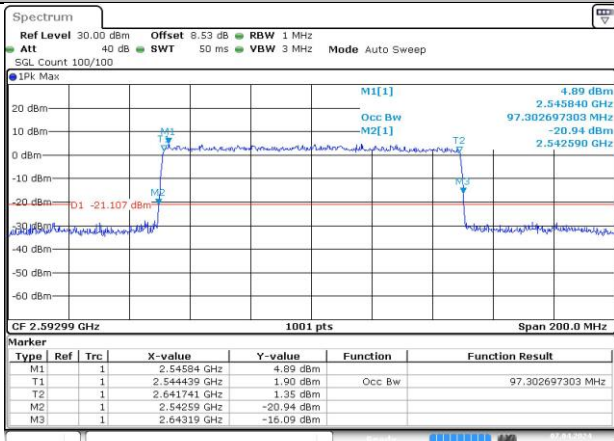
MCH_DFT-Pi2BPSK / Outer_Full



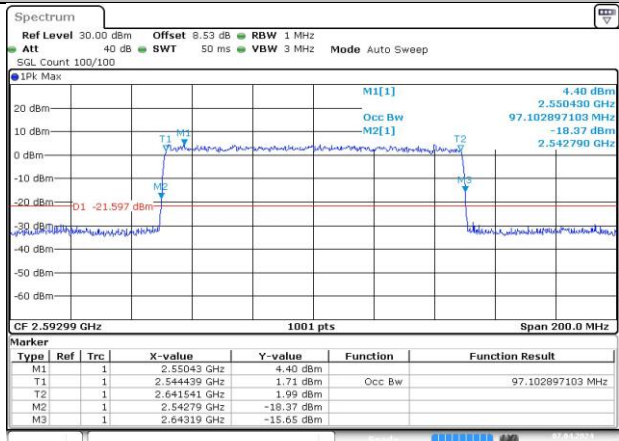
MCH_DFT-QPSK / Outer_Full



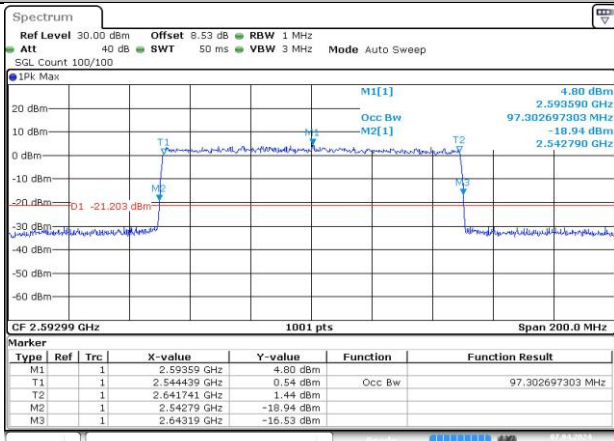
MCH_CP-QPSK / Outer_Full



MCH_CP-16QAM / Outer_Full



MCH_CP-64QAM / Outer_Full



MCH_CP-256QAM / Outer_Full

