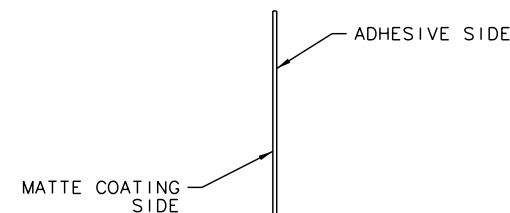
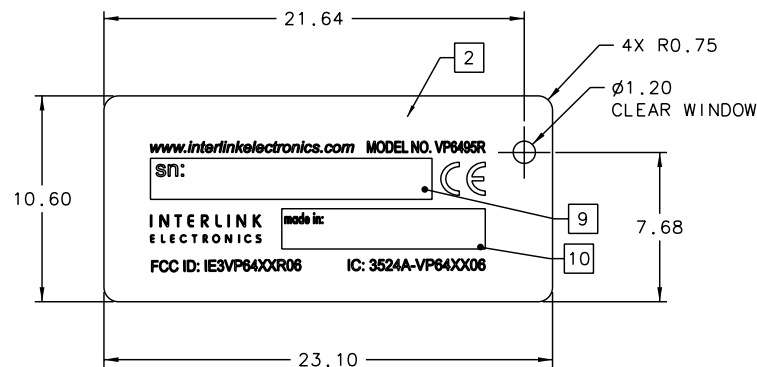
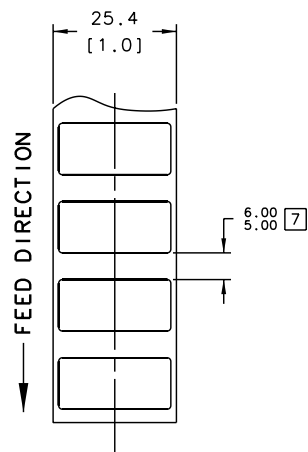


REVISION HISTORY			
REV	DESCRIPTION	DATE	APPROVED
P1	PRE PROD RELEASE PER PCO #1849	6/26/06	JT
P2	REVISED PER PCO #1899	8/22/06	JT
P3	REVISED PER PCO #1946	11/1/06	JT



SCALE: 1/1

- 10 PRINT COUNTRY OF MANUFACTURE IN THIS AREA.
- 9 PRINT SERIAL NO:CTMMYYXXXXX IN THE INDICATED AREA WHERE:
CT=COMPUTIME
MM=MONTH OF MANUFACTURE
YY=YEAR OF MANUFACTURE
XXXXX=A SEQUENTIAL NUMBER FROM 00000 TO 99999.
8. PART TO COMPLY WITH LATEST REVISION OF INTERLINK ELECTRONICS SPECIFICATION 80-20016, STANDARD FOR ENVIRONMENT RELATED SUBSTANCES CONTROL.
- 7 SPACING BETWEEN LABELS TO BE CONSISTENT THROUGHOUT ENTIRE ROLL.
6. ROLL DIAMETER TO BE 127mm (5") MAX WITH 25mm (1") CORE.
5. SURFACES AND PRINTING TO BE FREE FROM SCRATCHES AND DEFECTS.
4. MAXIMUM PUNCHING MISALIGNMENT IN ALL DIRECTIONS IS 0.2mm.
3. MAXIMUM BURR ALLOWED ON BACKSIDE IS 0.05mm.
- 2 PRINT GRAPHICS USING INTERLINK ELECTRONICS ARTWORK No. 14-56489
- 1 MATERIAL: 3M 7815, 0.26mm (0.01")THK OR APPROVED EQUIVALENT.
FINISH: MATTE TOP COATING

NOTES : UNLESS OTHERWISE SPECIFIED.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS. TOLERANCES ARE: DECIMALS .X ±.20 .XX ±.15		THE MATERIAL CONTAINED IN THIS DOCUMENT IS INTERLINK ELECTRONICS PROPRIETARY INFORMATION AND MUST NOT BE COPIED OR TRANSFERRED IN WHOLE OR IN PART WITHOUT THE EXPRESSED WRITTEN PERMISSION OF INTERLINK ELECTRONICS.		INTERLINK ELECTRONICS 546 Flynn Road, Camarillo, CA 93012	
MATERIAL		APPROVALS		TITLE	
1		DESIGNED PAT BAIR		DATE 6/23/06	
FINISH		CHECKED S.BAXTER		DATE 6/26/06	
1		APPROVED J.TICKLE		DATE 6/26/06	
FILE: TEAM BROWSER; PAUL REVERE		SIZE A3		Dwg NO. 24-58337	
SCALE 4/1		DO NOT SCALE DRAWING		SHEET 1 OF 1	

