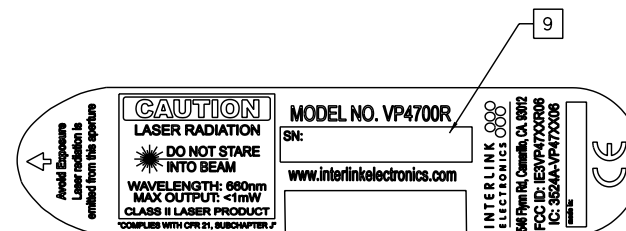
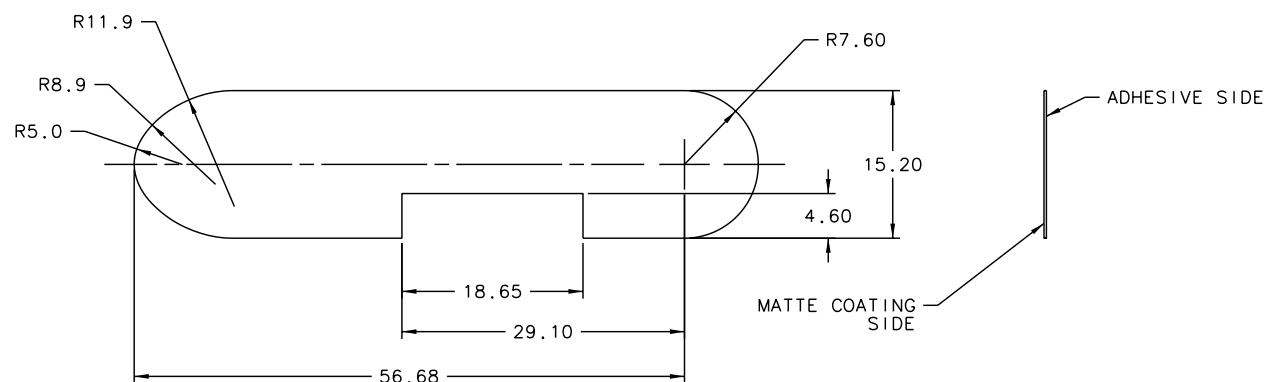
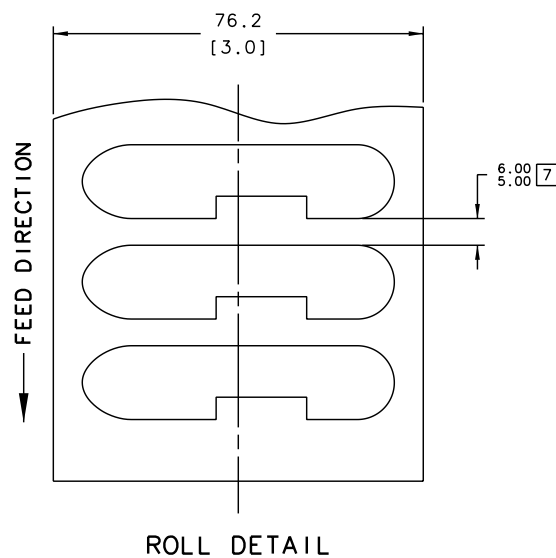


REVISION HISTORY			
REV	DESCRIPTION	DATE	APPROVED
P2	1ST DWG RELEASE PER PCO #1843	6/13/06	J.TICKLE
P3	REVISED PER PCO #1899	8/22/06	J.TICKLE
P4	REVISED PER PCO 1946	11/1/06	J.TICKLE



- 9 PRINT SERIAL NO:CTMMYYXXXXX IN THE INDICATED AREA WHERE:  
 CT=COMPUTIME  
 MM=MONTH OF MANUFACTURE  
 YY=YEAR OF MANUFACTURE  
 XXXXX=A SEQUENTIAL NUMBER FROM 00000 TO 99999.
8. PART TO COMPLY WITH LATEST REVISION OF INTERLINK ELECTRONICS SPECIFICATION 80-20016, STANDARD FOR ENVIRONMENT RELATED SUBSTANCES CONTROL.
- 7 SPACING BETWEEN LABELS TO BE CONSISTENT THROUGHOUT ENTIRE ROLL.
6. ROLL DIAMETER TO BE 127mm (5") MAX WITH 25mm (1") CORE.
5. SURFACES AND PRINTING TO BE FREE FROM SCRATCHES AND DEFECTS.
4. MAXIMUM PUNCHING MISALIGNMENT IN ALL DIRECTIONS IS 0.2mm.
3. MAXIMUM BURR ALLOWED ON BACKSIDE IS 0.05mm.
2. PRINT GRAPHICS USING INTERLINK ELECTRONICS ARTWORK No. 14-24624
- 1 MATERIAL: 3M 7815, 0.26mm (0.01")THK OR APPROVED EQUIVALENT.  
 FINISH: MATTE TOP COATING

NOTES : UNLESS OTHERWISE SPECIFIED.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS. TOLERANCES ARE: DECIMALS .X ± .20 .XX ± .15		THE MATERIAL CONTAINED IN THIS DOCUMENT IS INTERLINK ELECTRONICS PROPRIETARY INFORMATION AND MUST NOT BE COPIED OR TRANSFERRED IN WHOLE OR IN PART WITHOUT THE EXPRESSED WRITTEN PERMISSION OF INTERLINK ELECTRONICS.		<b>INTERLINK ELECTRONICS</b> 546 Flynn Road, Camarillo, CA 93012	
MATERIAL	1	APPROVALS	DATE	TITLE	
		DESIGNED	PAT BAIR	6/13/06	
		CHECKED	S.BAXTER	6/13/06	
FINISH	1	APPROVED	J.TICKLE	6/13/06	
FILE: TEAM BROWSER; PAUL REVERE		SIZE		A3	
		SCALE		2/1	
		DO NOT SCALE DRAWING		SHEET 1 OF 1	
		REV.		P4	

