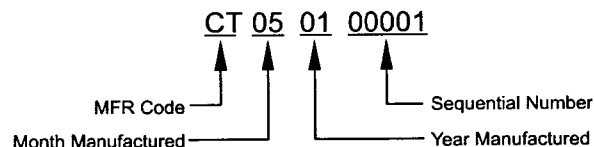
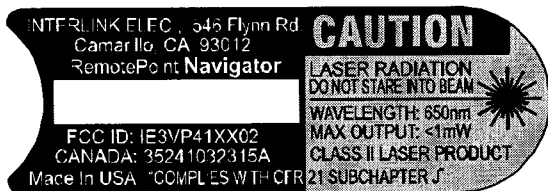
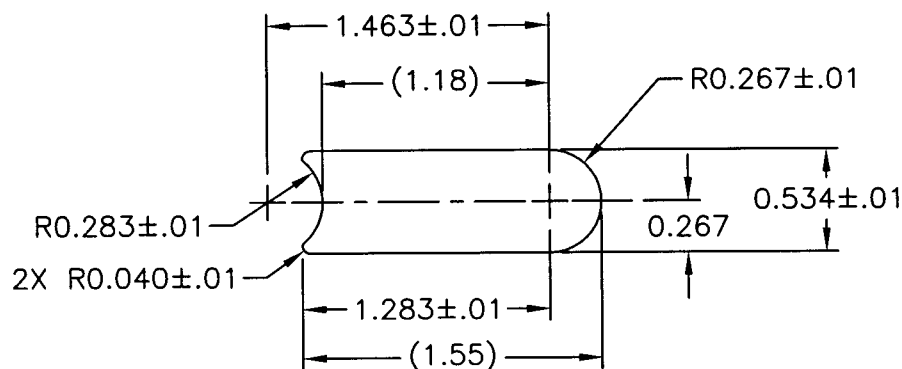


REVISION HISTORY			
REV	DESCRIPTION	DATE	APPROVED
P1	PRE-PROD RELEASE, PCO 653	9/11/02	J.TICKLE



2 SERIAL NUMBER LABEL WITH GRAPHICS

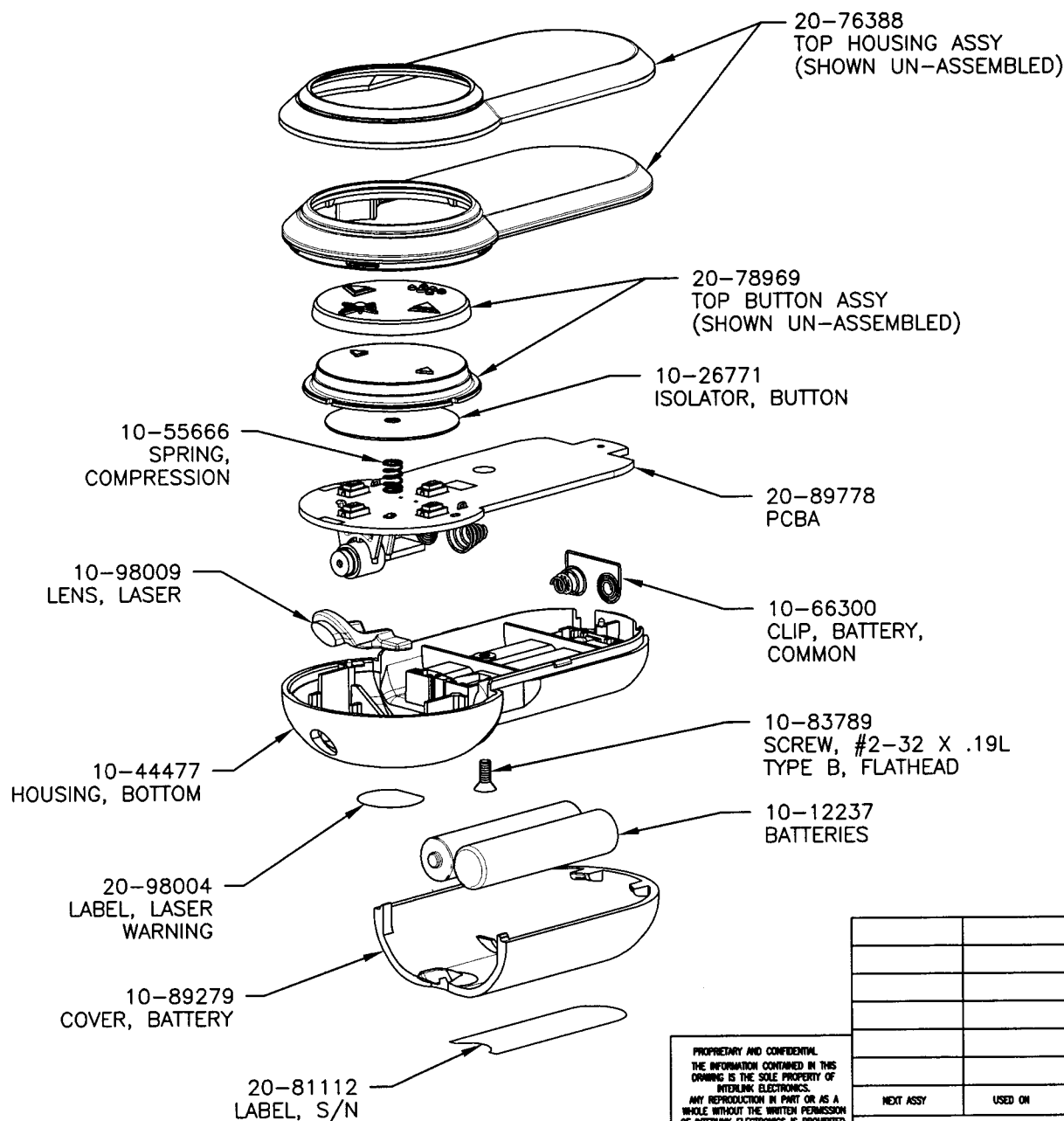
7 SERIAL NUMBER FORMAT



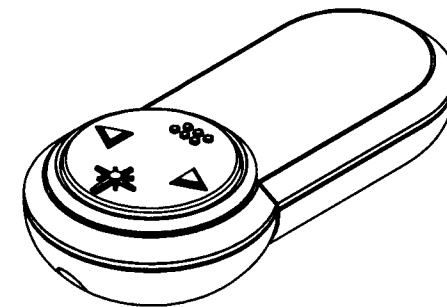
- 7 PRINT SERIAL NUMBER USING THE FORMAT SHOWN ABOVE.
6. PRINTING TO BE FREE FROM SCRATCHES AND/OR DEFECTS.
5. SURFACES TO BE FREE FROM SCRATCHES AND/OR DEFECTS.
4. MAXIMUM PUNCHING MISALIGNMENT IN ALL DIRECTIONS .2mm.
3. MAXIMUM BURR ALLOWED ON THE BACK SIDE .05mm.
- 2 USE INTERLINK ELECTRONICS ARTWORK (P/N 10-32499) SEE SHT 2.
1. MATERIAL: 3M 7887, .05mm (2MIL) PLOY BASE MATERIAL WITH PERMANENT REAR ADHESIVE.
- FINISH: MATTE FINISH.
- COLOR: PER IINTERLINK ELECTRONICS SUPPLIED ARTWORK.

NOTES : UNLESS OTHERWISE SPECIFIED.

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES. TOLERANCES ARE: DECIMALS .XX ±.01 XXX ±.005 ANGLES ±0.5°		THE MATERIAL CONTAINED IN THIS DOCUMENT IS INTERLINK ELECTRONICS PROPRIETARY INFORMATION AND MUST NOT BE COPIED OR TRANSFERRED IN WHOLE OR IN PART WITHOUT THE EXPRESSED WRITTEN PERMISSION OF INTERLINK ELECTRONICS.		INTERLINK ELECTRONICS 546 Flynn Road, Camarillo, CA 93012 	
MATERIAL		APPROVALS	DATE	TITLE LBL, SERIAL NUMBER, TX, RP NAVIGATOR	
		DESIGNED C.SANCHEZ	9/11/02		
FINISH		CHECKED		SIZE A	
		APPROVED J.TICKLE	9/11/02		
FILE:		SCALE NONE		DWG NO. 20-81112	
				REV. P1	
				SHEET 1 OF 2	



REVISIONS			
REV.	DESCRIPTION	DATE	APPROVED
P1	PRE-PRODUCTION RELEASE PCO 653	16JUL02	J. TICKLE
P2	REVISED PER PCO 655	18JUL02	J. TICKLE
P3	REVISED PER PCO 660	25JUL02	J. TICKLE
P4	REVISED PER PCO 662	29JUL02	C.SANCHEZ
P5	REVISED PER PCO 666	8/1/02	C.SANCHEZ
P6	REVISED PER PCO 674	8/7/02	C.SANCHEZ
P7	REVISED PER PCO 683	8/15/02	C.SANCHEZ
P8	REVISED PER PCO 688	9/04/02	P.BAIR
P9	REVISED PER PCO 702	9/06/02	P.BAIR
P10	REVISED PER PCO 708	9/10/02	P.BAIR
P11	REVISED PER PCO 716	9/11/02	P.BAIR
P12	REVISED PER PCO 720	9/23/02	P.BAIR
P13	REVISED PER PCO 726	9/26/02	P.BAIR



1. SEE SEPARATE BILL OF MATERIALS.

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OF INTERLINK ELECTRONICS IS PROHIBITED.

		UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES: X ± .05 JXX ± .01 JXXX ± .005 X/JXX ± 1/32 X ± 5" X.X' ± 1"	DATE	NAME	INTERLINK ELECTRONICS 546 Flynn Road, Camarillo, CA 93012			
			DRAWN	7/11/02	JPM	TITLE: TX,IE,CELICA		
			CHECKED					
			ENG APPR.					
			MFG APPR.					
		INTERPRET GEOMETRIC TOLERANCING PER: ANSI Y14.5	O.A.			SIZE DWG. NO. B 50-44412 REV P13 SCALE: 1:1 SHEET 1 OF 1		
		MATERIAL Ref. Discrete Parts	COMMENTS: SG: 2.44 WEIGHT: 105.34 GR					
		FINISH						
NEXT ASSY	USED ON							
APPLICATION		DO NOT SCALE DRAWING						