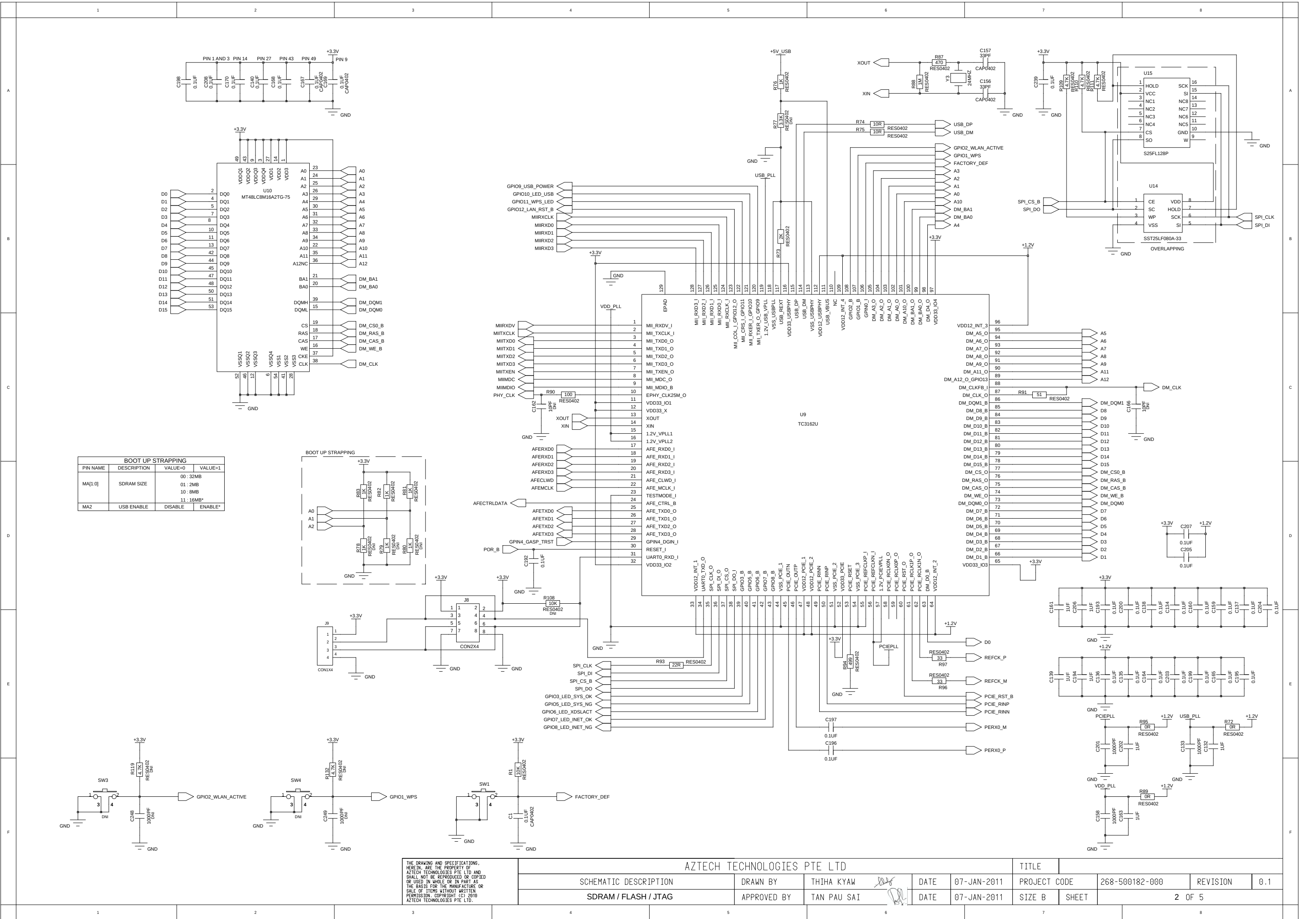




BOOT UP STRAPPING			
PIN NAME	DESCRIPTION	VALUE=0	VALUE=1
MA[1:0]	SDRAM SIZE	00 : 32MB	
		01 : 2MB	
		10 : 8MB	
		11 : 16MB*	
MA2	USB ENABLE	DISABLE	ENABLE*

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AZTECH TECHNOLOGIES PTE LTD

SCHEMATIC DESCRIPTION

SDRAM / FLASH / JTAG

DRAWN BY

THIHA KYAW

APPROVED BY

TAN PAU SAI

DATE

07-JAN-2011

DATE

07-JAN-2011

TITLE

PROJECT CODE

268-500182-000

SIZE B

SHEET

2 OF 5

REVISION

0.1

