

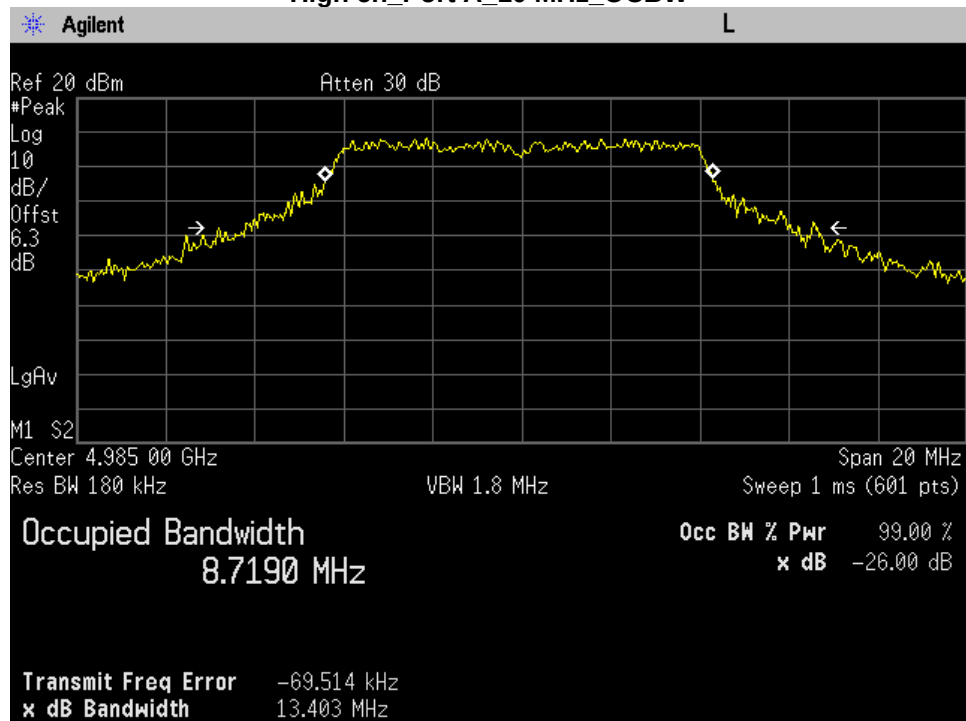


# Annex D

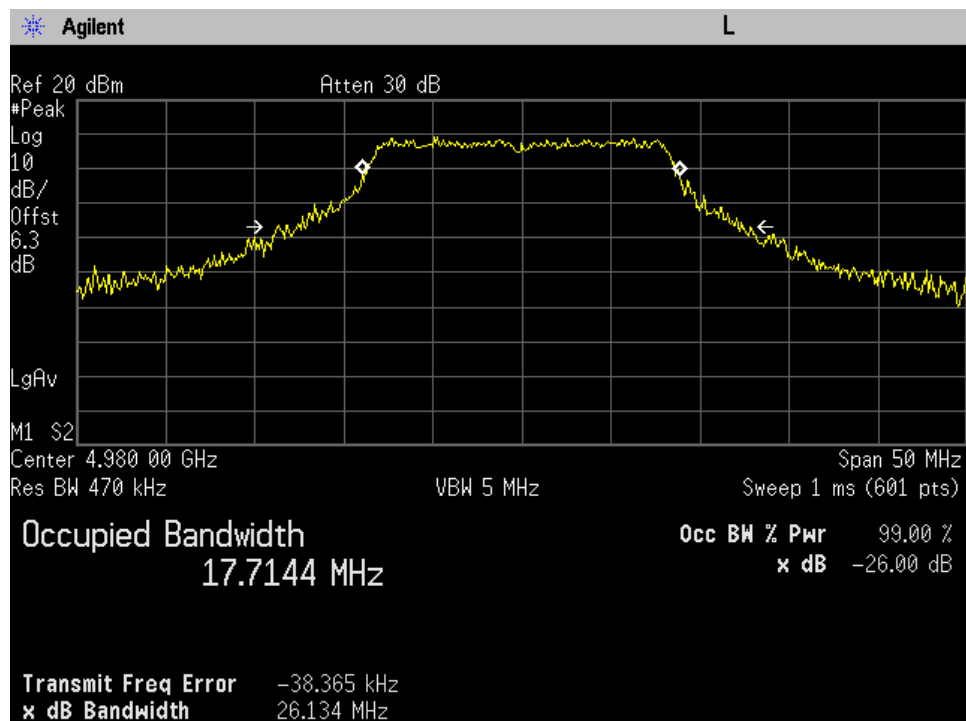
## Occupied Bandwidth and Masks



### High ch\_Port A\_10 MHz\_OCBW

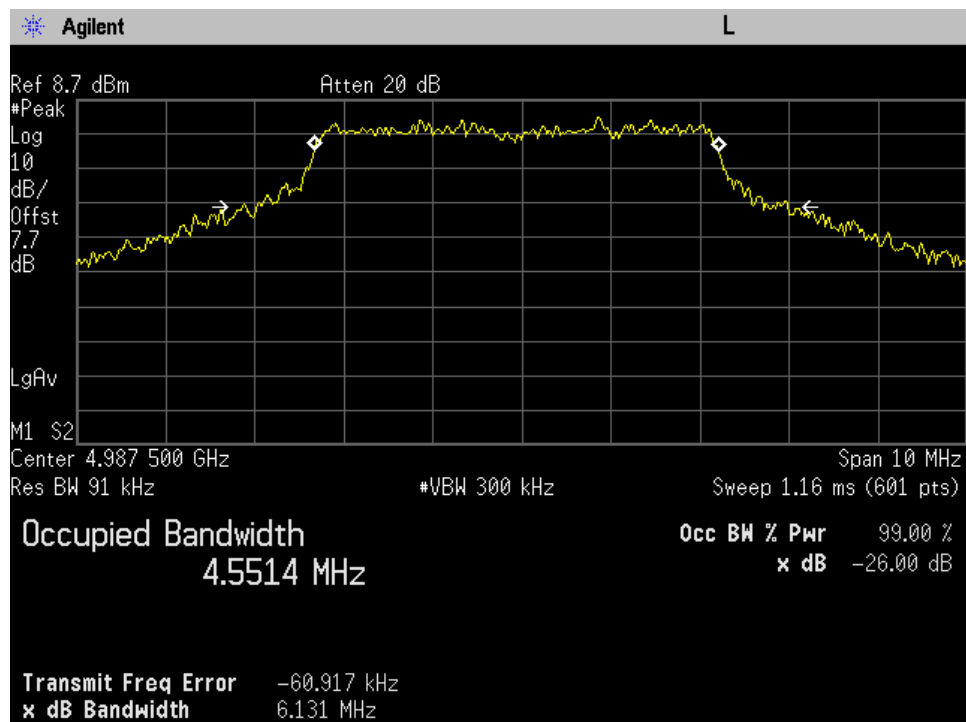


### High ch\_Port A\_20 MHz\_OCBW

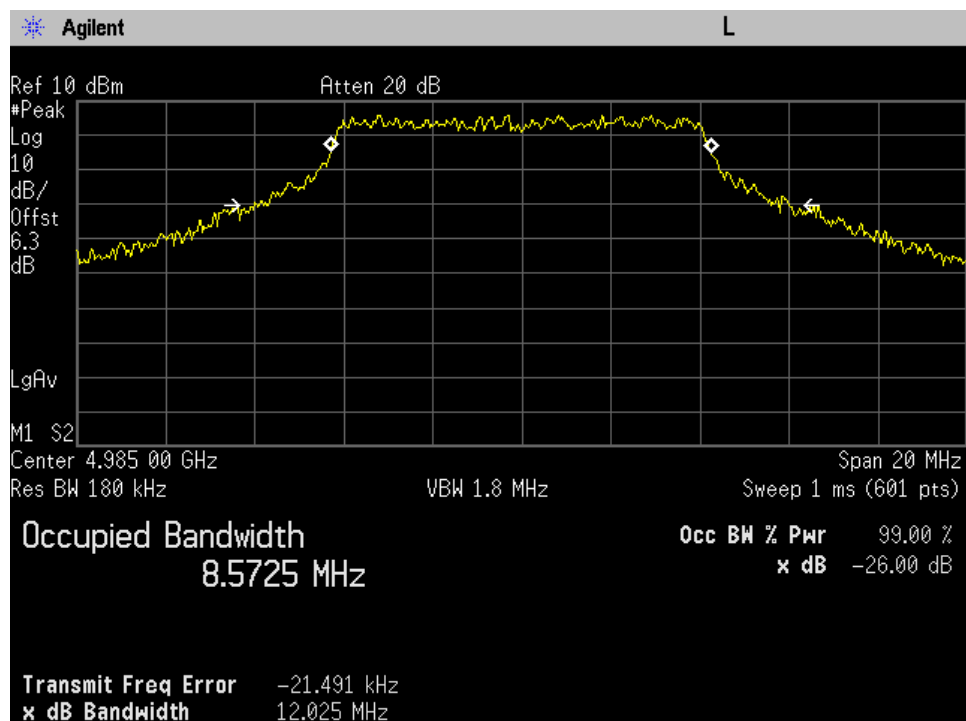




### High ch\_Port A\_5 MHz\_OCBW

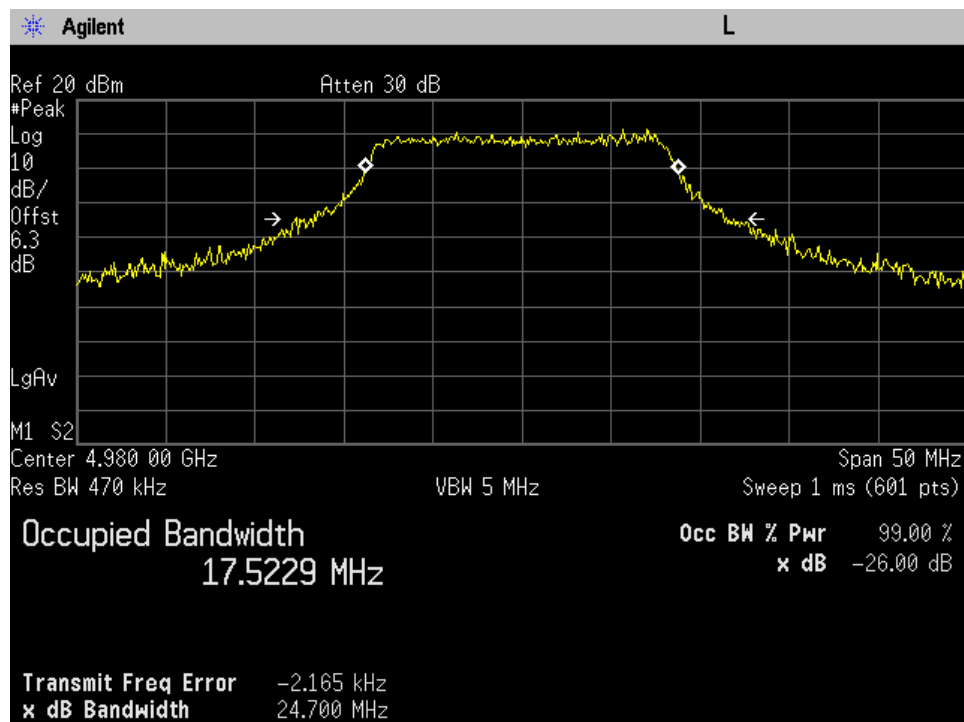


### High ch\_Port B\_10 MHz\_OCBW

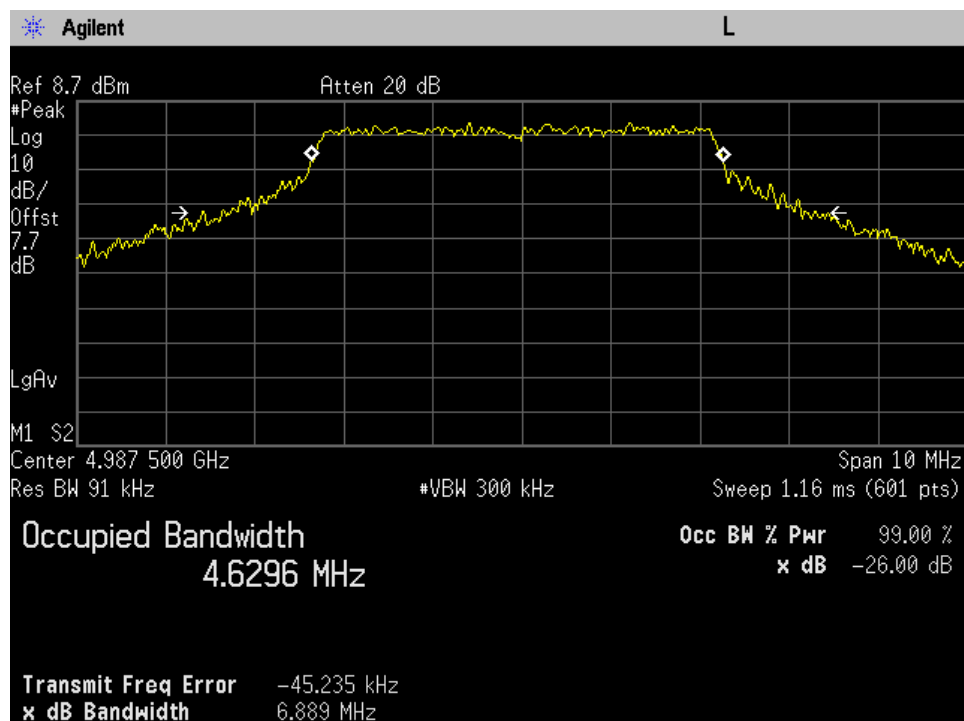




### High ch\_Port B\_20 MHz\_OCBW

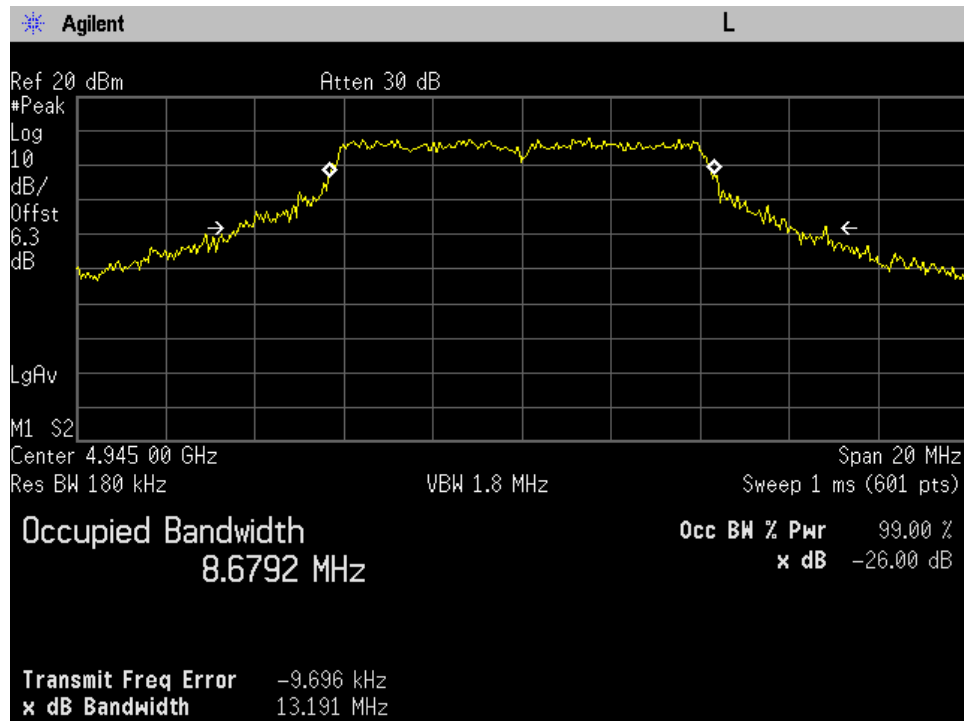


### High ch\_Port B\_5 MHz\_OCBW

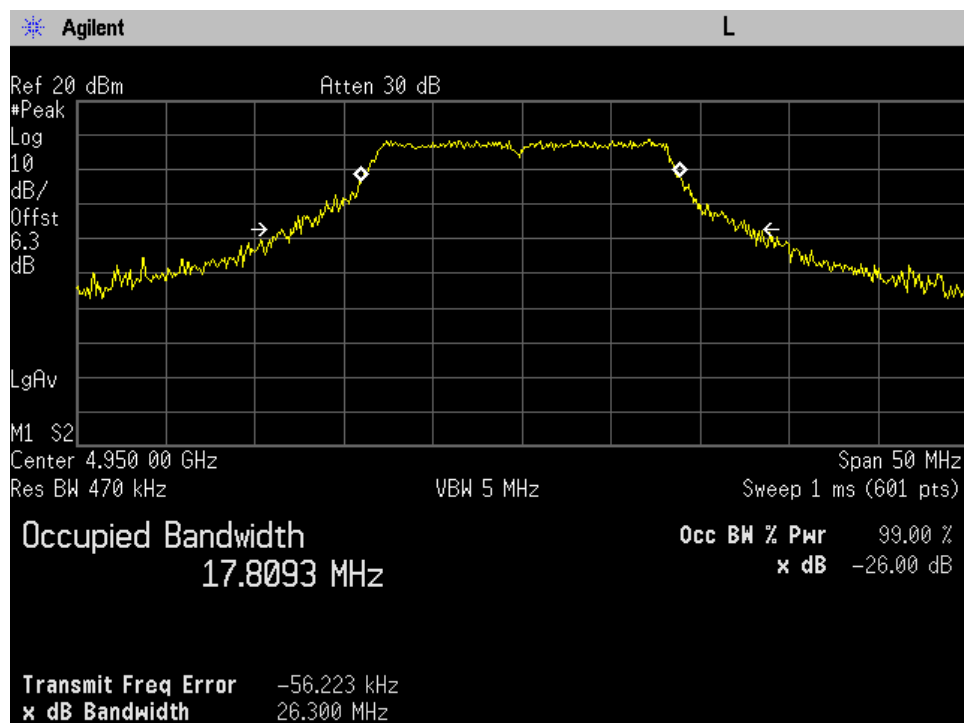




### Low ch\_Port A\_10 MHz\_OCBW

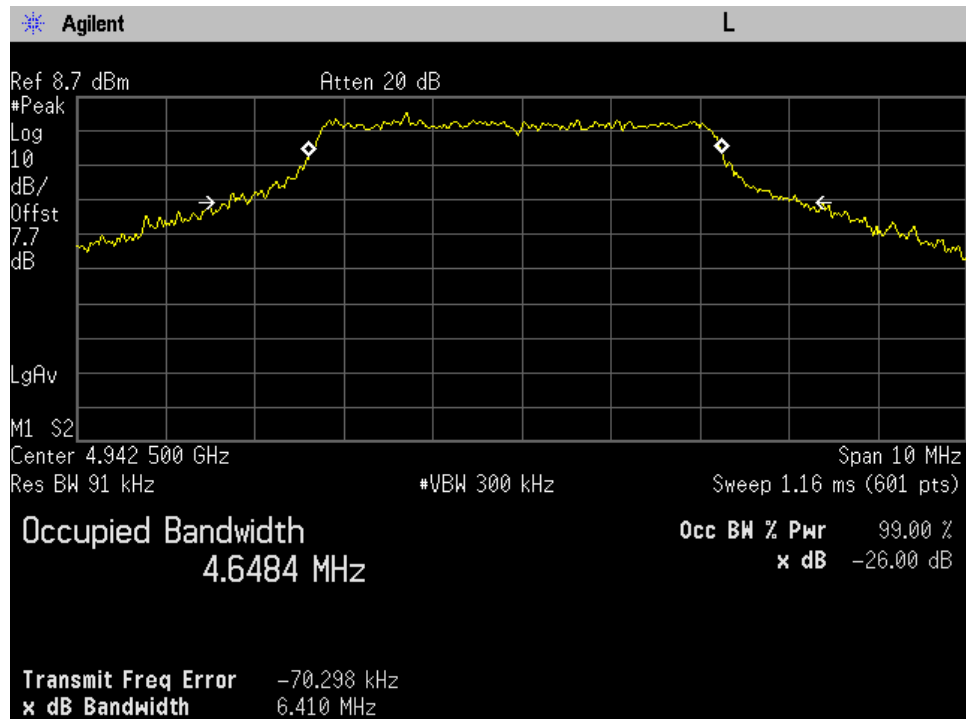


### Low ch\_Port A\_20 MHz\_OCBW

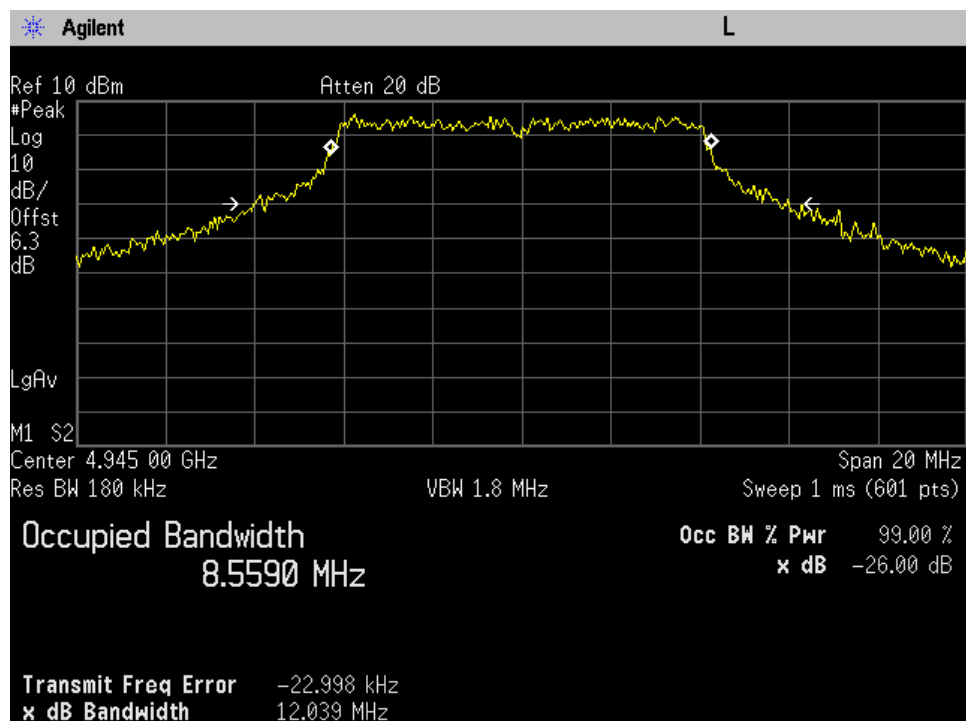




### Low ch\_Port A\_5 MHz\_OCBW

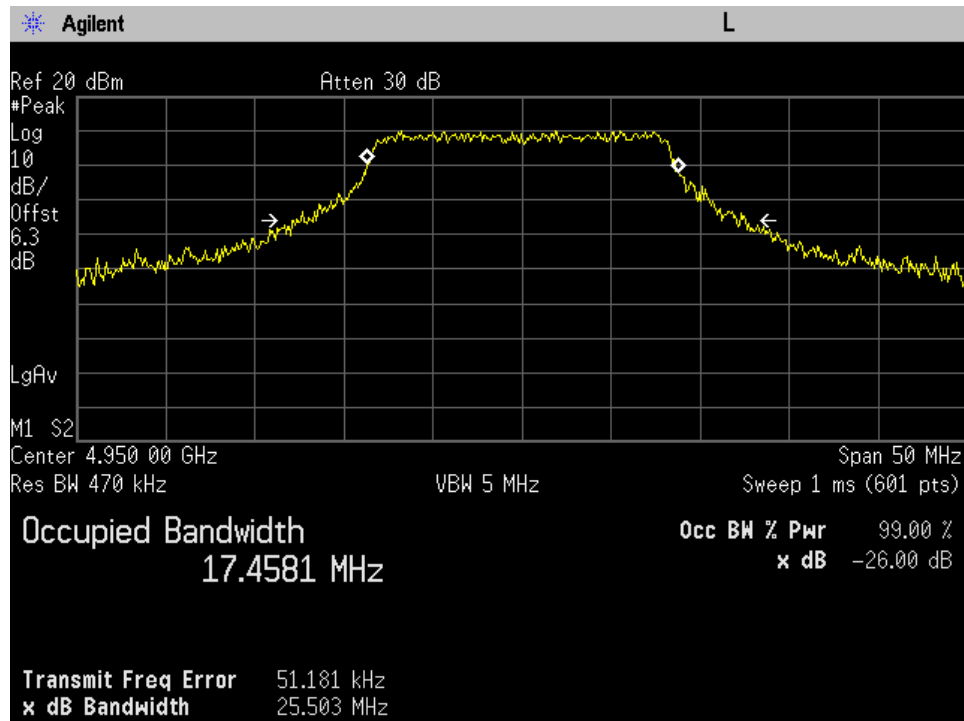


### Low ch\_Port B\_10 MHz\_OCBW

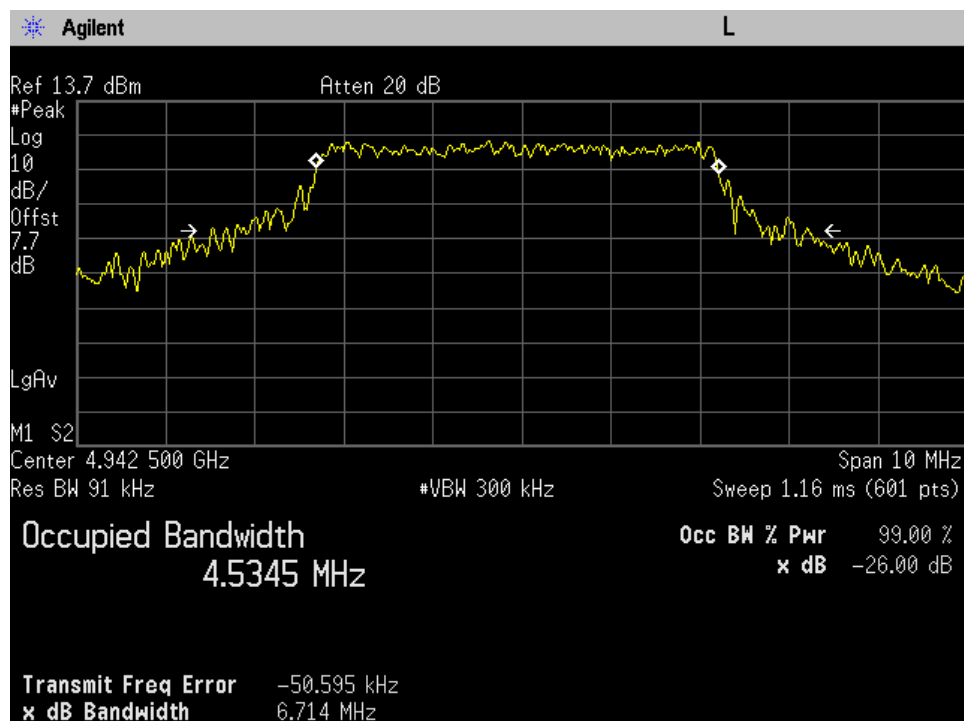




### Low ch\_Port B\_20 MHz\_OCBW

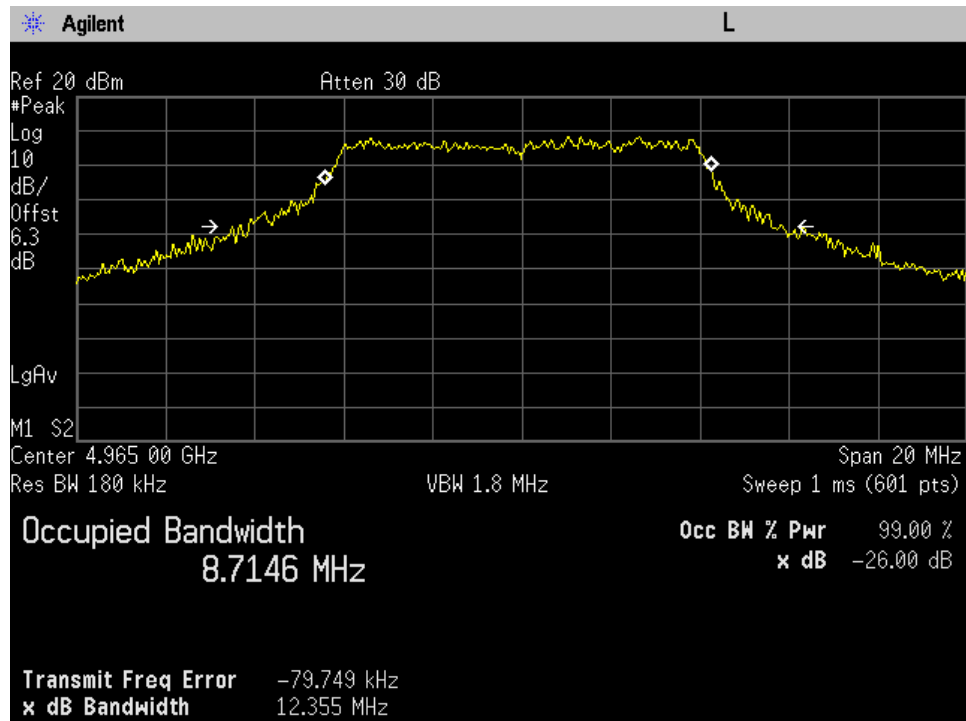


### Low ch\_Port B\_5 MHz\_OCBW

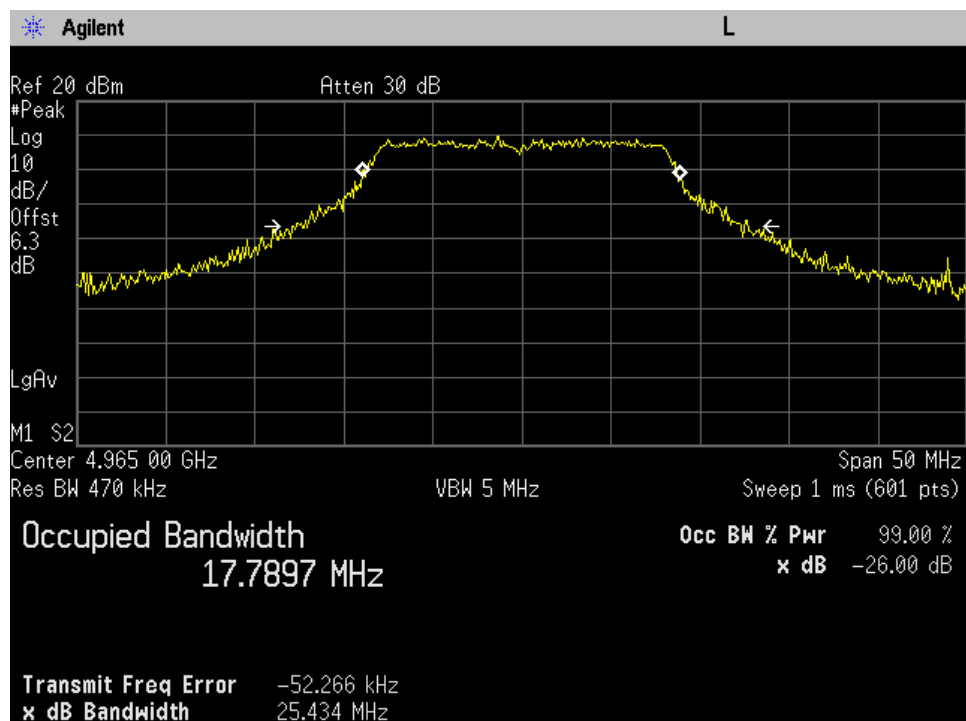




### Mid ch\_Port A\_10 MHz\_OCBW



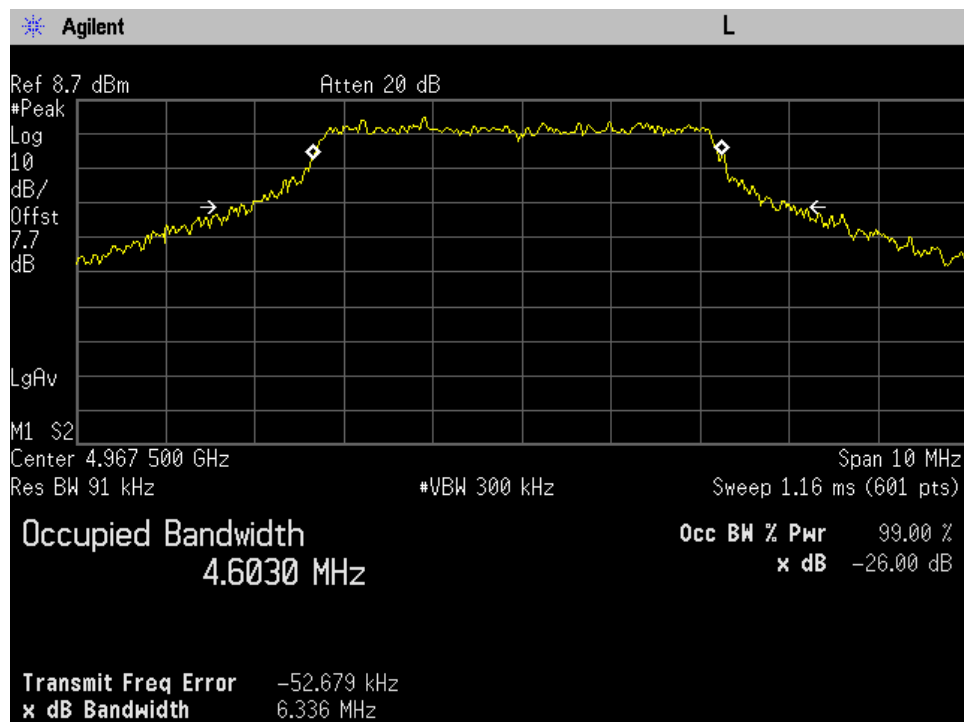
### Mid ch\_Port A\_20 MHz\_OCBW



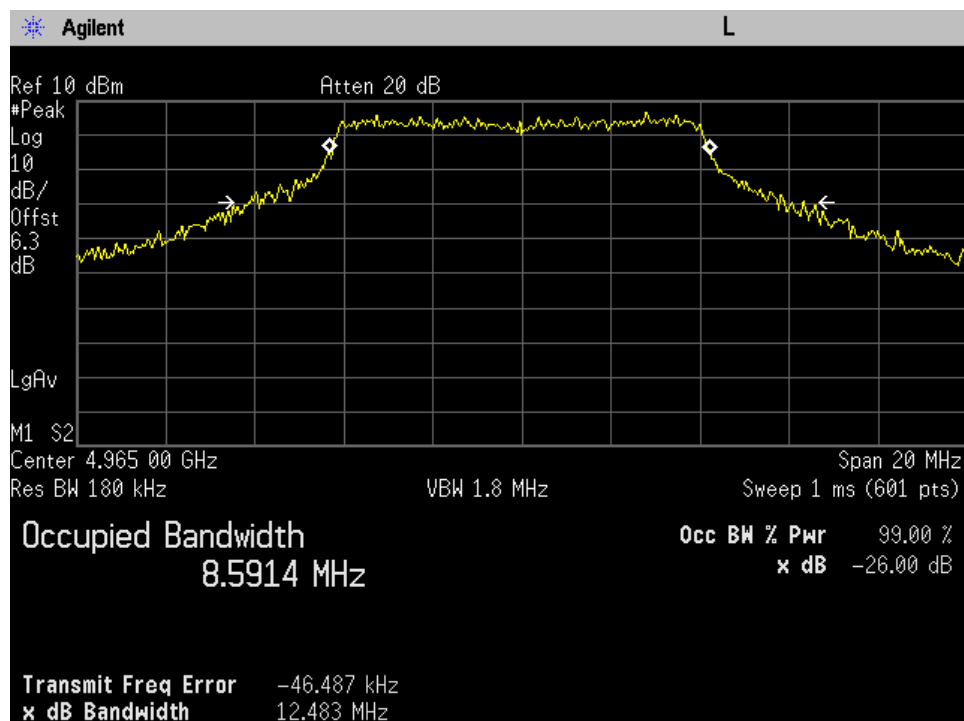




### Mid ch\_Port A\_5 MHz\_OCBW

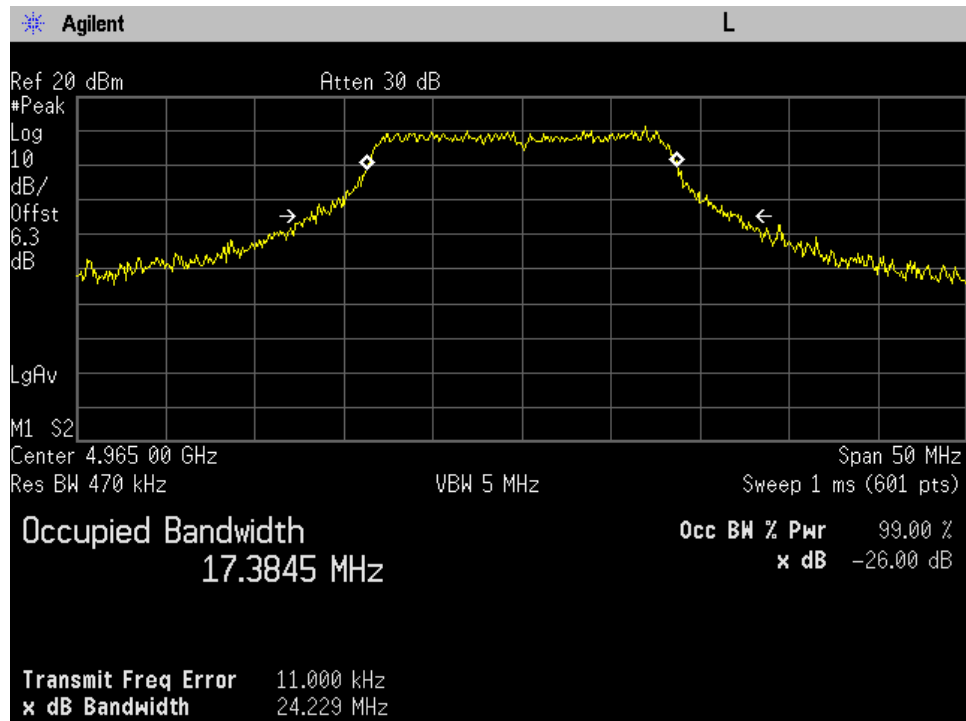


### Mid ch\_Port B\_10 MHz\_OCBW

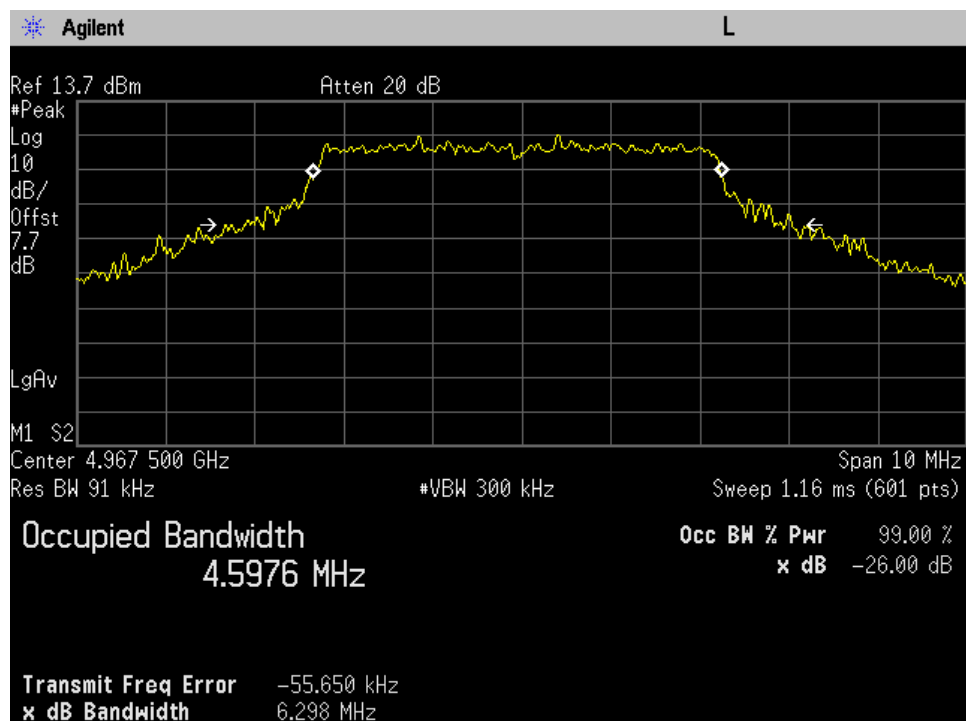




### Mid ch\_Port B\_20 MHz\_OCBW

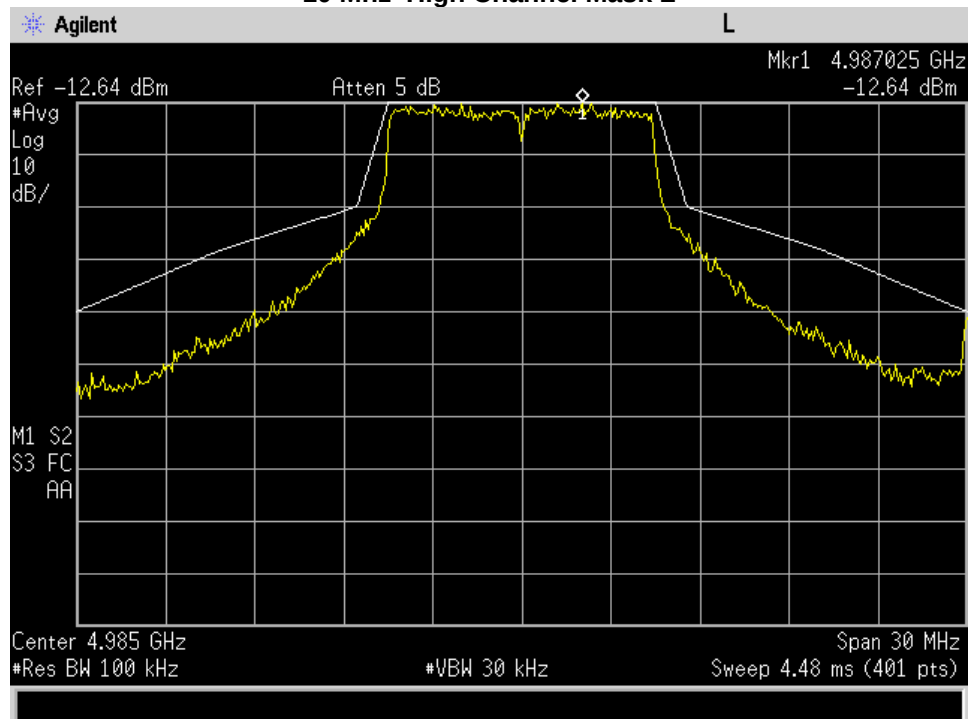


### Mid ch\_Port B\_5 MHz\_OCBW

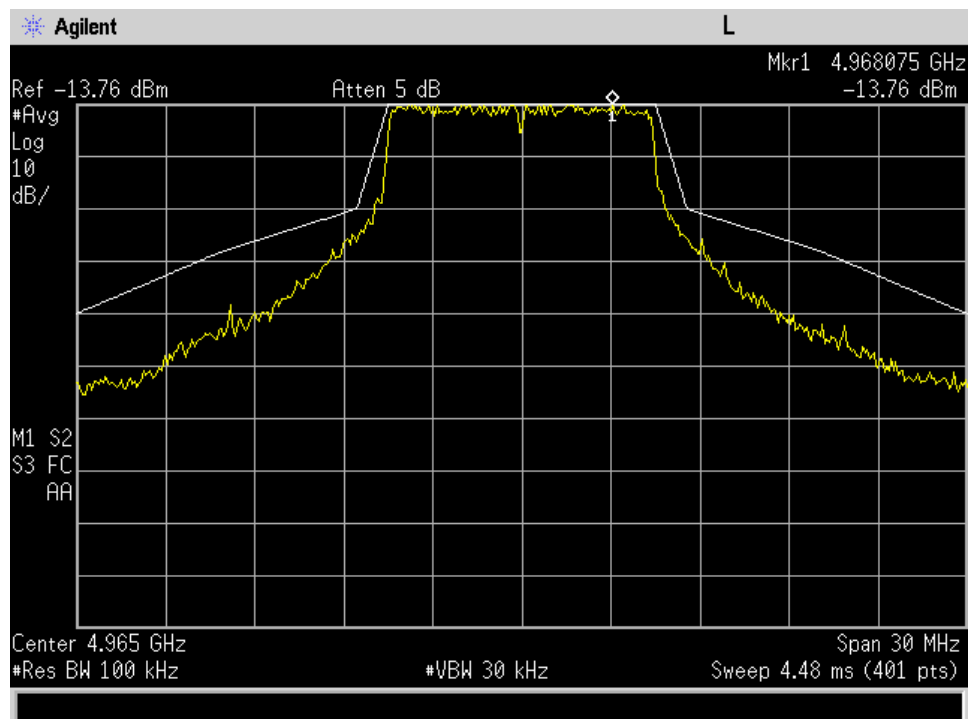




### 10 Mhz High Channel Mask L

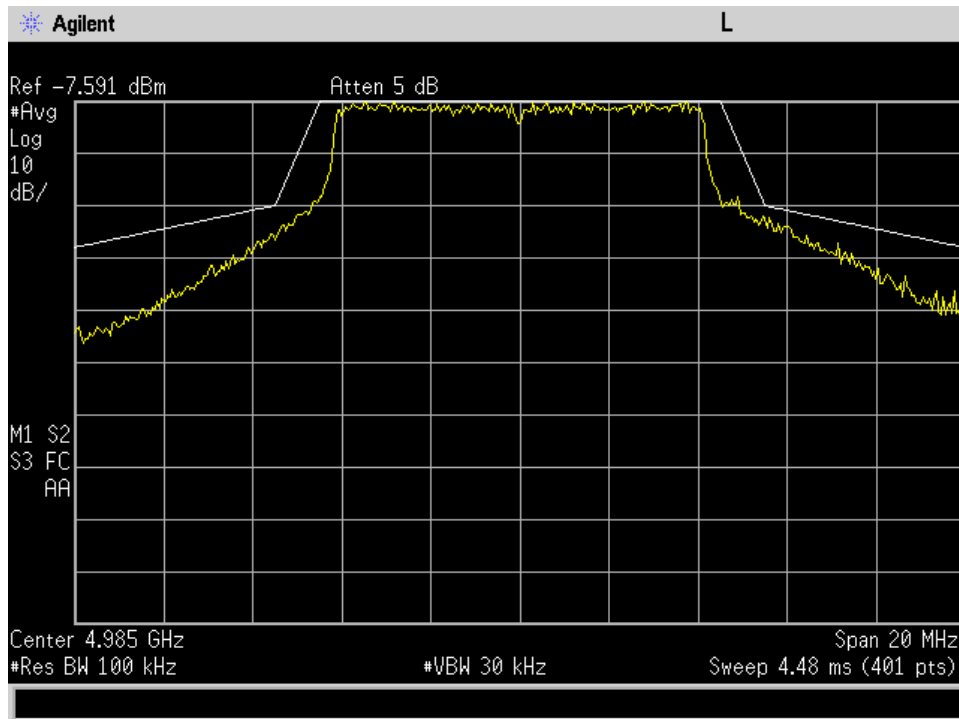


### 10 Mhz Mid Channel Mask L

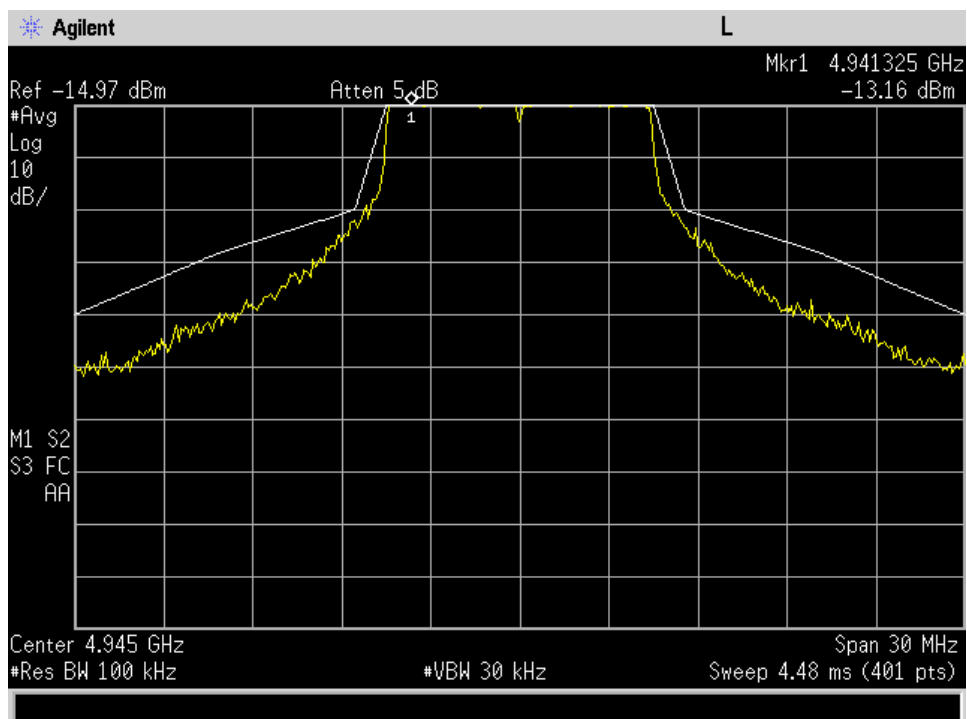




### 10 Mhz High Channel Mask L\_Port B

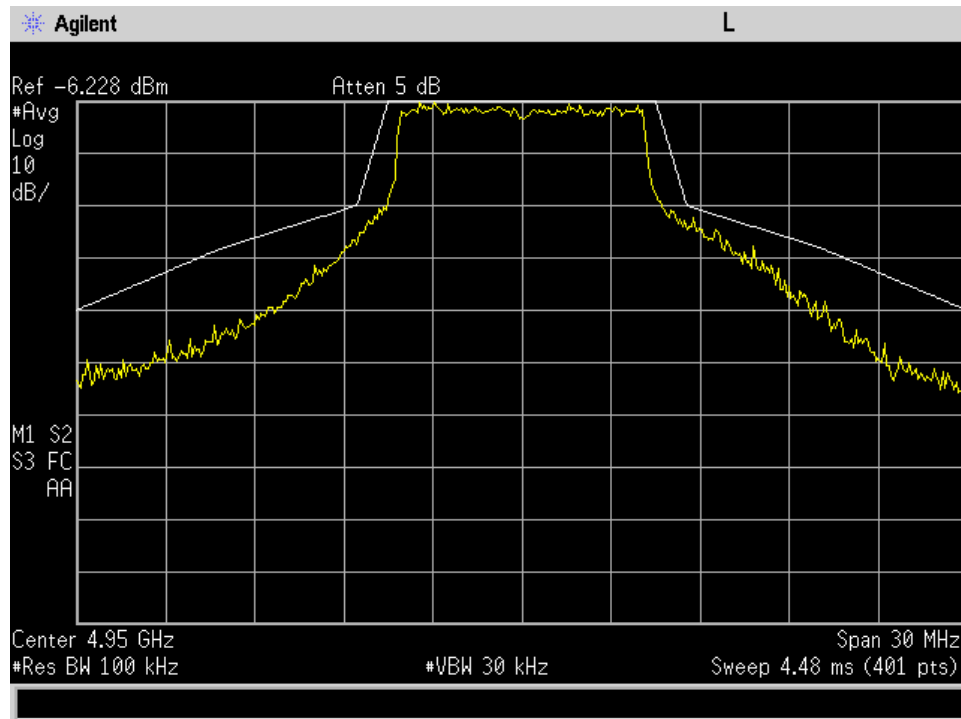


### 10 Mhz Low Channel Mask L

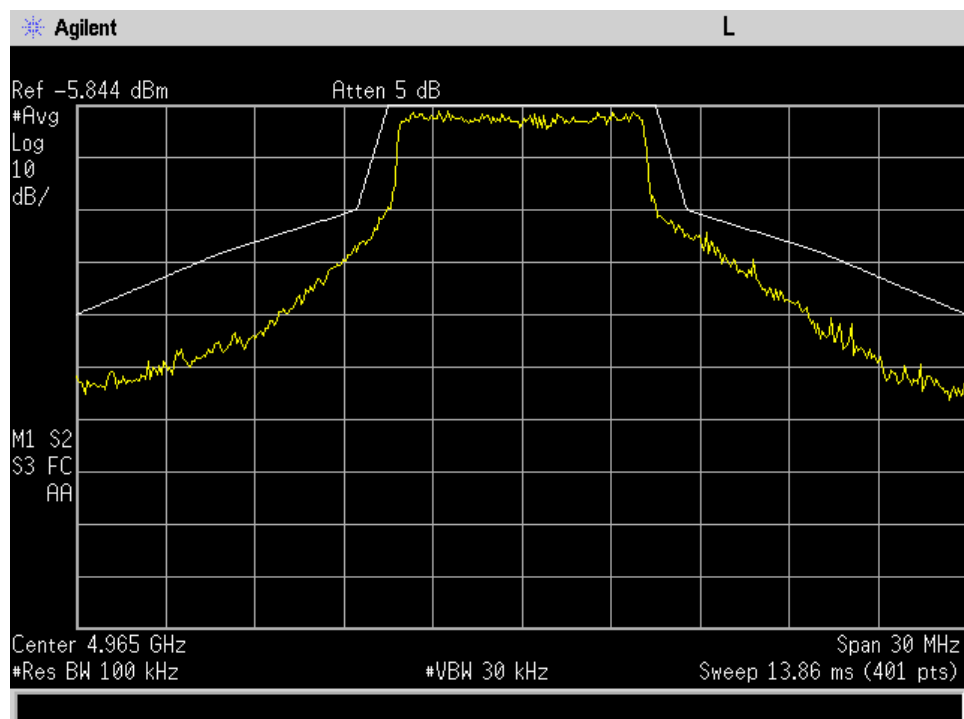




### 10 Mhz Low Channel Mask L\_Port B

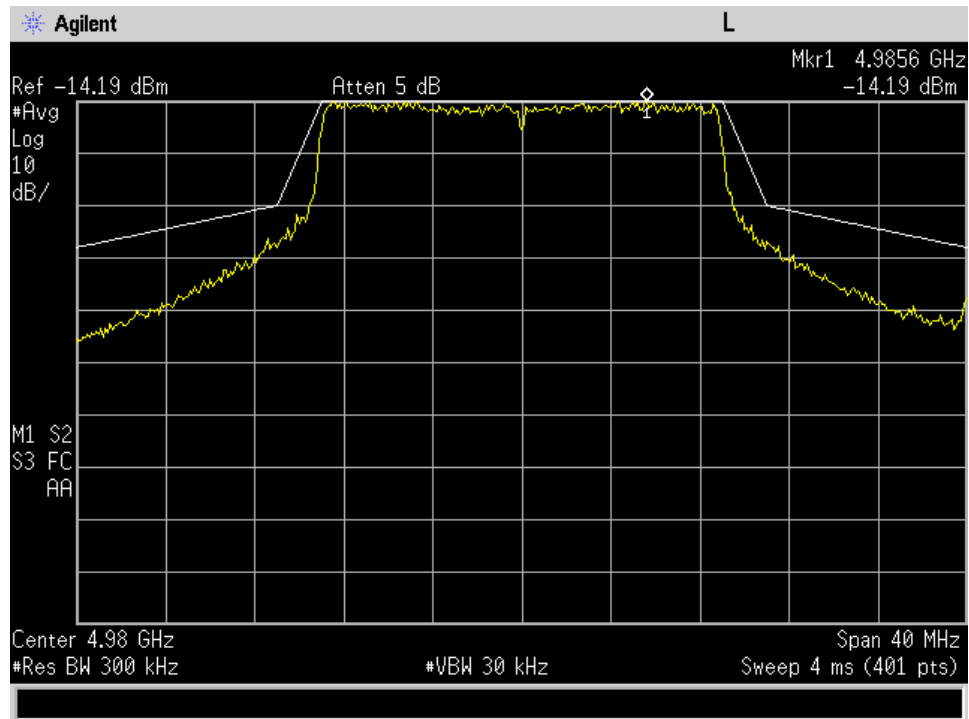


### 10 Mhz Mid Channel Mask L\_Port B

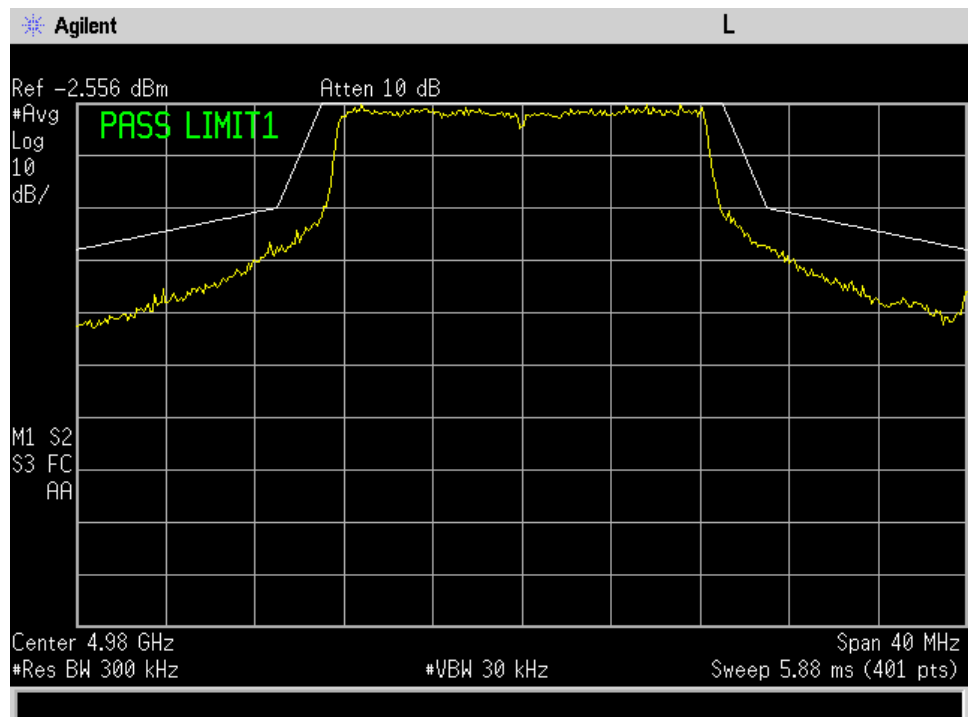




### 20 Mhz High Channel Mask L

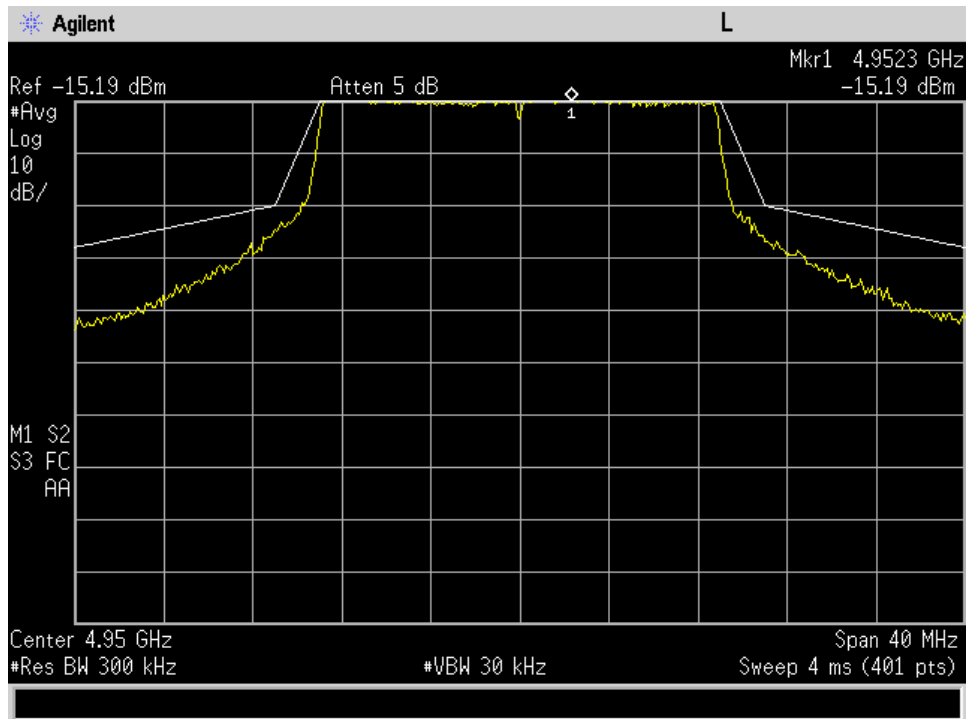


### 20 Mhz High Channel Mask L\_Port B

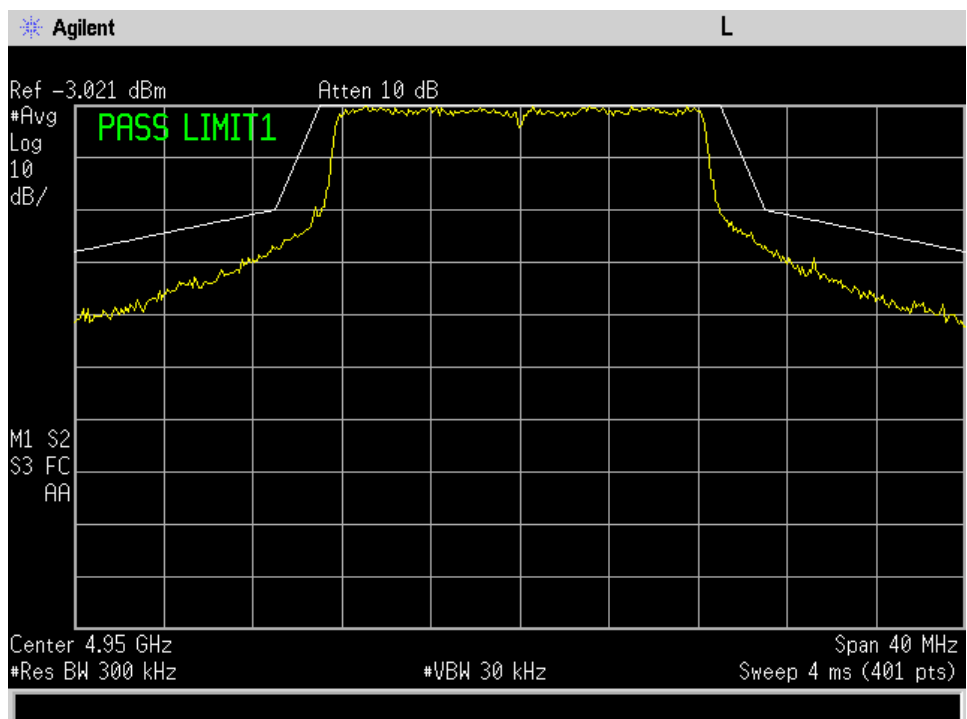




### 20 Mhz Low Channel Mask L

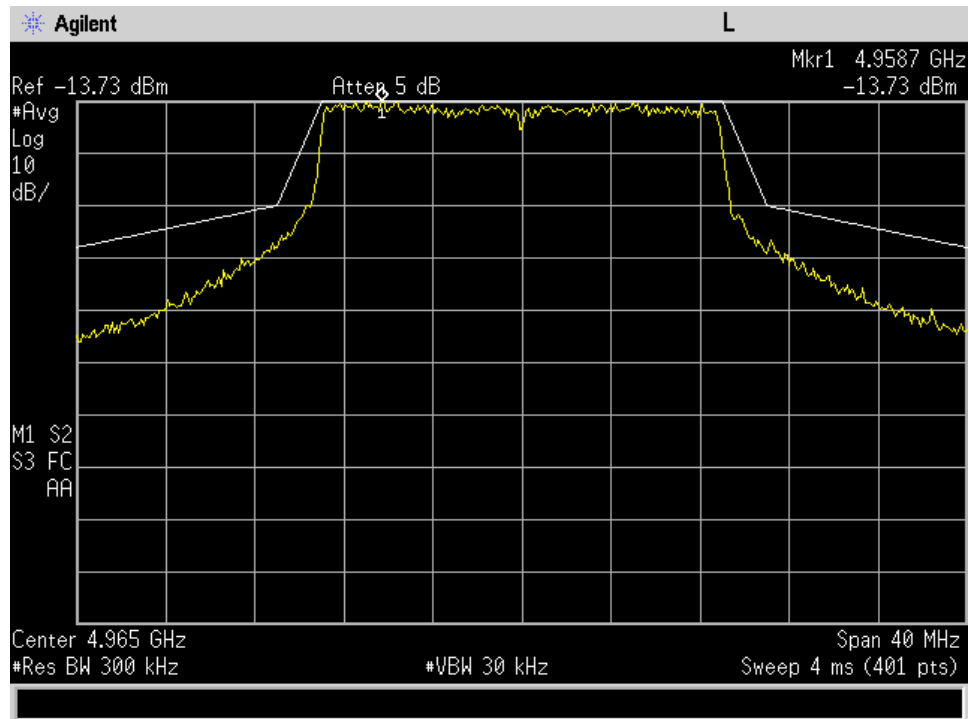


### 20 Mhz Low Channel Mask L\_Port B

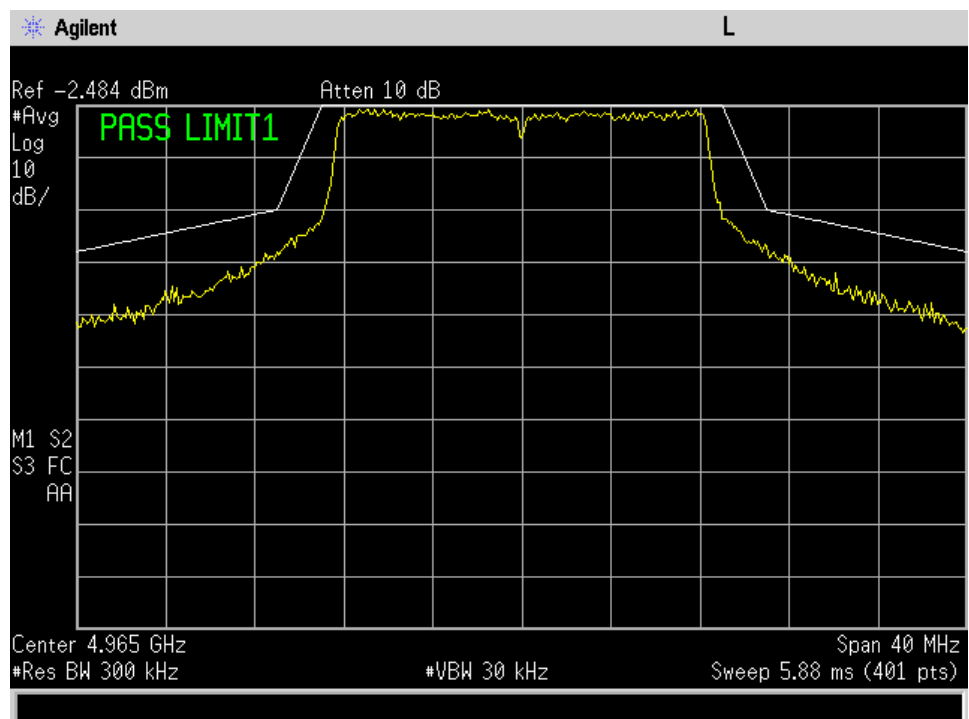




### 20 Mhz Mid Channel Mask L



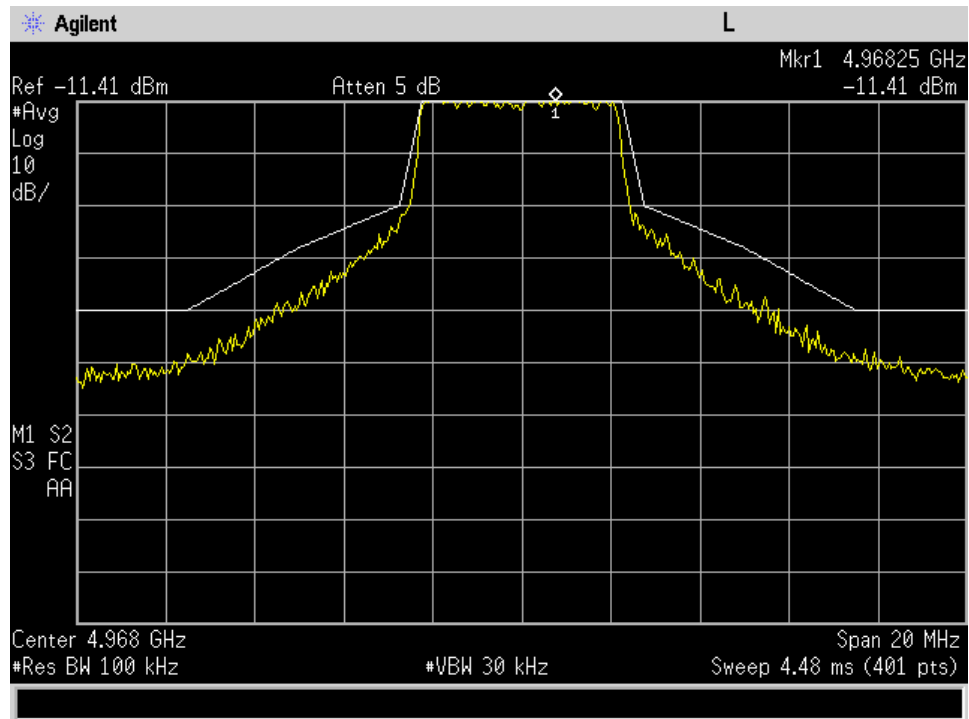
### 20 Mhz Mid Channel Mask L\_Port B



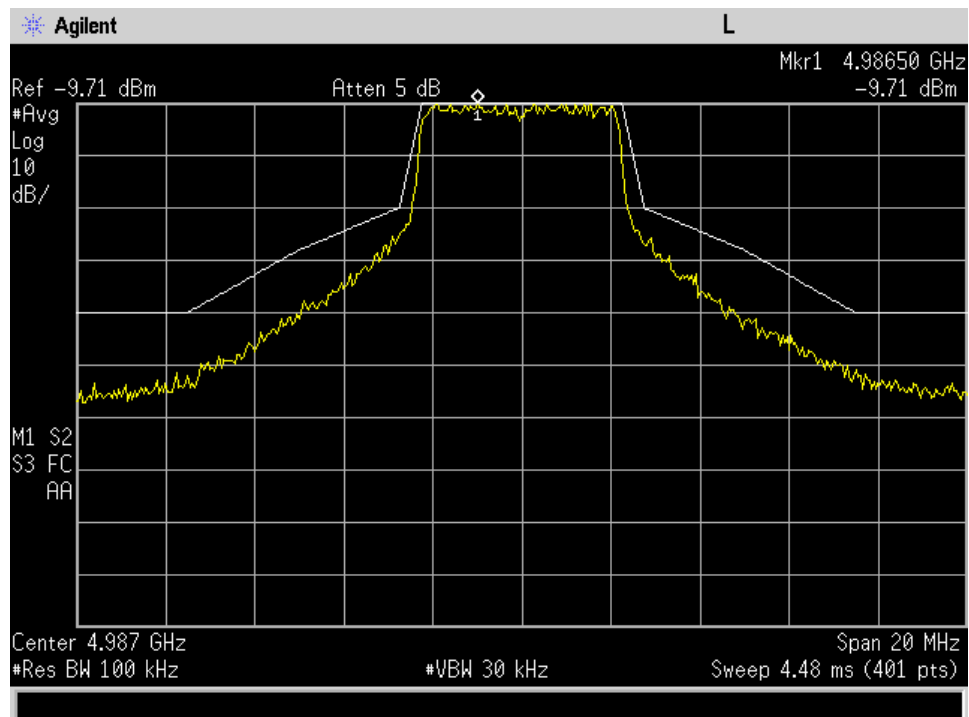




### 5 Mhz Mid Channel Mask L

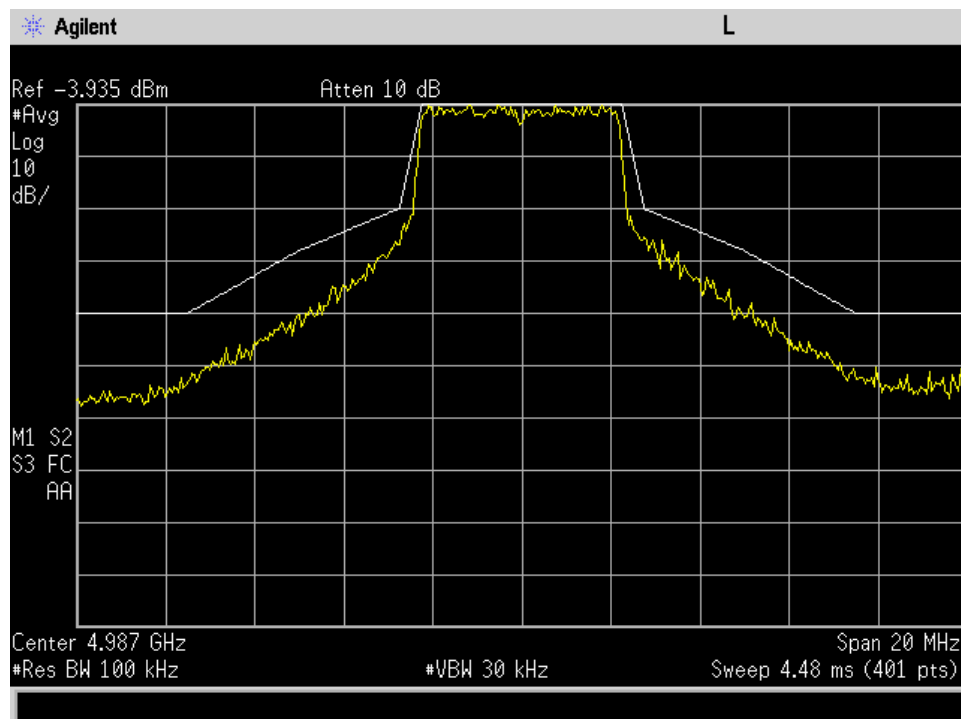


### 5 Mhz High Channel Mask L

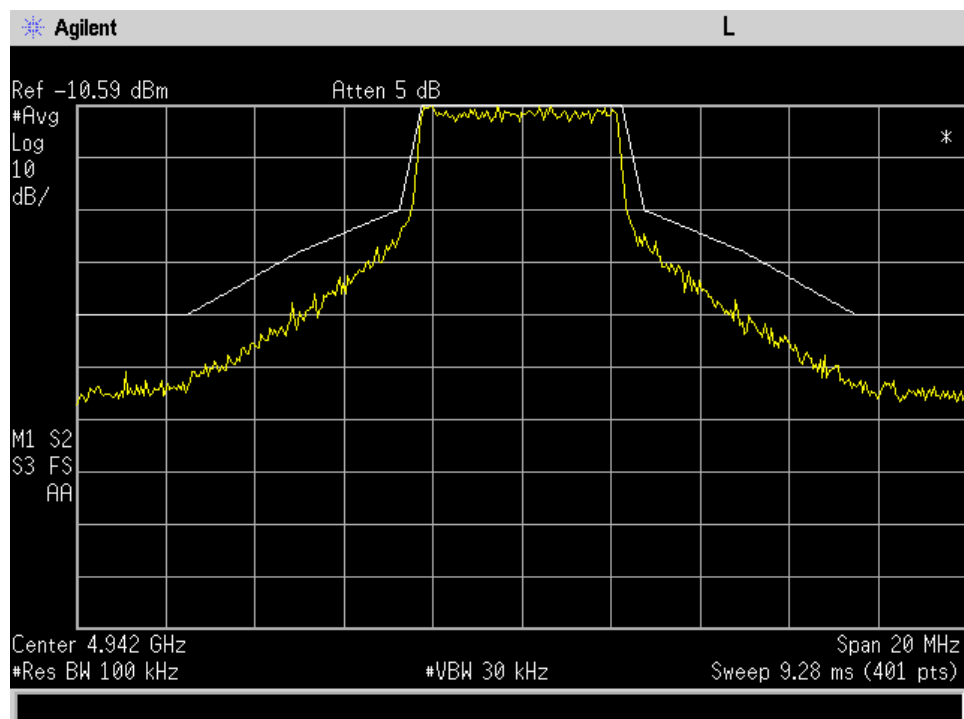




### 5 Mhz High Channel Mask L\_Port B

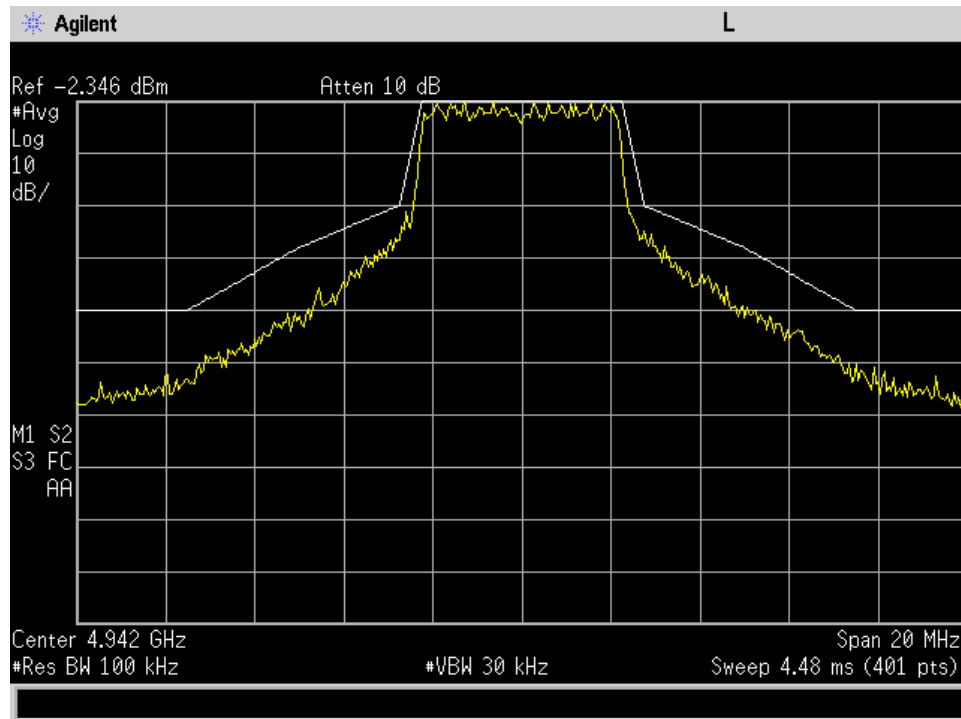


### 5 Mhz Low Channel Mask L





### 5 Mhz Low Channel Mask L\_Port B



### 5 Mhz Mid Channel Mask L\_Port B

