

Bluetooth® 5.2 Wireless Module Data Sheet

onsemi RSL15 Bluetooth 5.2 Soc

- Design Name: Type 2EG
- P/N: LBCA1HN2EG-917



Table of Contents

About This Guide	4
Audience & Purpose	4
Document Conventions	4
1 Product Information/General Information	5
1.1 Scope	5
1.2 Key features	5
1.3 Ordering Information	5
1.4 Block Diagram	7
1.5 Reference	7
2 Certification Information	8
2.1 Radio Certification	8
2.2 FCC Notice (USA)	8
2.3 FCC Labeling Requirements	9
2.4 ISED Notice (Canada)	10
2.5 IC Labeling Requirements	11
2.6 ESTI compliance (Europe)	11
2.7 Bluetooth Qualification	12
3 Dimensions, Marking and Terminal Configurations	13
3.1 Structure	14
4 Module Pin Descriptions	15
4.1 Pin Assignments	15
4.2 Pin Descriptions	16
5 Absolute Maximum Ratings	17
6 Operating Conditions	17
6.1 General Operating Conditions	17
6.2 Digital I/O Requirements	18
7 DC/RF Characteristics	19
7.1 Typical Power Consumption	19
7.2 RF Characteristics	20
8 Power Sequence	21
9 Application Information	21
9.1 Recommended PCB Landing Pattern	21
9.2 Host PCB Layout Recommendations	22
9.3 Layout Guidance for Using Internal PCB Antenna	22
9.4 Layout Guidance for Microstrip Design and External Antenna	24

9.5 Reference Circuit	26
10 Tape and Reel Packing.....	27
10.1 Dimensions of Tape (Plastic tape).....	27
10.2 Dimensions of Reel.....	27
10.3 Taping Diagrams.....	28
10.4 Leader and Tail Tape	29
10.5 Packaging (Humidity Proof Packing)	30
11 Notice	31
11.1 Storage Conditions.....	31
11.2 Handling Conditions	31
11.3 Standard PCB Design (Land Pattern and Dimensions)	31
11.4 Notice for Chip Placer	32
11.5 Soldering Conditions	32
11.6 Cleaning.....	32
11.7 Operational Environment Conditions	33
12 Preconditions to Use Our Products	34
Appendix.....	36
Revision History.....	37

Figures

Figure 1: Type 2EG Block Diagram.....	7
Figure 2: Type 2EG Dimensions, Marking and Terminal Configurations	13
Figure 3: Type 2EG Structure	14
Figure 4: Top View.....	15
Figure 5: Type 2EG Land Patterns.....	21
Figure 6: Host PCB Layout Recommendation, top view.....	22
Figure 7: Layout Guide for Antenna Performance (1).....	23
Figure 8: Layout Guide for Antenna Performance (2).....	23
Figure 9: Layout Guide for External Antenna (1).....	24
Figure 10: Layout Guide for External Antenna (2)	25
Figure 11: Dimensions of Tape (Plastic Tape)	27
Figure 12: Dimension of Reel.....	27
Figure 13: Taping Diagrams.....	28
Figure 15: Taping Specifications.....	28
Figure 15: Taping Diagrams (Close Up).....	28
Figure 16: Leader and Tail Tape	29
Figure 17: Peeling Off Force	29

Figure 18: Packaging	30
Figure 19: Reflow soldering standard conditions (Example)	32

Tables

Table 1: Document Conventions	4
Table 2: Ordering Information	5
Table 3: Radio Certification	8
Table 4: External Antenna to comply with FCC and ISED	8
Table 5: External Antenna to comply with ISED	10
Table 6: External Antenna to comply with ETSI	11
Table 7: Type 2EG Markings	13
Table 8: Type 2EG Dimensions	13
Table 9: Type 2EG Pin Names	15
Table 10: Type 2EG Pin Descriptions	16
Table 11: Type 2EG Absolute Maximum Ratings	17
Table 12: Type 2EG Operating Conditions	17
Table 13: Type 2EG Digital I/O Requirements	18
Table 14: RF Current Consumption	19
Table 15: Sleep Mode Current Consumption	19
Table 16: Type 2EG RF Characteristics	20

About This Guide

This data sheet summarizes the performance and other characteristics of Murata's Type 2EG. This specification is applied to the Type 2EG Bluetooth® 5.2 module.

Audience & Purpose

The audience of this guide is the developer who wants to integrate the Type 2EG module in their systems.

Document Conventions

Table 1 describes the document conventions.

Table 1: Document Conventions

Conventions	Description
	Attention/Warning Note Indicates very important note. Users are strongly recommended to review.
	Informational Note Intended for informational purposes. Users should review.
	Menu item Indicates menu navigation instructions. Example: Insert ➔ Tables ➔ Quick Tables ➔ Save Selection to Gallery 
Console input/output or code snippet	This text style denotes a console input/output or code snippet.
# Console I/O comment	This text style denotes a console input/output comment. This is for informational purposes only and does not denote actual console input/output.
// Code snippet comment	This text style denotes a code snippet comment. This comment may exist in the original code.
	External Hyperlink This symbol indicates a hyperlink to an external document or website. Example: Embedded Artists AB  Click on the text to open the external link.
	Internal Hyperlink This symbol indicates a hyperlink within the document. Example: Introduction  Click on the text to jump to the hyperlinked page and/or section.

1 Product Information/General Information

1.1 Scope

This specification is applied to the Type 2EG Bluetooth 5.2 wireless module.

1.2 Key features

- Bluetooth® v5.2
 - Higher throughput
 - Increased broadcast capacity
 - Improved channel co-existence algorithm (SCA)
 - Long range
 - Proximity
- onsemi RSL15 SoC
- Built-in ARM Cortex M33 core with 80kB RAM (including 64kB user RAM) and 512kB Flash
- Dimension 7.4 mm x 7.0 mm x 1.0 (max.) mm
- Packaging: LGA
- Antenna Configuration:
 - Built-in PCB antenna
 - Optional external antenna from pin pad
- Max. transmit power: 6 dBm
- Receive sensitivity: -96 dBm @ 1Mbps
- Ultra-low power
 - TX 4.3 mA @ 0dBm
 - RX 2.7 mA @ 1Mbps
 - Sleep mode 36nA @3V VBAT
- Host interface: UART, SPI
- Peripheral interfaces: 15 GPIO, ADC, DAC, PWM, I2C, UART, SPI (QSPI), PCM and Debug SWD
- Operating temperature range: -40 °C to 85 °C
- RoHS compliant
- MSL Level 3 in accordance with JEDEC J-STD-020
- Regulatory certificates (plan): FCC, ISED, ETSI

1.3 Ordering Information

Table 2: Ordering Information

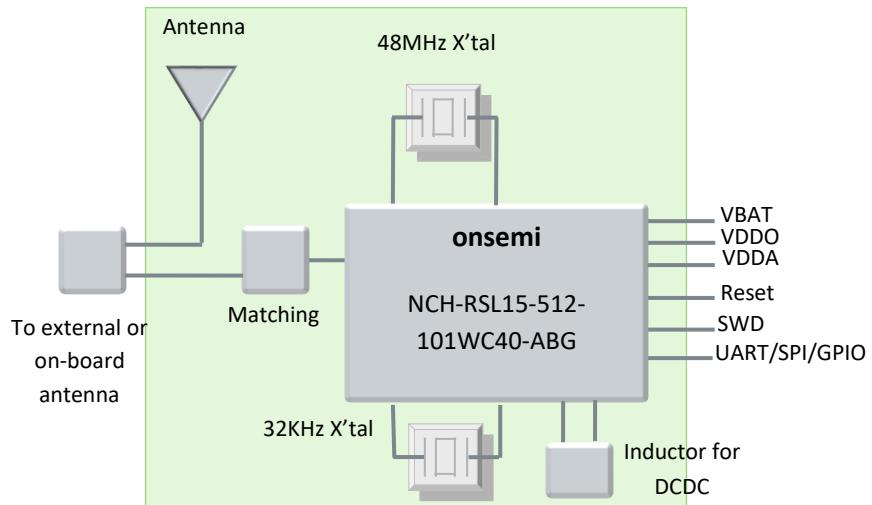
Ordering Part Number	Description
LBCA1HN2EG-917	Module
LBCA1HN2EG-SMP	Sample
LBCA1HN2EG-EVK	Evaluation kit



“Type 2EG” is design name of this module. Design name may be used in certification test report.

1.4 Block Diagram

Figure 1: Type 2EG Block Diagram



1.5 Reference

[1] onsemi Bluetooth® 5.2 Wireless MCU RSL15 Data Sheet, RSL15/D, Rev.1 May 2022

[2] Murata Type 2EG EVK Schematic

2 Certification Information

2.1 Radio Certification (To be confirmed)

Table 3: Radio Certification

Country	Standard	Certification ID
USA (FCC)	FCC Part 15 C	
Canada (ISED)	ISED RSS-247	
Europe	EN300 328, Ver. 2.2.2	
Japan	MIC Radio equipment for Article 2-1-19 of Certification Ordinance	

This radio transmitter has been approved by FCC and ISED to operate with the antenna types listed below with the maximum permissible gain indicated. Antenna types are included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

In the end product, the transmitter must be installed to provide a separation distance of at least 20cm with the Monopole Antenna and at 5mm with the PCB Antenna from all persons and must not be co-located or operation in conjunction with any other antenna or transmitter except in accordance with FCC multi-transmitter product procedures. User and installers must be provided with antenna installation instructions and transmitter operating conditions for satisfying the RF exposure compliance.

Table 4: Antenna to comply with FCC and ISED

Part Number	Vendor	Gain (dBi)	Type	Connector	Remarks
GW26.0152	Taoglas	1.8	Monopole	SMA	Necessary for FCC/ISED compliance only.
		-3.0	PCB		Necessary for FCC/ISED compliance only.

To be compliant with RED Directive while using TYPE 2EG module with external antenna, the total EIRP including the antenna gain should be less than or equal to 10dBm.

2.2 FCC Notice (USA)

Warning: Changes or modifications to this device not expressly approved by Murata could void the user's authority to operate the equipment.

This device complies with Part 15 of the FCC rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

This device complies with below part 15 of the FCC Rules. Part 15 Subpart C.

The FCC requires the OEM to be notified that any changes or modifications not expressly approved by Murata may void the user's authority to operate the equipment. While an application of the TYPE 2EG module in a product is not required to obtain a new FCC authorization for the module, this does not preclude the possibility that some other form of authorization or testing may be required for that end product.

Additional testing, Part 15 Subpart B disclaimer

The modular transmitter is only FCC authorized for the specific rule parts (i.e., FCC transmitter rules) listed on the grant, and that the host product manufacturer is responsible for compliance to any other FCC rules that apply to the host not covered by the modular transmitter grant of certification.

The final host product still requires Part 15 Subpart B compliance testing with the modular transmitter installed.

This module has been tested and found to comply with the following requirements for Modular Approval.

- Part 15.247 - Operation within the bands 902-928 MHz, 2400-2483.5 MHz, and 5725-5850 MHz.

Test Modes

This device uses various test mode programs for test set up which operate separate from production firmware. Host integrators should contact the grantee for assistance with test modes needed for module/host compliance test requirements.

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module. The end user manual shall include all required regulatory information/warning as shown in User manual.

2.3 FCC Labeling Requirements

When integrating the TYPE 2EG into a product the FCC labeling requirements must be met. This includes a clearly visible label on the outside of the finished product specifying the TYPE 2EG FCC identifier (FCC ID: HSW2EG) as well as the notice above. The exterior label can use wording such as "Contains Transmitter Module FCC ID: HSW2EG" or "Contains FCC ID: HSW2EG" although any similar wording that expresses the same meaning may be used.

The following information must be indicated on the host device of this module.

FCC Compliance Information

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

*If it is difficult to describe this statement on the host device due to the size, please describe in the user's manual and also either describe on the device packaging or on a removable label attached to the device.

The following statements must be described on the user manual of the host device of this module.

FCC CAUTION

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

This transmitter must not be co-located or operated in conjunction with any other antenna or transmitter.

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment and meets the FCC radio frequency (RF) Exposure Guidelines as this equipment has very low levels of RF energy. []*

[*]But it is desirable that it should be installed and operated keeping the radiator at least 20cm or more away from person's body.

2.4 ISED Notice (Canada)

This radio transmitter has been approved by FCC and ISED to operate with the antenna types listed below with the maximum permissible gain indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Cet émetteur radio a été approuvé par la FCC et ISED pour fonctionner avec les types d'antennes répertoriés ci-dessous avec le gain maximal autorisé indiqué. Les types d'antennes non inclus dans cette liste, ayant un gain supérieur au gain maximum indiqué pour ce type, sont strictement interdits pour une utilisation avec cet appareil.

This device contains licence-exempt transmitter(s)/receiver(s) that comply with Innovation, Science and Economic Development Canada's licence-exempt RSS(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

L'émetteur/récepteur exempt de licence contenu dans le présent appareil est conforme aux CNR d'Innovation, Sciences et Développement économique Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) L'appareil ne doit pas produire de brouillage; (2) L'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

This radio transmitter (4492A-2EG) has been approved by Innovation, Science and Economic Development Canada to operate with the antenna types listed below, with the maximum permissible gain indicated.

Antenna types not included in this list that have a gain greater than the maximum gain indicated for any type listed are strictly prohibited for use with this device.

Le présent émetteur radio (4492A-2EG) a été approuvé par Innovation, Sciences et Développement économique Canada pour fonctionner avec les types d'antenne énumérés ci-dessous et ayant un gain admissible maximal.

Les types d'antenne non inclus dans cette liste, et dont le gain est supérieur au gain maximal indiqué pour tout type figurant sur la liste, sont strictement interdits pour l'exploitation de l'émetteur.

Table 5: Antenna to comply with ISED

Part Number	Vendor	Gain (dBi)	Type	Connector	Remarks
GW26.0152	Taoglas	1.8	Monopole	SMA	Necessary for FCC/ISED compliance only.
		-3.0	PCB		Necessary for FCC/ISED

					compliance only.
--	--	--	--	--	------------------

2.5 IC Labeling Requirements

The host device should be properly labeled to identify the module within the host device. The Industry Canada certification label of a module shall be clearly visible at all times when installed in the host device, otherwise the host device must be labeled to display the Industry Canada certification number of the module, preceded by the words “Contains transmitter module”, or the word “Contains”, or similar wording expressing the same meaning, as follows:

Contains transmitter module IC: 4492A-2EG, where 4492A-2EG is the module's certification number.

The following statements must be described on the user manual of the host device of this module.

“This equipment complies with ISED radiation exposure limits set forth for an uncontrolled environment and meets RSS-102 of the ISED radio frequency (RF) Exposure rules as this equipment has very low levels of RF energy. (*1)

*Cet équipement est conforme aux limites d'exposition aux rayonnements énoncées pour un environnement non contrôlé et respecte les règles d'exposition aux fréquences radioélectriques (RF) CNR-102 de l'ISDE puisque cet appareil a une niveau très bas d'énergie RF. (*2)*

* In case of the Mobile equipment : Please add the following text.

(*1) But it is desirable that it should be installed and operated keeping the radiator at least 20cm or more away from person's body.

(*2) Cependant, il est souhaitable qu'il devrait être installé et utilisé en gardant une distance de 20 cm ou plus entre le radiateur et le corps humain.

2.6 ESTI compliance (Europe)

This device has been tested for use in the European Union. Both TYPE 2EG comply with the following regulation test.

ETSI EN300 328, Ver. 2.2.2

Table 6: External Antenna to comply with ETSI

Part Number	Vendor	Gain (dBi)	Type	Connector	Remarks
S181XX-2450S	Nearson	2.1	Dipole	SMA	Necessary for FCC/ISED compliance only.

If this device is used in a product, the OEM has responsibility to verify compliance of the final product to the EU standards. A Declaration of Conformity must be issued and kept on file as described in Annex II of the Radio and Telecommunications Terminal Equipment Directive.

2.7 Bluetooth Qualification

QDID: TBD



3 Dimensions, Marking and Terminal Configurations

Figure 2: Type 2EG Dimensions, Marking and Terminal Configurations

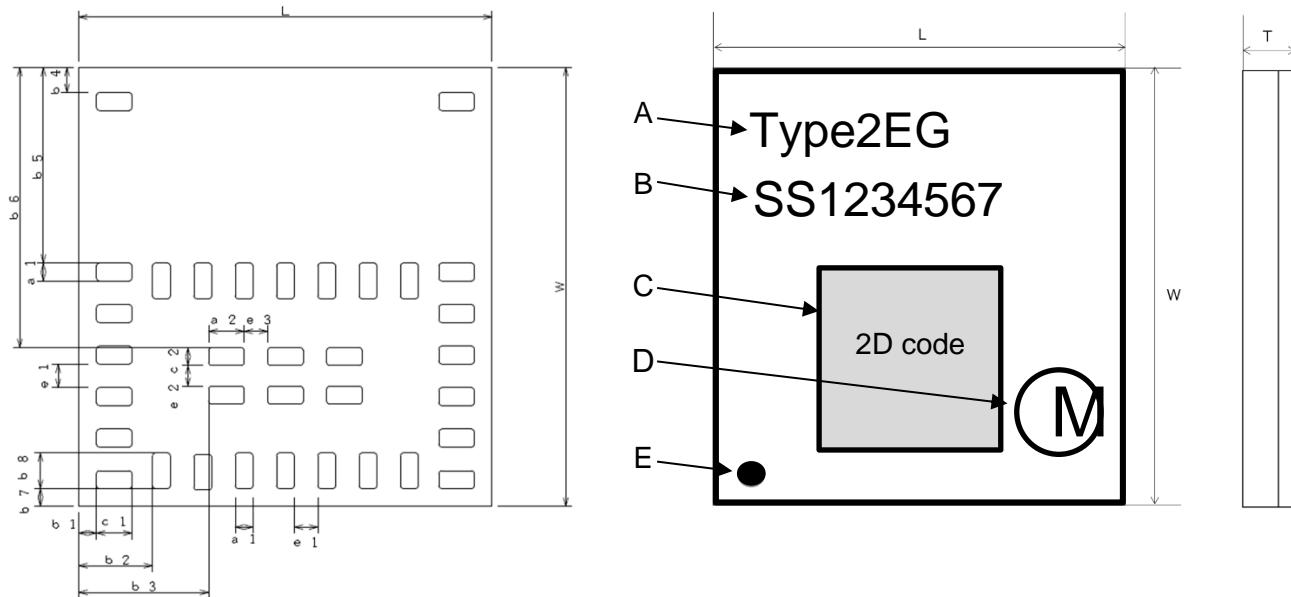


Table 7: Type 2EG Markings

Marking	Meaning
A	Module Type
B	Inspection Number
C	2D code
D	Murata Logo
E	Pin 1 Marking

Table 8: Type 2EG Dimensions

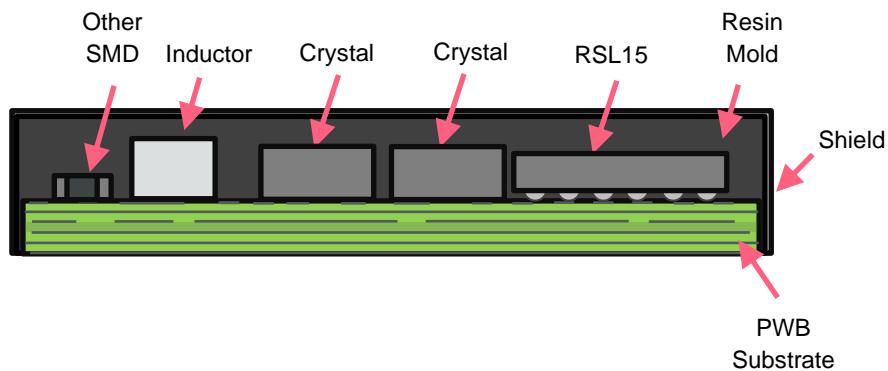
Mark	Dimensions (mm)	Mark	Dimensions (mm)	Mark	Dimensions (mm)
L	7.0 +/- 0.20	W	7.4 +/- 0.20	T	1.0 max.
a1	0.30 +/- 0.10	a2	0.60 +/- 0.10	b1	0.30 +/- 0.20
b2	1.25 +/- 0.20	b3	2.20 +/- 0.20	b4	0.425 +/- 0.20
b5	3.30 +/- 0.20	b6	4.725 +/- 0.20	b7	0.30 +/- 0.20
b8	0.60 +/- 0.10	c1	0.60 +/- 0.10	c2	0.30 +/- 0.10
e1	0.40 +/- 0.10	e2	0.35 +/- 0.10	e3	0.40 +/- 0.10



T dimension does not include height of solder bumps if there is any.

3.1 Structure

Figure 3: Type 2EG Structure



4 Module Pin Descriptions

4.1 Pin Assignments

Figure 4: Top View

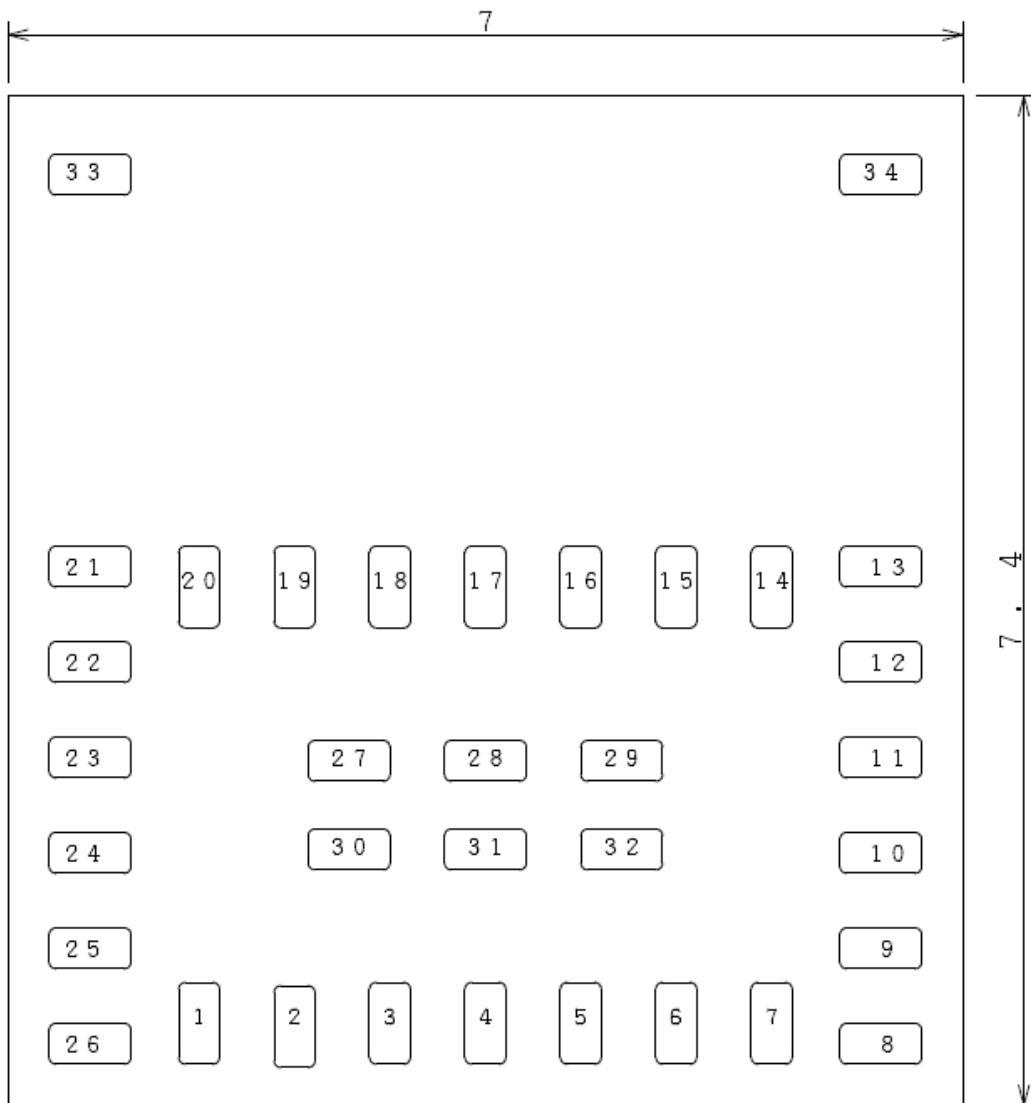


Table 9: Type 2EG Pin Names

No	Pin name						
1	GPIO13	10	GPIO11	19	GND	28	GND
2	GPIO12	11	GPIO4	20	ANT	29	GND
3	GPIO7	12	GPIO2	21	RF	30	GND
4	JTMS	13	NRST	22	GND	31	GND
5	JTCK	14	GPIO10	23	VBAT	32	GND
6	VDDO	15	GPIO1	24	GND	33	NC
7	GPIO14	16	GPIO0	25	VDDA	34	NC

No	Pin name						
8	GPIO8	17	GPIO3	26	GPIO5		
9	GPIO6	18	GPIO9	27	GND		

4.2 Pin Descriptions

Table 10: Type 2EG Pin Descriptions

No.	Pin name	Type	Connection to RSL15 pin name	Description
1	GPIO13	I/O	"C4"	General purpose I/O
2	GPIO12	I/O	"B4"	General purpose I/O
3	GPIO7	I/O	"B5"	General purpose I/O
4	SWDIO	I/O	"B6"	Serial Wire Debug I/O
5	SWCLK	I/O	"B7"	Serial Wire Debug clock
6	VDDO	PWR	"A7"	Module I/O voltage supply
7	GPIO14	I/O	"A6"	General purpose I/O
8	GPIO8	I/O	"A5"	General purpose I/O
9	GPIO6	I/O	"A4"	General purpose I/O
10	GPIO11	I/O	"A3"	General purpose I/O
11	GPIO4	I/O	"B3"	General purpose I/O
12	GPIO2	I/O	"A2"	General purpose I/O
13	NRST	I/O	"D1"	Reset
14	GPIO10	I/O	"A1"	General purpose I/O
15	GPIO1	I/O	"B1"	General purpose I/O
16	GPIO0	I/O	"C1"	General purpose I/O
17	GPIO3	I/O	"B2"	General purpose I/O
18	GPIO9	I/O	"C2"	General purpose I/O
19	GND	GND		
20	ANT	RF		Connection to integrated antenna
21	RF	RF		RF signal from/to IC
22	GND	GND		
23	VBAT	PWR	"G2"	Module core & RF voltage supply
24	GND	GND		
25	VDDA	PWR	"G3"	Charge pump output for analog and flash supplies
26	GPIO5	I/O	"C3"	General purpose I/O
27	GND	GND		
28	GND	GND		
29	GND	GND		

No.	Pin name	Type	Connection to RSL15 pin name	Description
30	GND	GND		
31	GND	GND		
32	GND	GND		
33	NC	NC		NC
34	NC	NC		NC

5 Absolute Maximum Ratings

Table 11: Type 2EG Absolute Maximum Ratings

Parameter	Minimum	Maximum	Unit
Storage Temperature	-40	+85	Degree C
Operating Temperature	-40	+85	Degree C
VBAT		3.63	V
VDDO		3.63	V
V _{in}	0	VDDO+0.3	V
RF input level		18	dBm



Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability. No damage assuming only one parameter is set at limit at a time with all other parameters are set within operating condition.

6 Operating Conditions

6.1 General Operating Conditions

Table 12: Type 2EG Operating Conditions

Parameter	Symbol	Note	Min	Typ.	Max	Unit
Operating Temperature			-40	25	+85	Degree C
VBAT supply rise time		Maximum rate of voltage rise			0.1	V/us
DC-DC Convert Input Voltage	VBAT	Buck Mode	1.4		3.6	V
		LDO Mode	1.2		3.6	V
Digital I/O Supply Input	VDDO		1.2		3.6	V



Operation beyond the recommended operating conditions is neither recommended nor guaranteed.

6.2 Digital I/O Requirements

Table 13: Type 2EG Digital I/O Requirements

Symbol	Parameter	Condition	Min	Typ.	Max	Unit
V_{IH}	Input high voltage	-	$0.75^* VDDO$	-	$VDDO + 0.3$	V
V_{IL}	Input low voltage	-	-0.3	-	$0.25^* VDDO^1$	V
V_{OH}	Output high voltage	-	$VDDO - 0.4$	-	$0.35^* VDDO$	V
V_{OL}	Output low voltage	-	-	-	0.4	V
	Weak Pull-up Resistor			250		kΩ
	Strong Pull-up Resistor			10		kΩ
	Pull-down Resistor			250		kΩ

1. For $VDDO < 1.8$ V, V_{IL} Max is $0.23^* VDDO$

7 DC/RF Characteristics

7.1 Typical Power Consumption



Conditions: 25deg.C, VBAT=VDDO=3.0V

Table 14: RF Current Consumption

Operating Condition	VBAT	DC Conversion	Min	Typ.	Max	Unit
Radio Receive Mode Rx @ 1 Mbps, 2.4 GHz 8 MHz system clock Cortex-M33 running BLE baseband only All Peripherals Disabled 64 kB RAM enabled	3.0	Buck Mode		2.7		mA
	1.25	LDO Mode		5.8		
Radio Transmit Mode Tx @ 1 Mbps, 2.4 GHz, 0 dBm 8 MHz system clock Cortex-M33 running BLE baseband only All Peripherals Disabled 64 kB RAM enabled	3.0	Buck Mode		4.3		mA
	1.25	LDO Mode		9.1		
Radio Transmit Mode Tx @ 1 Mbps, 2.4 GHz, 6 dBm 8 MHz system clock Cortex-M33 running BLE baseband only All Peripherals Disabled 64 kB RAM enabled	3.0	Buck Mode		11.4		mA
	1.25	LDO Mode		24.1		

Table 15: Sleep Mode Current Consumption

Operating Condition	Wakeup Source	VBAT	DC Conversion	Min	Typ.	Max	Unit
Clocks stopped All peripherals disabled No RAM retained 32 kHz RC32 inactive 32 kHz XTAL32K inactive	GPIO	3.0	Buck Mode		36		nA

7.2 RF Characteristics



Conditions: 25deg.C, VBAT=VDDO=3.0V

Table 16: Type 2EG RF Characteristics

RF Characteristics		Min	Typ.	Max	Unit
Center frequency		2402	-	2480	MHz
Channel Spacing		-	2	-	MHz
Number of RF Channels		-	40	-	-
Max output power *	DCDC mode	-	6	-	dBm
	LDO mode		6		dBm
Modulation Characteristics					
$\Delta f_{1\text{avg}}$		225	-	275	kHz
$\Delta f_{2\text{max}}$ (at 99.9%)		185	-	-	kHz
$\Delta f_{2\text{avg}} / \Delta f_{1\text{avg}}$		0.8	-	-	-
Carrier frequency offset and drift					
1) Frequency offset: $ f_n - f_{\text{TX}} $		-	-	150	kHz
2) Frequency drift: $ f_0 - f_n $		-	-	50	kHz
3) Drift rate #0: $ f_1 - f_0 $		-	-	20	kHz
4) Drift rate #n: $ f_n - f_{n-5} $		-	-	20	kHz
Receiver sensitivity (PER < 30.8%)	DCDC mode	-	-96	-70	dBm
	LDO mode	-	-96	-70	dBm
Maximum input signal level (PER < 30.8%)		-10	-	-	dBm

8 Power Sequence

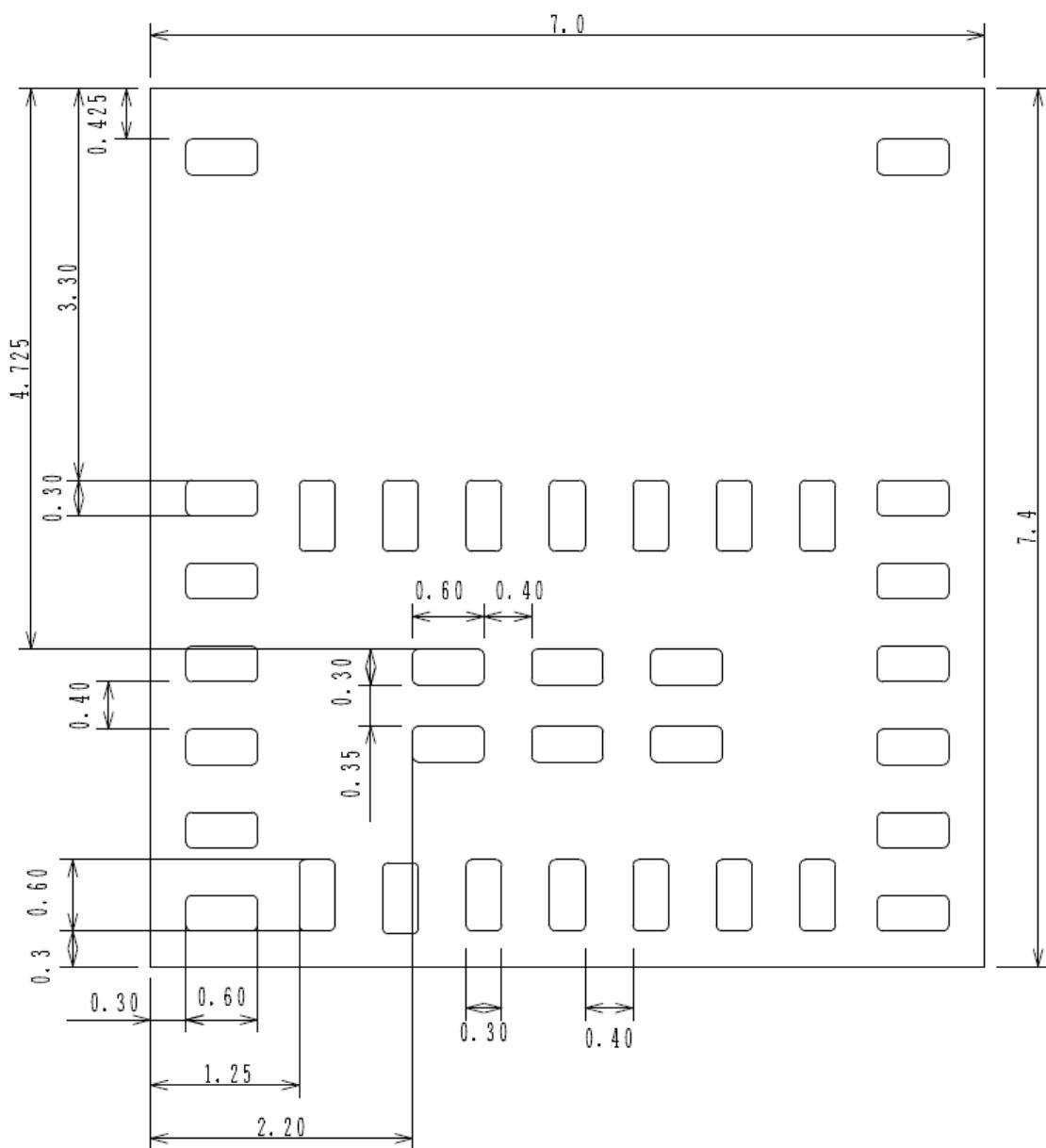
For device start up sequence, please refer to onsemi RSL15 datasheet [1].

9 Application Information

9.1 Recommended PCB Landing Pattern

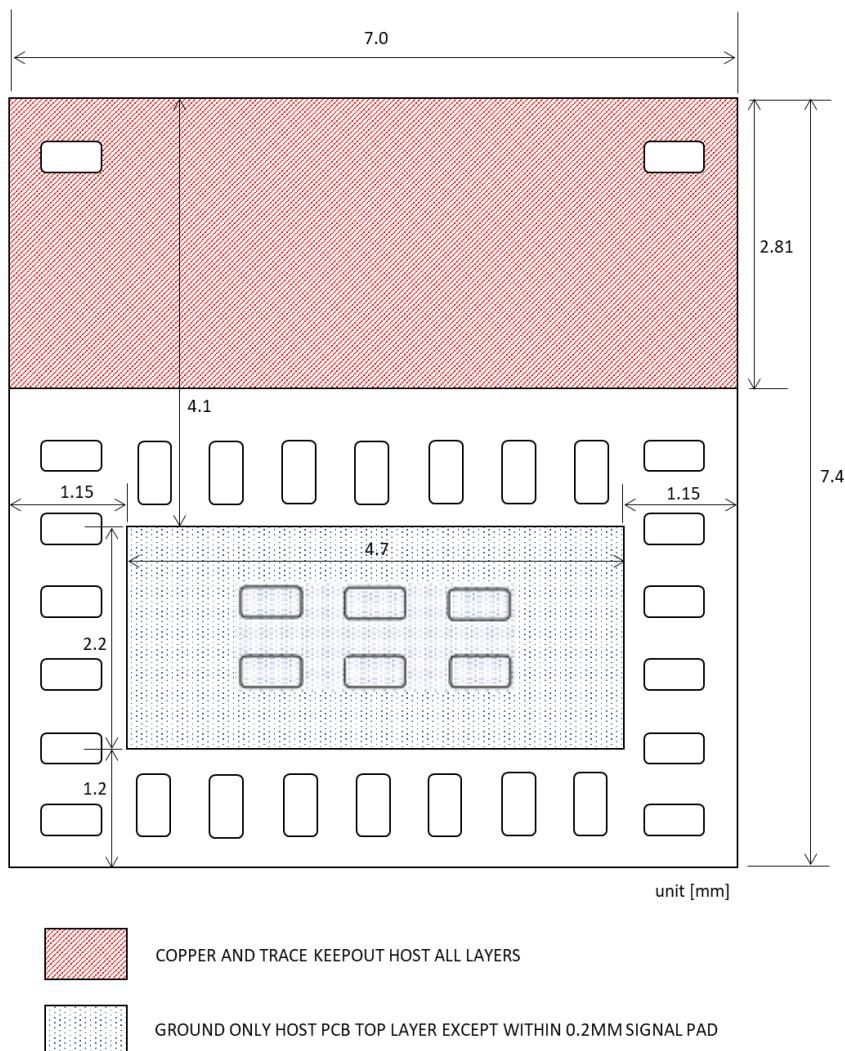
We recommend the host PCB land pattern be the same as the module pin pad pattern.

Figure 5: Type 2EG Land Patterns



9.2 Host PCB Layout Recommendations

Figure 6: Host PCB Layout Recommendation, top view



9.3 Layout Guidance for Using Internal PCB Antenna

The TYPE 2EG module is certified with an internal PCB antenna physically located on the module. For optimum EIRP (Effectively Isotropically Radiated Power) when use the on-board antenna, please follow the following recommendations in the customer host circuit board design.

- A. The module is recommended to be placed on top left corner of the host circuit board.
- B. Around the antenna area, all layers of the customer circuit board should be free of any metal objects. Specifically, there should be no ground plane, traces, or metal shield case.
- C. Host circuit PCB length is longer than 40mm to get optimal performance by using ground plane.
- D. Right side of antenna area also should be free of grounds.
- E. Metal and plastic materials should be away from the module. (More than 3mm)

F. To use this internal antenna, the integrator must provide a simple two-component matching circuit between pins 20 and 21 of the module. This circuit will connect the RF I/O of the module directly to the PCB antenna. The picture below shows the location of pins 20 and 21.

Figure 7: Layout Guide for Antenna Performance (1)

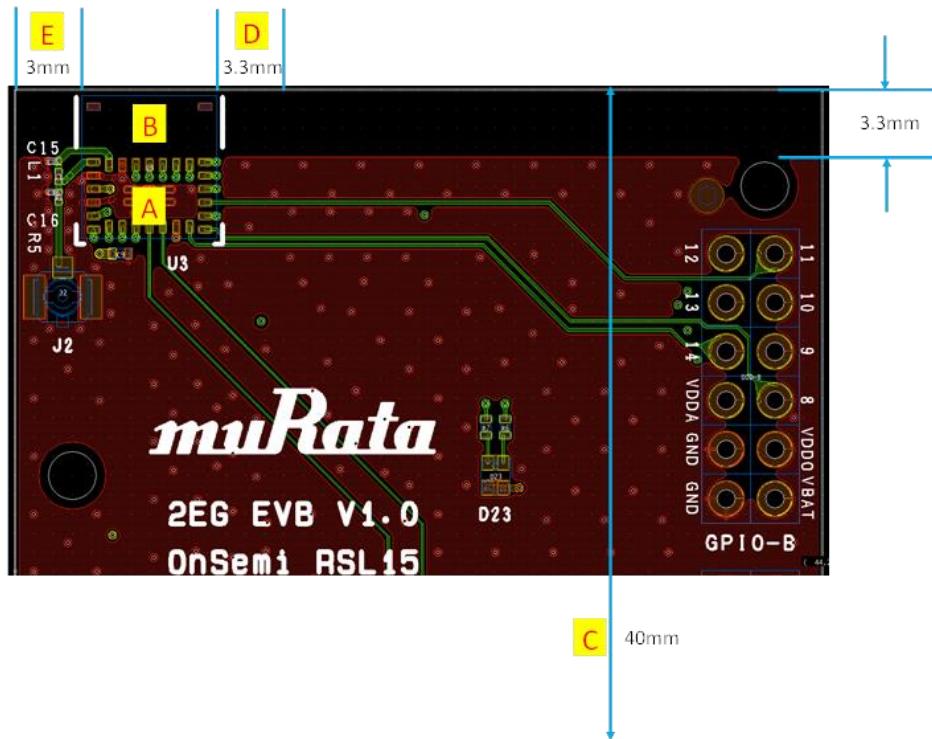
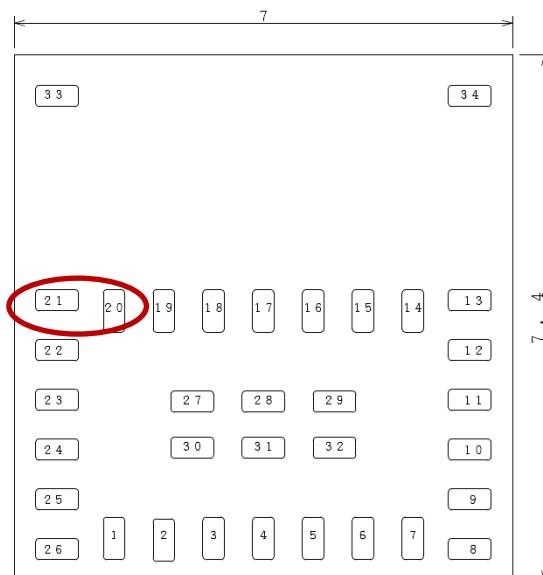
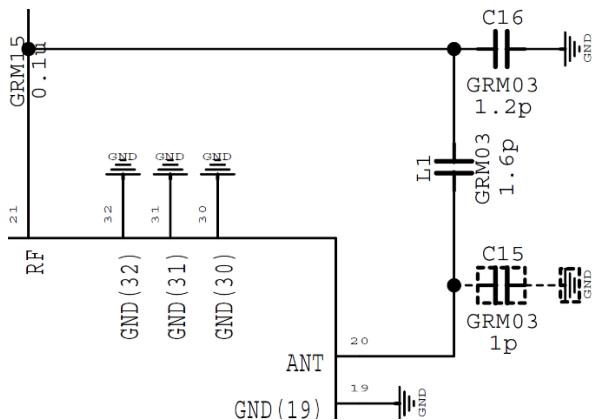


Figure 8: Layout Guide for Antenna Performance (2)





The Integrator must place the matching circuit shown below between Pins 20 and 21 of the Module to duplicate the structure used during FCC/ISED testing of the Type2EG module. Use the exact PNs provided for L1 and C15 and C16 to meet regulatory requirements.

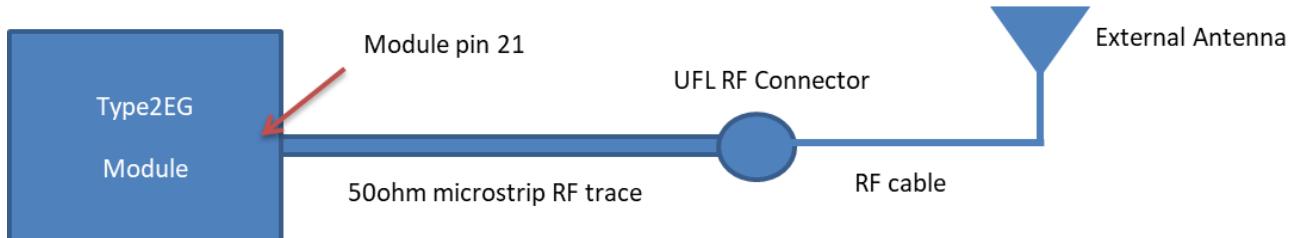
On the integrator's support board, place L1 (1.6pF) between Pin 20 and Pin 21 of the Module. Place C16 adjacent to Pin 21. The other side of C16 should be attached to RF Ground.

9.4 Layout Guidance for Microstrip Design and External Antenna

The TYPE 2EG module is certified with an internal PCB antenna and two external antennae, a 2.1 dBi omni antenna.

The omni antenna should be connected to the Type2EG module using 50ohm microstrip RF trace and a U.FL RF connector as shown below. The microstrip RF trace and U.FL connector are placed on the customer's PCB and are external to the Type2EG module. The omni antenna is then connected to this UFL Connector via a 50ohm RF adapter cable.

Figure 9: Layout Guide for External Antenna (1)

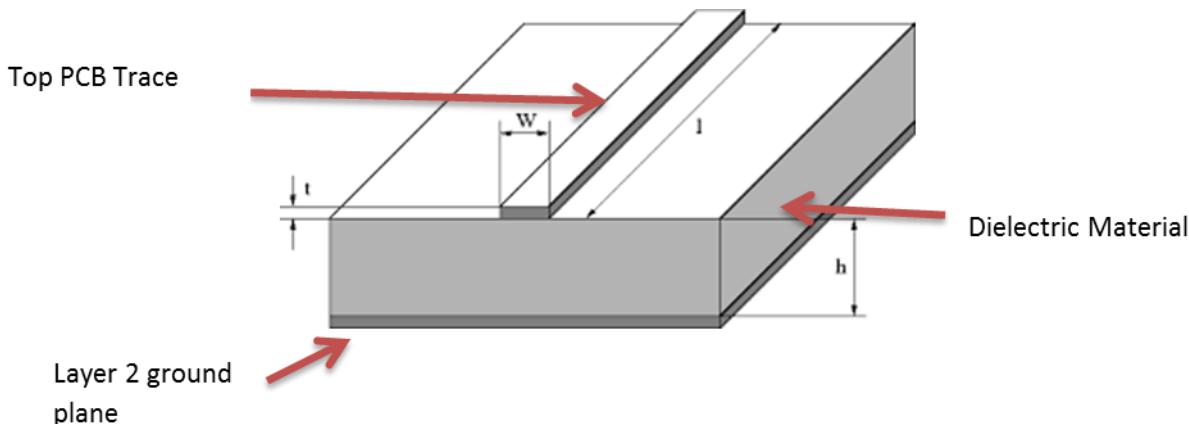


The design of the 50ohm microstrip RF trace on the customer's PCB is crucially important. Compliant operation of the Type2EG module is dependent on proper construction of this 50ohm line and the following guidelines must be followed to ensure legal operation of the product.

The diagram below shows the required microstrip structure to be routed between module pin 21 and the UFL connector.

The top PCB trace carries the RF energy from module to UFL connector. The Layer2 ground plane provides a return path for the circuit. The Dielectric material (along with the dimensions of the microstrip structures) determines the characteristic impedance of the microstrip transmission line.

Figure 10: Layout Guide for External Antenna (2)



Note the representative dimensions shown in the drawing above. It is imperative that the module customer (the integrator) use the exact dimensions we recommend to ensure a 50-ohm impedance for this transmission line.

The following dimensions and/or ratios should be used to set the microstrip impedance to 50 ohms.

Dielectric (PCB) Material – We recommend standard FR-4 PCB material. Other dielectrics will work but will require recalculation of microstrip dimensions. The following guidance is predicated on the use of FR-4 Dielectric. If FR-4 is not used for PCB material, please contact Murata Electronics at (678) 684-2009 to determine new dimensions for microstrip structure.

H (Dielectric Height) – this is the thickness of dielectric between the trace layer (layer 1) and the ground plane on layer 2. Note that layer 2 must be electrical ground. We recommend a dielectric thickness of 8-15 mils. This range provides the customer with some flexibility in board construction.

t (trace thickness) – Microstrip impedance is not severely affected by the thickness dimension. Standard 1oz or 2oz copper deposition is recommended. Equivalent thickness is 1-2 mils.

W (trace width) – this is the crucial dimension. This width must be set correctly to obtain the desired 50 ohms impedance. When using FR-4 dielectric, the width (W) of the microstrip trace should be set to:

$$W = H * 1.8$$

Where W is microstrip trace width and H is Dielectric height. Note that both values must be measured in identical units (mils or mm)

Example:

$$H = 12 \text{ mils}, \quad W = 12 * 1.8 = 21.6 \text{ mils}$$

$$H = 0.4 \text{ mm} \quad W = 0.4 * 1.8 = 0.72 \text{ mm}$$

l (trace length) – the impedance of the microstrip line is not dependent on its length. However, regulatory and performance limitations practically determine the actual length to be used by the customer (integrator). The length of this microstrip line must be longer than 7 mm to mimic the length used during FCC/ISED certification of the TYPE 2EG module. Lengths longer than 7 mm are acceptable although additional signal loss will occur as a result. Given these restrictions, Murata recommends microstrip trace lengths between 7 mm and 25 mm.

In any event, the microstrip line must operate over the same Dielectric-Ground Plane configuration shown above to act as a 50-ohm transmission line. Do not run the microstrip trace through sections of PCB that do not have the Dielectric-Ground plane configuration shown above.

A reliable 50-ohm transmission line will be produced if the above guidance is closely followed. Any deviations from the guidance above may cause the module to operate in noncompliant manner. Any implementation questions or concerns should be directed to Murata module technical support.

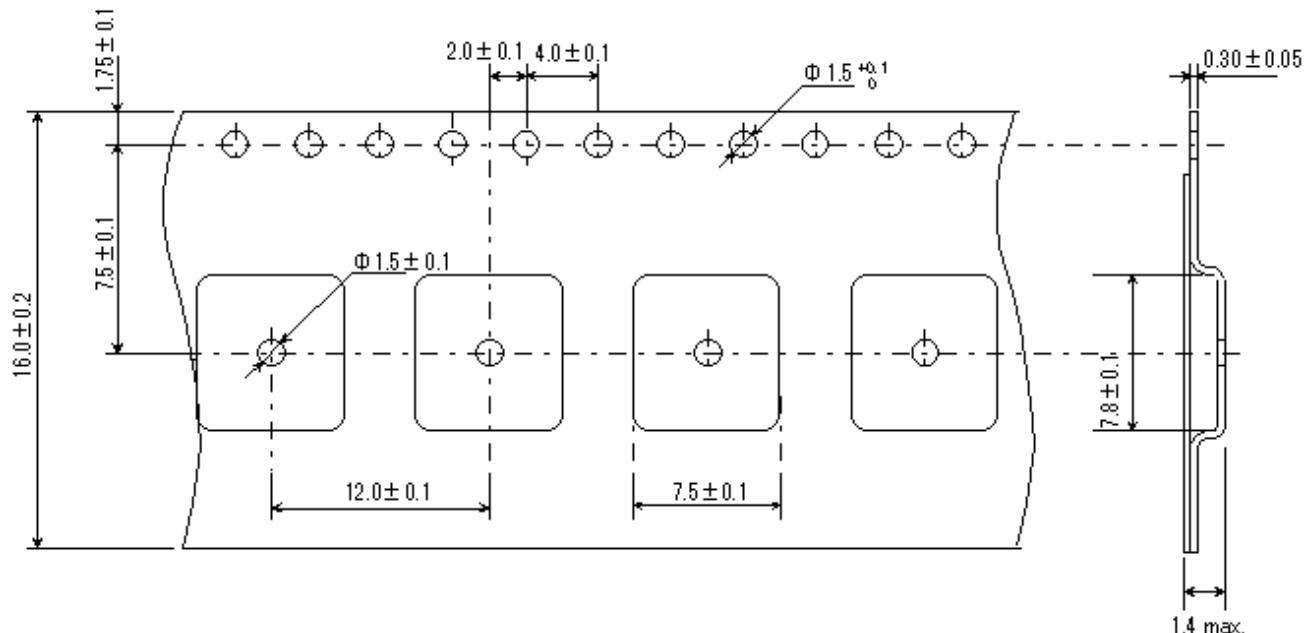
9.5 Reference Circuit

Please refer to Type 2EG EVK schematics [2].

10 Tape and Reel Packing

10.1 Dimensions of Tape (Plastic tape)

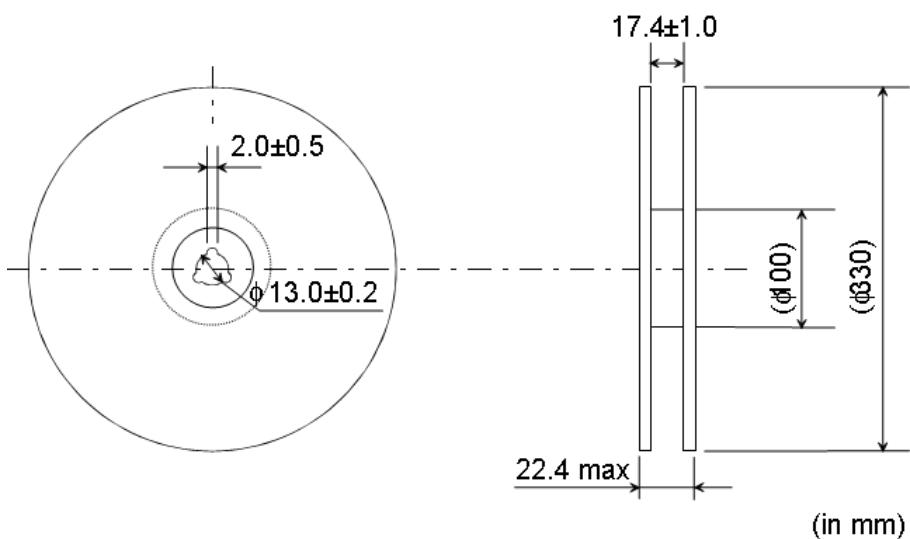
Figure 11: Dimensions of Tape (Plastic Tape)



Cumulative tolerance of max. 40.0 ± 0.15 every 10 pitches

10.2 Dimensions of Reel

Figure 12: Dimension of Reel



10.3 Taping Diagrams

Figure 13: Taping Diagrams

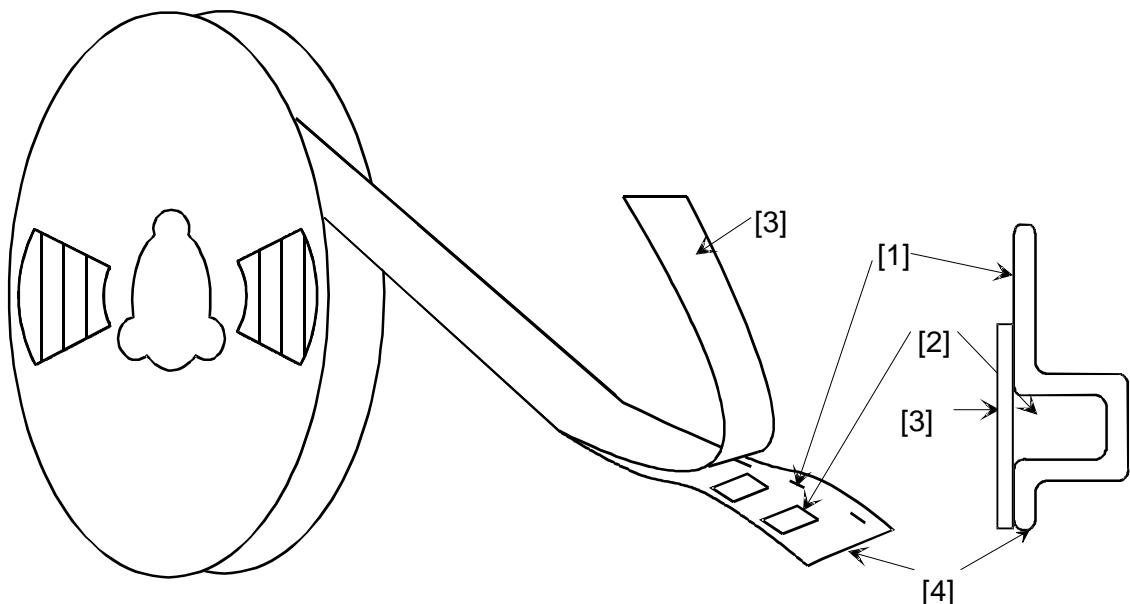
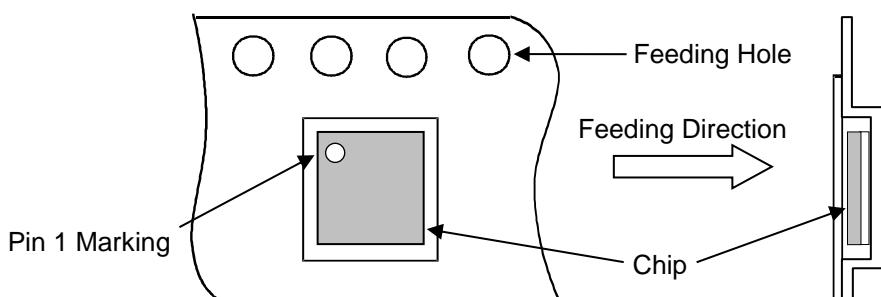


Figure 14: Taping Specifications

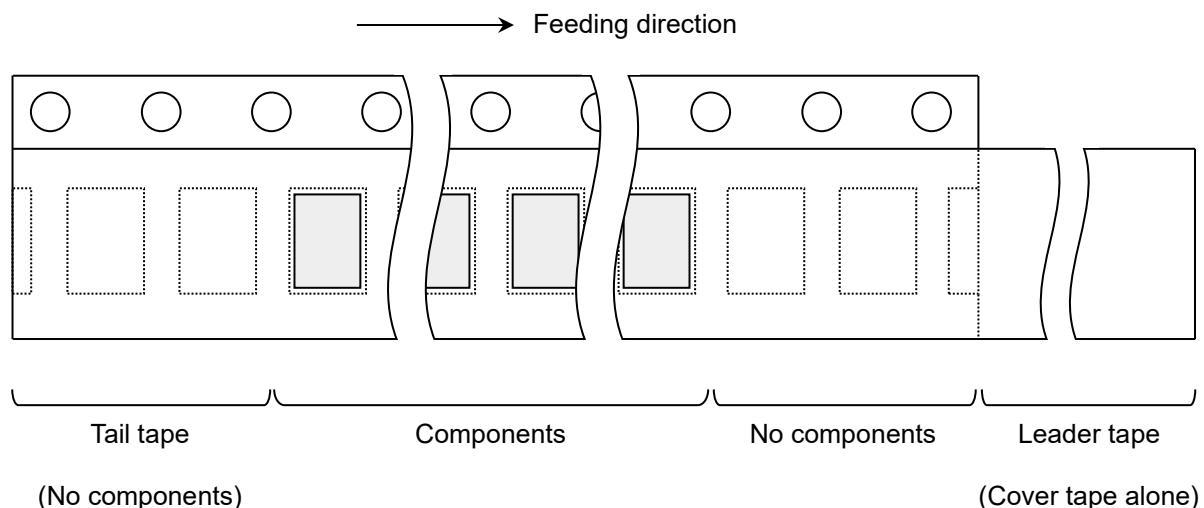
Mark	Description
1	Feeding hole. As specified in Dimensions of Tape (Plastic tape) [4] .
2	Hole for Chip. As specified in Dimensions of Tape (Plastic tape) [4] .
3	Cover tape. 62 μ M in thickness.
4	Base tape. As specified in Dimensions of Tape (Plastic tape) [4] .

Figure 15: Taping Diagrams (Close Up)



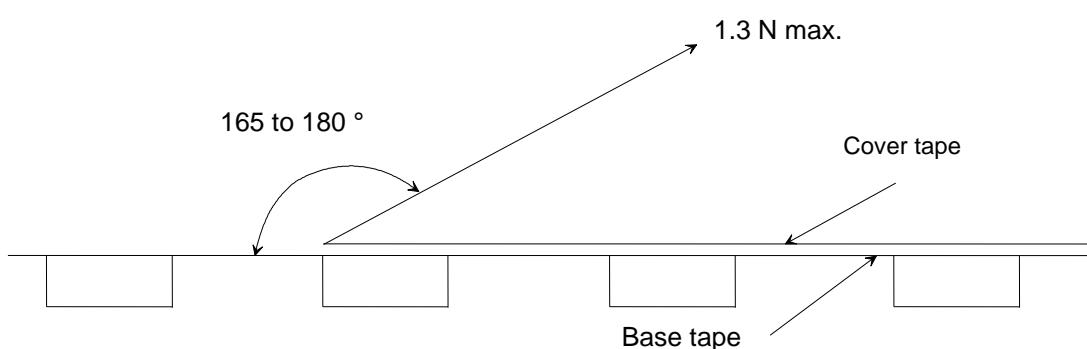
10.4 Leader and Tail Tape

Figure 16: Leader and Tail Tape



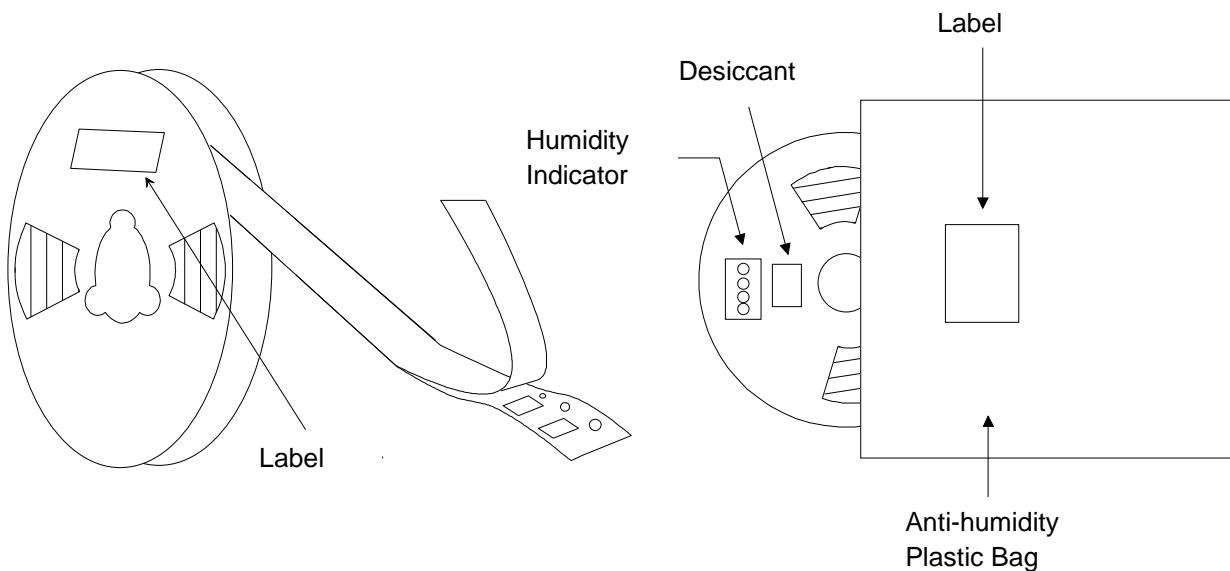
- The tape for chips is wound clockwise, the feeding holes to the right side as the tape is pulled toward the user.
- The cover tape and base tape are not adhered at no components area for 250mm min.
- Tear off strength against pulling of cover tape: 5N min.
- Packaging unit: 1000pcs. / reel
- Material
 - Base tape: Plastic
 - Real: Plastic
 - Cover tape, cavity tape and reel are made the anti-static processing.
- Peeling of force: 1.3N max. in the direction of peeling as shown below.

Figure 17: Peeling Off Force



10.5 Packaging (Humidity Proof Packing)

Figure 18: Packaging



Tape and reel must be sealed with the anti-humidity plastic bag. The bag contains the desiccant and the humidity indicator.

11 Notice

11.1 Storage Conditions

Please use this product within 6month after receipt.

- The product shall be stored without opening the packing under the ambient temperature from 5 to 35 °C and humidity from 20 ~ 70 %RH.
(Packing materials, in particular, may be deformed at the temperature over 40 °C)
- The product left more than 6months after reception, it needs to be confirmed the solderability before used.
- The product shall be stored in noncorrosive gas (Cl₂, NH₃, SO₂, NO_x, etc.).
- Any excess mechanical shock including, but not limited to, sticking the packing materials by sharp object, and dropping the product, shall not be applied in order not to damage the packing materials.

This product is applicable to MSL3 (Based on IPC/JEDEC J-STD-020)

- After the packing opened, the product shall be stored at <30 °C / <60 %RH and the product shall be used within 168 hours.
- When the color of the indicator in the packing changed, the product shall be baked before soldering.

Baking condition: 125 +5/-0 °C, 24 hours, 1 time

The products shall be baked on the heat-resistant tray because the material (Base Tape, Reel Tape and Cover Tape) is not heat-resistant.

11.2 Handling Conditions

Be careful in handling or transporting products because excessive stress or mechanical shock may break products.

Handle with care if products may have cracks or damages on their terminals, the characteristics of products may change. Do not touch products with bare hands that may result in poor solder ability and destroy by static electrical charge.

11.3 Standard PCB Design (Land Pattern and Dimensions)

All the ground terminals should be connected to the ground patterns. Furthermore, the ground pattern should be provided between IN and OUT terminals. Please refer to the specifications for the standard land dimensions.

The recommended land pattern and dimensions is as Murata's standard. The characteristics of products may vary depending on the pattern drawing method, grounding method, land dimensions, land forming method of the NC terminals and the PCB material and thickness. Therefore, be sure to verify the characteristics in the actual set. When using non-standard lands, contact Murata beforehand.

11.4 Notice for Chip Placer

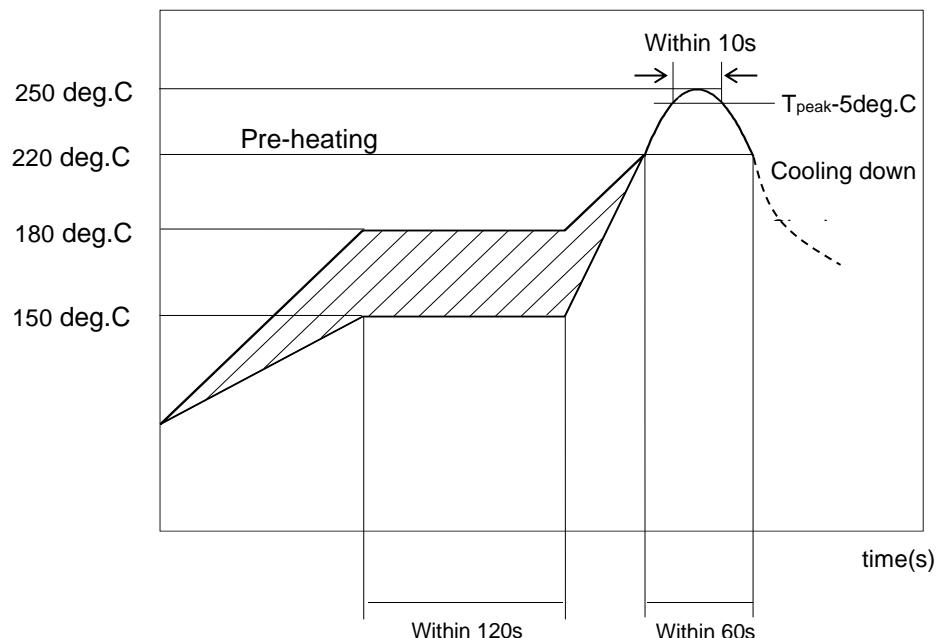
When placing products on the PCB, products may be stressed and broken by uneven forces from a worn-out chucking locating claw or a suction nozzle. To prevent products from damages, be sure to follow the specifications for the maintenance of the chip placer being used. For the positioning of products on the PCB, be aware that mechanical chucking may damage products.

11.5 Soldering Conditions

The recommendation conditions of soldering are as in the following figure.

Soldering must be carried out by the above-mentioned conditions to prevent products from damage. Set up the highest temperature of reflow within 260 °C. Contact Murata before use if concerning other soldering conditions.

Figure 19: Reflow soldering standard conditions (Example)



Please use the reflow within 2 times.

Use rosin type flux or weakly active flux with a chlorine content of 0.2 wt. % or less.

11.6 Cleaning

Since this Product is Moisture Sensitive, any cleaning is not recommended. If any cleaning process is done the customer is responsible for any issues or failures caused by the cleaning process.

11.7 Operational Environment Conditions

Products are designed to work for electronic products under normal environmental conditions (ambient temperature, humidity and pressure). Therefore, products have no problems to be used under the similar conditions to the above-mentioned. However, if products are used under the following circumstances, it may damage products and leakage of electricity and abnormal temperature may occur.

- In an atmosphere containing corrosive gas (Cl₂, NH₃, SO_x, NO_x, etc.).
- In an atmosphere containing combustible and volatile gases.
- Dusty place.
- Direct sunlight place.
- Water splashing place.
- Humid place where water condenses.
- Freezing place.

If there are possibilities for products to be used under the preceding clause, consult with Murata before actual use.

As it might be a cause of degradation or destruction to apply static electricity to products, do not apply static electricity or excessive voltage while assembling and measuring.

12 Preconditions to Use Our Products



PLEASE READ THIS NOTICE BEFORE USING OUR PRODUCTS.

Please make sure that your product has been evaluated and confirmed from the aspect of the fitness for the specifications of our product when our product is mounted to your product.

All the items and parameters in this product specification/datasheet/catalog have been prescribed on the premise that our product is used for the purpose, under the condition and in the environment specified in this specification. You are requested not to use our product deviating from the condition and the environment specified in this specification.

Please note that the only warranty that we provide regarding the products is its conformance to the specifications provided herein. Accordingly, we shall not be responsible for any defects in products or equipment incorporating such products, which are caused under the conditions other than those specified in this specification.

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- Burning / explosion control equipment
- Aerospace equipment
- Undersea equipment.
- Medical equipment.

- Traffic signal equipment.
- Disaster prevention / crime prevention equipment.
- Transportation equipment (vehicles, trains, ships, elevator, etc.).
- Application of similar complexity and/ or reliability requirements to the applications listed in the above.

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Moreover, you must comply with "foreign exchange and foreign trade law", the "U.S. export administration regulations", etc.

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Appendix

Revision History



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