

The diagram illustrates the system architecture for the STSL 82933. At the center is the **Siemens STSL 82933** block. It is connected to several peripheral components:

- KEY MATRIX**: Connected to the top of the STSL 82933 block via an **8 LINES** bus.
- LED**: Connected to the bottom of the STSL 82933 block via a **3 LINES** bus.
- XTAL**: A crystal oscillator connected to the side of the STSL 82933 block.
- INTERFACE** (top): A block connected to the right side of the STSL 82933 block via a **2 LINES** bus. This interface block has four outputs: **CLK**, **/DATA**, **POWER**, and **GND**.
- INTERFACE** (bottom): A block connected to the bottom of the STSL 82933 block via a **2 LINES** bus. This interface block has two outputs: **D-** and **D+**.

Size	Document Number	REV
A	BLOCK DIAGRAM	
Date:	May 15, 1998	Sheet of