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DISCLOSURE OF CONFIDENTIAL INFORMATION GENERALLY
(18 USC 1905)
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ZONE		REV	DESCRIPTION	DATE	APPROVED
		—	INITIAL RELEASE	17DEC98	R.BACULA
	A		SH ID# REV. PL AFFECTED. USE ALL PARTS IN HOUSE, IN STOCK AND CURRENTLY ON ORDER (EFFECTIVITY IS NEXT BUY)	97473(U) 18 Mag RICHARDS	

NOTES:

1. ASSEMBLE AND SOLDER PER ITEM 701.
2.

CAUTION
ELECTROSTATIC SENSITIVE COMPONENT
HANDLE WITH CARE

THIS ASSEMBLY INCLUDES COMPONENTS WHICH ARE SUBJECT TO DAMAGE BY ELECTROSTATIC CHARGES; THEREFORE, ALL COMPONENTS SHALL BE HANDLED IN ACCORDANCE WITH GUIDELINES FOR ELECTROSTATIC DISCHARGE CONTROL.
3. MAXIMUM COMPONENT HEIGHT SIDE B SHALL BE .500 MAX.
4. COMPONENT LEAD PROTRUSION SHALL BE .090 MAXIMUM; BARE COPPER PERMISSIBLE ON COMPONENT LEADS AT TRIMMED SURFACES.
5. SQUARE PADS ON PRINTED WIRING BOARD INDICATE PIN 1.
6. SOME COMPONENTS ARE SHOWN OVERSIZED TO PROVIDE SPACE FOR REFERENCE DESIGNATORS.
7.

COAT CCA USING ITEM 702 PER M7010975, EXCEPT J2 AND J3 SOCKETS. ITEM 3, PINS OF J1 AND J4 (.06 MAX FROM PLASTIC SURFACE WHICH PIN IS INSERTED, ON SIDE B) AND AREAS NOTED.
8.

SHADED COMPONENTS NOT INSTALLED.
9.

ADHERE CABLE TO SIDE B OF PWB USING ITEM 703 (RTV) AS SHOWN. CABLE MUST BE CLEAR OF MOUNTING HOLE AS SHOWN.
10.

MAKE CUTS PER TABLE I, LEFT AND RIGHT AS VIEWED FROM SIDE B.
11.

INSTALL BUS WIRES PER TABLE II, LEFT AND RIGHT AS VIEWED FROM SIDE B.

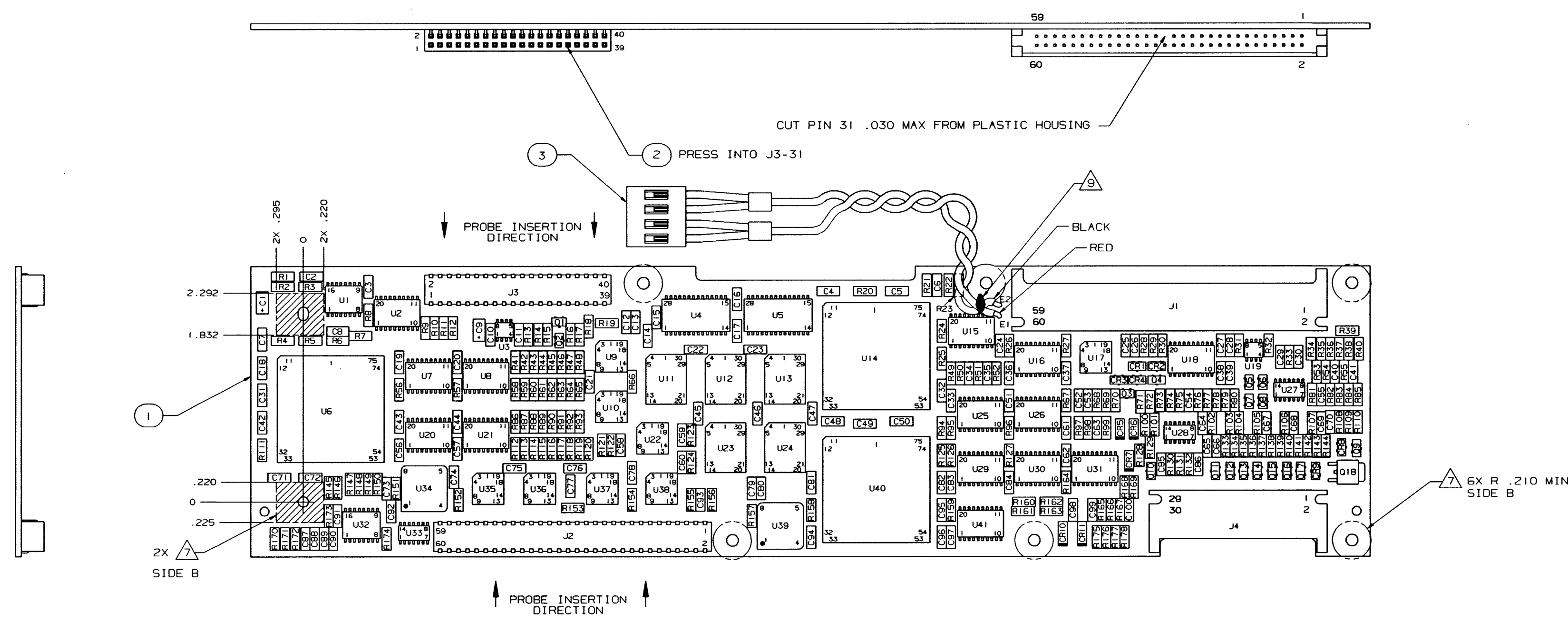
TABLE I 10 CUT RUN DATA				
CONDUCTOR			FROM CIRCUIT POINT	TO CIRCUIT POINT
CUT NO.	PWB SIDE			
1	B		U38 PIN 8 VIA	U38 PIN 8 PAD

TABLE II 11 BUS WIRE DATA				
CONDUCTOR			FROM CIRCUIT POINT	TO CIRCUIT POINT
COND. NO.	ITEM NO.	PWB SIDE		
601	704	A	J3-21	J1-27
602	704	B	U30-19 PAD	U38-8 PAD

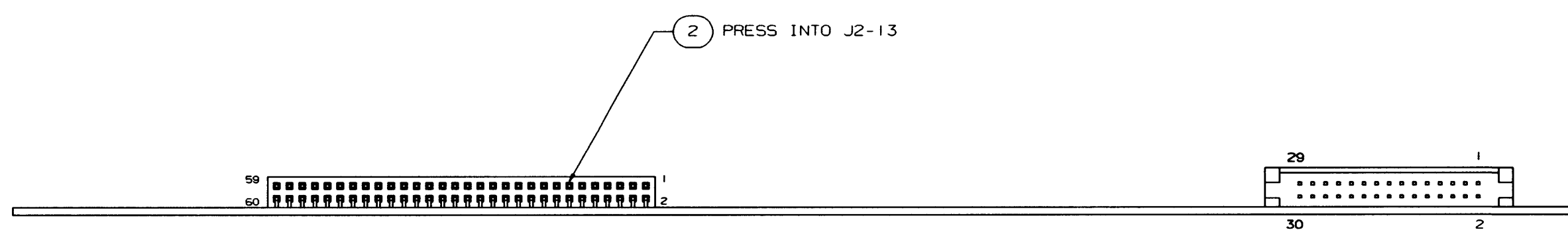
REV STATUS OF PARTS LIST SHALL BE SAME AS
BASIC DRAWING NUMBER
SEE SEPARATE PARTS LIST

454443424140393837363534333231302928272625242322212019181716151413121110987654321										REV	SHEET	CONTRACT		OPERATIONAL NOTE	
FOR HONEYWELL REFERENCE										DATE	INDEX	DRAWN BY		SIMILAR ASSEMBLY	
MFG REF NO.										DATE		D. MACH			
DPA NO. A-20380										DATE		DESIGNED BY			
PRODUCT LINE NO. 6720										DATE		CHECKED BY		Honeywell	
AW/PS ANALYSIS EB 7510800										DATE		E. THIESSEN		COMMERCIAL FLIGHT	
REF DWG										DATE		APVD FOR MFG		SYSTEMS GROUP	
REF SPEC										DATE		S. YATEEM		PHOENIX, ARIZONA	
										DATE		APVD FOR DE		TITLE	
										DATE		APVD FOR ENGRS		CIRCUIT CARD ASSEMBLY-	
										DATE		M. HOWK		DADC / IAC PROC (A2)	
										DATE		APVD FOR ENGRS		SIZE/CAGE CODE	
										DATE		APVD FOR		E55939	
										DATE				DRAWING NO.	
										DATE				7517450-902	
										DATE				REV	
										DATE				A	
										DATE				SHEET 1 OF 17	
										DATE				COMPUTER GENERATED	

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
			APPROVED



VIEWING SIDE B



VIEWING SIDE A

SEE FIRST SHEET FOR CONTRACT NUMBER, USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.

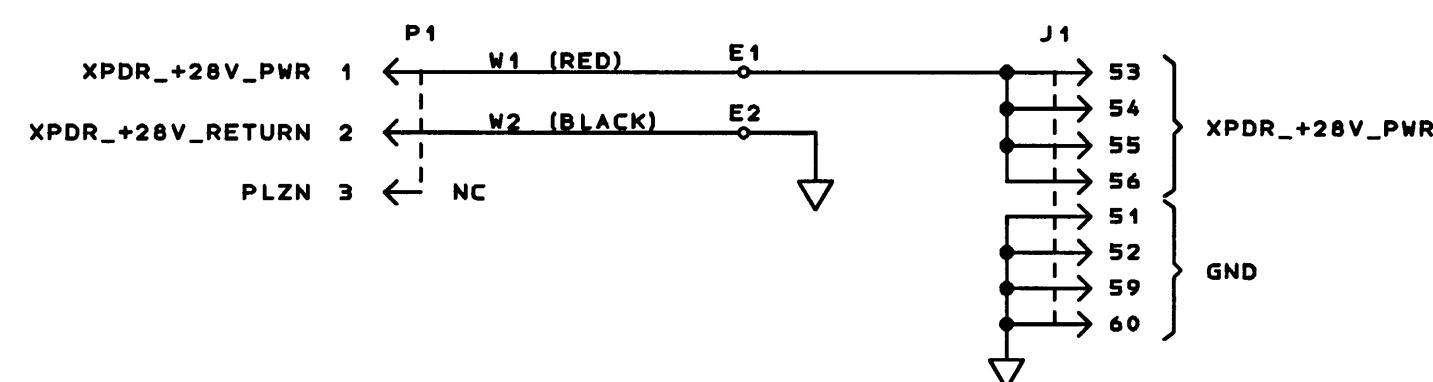
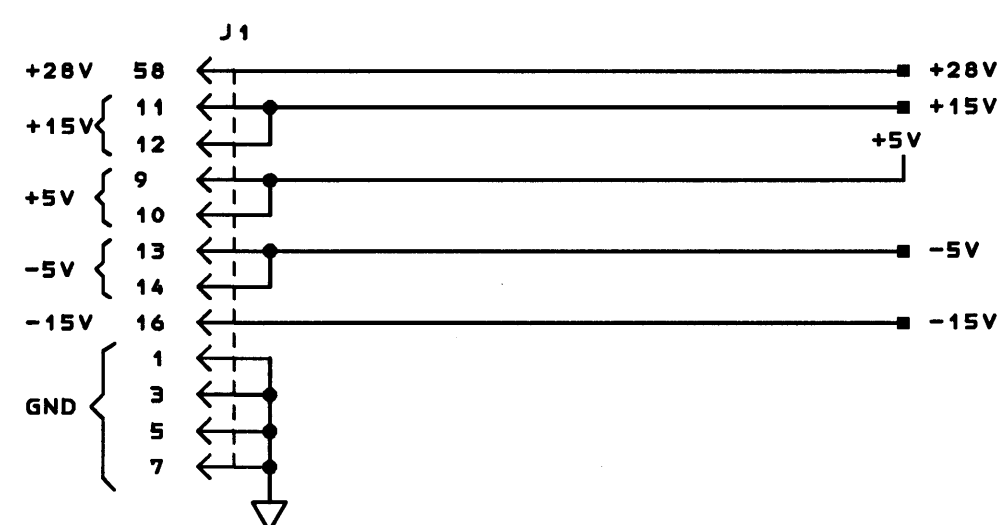
SIZE/CAGE CODE
E 55939

DRAWING NO.
7517450-902

REV
-

SCALE 2/1

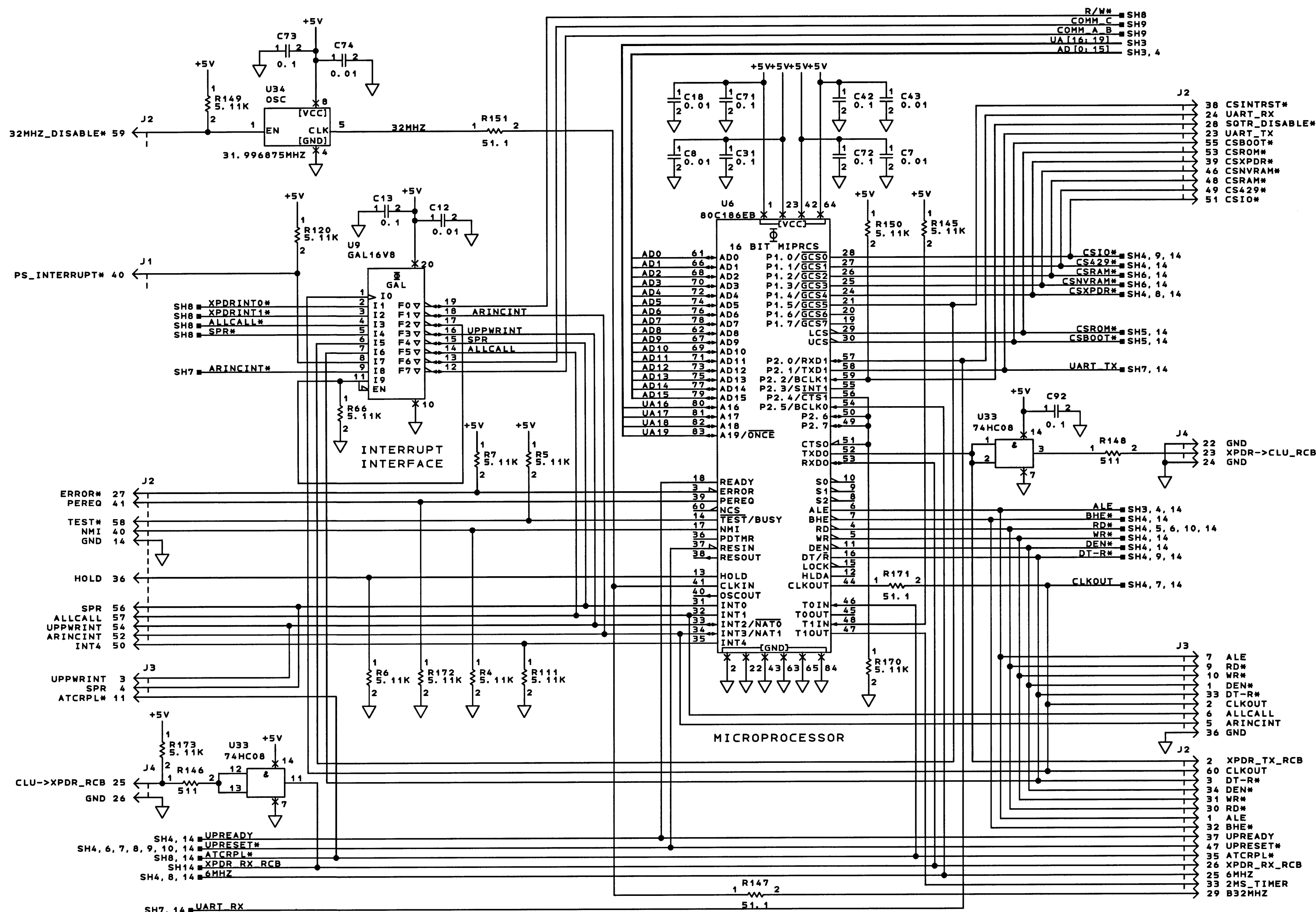
SHEET 2



- NOTES:
1. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 2. ALL RESISTANCE VALUES ARE IN OHMS.
 3. ALL INDUCTANCE VALUES ARE IN MICROHENRIES.
 4. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS PREFIX PART DESIGNATION WITH SUBASSEMBLY DESIGNATION.
 5. INTERPRET DRAWING PER ANSI Y32.2-1975 AND ANSI/IEEE STD 991-1986. LOGIC SYMBOLS CONFORM TO ANSI/IEEE STD 91-1984.

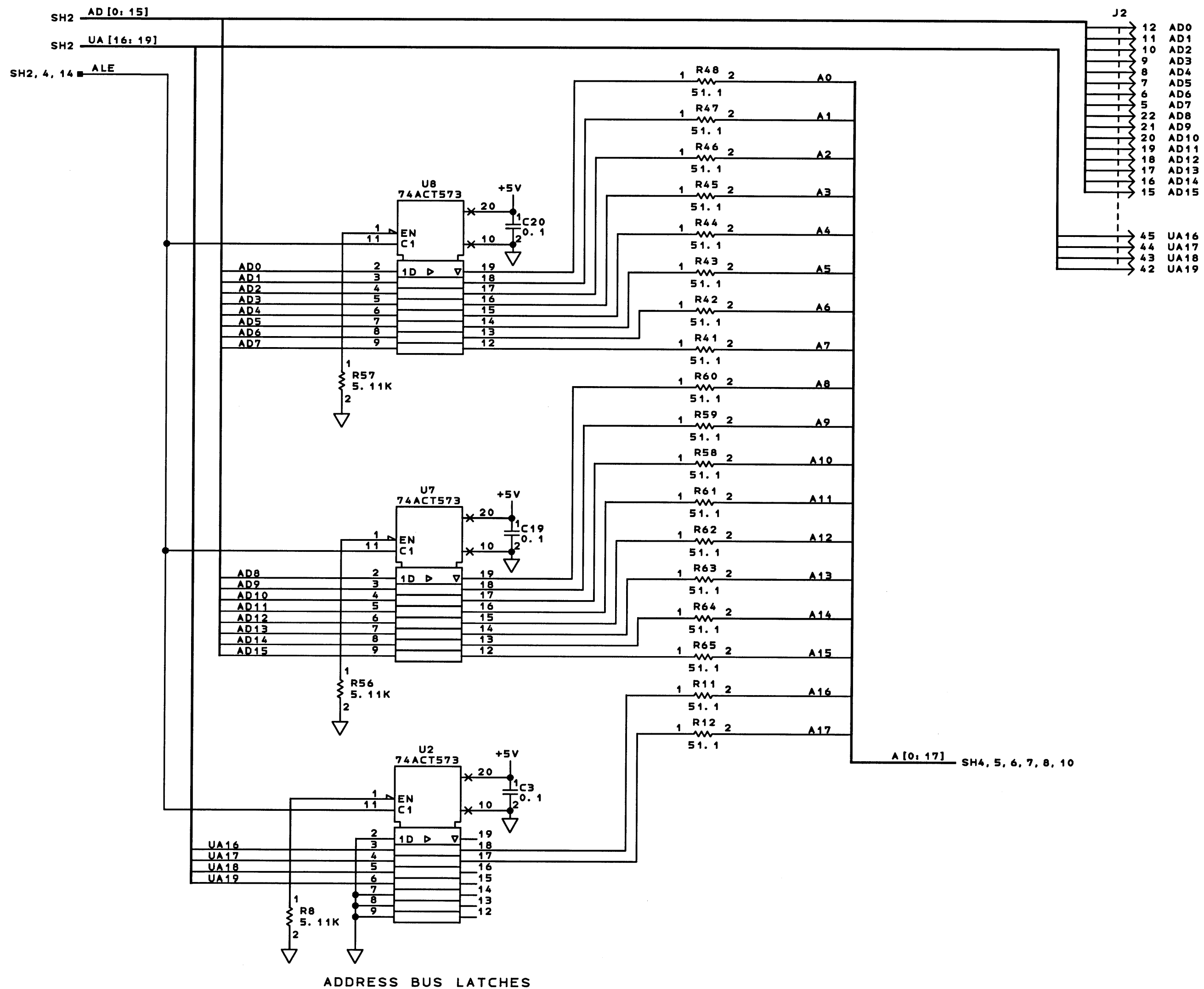
SCHEMATIC DIAGRAM SHEET NO. 1 OF 14

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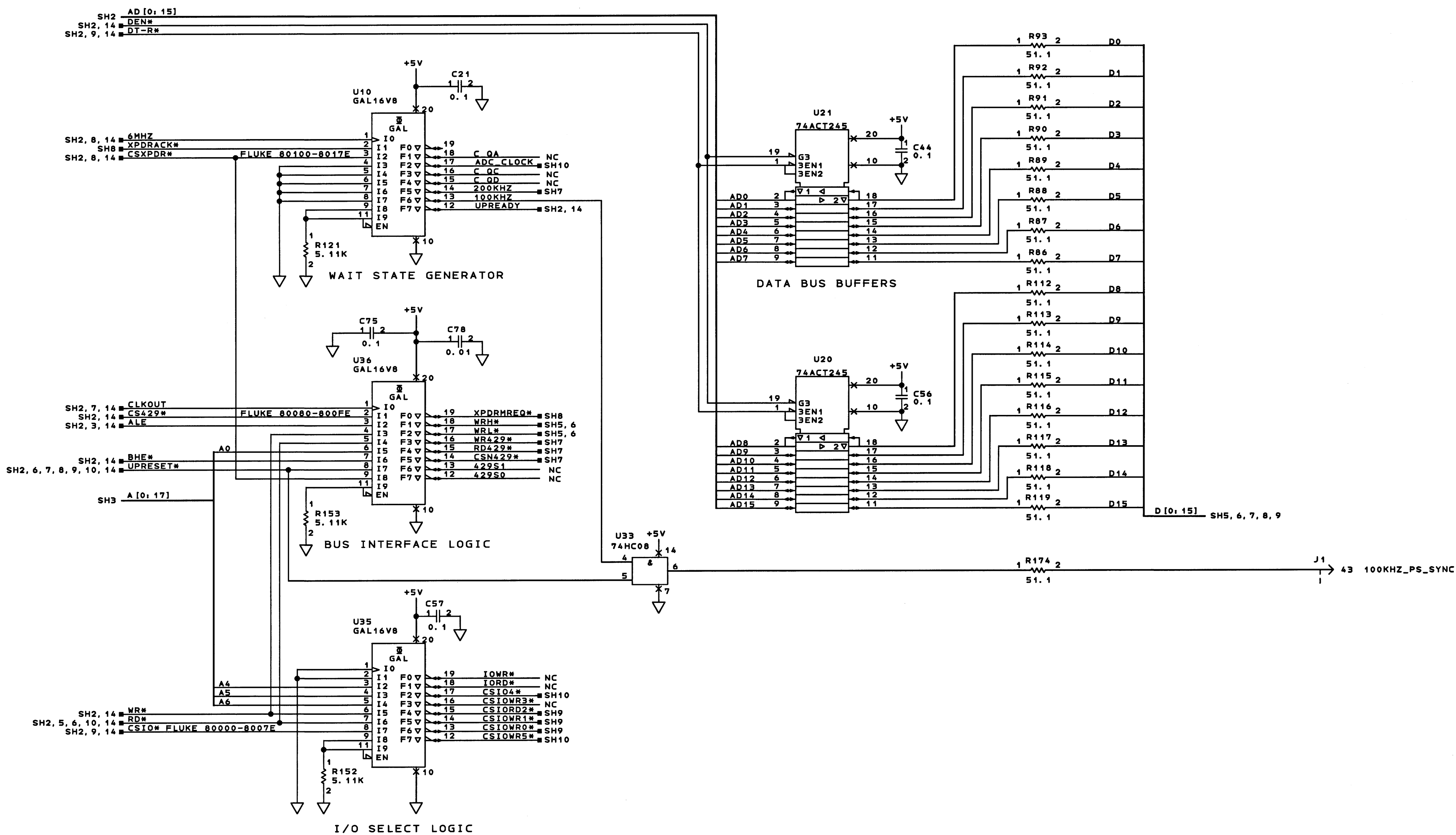
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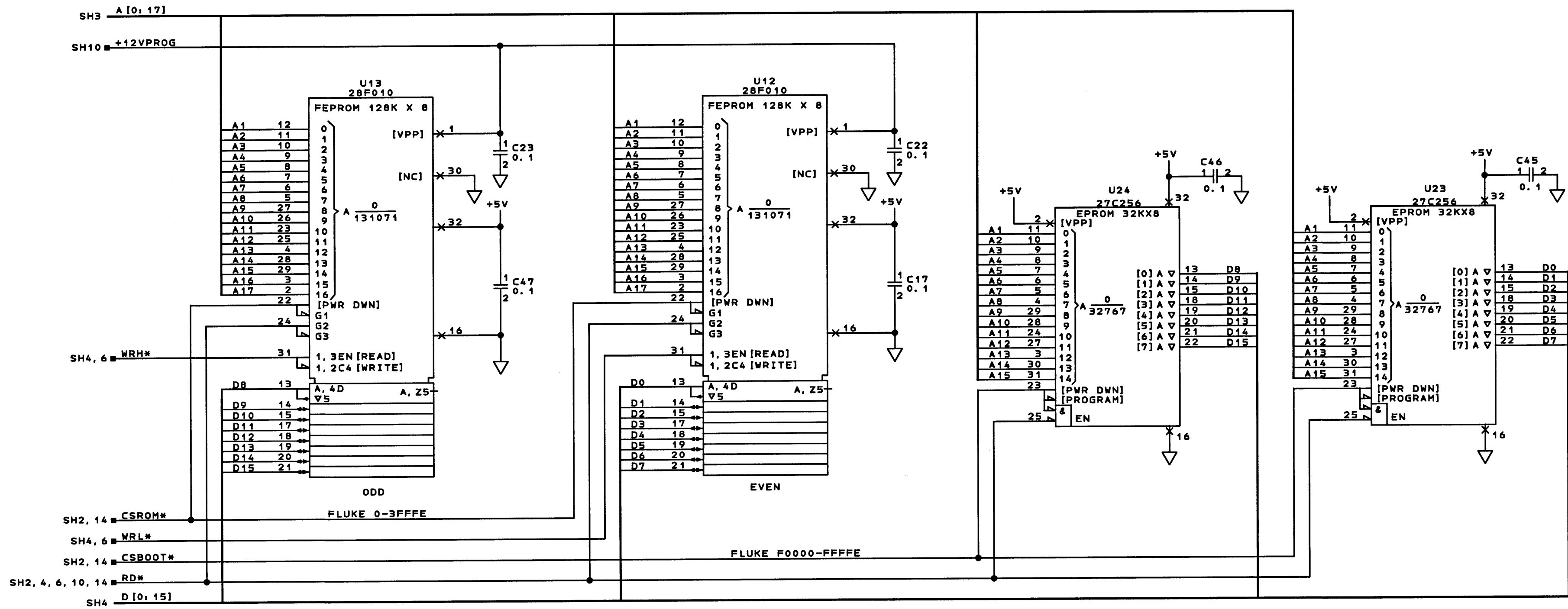
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SCHEMATIC DIAGRAM SHEET NO. 4

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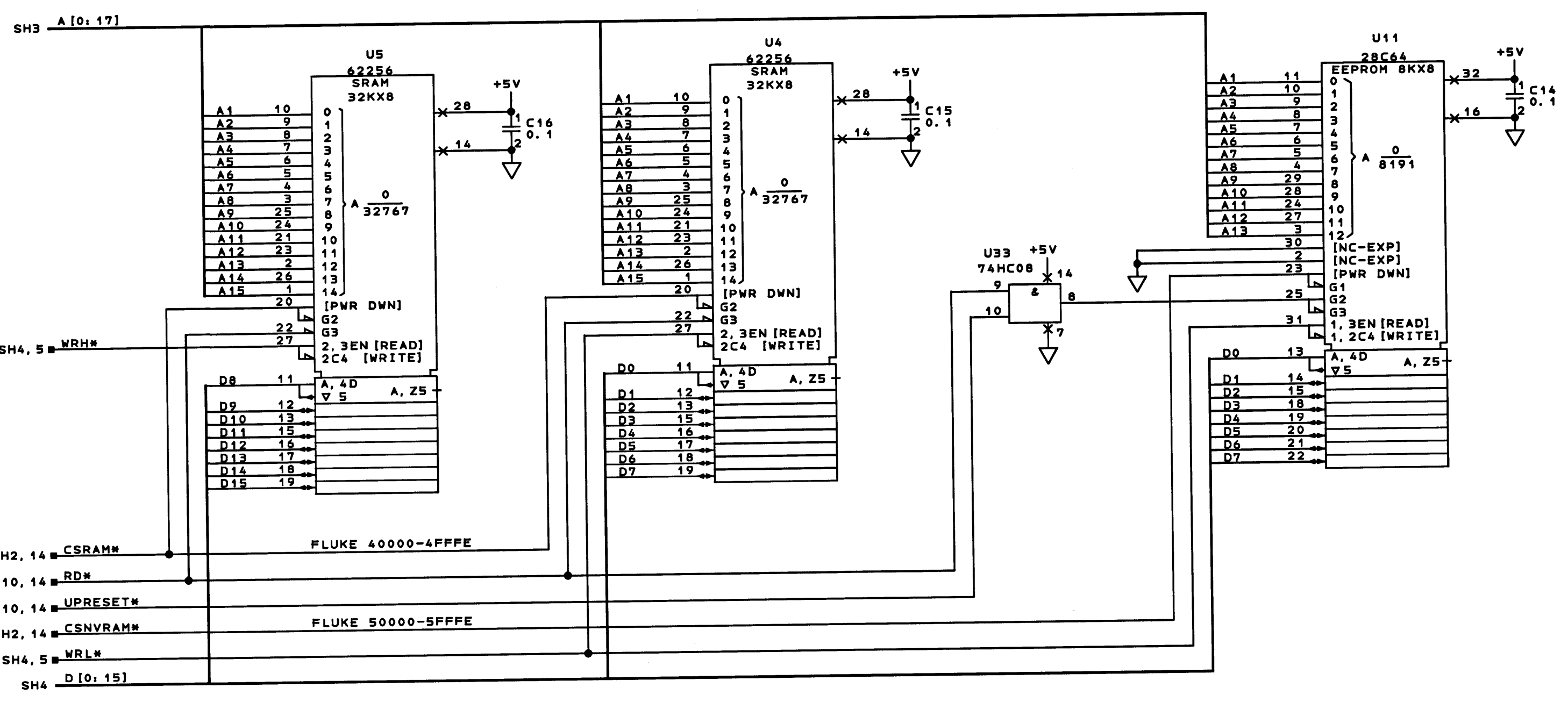


REPROGRAMMABLE CODE ROM

SYSTEM BOOT ROM

SCHEMATIC DIAGRAM SHEET NO. 5

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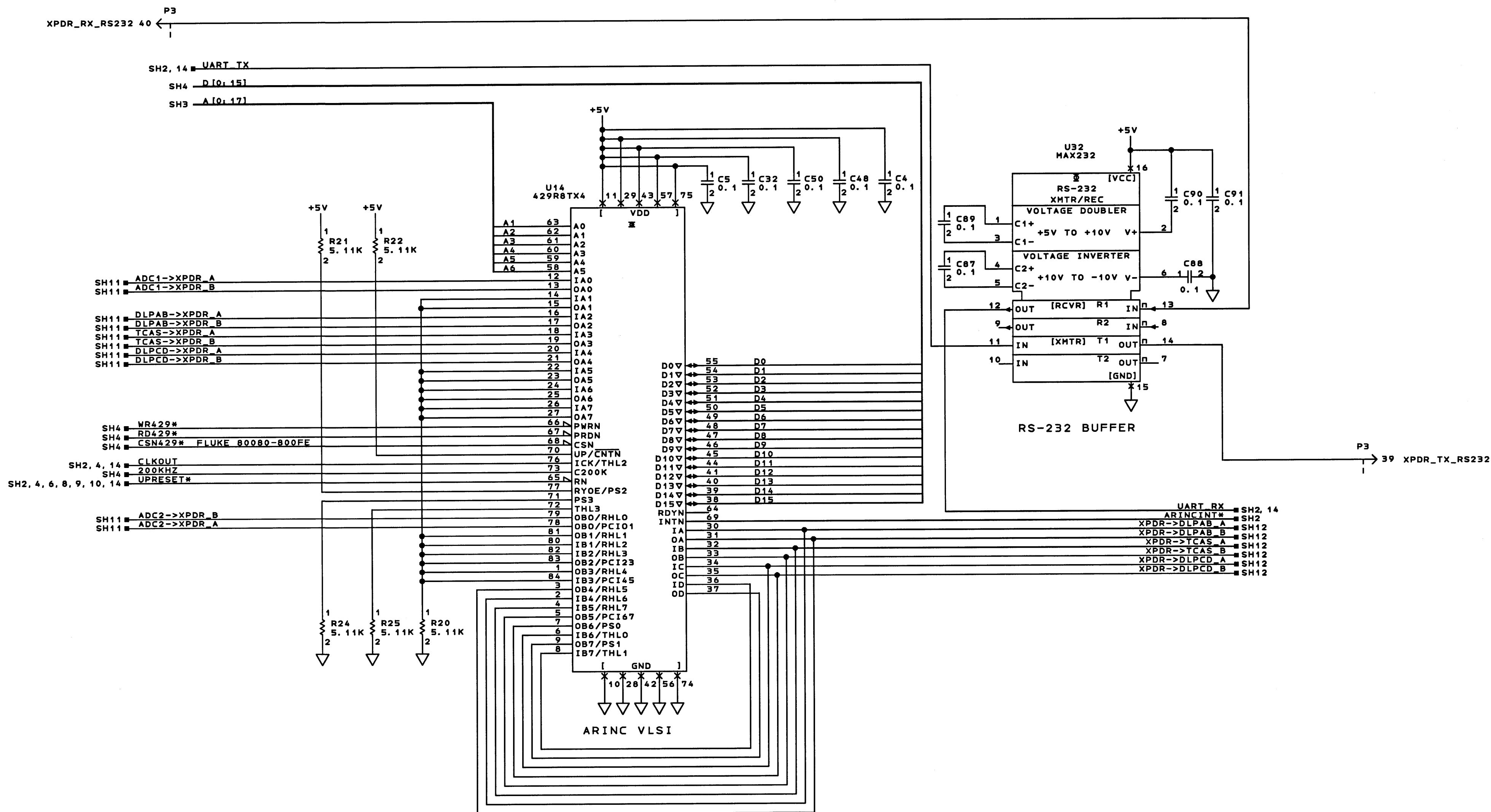


LOCAL RAM

NON-VOLATILE MEMORY

SCHEMATIC DIAGRAM SHEET NO. 6

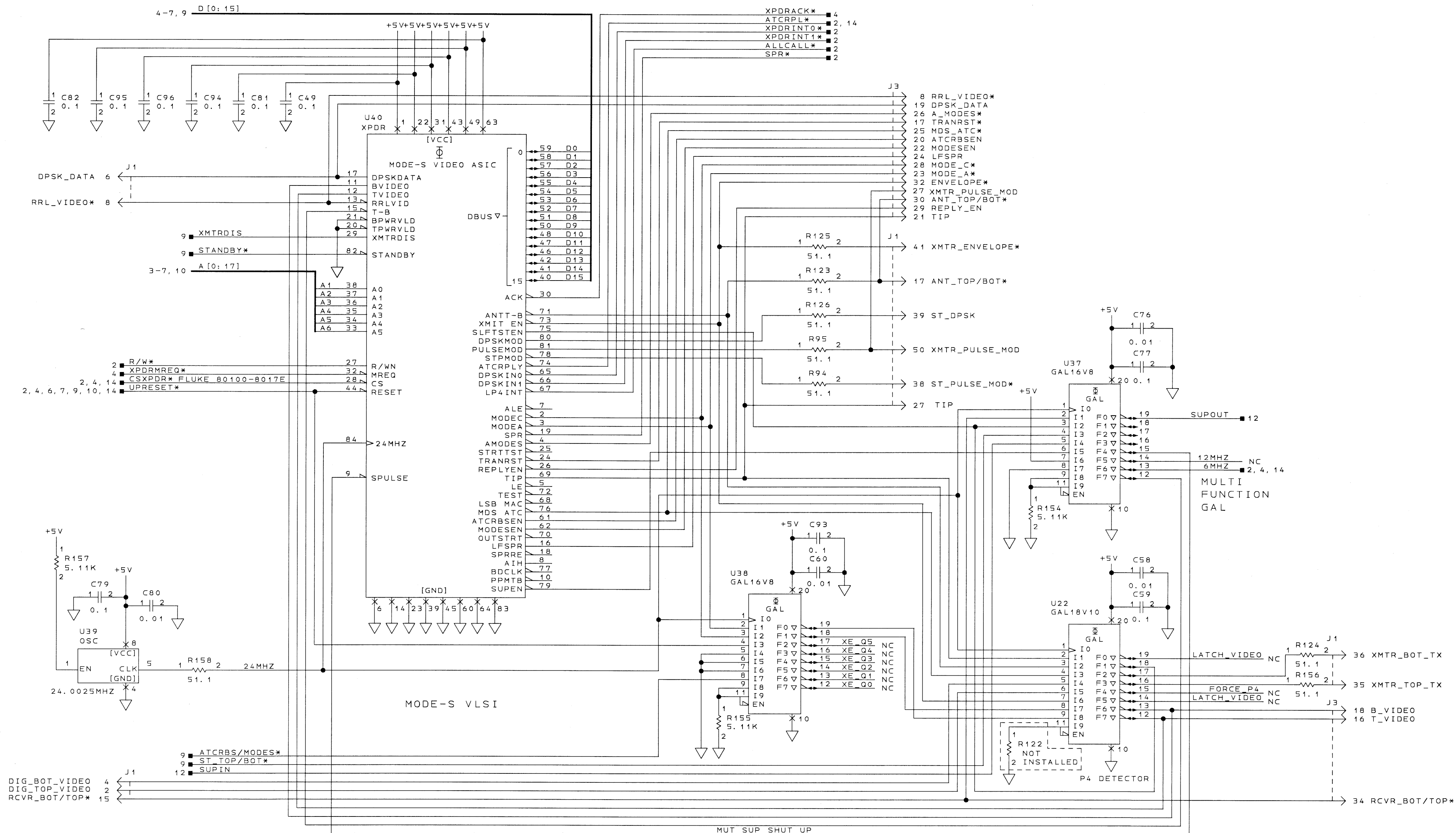
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SCHEMATIC DIAGRAM SHEET NO. 7

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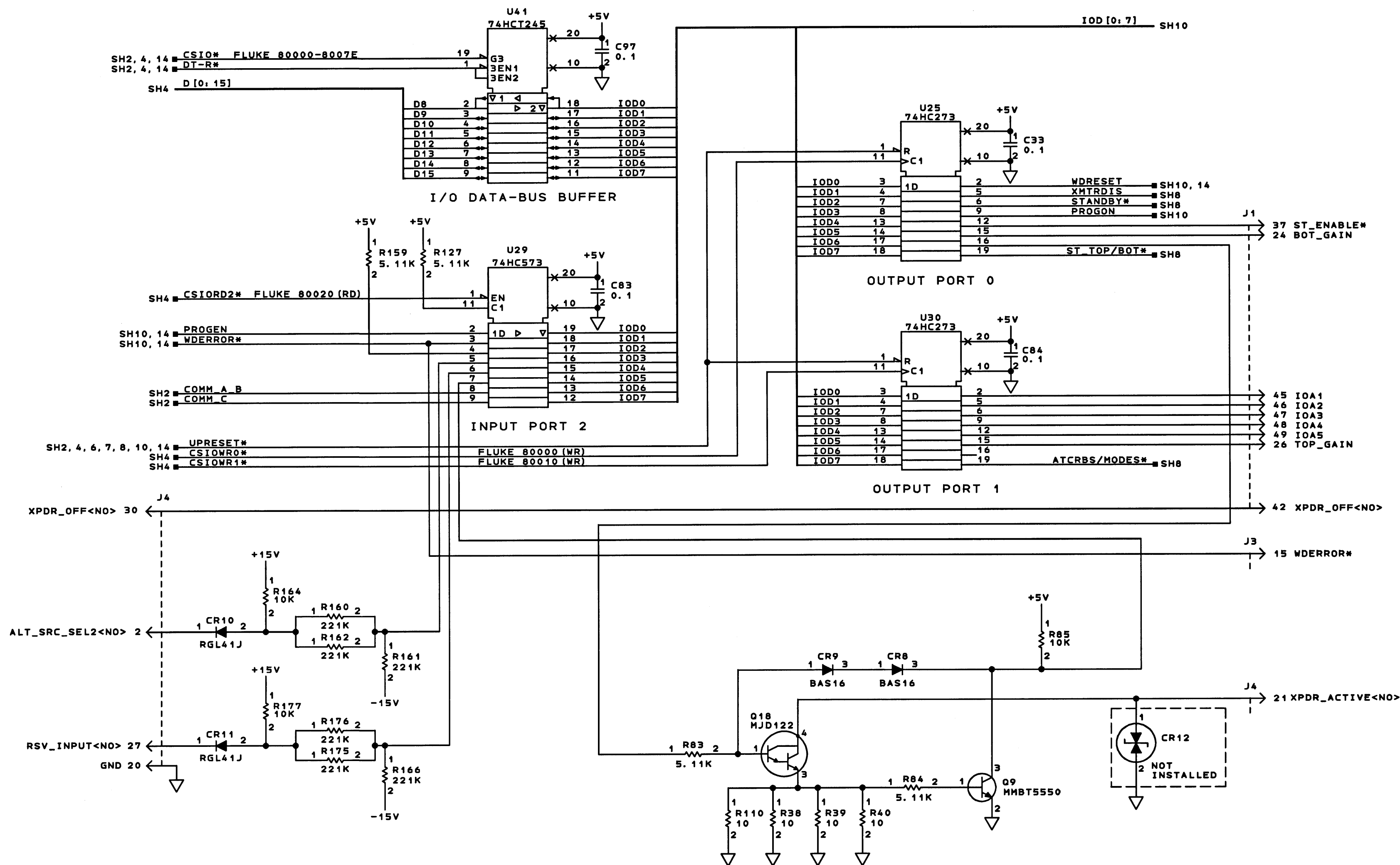
REV		REVISIONS		DATE	APPROVED
ZONE	REV	DESCRIPTION			
A	1	PARTS LIST DATA BASE CHANGE ONLY.	97473 (U)	Mar 99	R. RICHARDS
		SEE DWG SHEET 1.			



SCHEMATIC DIAGRAM SHEET NO. 8

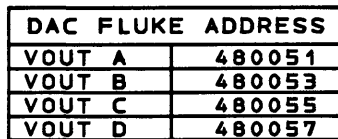
COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
		E	55939	7517450-902	A
		SCALE		SHEET 10	

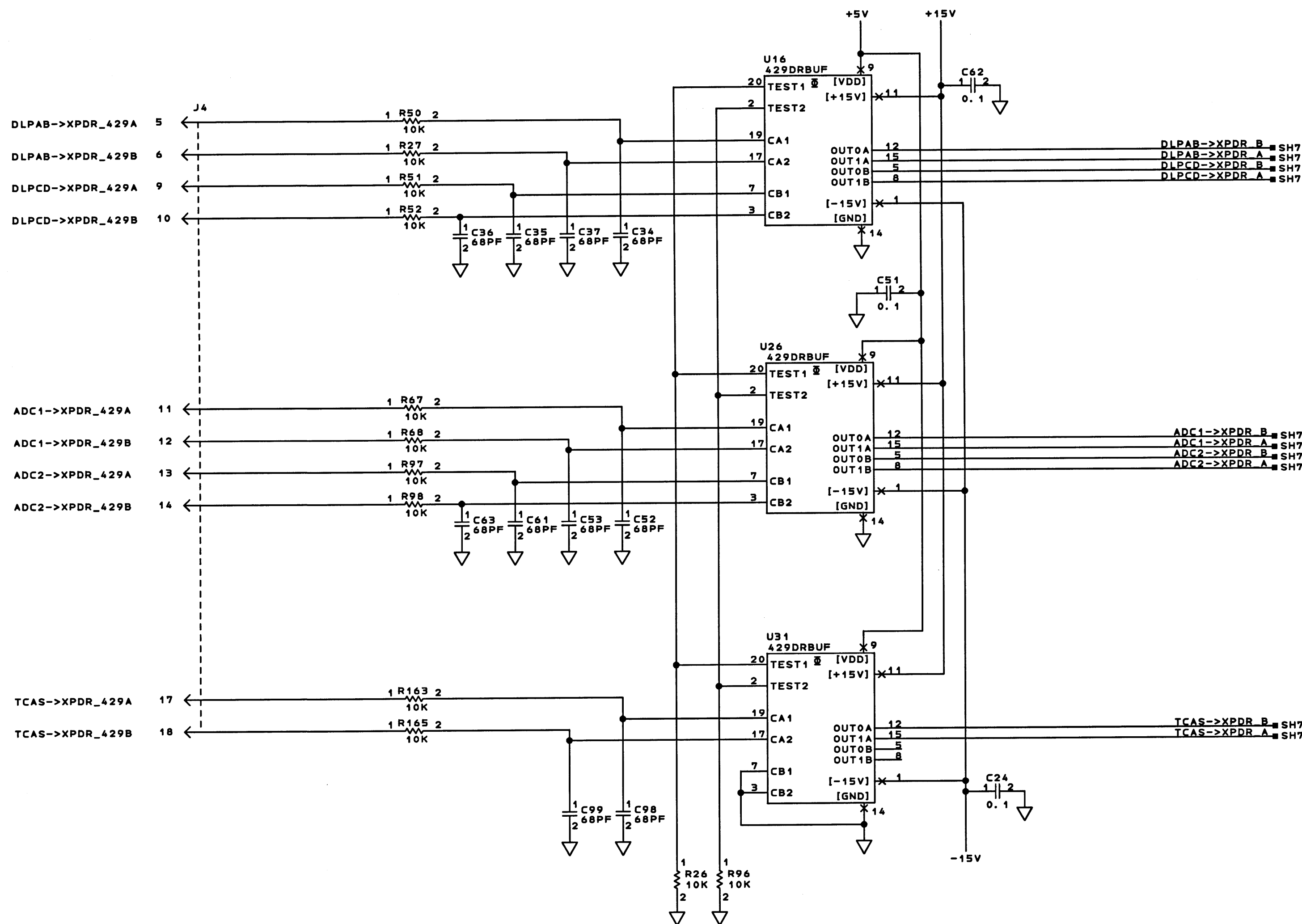


SCHEMATIC DIAGRAM SHEET NO. 9

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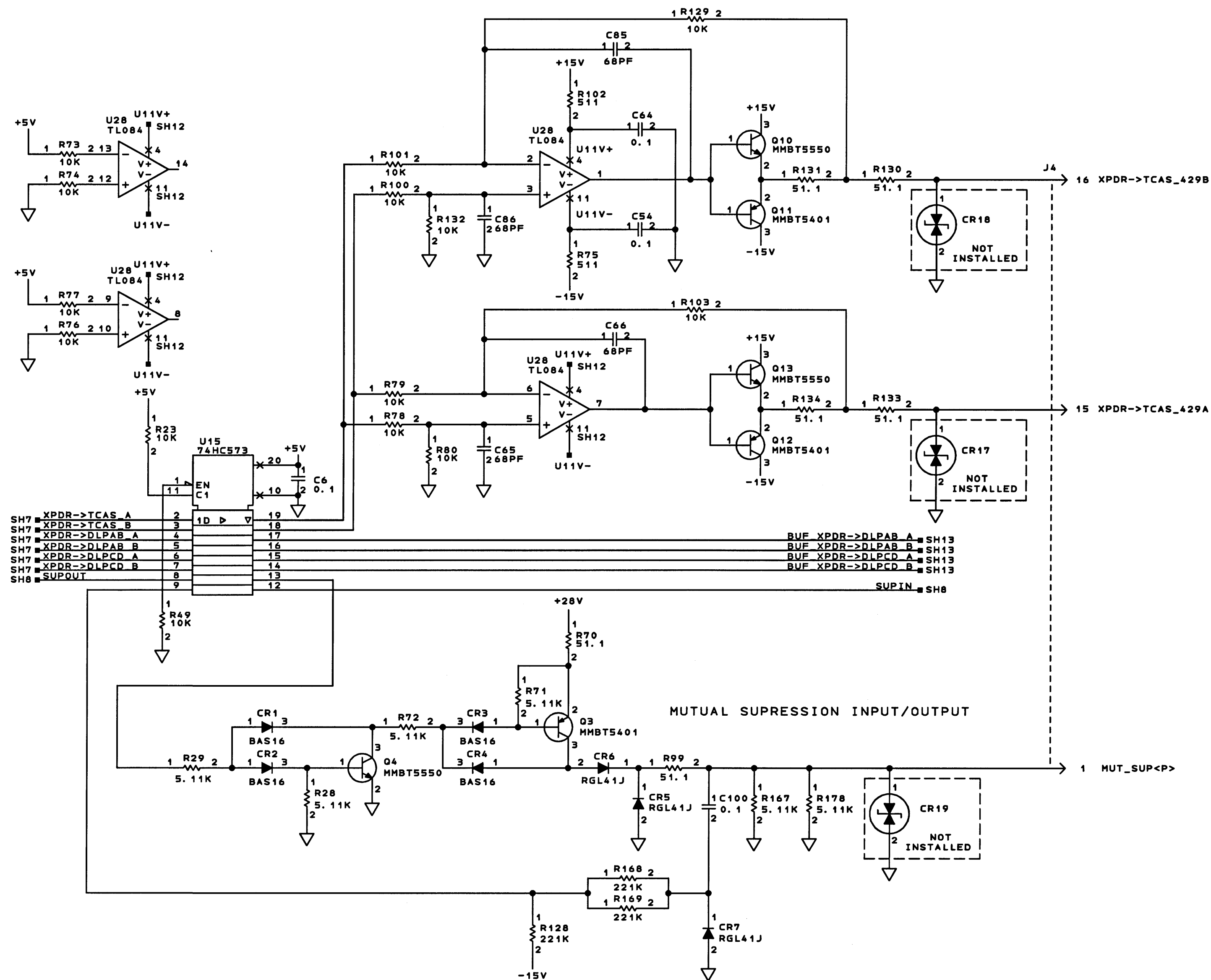


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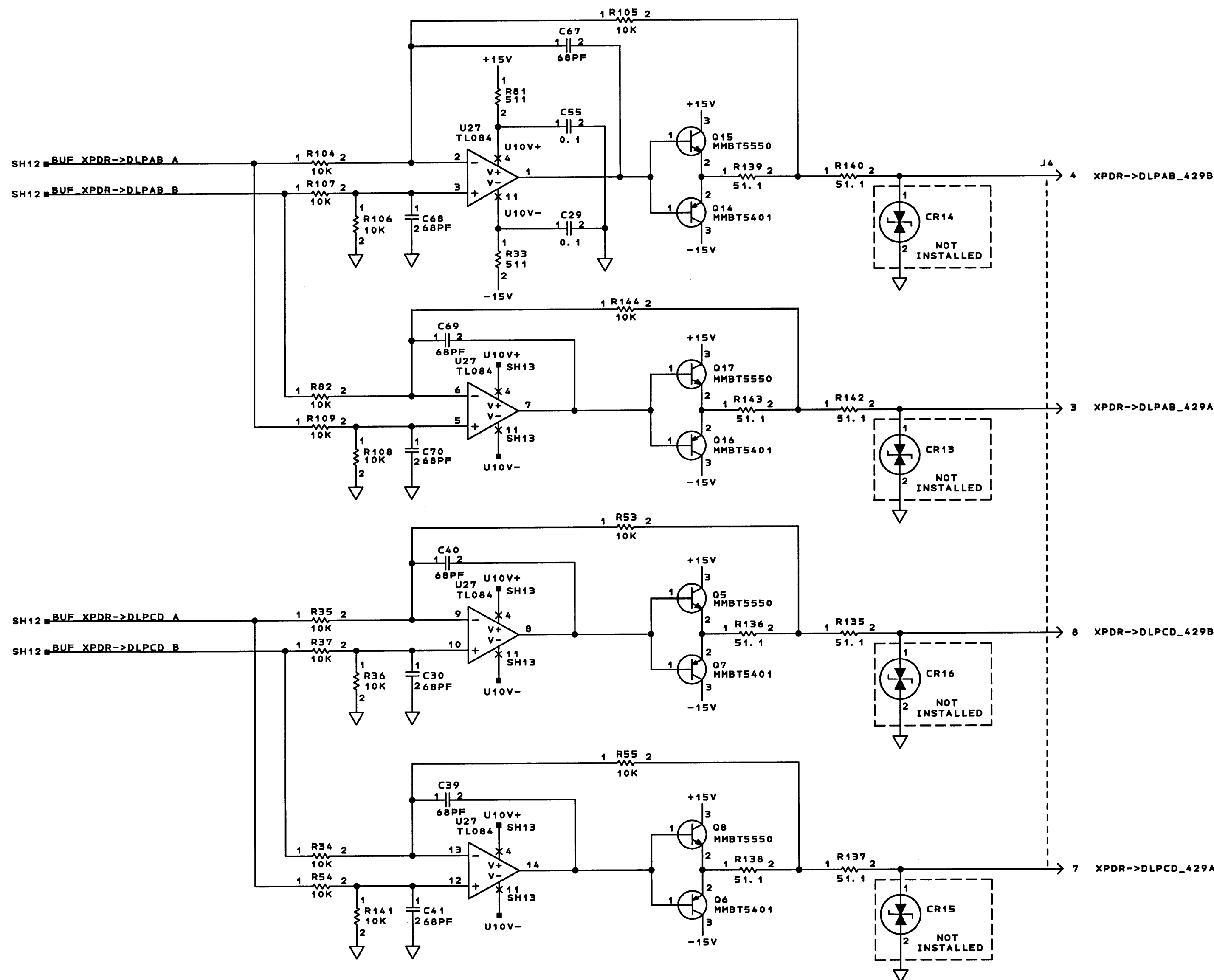
ARINC 429 INPUTS
SCHEMATIC DIAGRAM SHEET NO. 11

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ARINC 429 OUTPUTS, DISCRETE IN/OUT
SCHEMATIC DIAGRAM SHEET NO. 12

COMPUTER GENERATED



ARINC 429 OUTPUTS
SCHEMATIC DIAGRAM SHEET NO. 13

COMPUTER GENERATED

J1		CON60		J1	
SH1	GND	1	DIG TOP VIDEO	SH8	
SH1	GND	3	DIG BOT VIDEO	SH8	+5V
SH1	GND	5	DPSK DATA	SH8	
SH1	GND	7	RRL VIDEO*	SH8	
SH1	+5V	9	+5V	SH1	
SH1	+15V	11	+15V	SH1	
SH1	-5V	13	-5V	SH1	
SH8	RCVR BOT/TOP*	15	-15V	SH1	
SH8	ANT TOP/BOT*	17	BOT MTL ADJ	SH10	
SH10	TOP MTL ADJ	19	RRL CONTROL	SH10	
SH10	RESERVE ADJ_1	21	SPARE 209	NC	
NC	SPARE 210	23	BOT GAIN	SH9	
NC	SPARE 207	25	TOP GAIN	SH9	
SH8	TIP	27	SPARE 201	NC	
NC	SPARE 202	29	SPARE 203	NC	
NC	PLZN PLUG	31	SPARE 204	NC	
NC	SPARE 205	33	SPARE 206	NC	
SH8	XMTR TOP TX	35	XMTR BOT TX	SH8	
SH9	ST_ENABLE*	37	ST PULSE MOD*	SH8	
SH8	ST DPSK	39	PS INTERRUPT*	SH2	
SH8	XMTR ENVELOPE*	41	XPDR OFF<NO>	SH1	
SH3	100KHZ_PS_SYNC	43	MUXED MONITORS	SH10	
SH9	IOA1	45	IOA2	SH9	
SH9	IOA3	47	IOA4	SH9	
SH9	IOA5	49	XMTR PULSE MOD	SH8	
SH1	GND	51	GND	SH1	
SH1	XPDR +28V_PWR	53	XPDR +28V_PWR	SH1	
SH1	XPDR +28V_PWR	55	XPDR +28V_PWR	SH1	
NC	SPARE 211	57	+28V	SH1	
SH1	GND	59	GND	SH1	

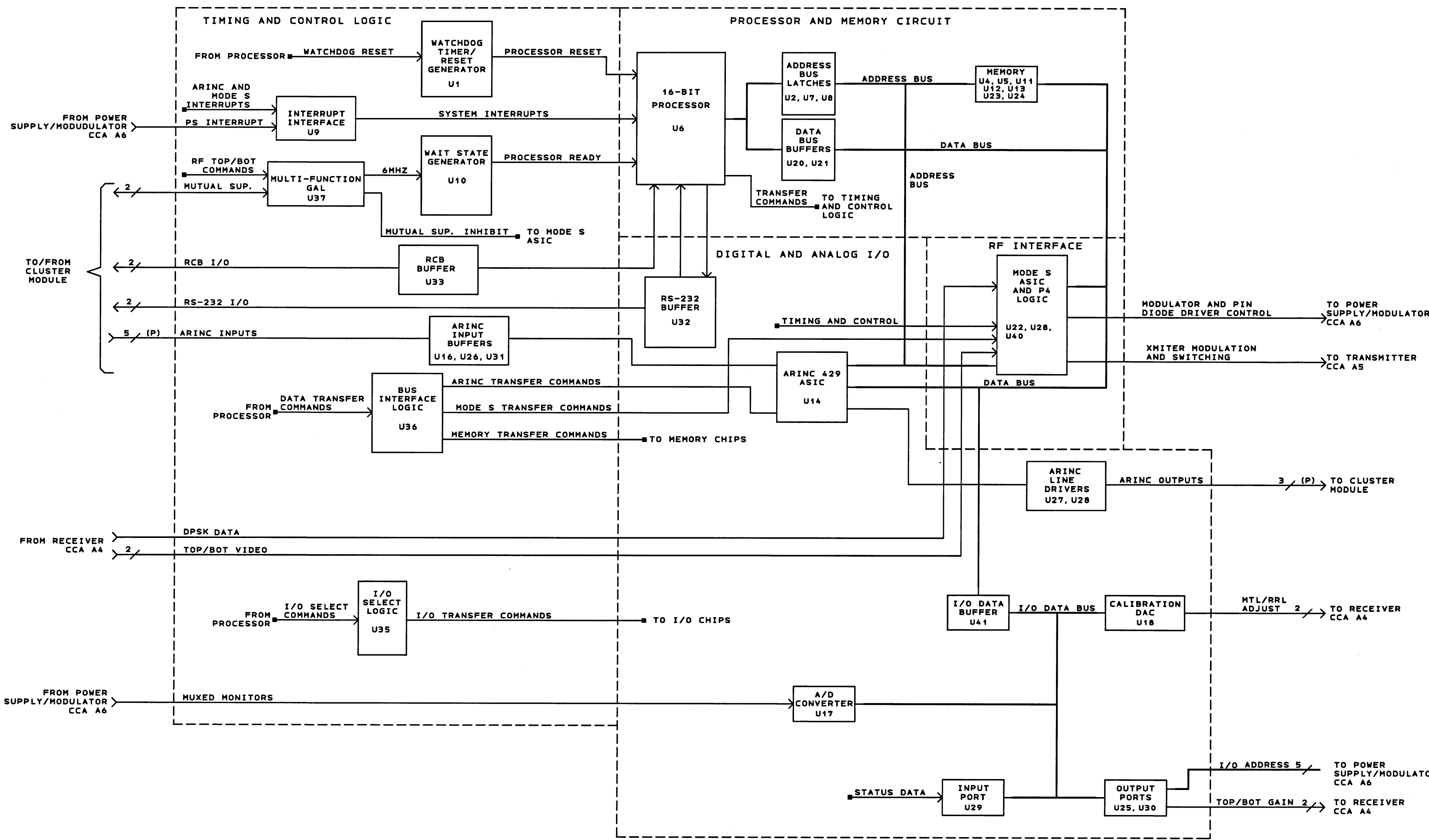
J2		CON60		J2	
SH2	ALE	1	XPDR TX RCB	SH2	
SH2	DT-R*	3	+5V	SH1	
SH2	AD7	5	AD6	SH2	
SH2	AD5	7	AD4	SH2	
SH2	AD3	9	AD2	SH2	
SH2	AD1	11	AD0	SH2	
NC	PLZN PLUG	13	GND	SH1	
SH2	AD15	15	AD14	SH2	
SH2	AD13	17	AD12	SH2	
SH2	AD11	19	AD10	SH2	
SH2	AD9	21	AD8	SH2	
SH2	UART TX	23	UART RX	SH2, 7, 14	
SH2	4MHZ	25	XPDR RX RCB	SH2	
SH2	ERROR*	27	SQTR DISABLE*	SH2	
SH2	B32MHZ	29	RD*	SH2	
SH2	WR*	31	BHE*	SH2	
SH2	2MS_TIMER	33	DEN*	SH2	
SH2	ATCRPL*	35	HOLD	SH2	
SH2	UPREADY	37	CSINTRST*	SH2	
SH2	CSXPDR*	39	NMI	SH2	
SH2	PEREQ	41	UA19	SH2	
SH2	UA18	43	UA17	SH2	
SH2	UA16	45	CSNVRAM*	SH2	
SH2	UPRESET*	47	CSRAM*	SH2	
SH2	CS429*	49	INT4	SH2	
SH2	CS10*	51	ARINCINT	SH2	
SH2	CSROM*	53	UPPWRINT	SH2	
SH2	CSBOOT*	55	SPR	SH2	
SH2	ALLCALL	57	TEST*	SH2	
SH2	32MHZ_DISABLE*	59	CLKOUT	SH2	

J4		CON30		J4	
SH12	MUT_SUP<P>	1	ALT_SRC_SEL2<NO>	SH9	
SH13	XPDR->DLPAB_429A	3	XPDR->DLPAB_429B	SH13	
SH11	DLPAB->XPDR_429A	5	DLPAB->XPDR_429B	SH11	
SH13	XPDR->DLPCD_429A	7	XPDR->DLPCD_429B	SH13	
SH11	DLPCD->XPDR_429A	9	DLPCD->XPDR_429B	SH11	
SH11	ADC1->XPDR_429A	11	ADC1->XPDR_429B	SH11	
SH11	ADC2->XPDR_429A	13	ADC2->XPDR_429B	SH11	
SH12	XPDR->TCAS_429A	15	XPDR->TCAS_429B	SH12	
SH11	TCAS->XPDR_429A	17	TCAS->XPDR_429B	SH11	
NC	SQUAT_SWITCH<NO>	19	GND	SH9	
SH9	XPDR_ACTIVE<NO>	21	GND	SH2	
SH2	XPDR->CLU_RCB	23	GND	SH2	
SH2	CLU->XPDR_RCB	25	GND	SH2	
SH9	RSV_INPUT<NO>	27	RESERVED	NC	
NC	RESERVED	29	XPDR_OFF<NO>	SH9	

J3		CON40		J3	
SH2	DEN*	1	CLKOUT	SH2	
SH2	UPPWRINT	3	SPR	SH2	
SH2	ARINCINT	5	ALLCALL	SH2	
SH2	ALE	7	RRL VIDEO*	SH2	
SH2	RD*	9	WR*	SH2	
SH2	ATCRPL*	11	UPRESET*	SH2	
SH2	WDRESET	13	WDRESET_1	SH10	
SH9	WDERROR*	15	T_VIDEO	SH10	
SH8	TRANRST*	17	B_VIDEO	SH8	
SH8	DPSK_DATA	19	ATCRBSEN	SH8	
SH8	TIP	21	MODESEN	SH8	
SH8	MODE_A*	23	LFSPR	SH8	
SH8	MDS_ATC*	25	A_MODES*	SH8	
SH8	XMTR PULSE_MOD	27	MODE_C*	SH8	
SH8	REPLY_EN	29	ANT_TOP/BOT*	SH8	
NC	PLZN PLUG	31	ENVELOPE*	SH8	
SH2	DT-R*	33	RCVR BOT/TOP*	SH8	
SH10	PROGEN	35	GND	SH1	
SH10	+15V (PROG +15V)	37	PROG_VIN	SH10	
SH7	XPDR_TX_RS232	39	XPDR_RX_RS232	SH7	

CONNECTOR PIN SUMMARY
CAD REFERENCE/REQUIREMENT
SCHEMATIC DIAGRAM SHEET NO. 14

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SIMPLIFIED INTERFACE BLOCK DIAGRAM
SCHEMATIC DIAGRAM SHEET NO. 15

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