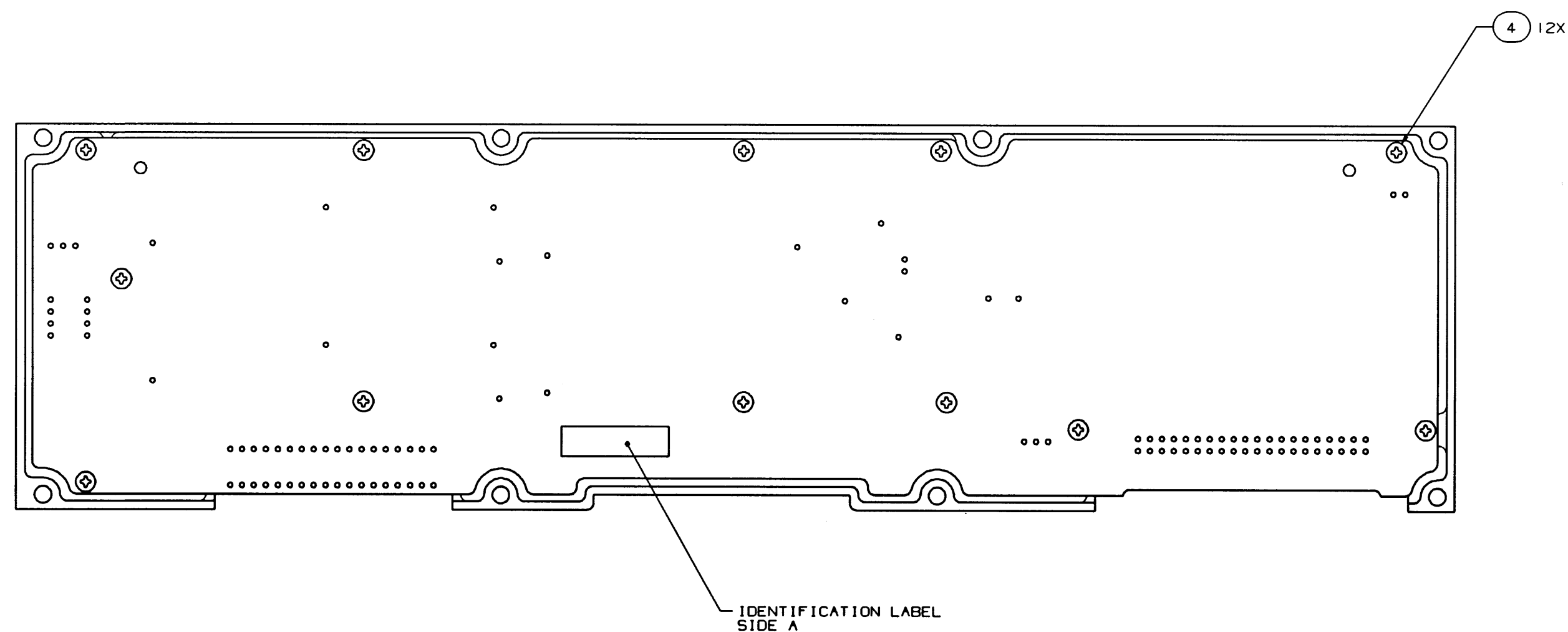
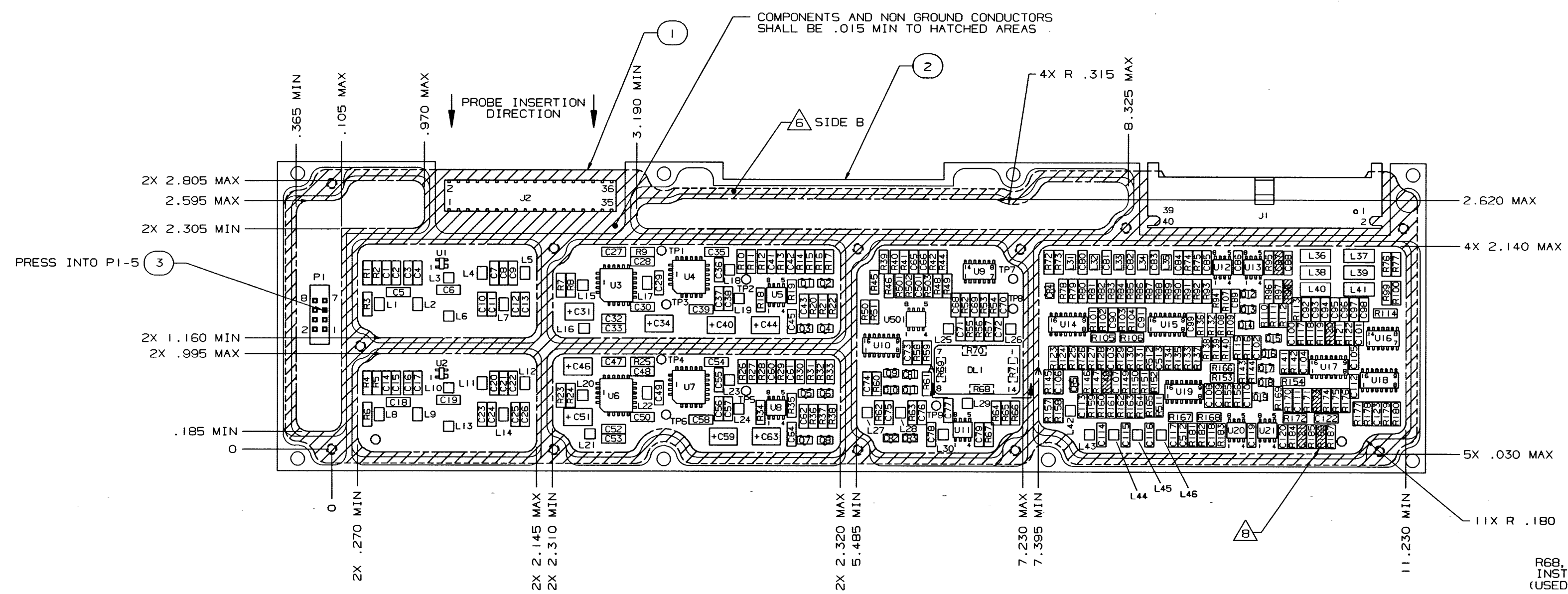
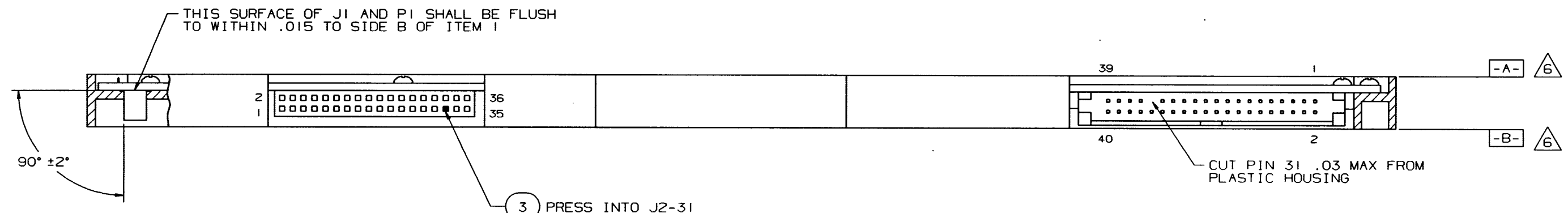
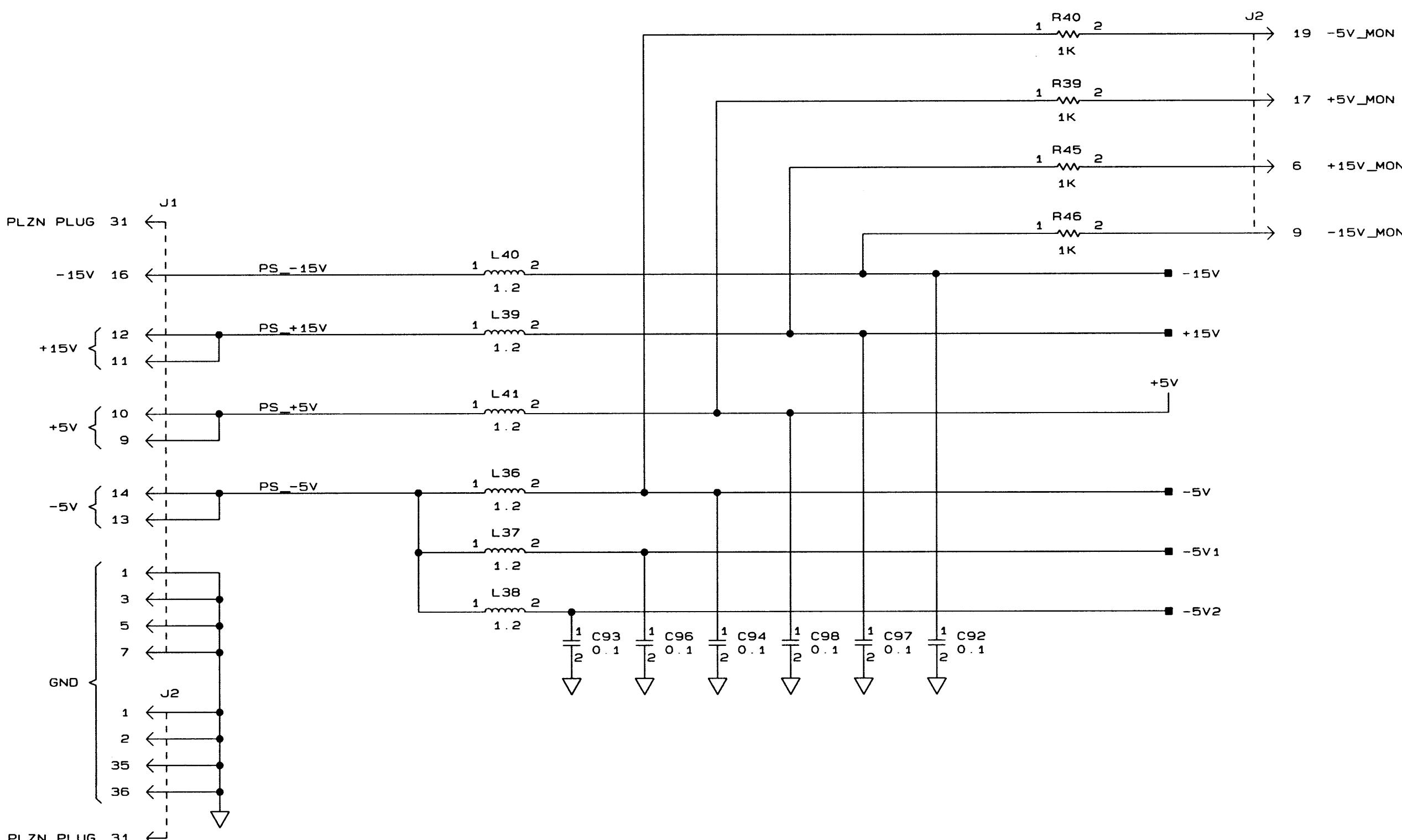


REVISIONS			
ZONE	REV	DESCRIPTION	DATE
			APPROVED



REVISIONS			
ZONE	REV	DESCRIPTION	DATE

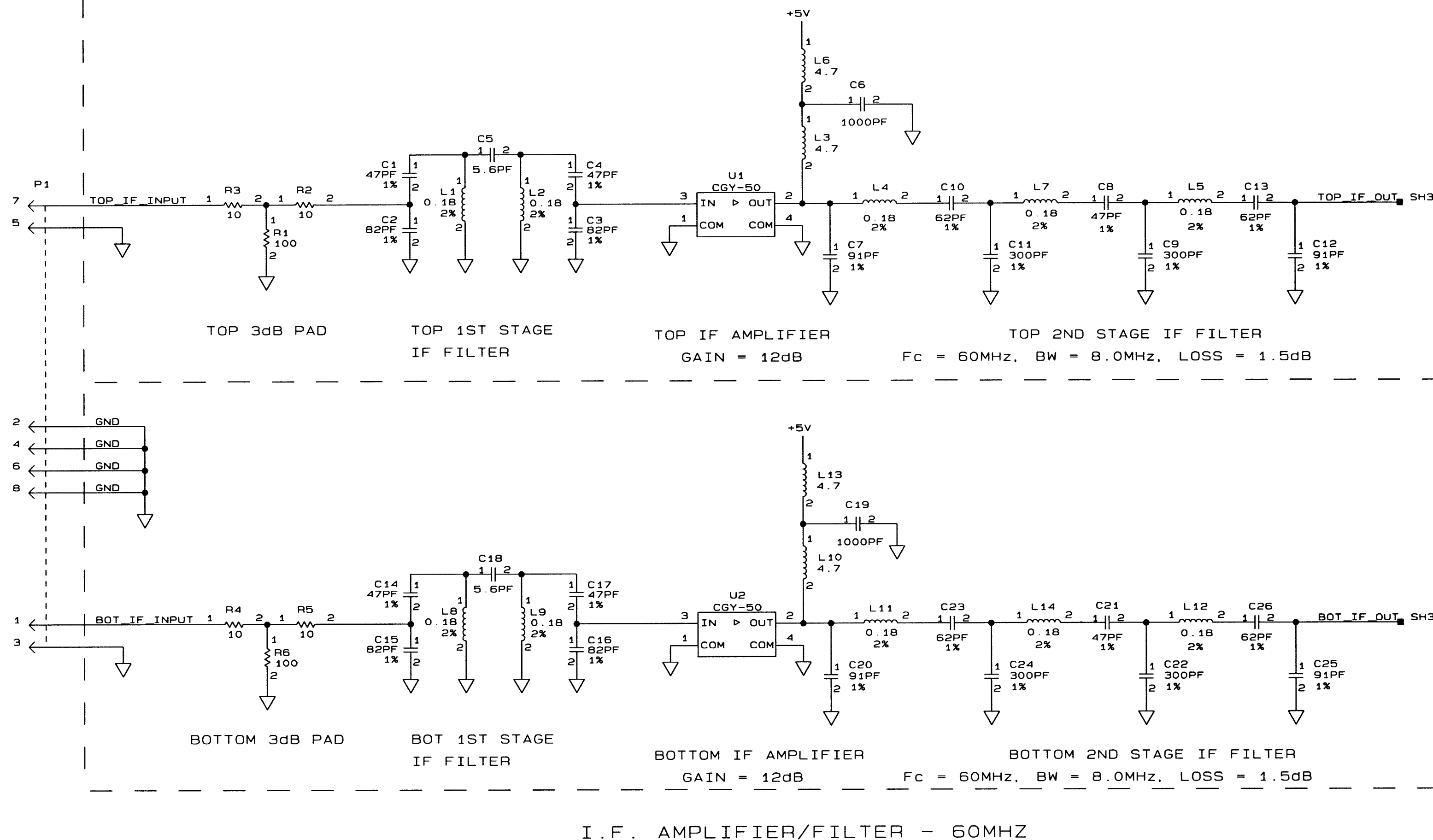


NOTES:
 UNLESS OTHERWISE SPECIFIED:
 1. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 2. ALL RESISTANCE VALUES ARE IN OHMS.
 3. ALL INDUCTANCE VALUES ARE IN MICROHENRIES.
 4. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN;
 FOR COMPLETE DESIGNATIONS PREFIX PART
 DESIGNATION WITH SUBASSEMBLY DESIGNATION.
 5. INTERPRET DRAWING PER ANSI Y32.2-1975 AND
 ANSI/IEEE STD 991-1986. LOGIC SYMBOLS
 CONFORM TO ANSI/IEEE STD 91-1984.
 6. RESISTORS R68-R71 ARE USED ONLY TO SPACE DL1
 OFF OF THE PWB. ELECTRICAL CONTACT BETWEEN
 ANY OF THESE RESISTORS AND DL1 IS PERMISSIBLE.

POWER LINE FILTERS
 SCHEMATIC DIAGRAM SHEET NO. 1 OF 12

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
		E	55939	7517440-904	-
		SCALE —		SHEET 3	

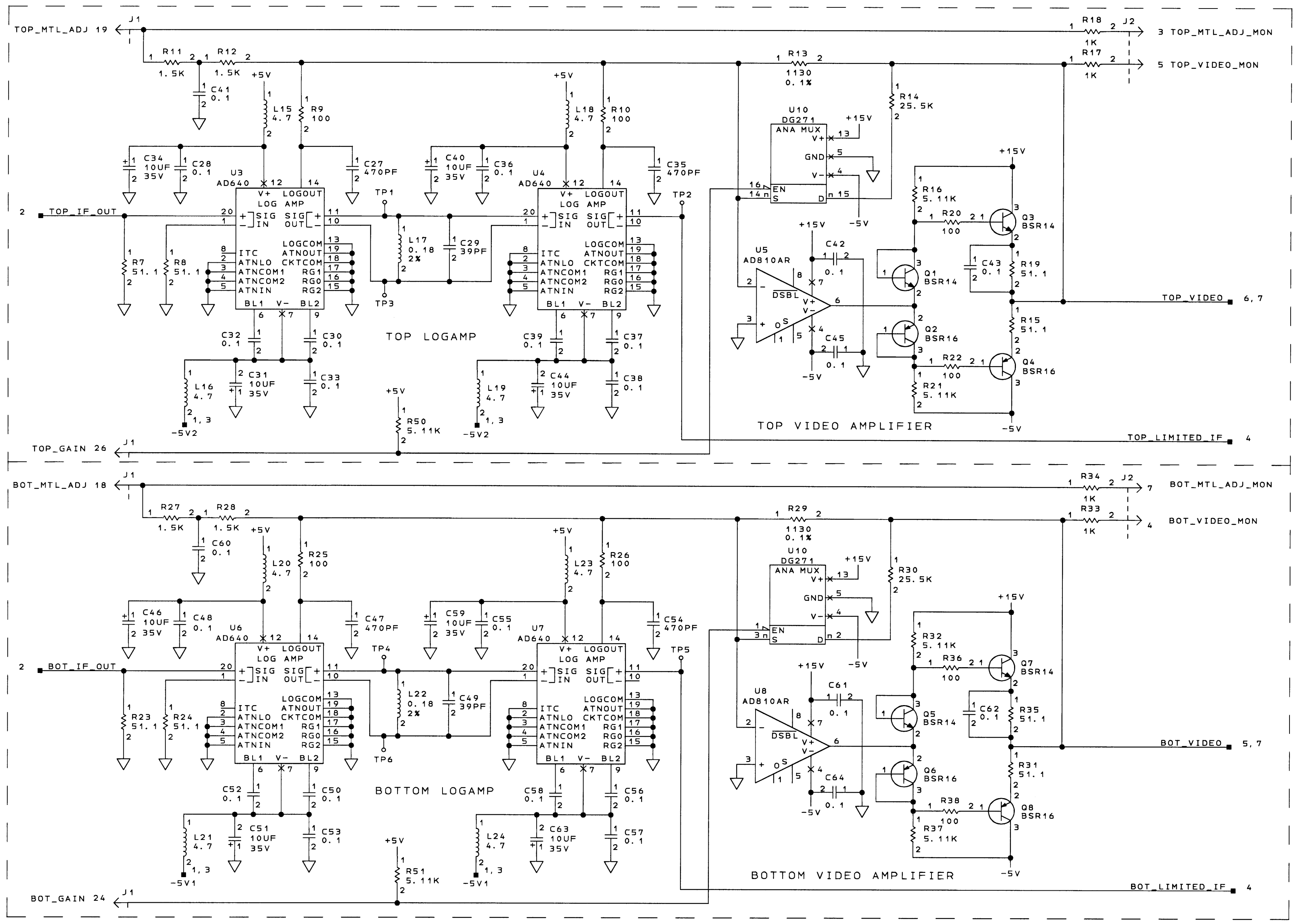


TOP AND BOT IF AMPLIFIER AND
IF FILTER

SCHEMATIC DIAGRAM SHEET NO. 2

COMPUTER GENERATED

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
C4	F4	U5 AND U8 WERE OP-160.	20 July 99
		SEE DWG SH 1.	104102 (U)



LOG AMPLIFIERS

TOP AND BOT LOG AMPS
TOP AND BOT VIDEO AMPS

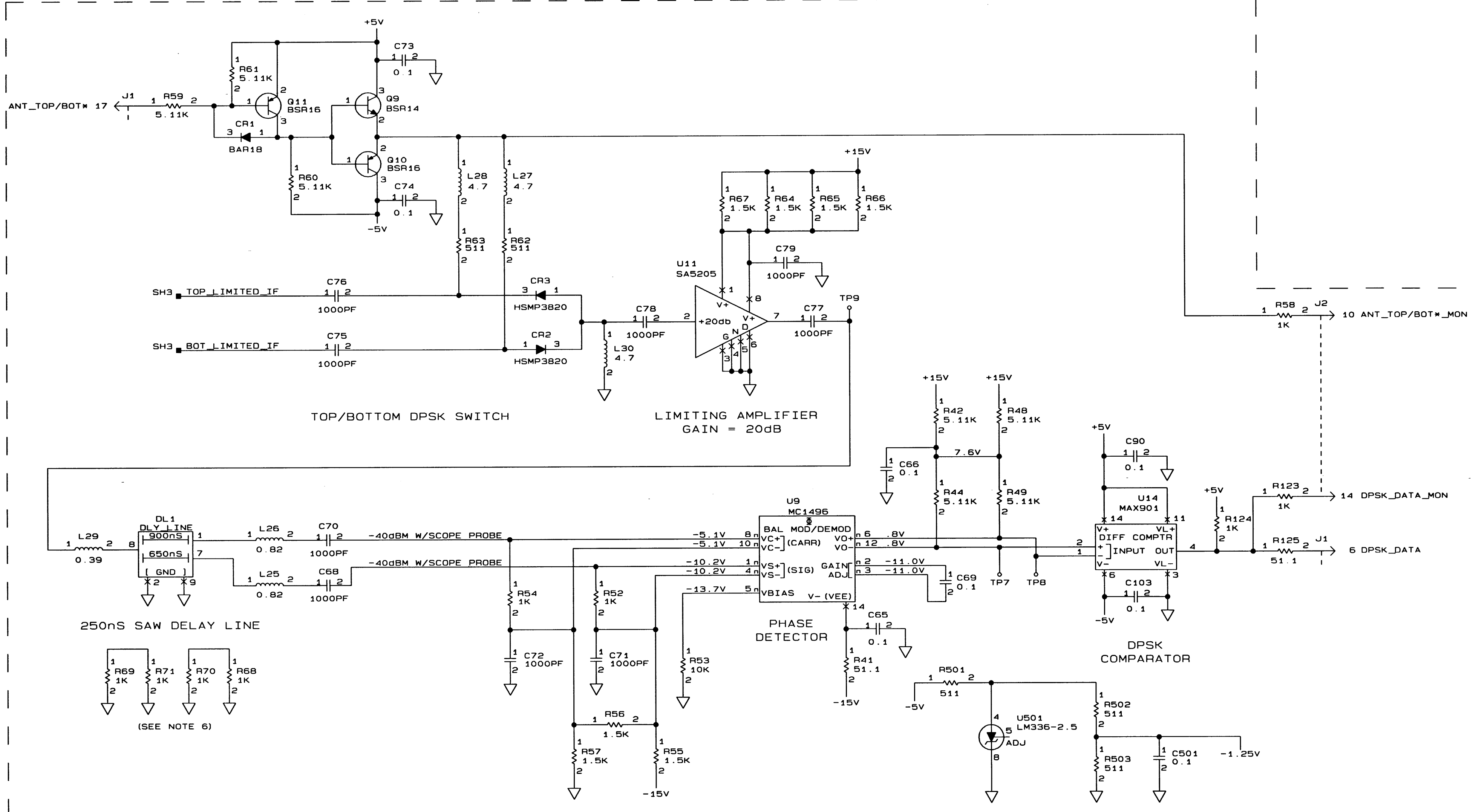
SCHEMATIC DIAGRAM SHEET NO. 3

COMPUTER GENERATED

Honeywell		SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE CAGE CODE E 55939	DRAWING NO. 7517440-904	REV B
8		7	6	5	4

REVISIONS		DATE	APPROVED
ZONE	REV		
A			

PARTS LIST DATA BASE
CHANGE ONLY
92041 (U)
SEE DWG SHEET 1



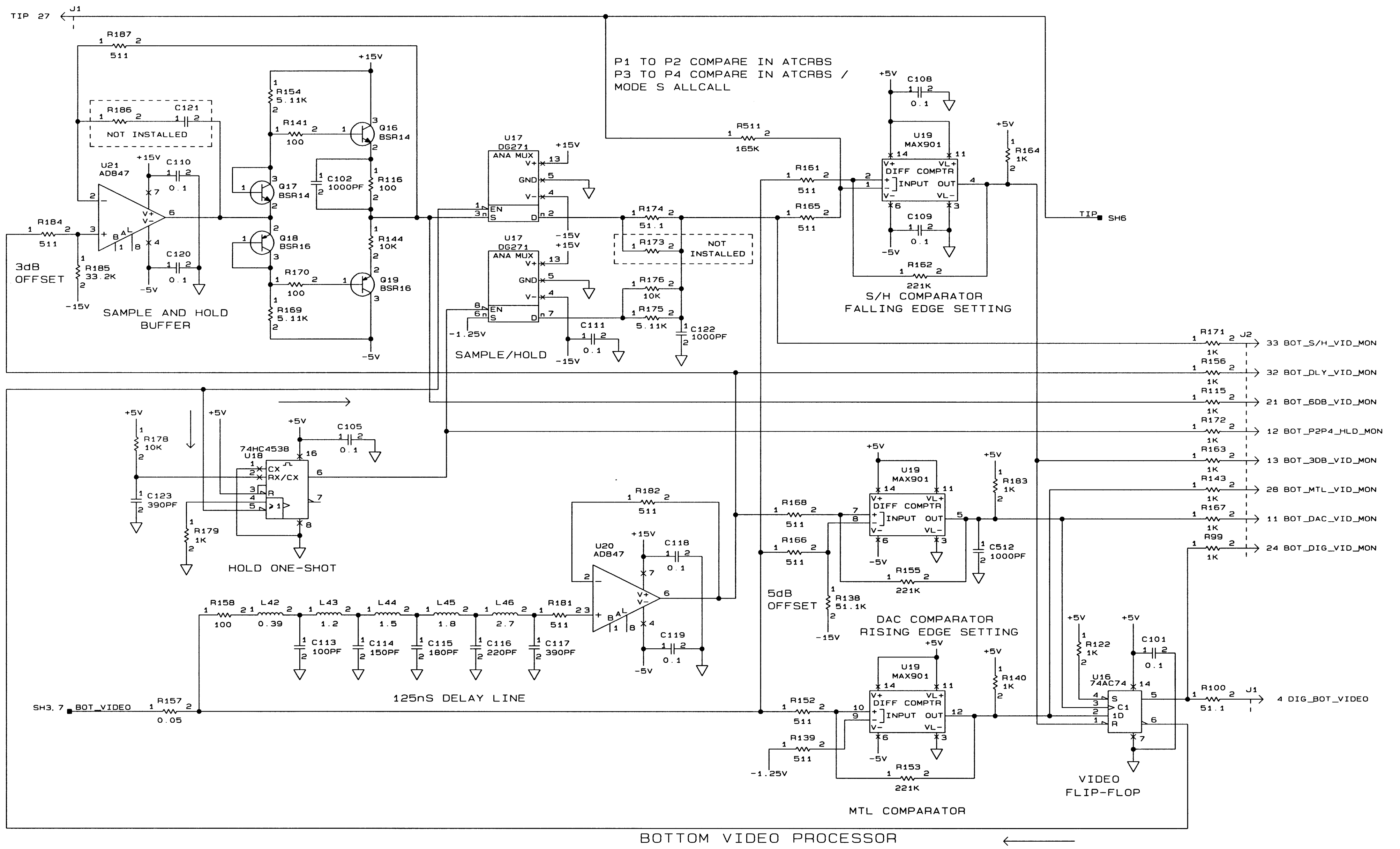
DPSK DEMODULATOR

TOP AND BOT
DPSK SWITCH/DPSK DEMODULATOR

SCHEMATIC DIAGRAM SHEET NO. 4

COMPUTER GENERATED

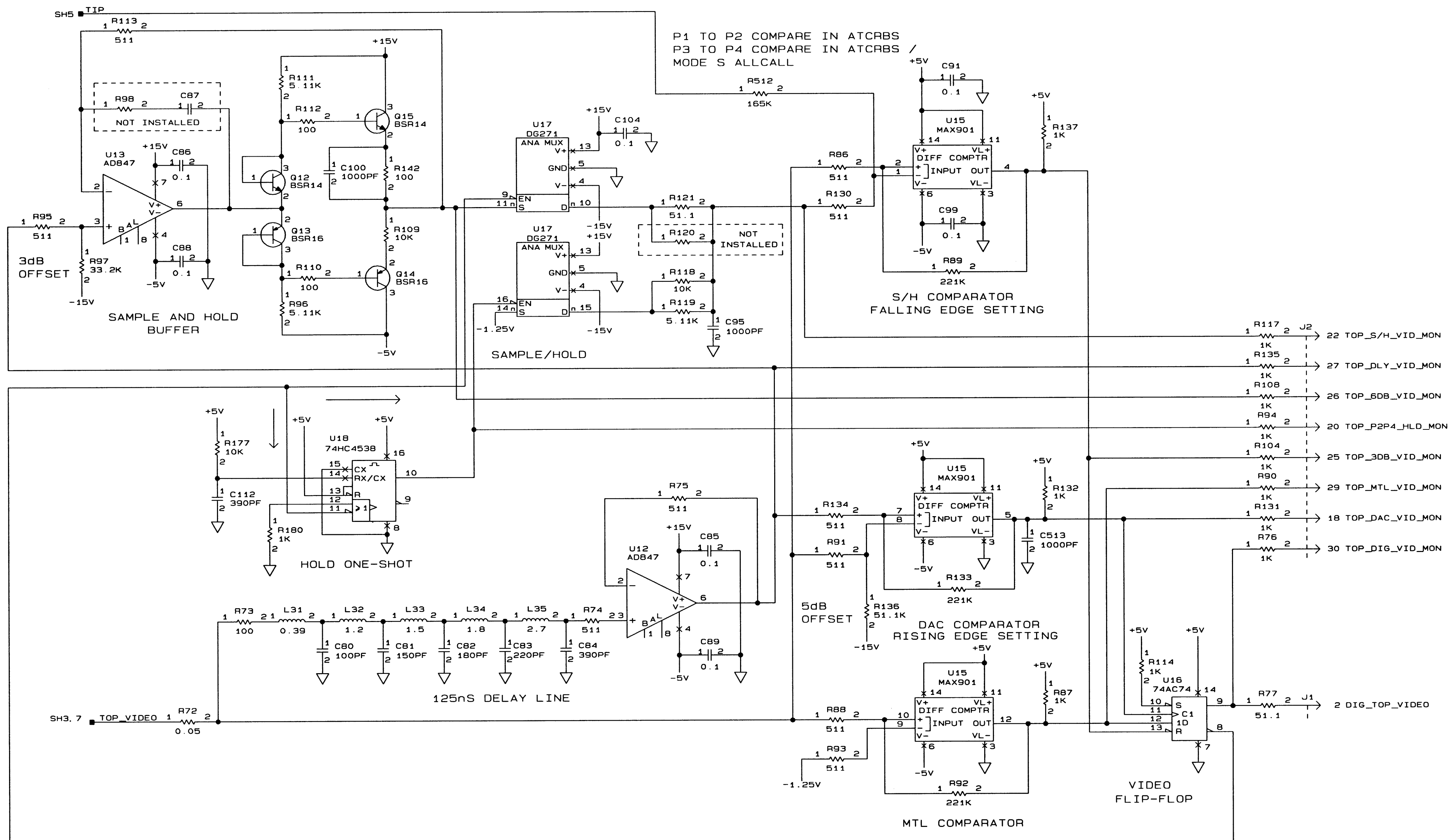
Honeywell		SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
			E	55939	7517440-904	A
			SCALE			SHEET 6



BOTTOM VIDEO PROCESSOR

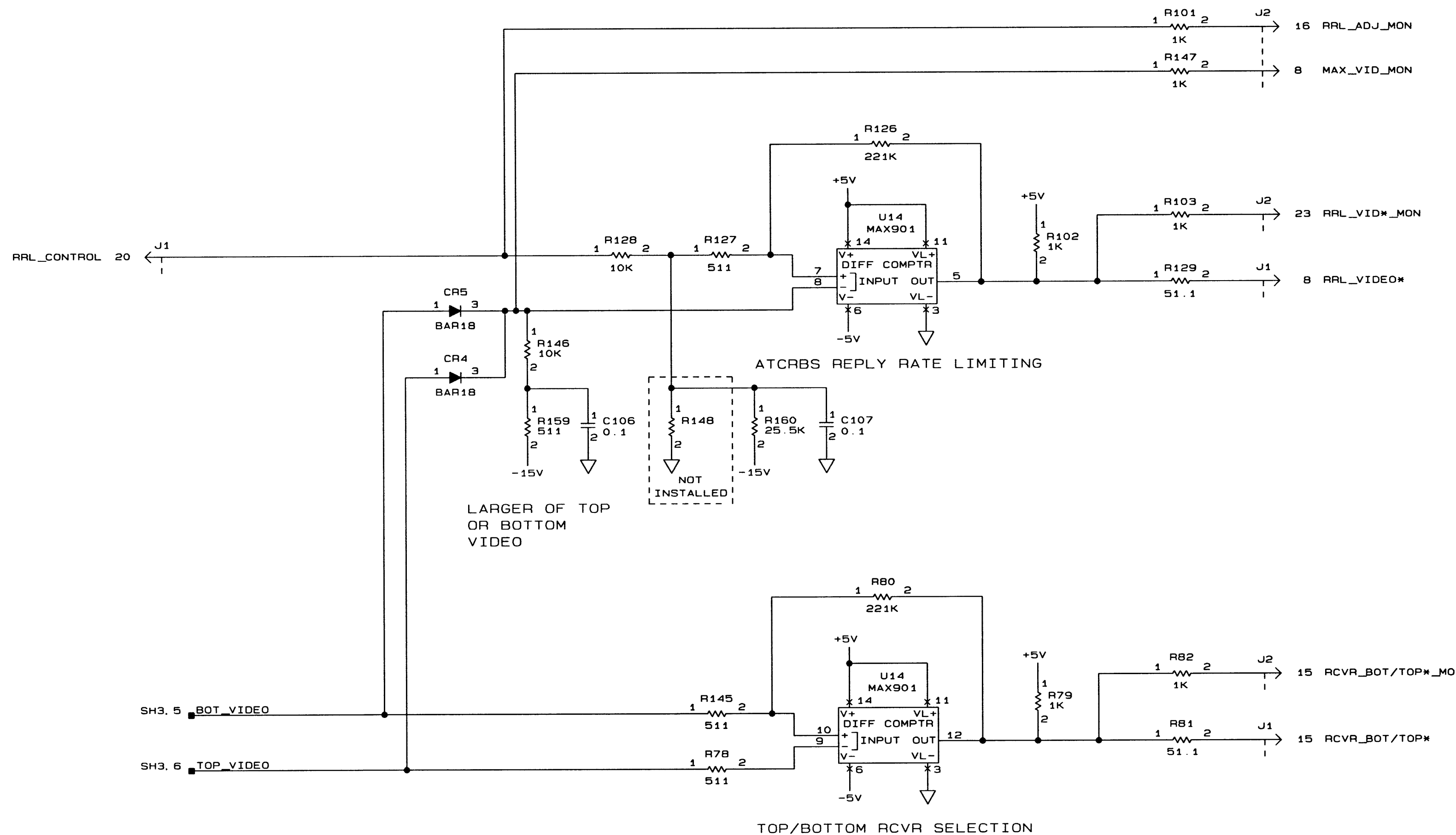
BOT VIDEO PROCESSOR
SCHEMATIC DIAGRAM SHEET NO. 5

COMPUTER GENERATED



TOP VIDEO PROCESSOR
SCHEMATIC DIAGRAM SHEET NO. 6

COMPUTER GENERATED



ATCRBS REPLY RATE LIMITING/TOP &
BOT RCVR SELECTION

SCHEMATIC DIAGRAM SHEET NO. 7

COMPUTER GENERATED

RCVR TO PROC TO PS/MOD

J1 CON40			
SH1	GND	1	DIG TOP VIDEO
SH1	GND	3	DIG BOT VIDEO
SH1	GND	5	DPSK DATA
SH1	GND	7	RRL VIDEO*
SH1	PS +5V	9	PS +5V
SH1	PS +15V	11	PS +15V
SH1	PS -5V	13	PS -5V
SH7	RCVR BOT/TOP*	15	PS -15V
SH4	ANT TOP/BOT*	17	BOT MTL ADJ
SH3	TOP MTL ADJ	19	RRL CONTROL
	(RESERVE ADJ 1)	21	(SPARE 207)
	(SPARE 210)	23	BOT GAIN
	(SPARE 207)	25	TOP GAIN
SH5, 6	TIP	27	(SPARE 201)
	(SPARE 202)	29	(SPARE 203)
	PLZN PLUG	31	(SPARE 204)
	(SPARE 205)	33	(SPARE 206)
	(XMTR BOT TX)	35	(XMTR TOP TX)
	(ST_ENABLE*)	37	(ST_PULSE_MOD*)
	(ST_DPSK)	39	(PS_INTERRUPT*)

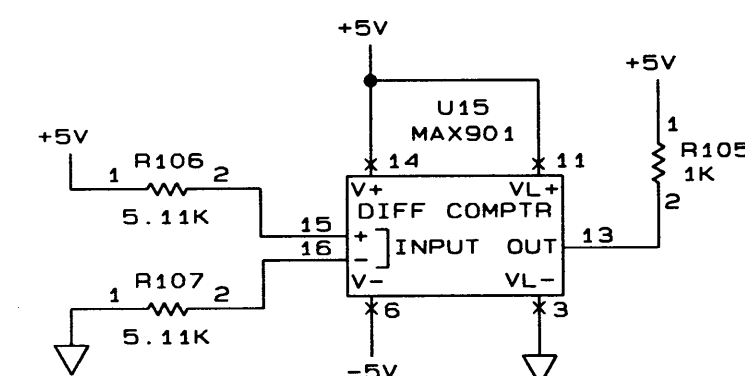
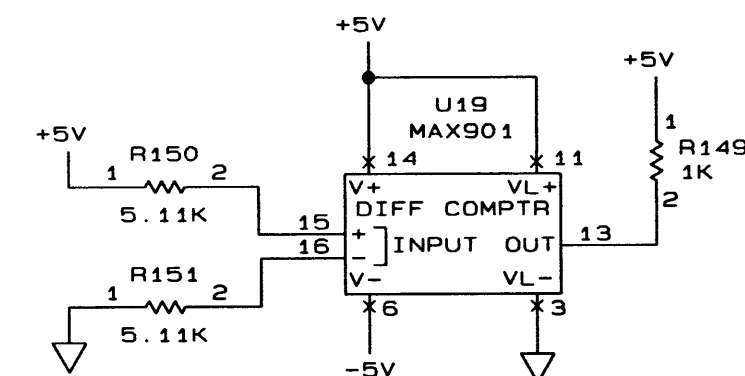
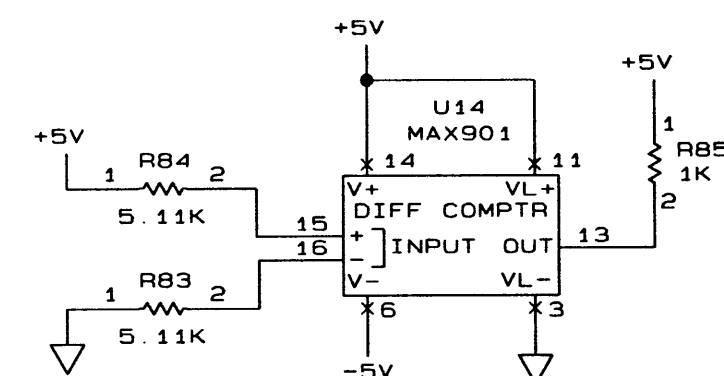
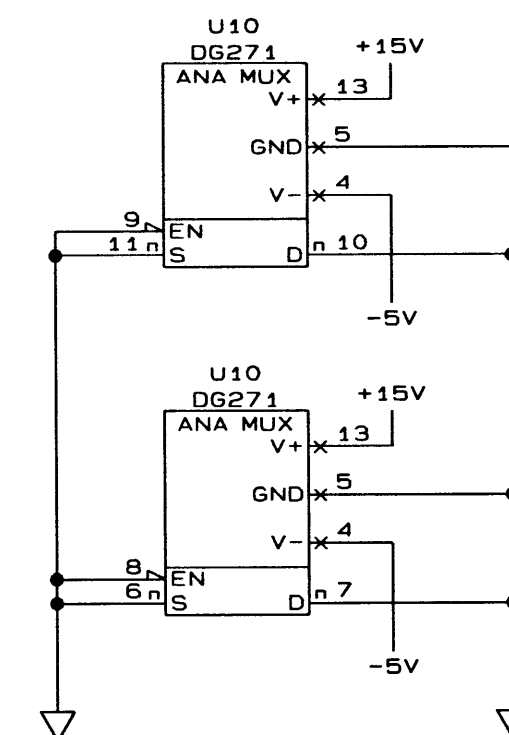
TEST POINT CONNECTOR

J2 CON36			
SH1	GND	1	GND
SH3	TOP MTL ADJ MON	3	BOT VIDEO MON
SH3	TOP VIDEO MON	5	+15V MON
SH3	BOT MTL ADJ MON	7	MAX VID MON
SH1	-15V MON	9	ANT TOP/BOT* MON
SH5	BOT DAC VID MON	11	BOT P2P4 HLD MON
SH5	BOT 3DB VID MON	13	DPSK DATA MON
SH7	RCVR BOT/TOP* MO	15	RRL ADJ MON
SH1	+5V MON	17	TOP DAC VID MON
SH1	-5V MON	19	TOP P2P4 HLD MON
SH5	BOT 6DB VID MON	21	TOP S/H VID MON
SH7	RRL VID* MON	23	BOT DIG VID MON
SH6	TOP 3DB VID MON	25	TOP 6DB VID MON
SH6	TOP DLY VID MON	27	BOT MTL VID MON
SH6	TOP MTL VID MON	29	TOP DIG VID MON
SH1	PLZN PLUG	31	BOT DLY VID MON
SH5	BOT S/H VID MON	33	(SPARE)
SH1	GND	35	GND

XMTR TO RCVR

P1 CON8			
SH2	BOT IF INPUT	1	GND
SH2	GND	3	GND
SH2	GND (PLZN)	5	GND
SH2	TOP IF INPUT	7	GND

INTERCONNECT



SPARES

CONNECTOR PIN SUMMARY
AND SPARE COMPONENTS
CAD REFERENCE/REQUIREMENT

SCHEMATIC DIAGRAM SHEET NO. 8

COMPUTER GENERATED

RCVR TO PROC TO PS/MOD

J1 CON40			
SH1	GND	1	DIG TOP VIDEO
SH1	GND	3	DIG BOT VIDEO
SH1	GND	5	DPSK DATA
SH1	GND	7	RRL VIDEO*
SH1	PS +5V	9	PS +5V
SH1	PS +15V	11	PS +15V
SH1	PS -5V	13	PS -5V
SH7	RCVR BOT/TOP*	15	PS -15V
SH4	ANT TOP/BOT*	17	BOT MTL ADJ
SH3	TOP MTL ADJ	19	RRL CONTROL
	(RESERVE ADJ 1)	21	(SPARE 207)
	(SPARE 210)	23	BOT GAIN
	(SPARE 207)	25	TOP GAIN
SH5, 6	TIP	27	(SPARE 201)
	(SPARE 202)	29	(SPARE 203)
	PLZN PLUG	31	(SPARE 204)
	(SPARE 205)	33	(SPARE 206)
	(XMTR BOT TX)	35	(XMTR TOP TX)
	(ST_ENABLE*)	37	(ST_PULSE_MOD*)
	(ST_DPSK)	39	(PS_INTERRUPT*)

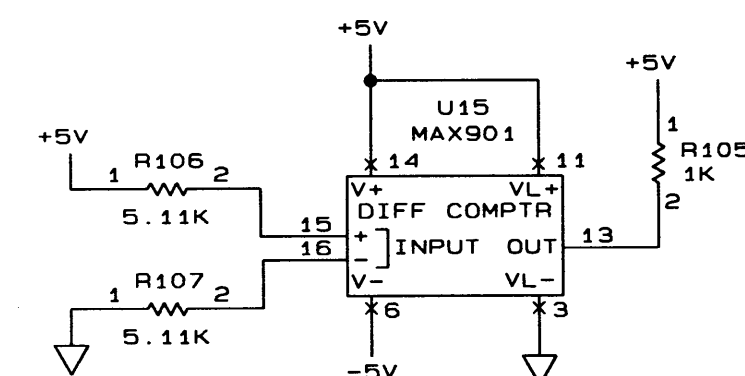
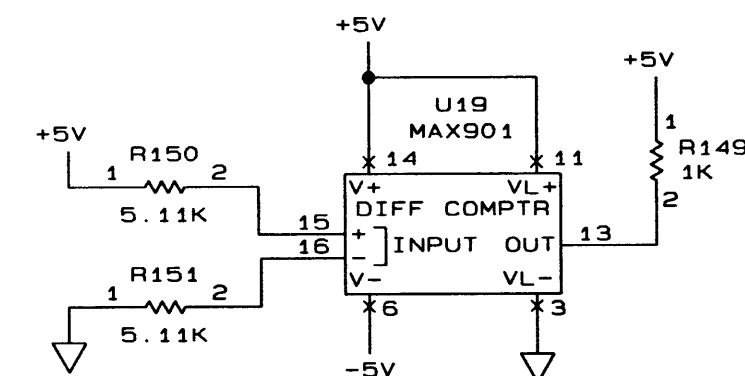
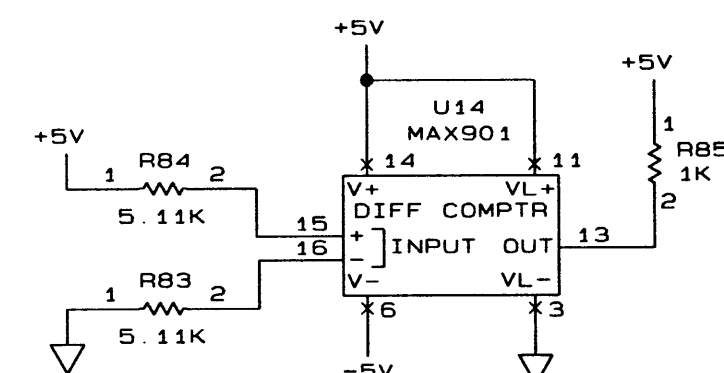
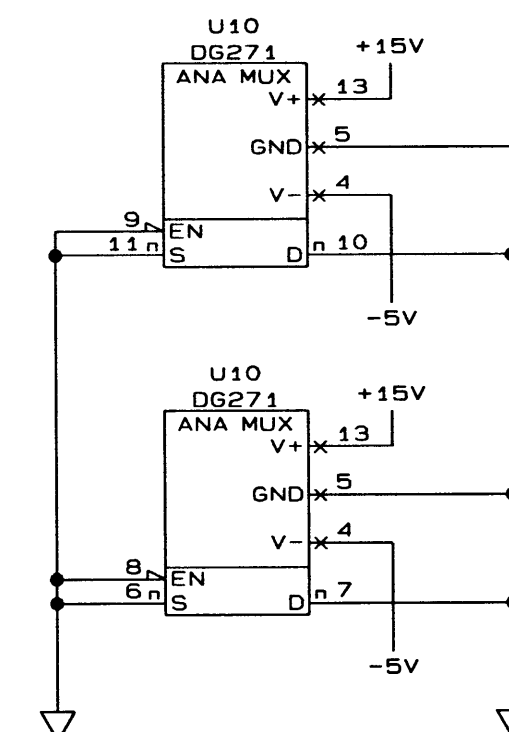
TEST POINT CONNECTOR

J2 CON36			
SH1	GND	1	GND
SH3	TOP MTL ADJ MON	3	BOT VIDEO MON
SH3	TOP VIDEO MON	5	+15V MON
SH3	BOT MTL ADJ MON	7	MAX VID MON
SH1	-15V MON	9	ANT TOP/BOT* MON
SH5	BOT DAC VID MON	11	BOT P2P4 HLD MON
SH5	BOT 3DB VID MON	13	DPSK DATA MON
SH7	RCVR BOT/TOP* MO	15	RRL ADJ MON
SH1	+5V MON	17	TOP DAC VID MON
SH1	-5V MON	19	TOP P2P4 HLD MON
SH5	BOT 6DB VID MON	21	TOP S/H VID MON
SH7	RRL VID* MON	23	BOT DIG VID MON
SH6	TOP 3DB VID MON	25	TOP 6DB VID MON
SH6	TOP DLY VID MON	27	BOT MTL VID MON
SH6	TOP MTL VID MON	29	TOP DIG VID MON
SH1	PLZN PLUG	31	BOT DLY VID MON
SH5	BOT S/H VID MON	33	(SPARE)
SH1	GND	35	GND

XMTR TO RCVR

P1 CON8			
SH2	BOT IF INPUT	1	GND
SH2	GND	3	GND
SH2	GND (PLZN)	5	GND
SH2	TOP IF INPUT	7	GND

INTERCONNECT



SPARES

CONNECTOR PIN SUMMARY
AND SPARE COMPONENTS
CAD REFERENCE/REQUIREMENT

SCHEMATIC DIAGRAM SHEET NO. 8

COMPUTER GENERATED

COMPUTER GENERATED

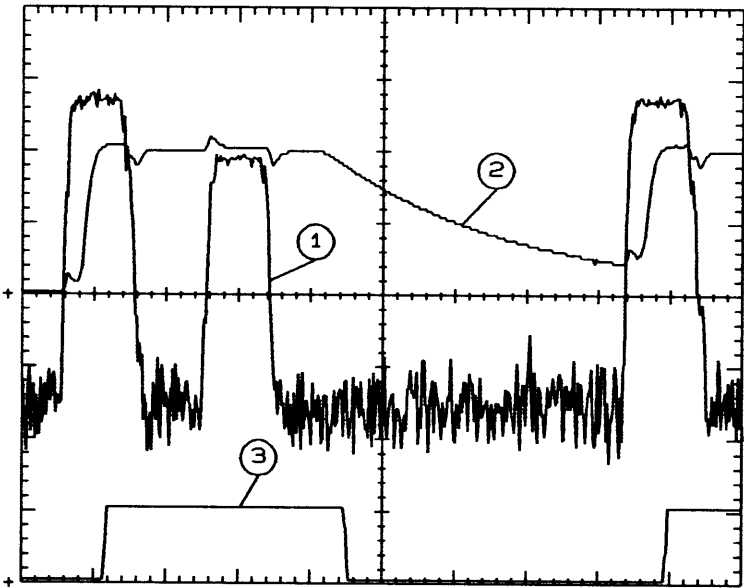
Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV.
		E	55939	7517440-904	-
		SCALE	SHEET		12

TEST POINT TABLE

SCHEMATIC DIAGRAM SHEET NO. 10

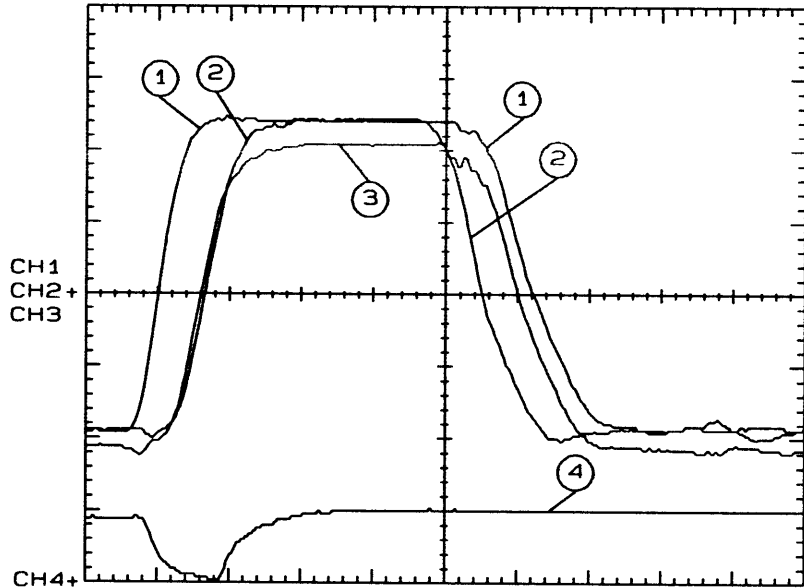
COMPUTER GENERATED

WAVEFORM 1



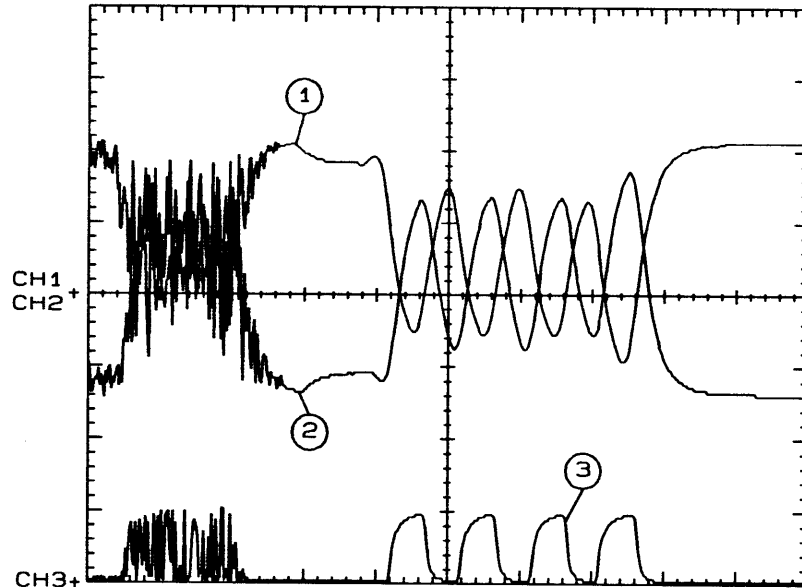
CH1, J2-05/J2-04, 0.5V/DIV, TOP/BOT VIDEO
CH2, J2-22/J2-33, 0.5V/DIV, TOP/BOT S/H VIDEO
CH3, J2-20/J2-33, 5.0V/DIV, TOP/BOT P2/P4 HOLD
TIMEBASE : 1uS/DIV

WAVEFORM 2



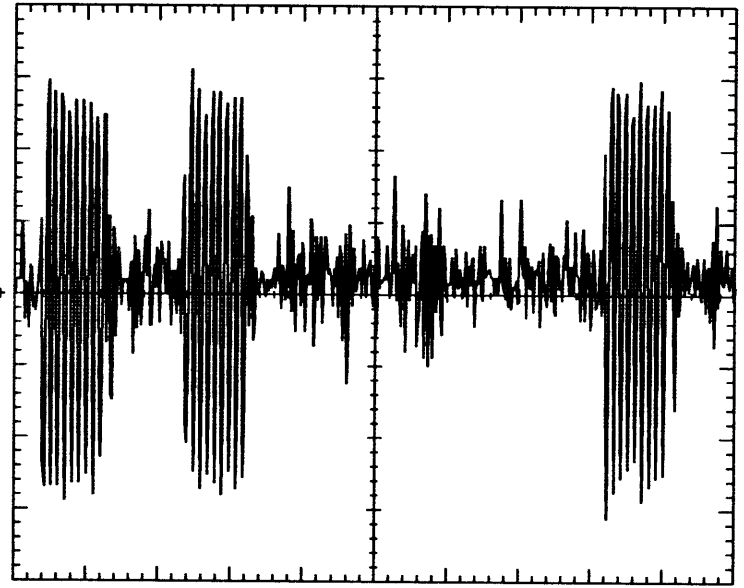
CH1, J2-05/J2-04, 0.5V/DIV, TOP/BOT VIDEO
CH2, J2-27/J2-32, 0.5V/DIV, TOP/BOT DLY VIDEO
CH3, J2-26/J2-21, 5.0V/DIV, TOP/BOT 60B VIDEO
CH4, J2-18/J2-11, 5.0V/DIV, TOP/BOT DAC VIDEO
TIMEBASE : 200nS/DIV

WAVEFORM 3



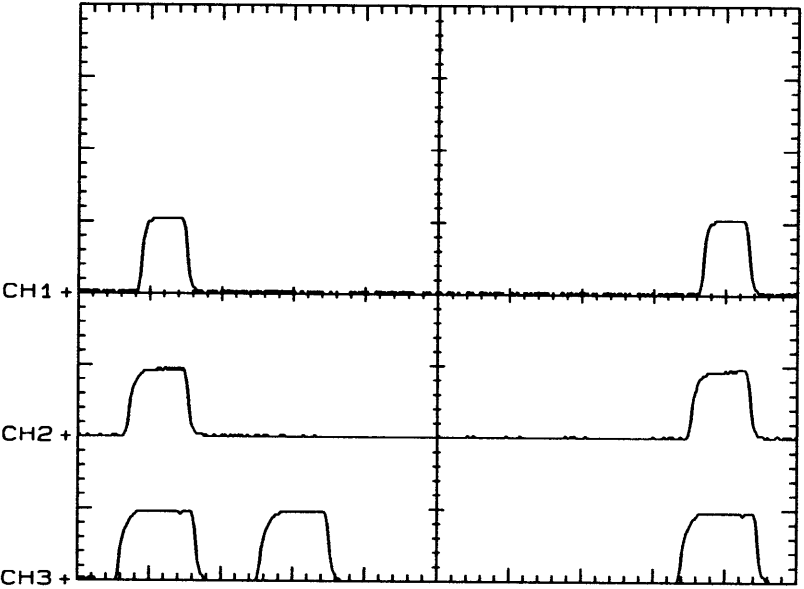
CH1, TP8, 1.0V/DIV, PHASE DET (+)
CH2, TP7, 1.0V/DIV, PHASE DET (-)
CH3, J2-14, 5.0V/DIV, DPSK DATA
TIMEBASE : 500nS/DIV

WAVEFORM 4



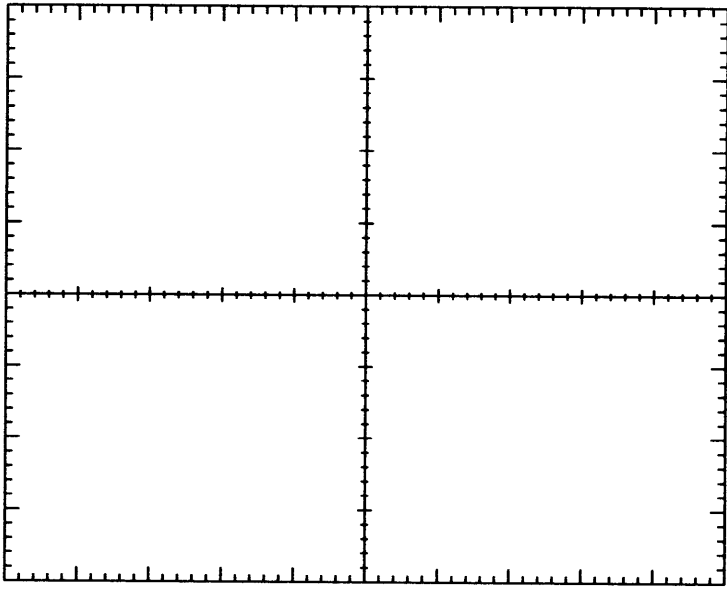
CH1, TP1/ETC, 20mV/DIV, LOGAMP INTERSTAGE
SAME AS TP7, TP8, TP10
TIMEBASE : 1uS/DIV
SIGNAL LEVEL : -32dBm

WAVEFORM 5

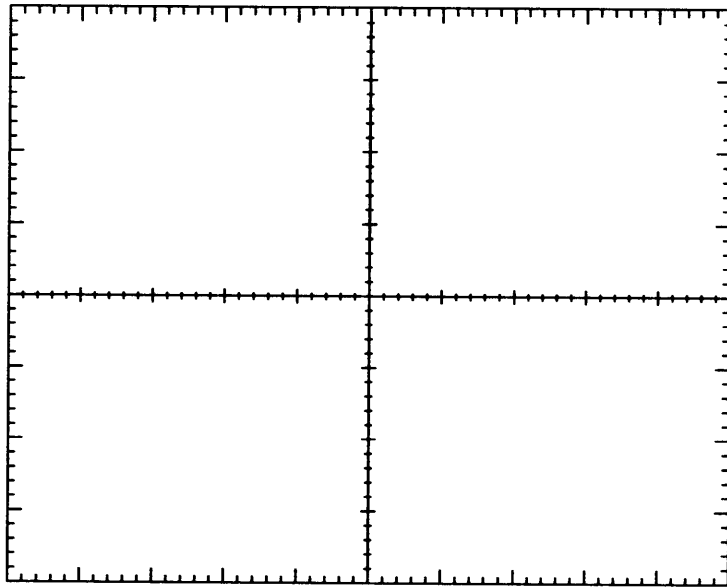


CH1, J2-30/J2-24, 5.0V/DIV, TOP/BOT DIG VIDEO
CH2, J2-25/J2-13, 5.0V/DIV, TOP/BOT 30B VIDEO
CH3, J2-28/J2-07, 5.0V/DIV, TOP/BOT MTL VIDEO
TIMEBASE : 1uS/DIV

WAVEFORM 6

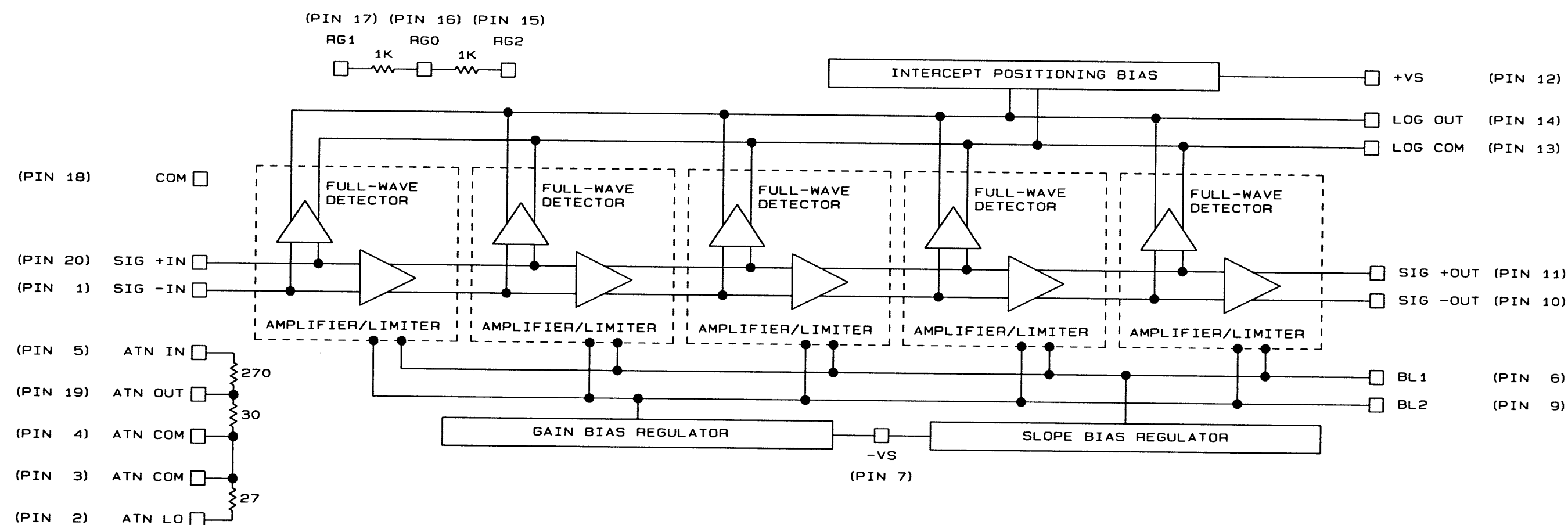


WAVEFORM 7



TEST POINT WAVEFORMS
SCHEMATIC DIAGRAM SHEET NO. 11

COMPUTER GENERATED



AD640 LOGAMP BLOCK DIAGRAM
SCHEMATIC DIAGRAM SHEET NO. 12

COMPUTER GENERATED