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PROPRIETARY NOTICE
THIS DOCUMENT AND THE INFORMATION DISCLOSED HEREIN ARE PROPRIETARY DATA OF HONEYWELL INC. NEITHER THIS DOCUMENT NOR THE INFORMATION CONTAINED HEREIN SHALL BE REPRODUCED, USED, OR DISCLOSED TO OTHERS WITHOUT THE WRITTEN AUTHORIZATION OF HONEYWELL INC.

NOTICE
FREEDOM OF INFORMATION ACT (5 USC 552) AND
DISCLOSURE OF CONFIDENTIAL INFORMATION GENERALLY
(18 USC 1905)
THIS DOCUMENT IS BEING FURNISHED IN CONFIDENCE BY HONEYWELL INC. THE INFORMATION DISCLOSED HEREIN FALLS WITHIN EXEMPTION (b) (4) OF 5 USC 552 AND THE PROHIBITIONS OF 18 USC 1905.

TABLE I
CUT RUN DATA
14

CUT NO.	PWB SIDE	FROM CIRCUIT POINT	TO CIRCUIT POINT
1	B	U23-11 LAND	U23-11 PROBE PAD

TABLE II
BUSWIRE DATA
15

CONDUCTOR		PWB SIDE	FROM CIRCUIT POINT	TO CIRCUIT POINT
COND. NO.	ITEM NO.			
601	703	B	U23-11 LAND	U23-40
602	703	B	U12-28	C57-TOP (+)
603	703	B	A1-OUT1	R75-BOTTOM PAD
604	703	B	A1-OUT2	R81-BOTTOM PAD
605	703	B	A1-3.3V IN	U2-5 PAD
606	703	B	A1-SIG IN	U2-4 PAD
607	703	B	A1-GND	U2-3 PAD
608	703	B	CR999-1	U27-9
609	703	B	CR999-3	U27-5
610	703	B	C901-TOP	U12-14
611	703	B	C901-BOTTOM	R901-BOTTOM
612	703	B	R901-TOP	U12-19
613	703	B	R902-TOP	C901-TOP
614	703	B	R902-BOTTOM	U12-13

TABLE III
COMPONENT INSTALLATION
16

COMPONENT		TO CIRCUIT POINT	PWB SIDE	SLVG ITEM NO.
REF DES	PIN			
R998	RIGHT	J3-1	A	705
	LEFT	J3-11		
R999	RIGHT	J2-A26	A	705
	LEFT	J2-A22		

TABLE IV
FIRMWARE
22

TT PN	CK SUM	VERSION
TT7030603-102	B3C0	2

NOTES:
1. ASSEMBLE AND SOLDER PER ITEM 701.
2. THIS ASSEMBLY INCLUDES COMPONENTS WHICH ARE SUBJECT TO DAMAGE BY ELECTROSTATIC CHARGES; THEREFORE, ALL COMPONENTS SHALL BE HANDLED IN ACCORDANCE WITH GUIDELINES FOR ELECTROSTATIC DISCHARGE CONTROL.
3. MAXIMUM COMPONENT HEIGHT SIDE B SHALL BE .450. EXCEPT AREA "A" SHALL BE .350. THIS AREA TO BE SKYLINE WITH COMPONENTS ON 7026232-9XX.
4. COMPONENT LEAD PROTRUSION SHALL BE .060 MAXIMUM EXCEPT P1, J2, AND J3; BARE COPPER PERMISSIBLE ON COMPONENT LEADS AT TRIMMED SURFACES. P1, J2, AND J3 LEADS ARE NOT TO BE TRIMMED.
5. SQUARE PADS ON PRINTED WIRING BOARD INDICATE PIN 1.
6. COAT ASSEMBLY PER ITEM 702 EXCEPT TEST POINTS AND AREAS NOTED.
7. SOME COMPONENTS SHOWN OVERSIZE TO PROVIDE SPACE FOR DESIGNATORS.
8. SHADED COMPONENTS ARE NOT INSTALLED.
9. MARK APPLICABLE PART NUMBER, DASH NUMBER, REVISION STATUS, AND UCN PER ITEM 707. LOCATE APPROXIMATELY WHERE SHOWN.
10. INSTALL P1 FLUSH TO WITHIN .010 OF PWB. TRIM PINS A5 AND F4 FLUSH TO WITHIN .020 OF TOP SURFACE OF UPPER CROSS BAR FOR KEYING.
11. INSTALL KEYING PIN (ITEM 142) INTO J2-D21.
12. FOR KEYING PURPOSES, REMOVE J3-14 FLUSH TO CROSSBAR.
13. SECURE C40 AND C41 TO PWB (ITEM 1) AND EACH OTHER USING ITEM 706.
14. MAKE CUTS PER TABLE I, LEFT OR RIGHT, TOP OR BOTTOM AS VIEWED FROM INDICATED SIDE.
15. INSTALL BUS WIRES PER TABLE II, LEFT OR RIGHT, TOP OR BOTTOM AS VIEWED FROM INDICATED SIDE. ITEM 708 IS AN ACCEPTABLE SUBSTITUTE FOR ITEM 703.
16. INSTALL COMPONENTS PER TABLE III, LEFT OR RIGHT, TOP OR BOTTOM, AS VIEWED FROM INDICATED SIDE.
17. SECURE CR999 UPSIDE DOWN ON TOP OF U27 USING ADHESIVE (ITEM 704) APPROXIMATELY AS SHOWN AND WIRE PER TABLE II.
18. SECURE T1, L4, AND L5 TO PWB (ITEM 1) USING ITEM 706 ON OPPOSITE SIDES OF CASE. EXTERNAL FILLET SHALL BE .125 MIN. SECURE AFTER SOLDERING PART TO PWB.
19. SECURE C901, R901, AND R902 TO TOP OF U12 USING ADHESIVE (ITEM 704).

21. TORQUE ITEMS 7 AND 8 TO 5±1 IN-LBS.
22. PROGRAM U24 CPLD WITH FIRMWARE (ITEM 709) LISTED IN TABLE IV AT HONEYWELL BEFORE INSTALLATION OF CCA IN NEXT HIGHER ASSY.
23. SECURE A1 TO PWB USING ITEM 706. LOCATE WHERE SHOWN. EXCEPT A1 SHALL NOT COVER ANY PADS, VIAS, OR TRACES ON PWB (ITEM 1). WIRE PER TABLE II.

REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
	XB	INITIAL X RELEASE OF SHEETS 1 THRU 29	18MAY01	R.BACULA
	XC	NOTES: NOTE 4 WAS: COMPONENT---MAX: BARE--- SURFACES. ADD NOTE 10 THRU 18. TABLES: ADD TBL 1,11,111. SH IDX REV. PL AFFECTED.	06NOV01	E.THIESSEN A.WHIDDEN
	XD	NOTES: ADD NOTE 19,20. TABLES: TBL 11: ADD COND. NO. 602. TBL 111: ADD C901,R901,R902 SH IDX REV. PL AFFECTED.	20NOV01	E.THIESSEN A.WHIDDEN
	XE	SH IDX REV. PL AFFECTED.	02JAN02	E.THIESSEN A.WHIDDEN
	XF	PER MARKED PRINT	01MAR02	P.SCHWERTMAN
	XG	PER MARKED PRINT	01MAR02	P.SCHWERTMAN
	XH	PER MARKED PRINT	26MAR02	R.BACULA
	XJ	EXTENSIVELY REVISED SH IDX REV. PL AFFECTED.	08JUL02	E.THIESSEN A.WHIDDEN
	XK	NOTES: RMV NOTE 17. U20 NOTE 19,20,21. TABLES: TBL 1: ADD COND. NO. 608 THRU 614. TBL 11: RMV C901,C901,R901,R902. WAS SH OF 29. SH IDX REV. PL NOT AFFECTED.	16AUG02	E.WHIDDEN

REV STATUS OF PARTS LIST SHALL BE SAME AS BASIC DRAWING NUMBER SEE SEPARATE PARTS LIST

(SCHEMATIC: SHEETS 4 THRU 30)

5231-040 (ASME Y14.5M-1994) HONEYWELL INC. (REV 3-97)

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APPLICATION

7026230-902

1

45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1																																			
FOR HONEYWELL REFERENCE																																	INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994																	UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES; TOLERANCES: ANGLES ± — 2 PL DEC ± — 3 PL DEC ± —																													
MFG REF NO.																																	MATERIAL PROCESS FINISH																	MILITARY DESIGNATION										PROCESS FINISH SEE 00.40										ITEM CODE (PER EBB99384) 3									
DPA NO. A24530																																	MATERIAL																																														
PRODUCT LINE NO. 3829																																	PROCESS																																														
AW/PS ANALYSIS EB 7026149																																	FINISH																																														
REF DWG																																	REF SPEC																																														
7026201																																	7026201																																														
7026202																																	7026202																																														
7026203																																	7026203																																														
NEXT ASSY																																	USED ON (SYS)																																														

THIRD ANGLE PROJECTION

DWG CLASS

FIRST USED ON 7026203

CHECKED BY E. P. H. 14 AUG 02

APVD FOR MFG DATE

APVD FOR ENG DATE

APVD FOR DATE

CONTRACT VDR

DRAWN BY A. WHIDDEN DATE 12 APR 01

DESIGNED BY DATE

OPERATIONAL NOTE

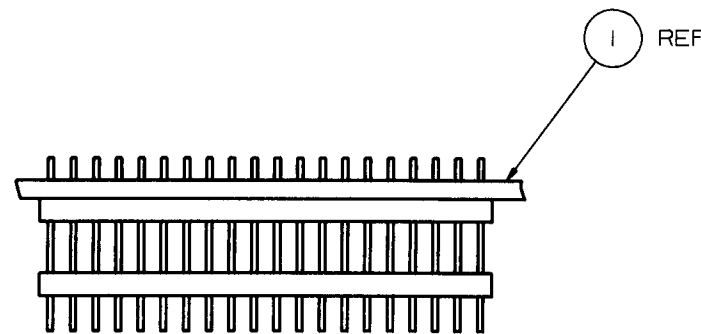
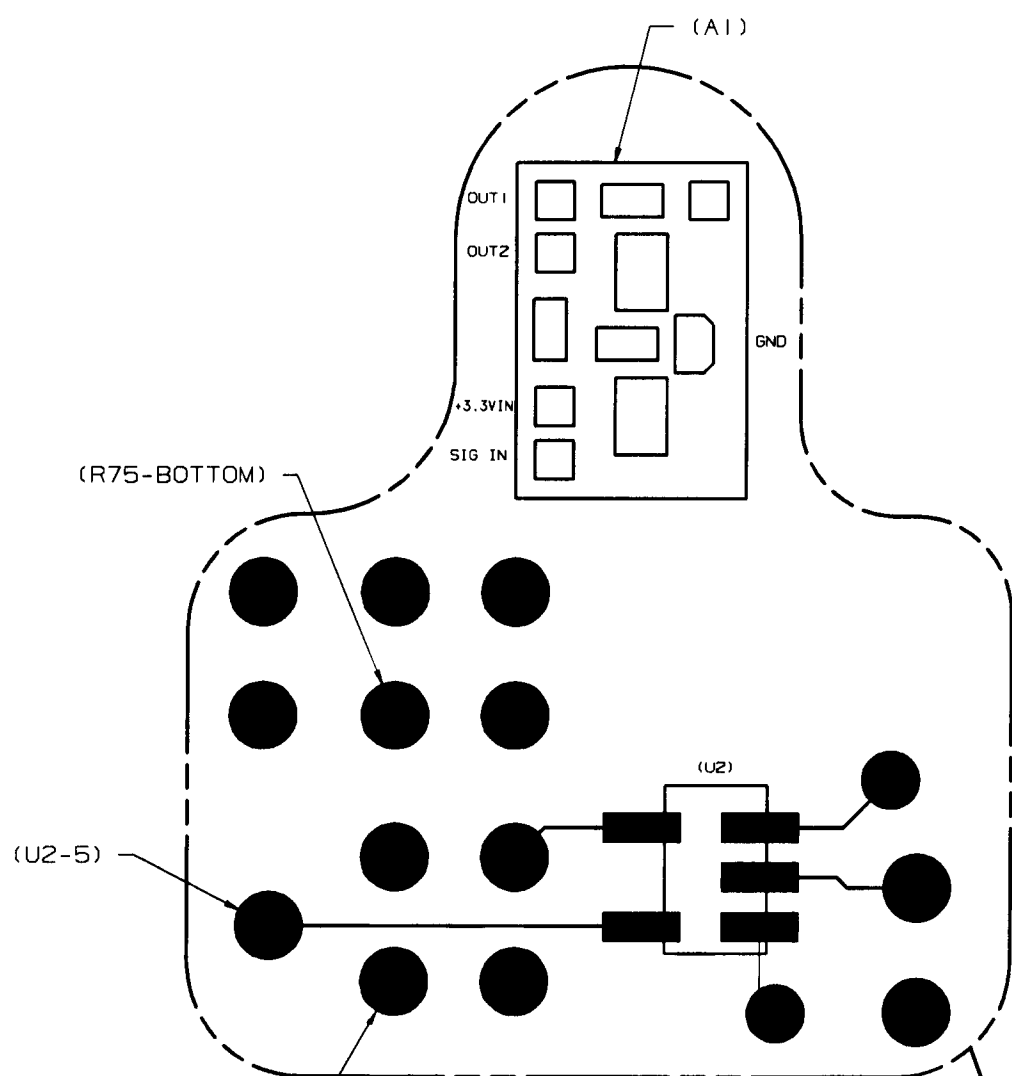
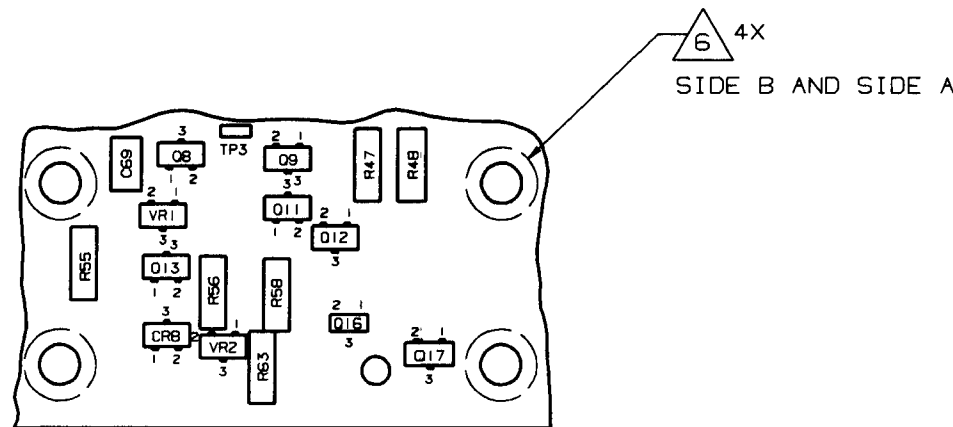
SIMILAR ASSEMBLY

Honeywell COMMERCIAL FLIGHT SYSTEMS GROUP PHOENIX, ARIZONA

CIRCUIT CARD ASSEMBLY- POWER SUPPLY, CPU

SHEET 1 OF 30

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XC	ADD NOTE 10, 11, 12, 13, 17, 18 CALLOUT. DIM: ADD .355±.020. UPDTD VIEW.	08NOV01
	XD	ADD NOTE 19, 20 CALLOUT. UOTD VIEW.	20NOV01
	XF	PER MARKED PRINT.	01MAR02
	XJ	EXTENSIVELY REVISED.	
	XK	EXTENSIVELY REVISED.	

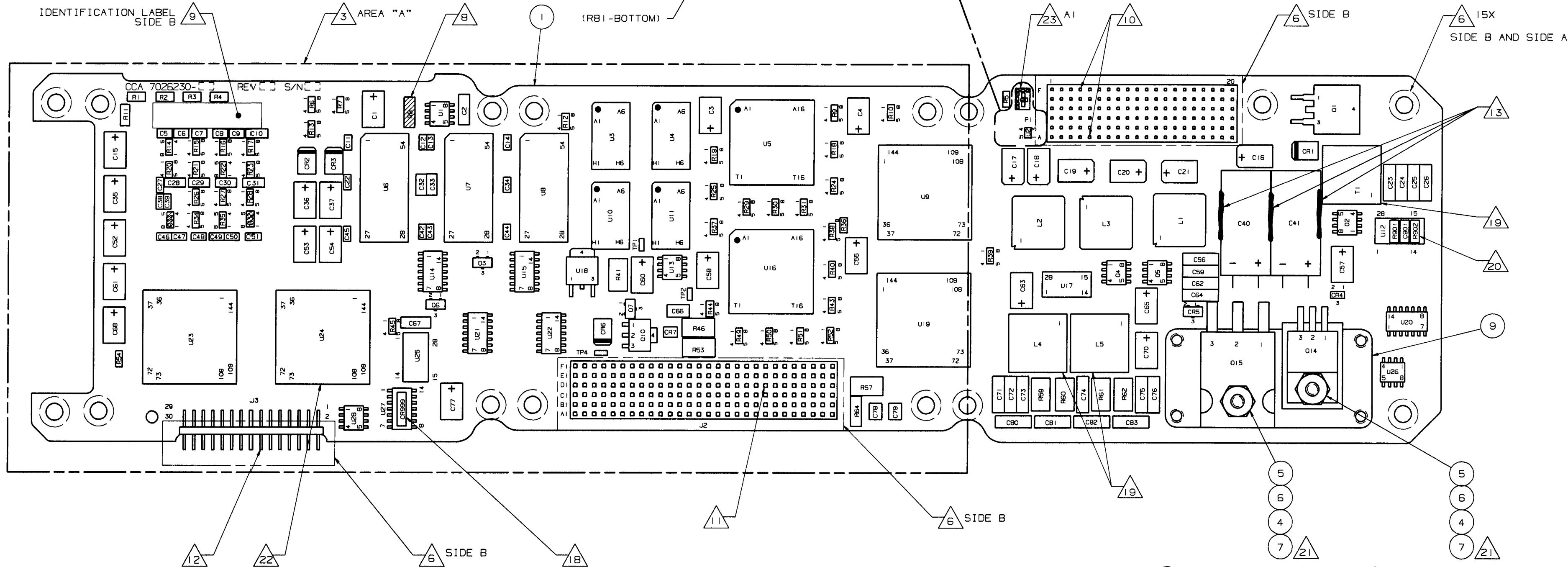


IDENTIFICATION LABEL SIDE B

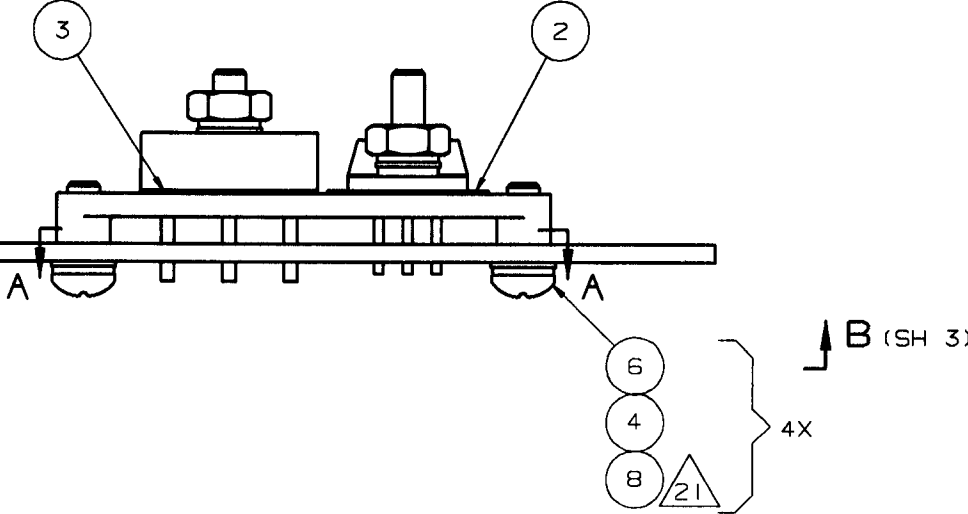
9

3 AREA "A"

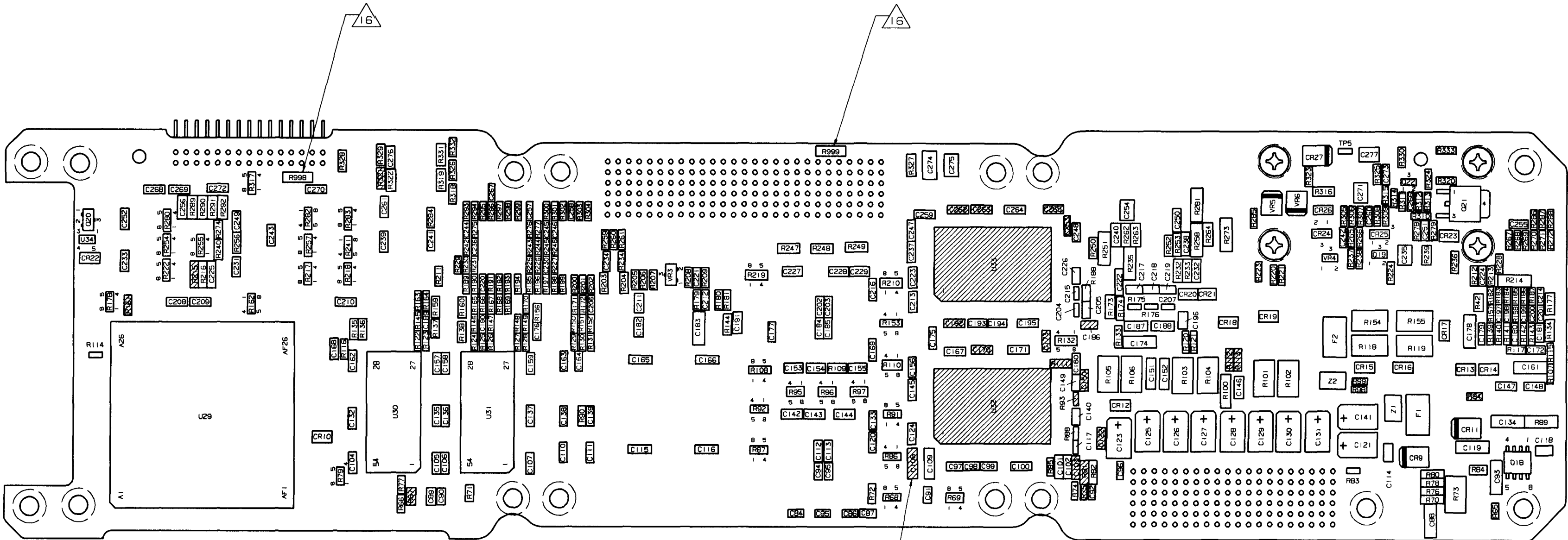
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VIEWING SIDE B



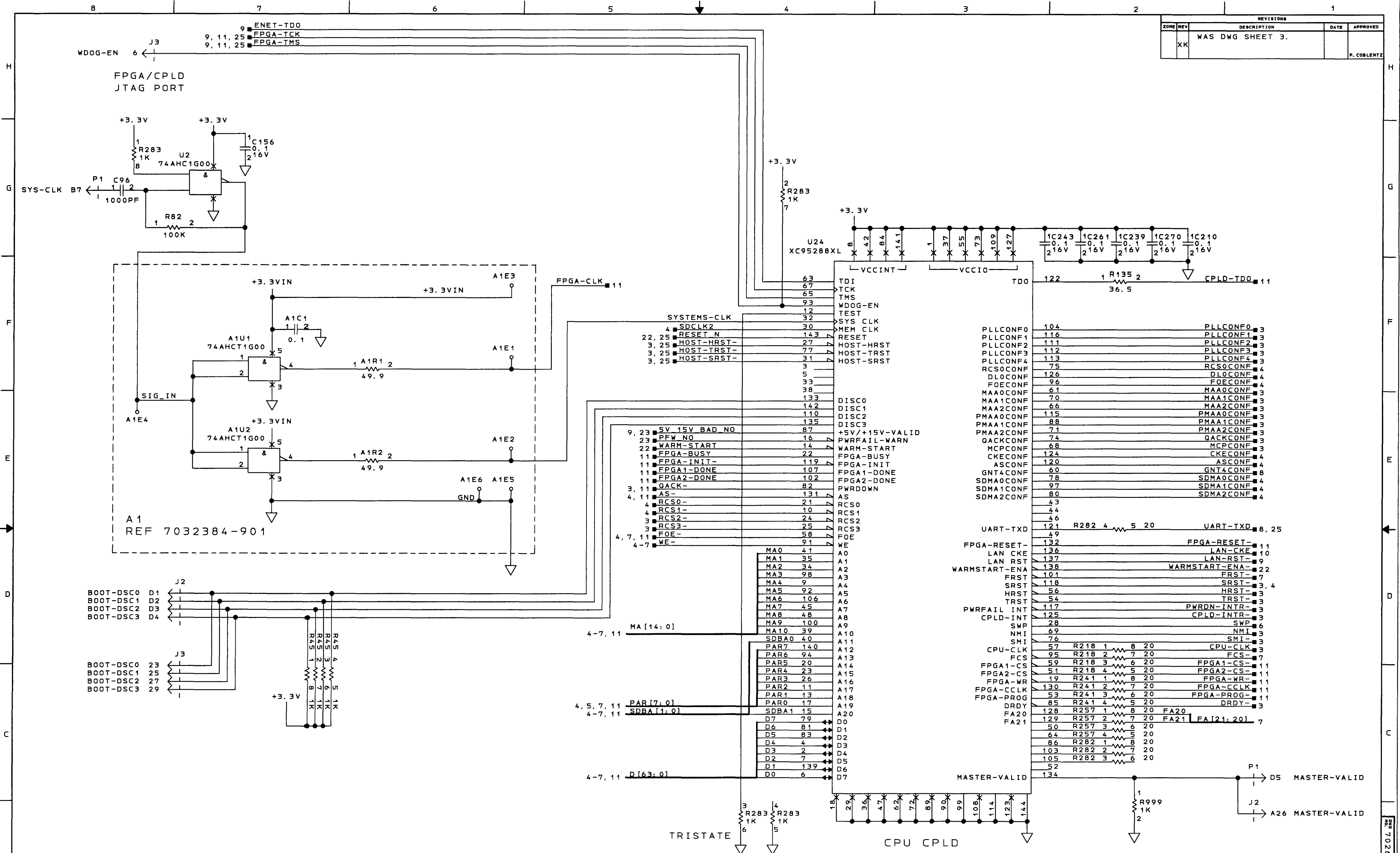
REVISIONS			
ZONE	REV	DESCRIPTION	DATE
XK		ADD THIS SHEET.	
			A. WHIDDEN



VIEWING SIDE A

VIEW B-B (SH 2)

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
XK		WAS DWG SHEET 3.	
			P. COBLENTZ



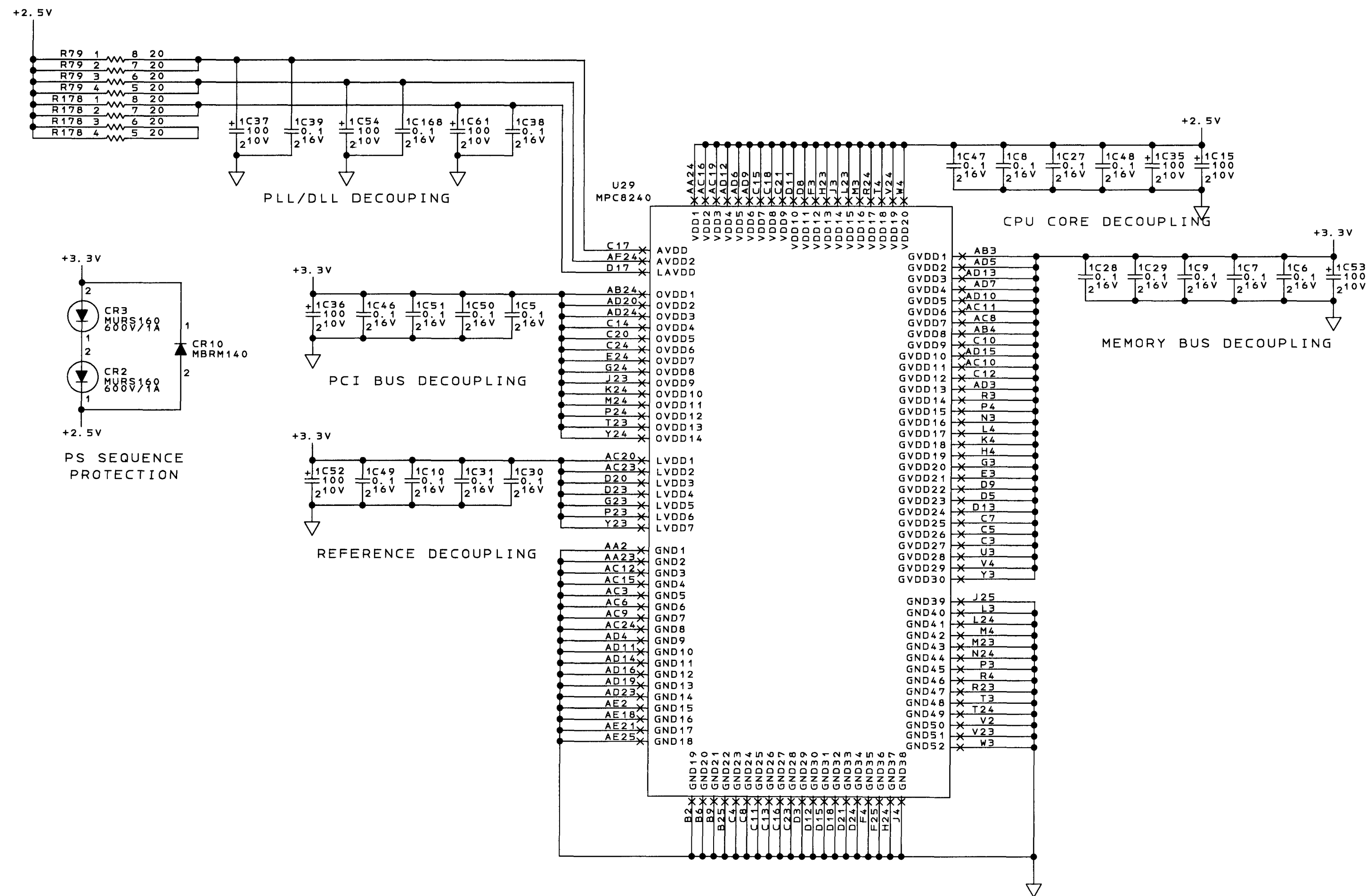
NOTES:
 1. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 2. ALL RESISTANCE VALUES ARE IN OHMS.
 3. ALL INDUCTANCE VALUES ARE IN MICROHENRIES.
 4. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN;
 FOR COMPLETE DESIGNATIONS PREFIX PART
 DESIGNATION WITH SUBASSEMBLY DESIGNATION.
 5. INTERPRET DRAWING PER ANSI Y32.2-1975 AND
 ANSI/IEEE STD 91-1986. LOGIC SYMBOLS
 CONFORM TO ANSI/IEEE STD 91-1984.

PROCESSOR
 MPC8240 CONTROL LOGIC
 SCHEMATIC DIAGRAM SHEET NO. 1 OF 27

COMPUTER GENERATED

7-25-2002 15:56	PAUL COBLENTZ	SIZE	CAGE CODE	DRAWING NO.	REV
Honeywell		E	55939	7026230-902	XK
		SCALE			SHEET 4

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
XK		WAS DWG SHEET 4.		P. COBLENTZ

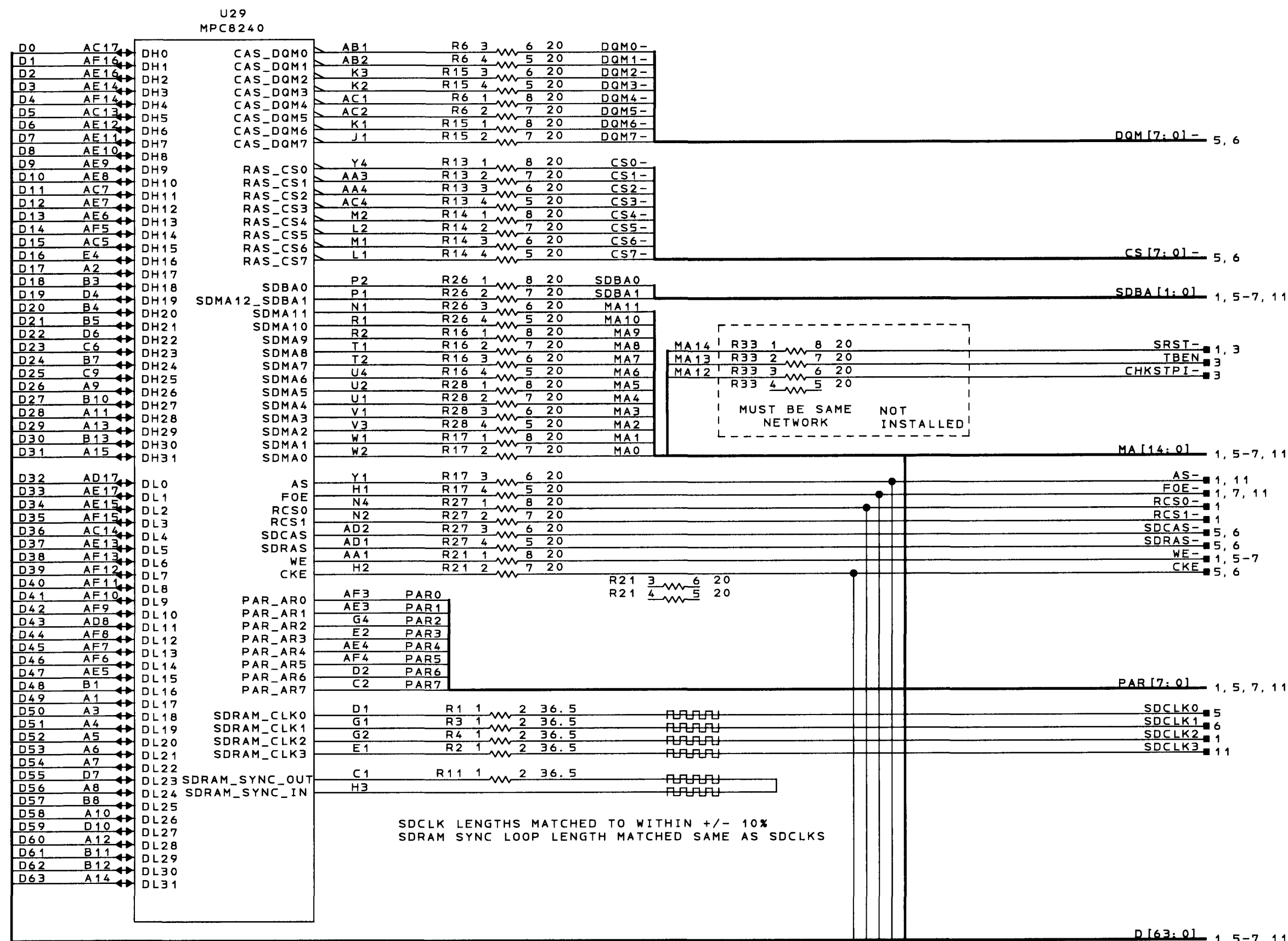


PROCESSOR
MPC8240 PROCESSOR POWER
SCHEMATIC DIAGRAM SHEET NO. 2

COMPUTER GENERATED

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		SCALE			SHEET 5

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 6.	
			P. COBLENTZ



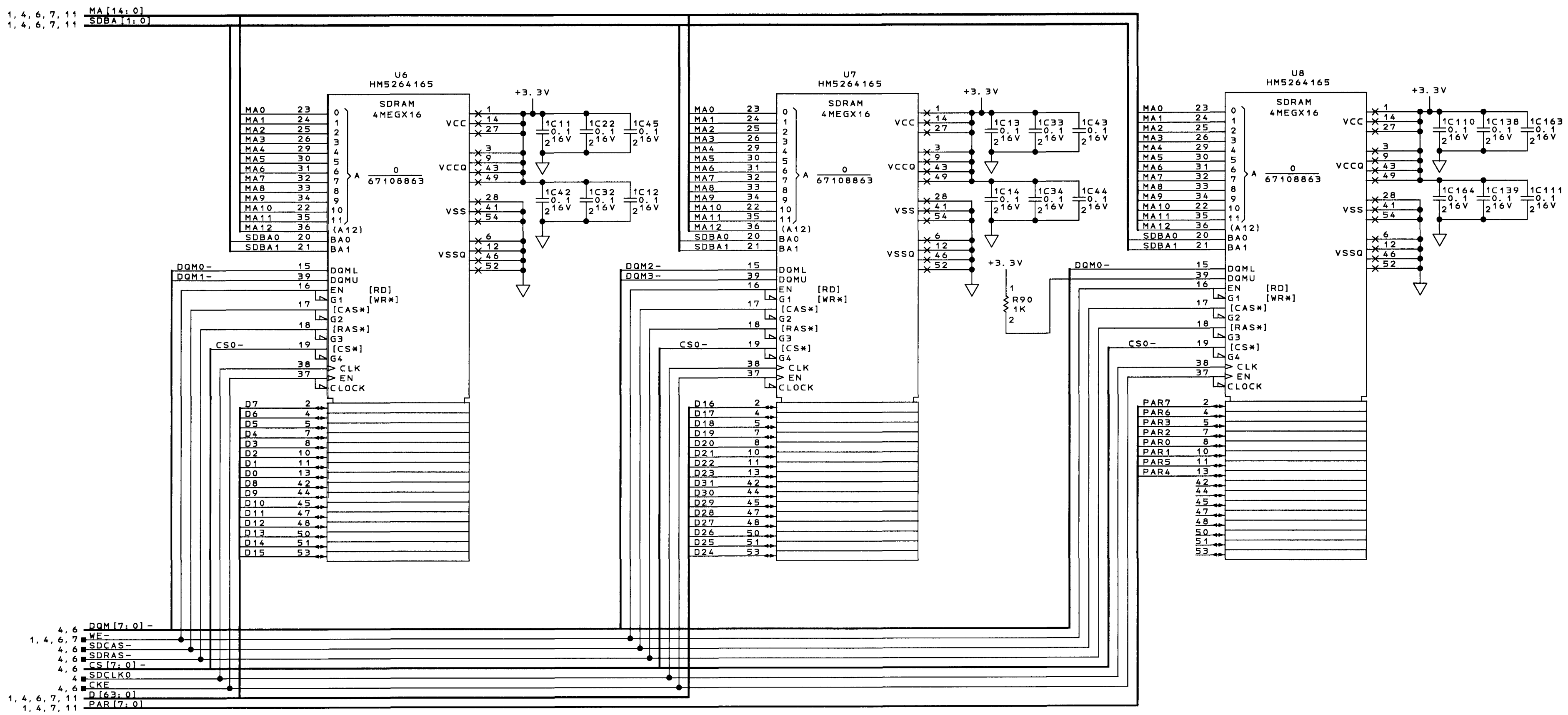
- 1 CKECONF
- 1 RCS0CONF
- 1 FOECONF
- 1 ASCONF
- 1 SDMA0CONF
- 1 SDMA1CONF
- 1 SDMA2CONF
- 1 DLOCONF

PROCESSOR
MPC8240 MEMORY INTERFACE
SCHEMATIC DIAGRAM SHEET NO. 4

COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	REV	7
Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER, USE ON DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	E 55939
SCALE		DRAWING NO.	7026230-902
		SHEET	7

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 7.	
			P. COBLENTZ

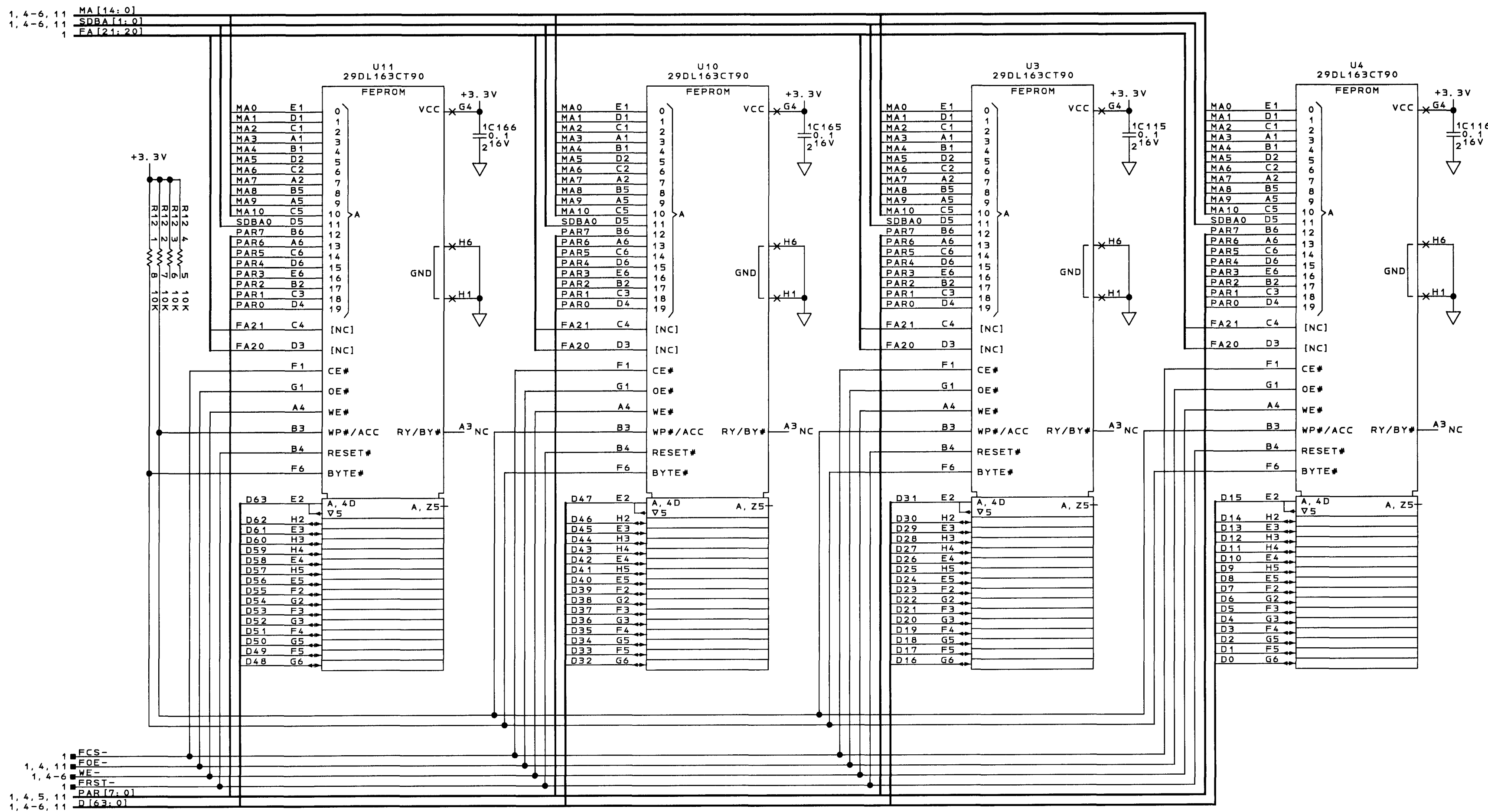


PROCESSOR
SDRAM MEMORY
SCHEMATIC DIAGRAM SHEET NO. 5

COMPUTER GENERATED

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Honeywell				SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.		SCALE	
				SHEET 8			

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 9.	
			P. COBLENTZ

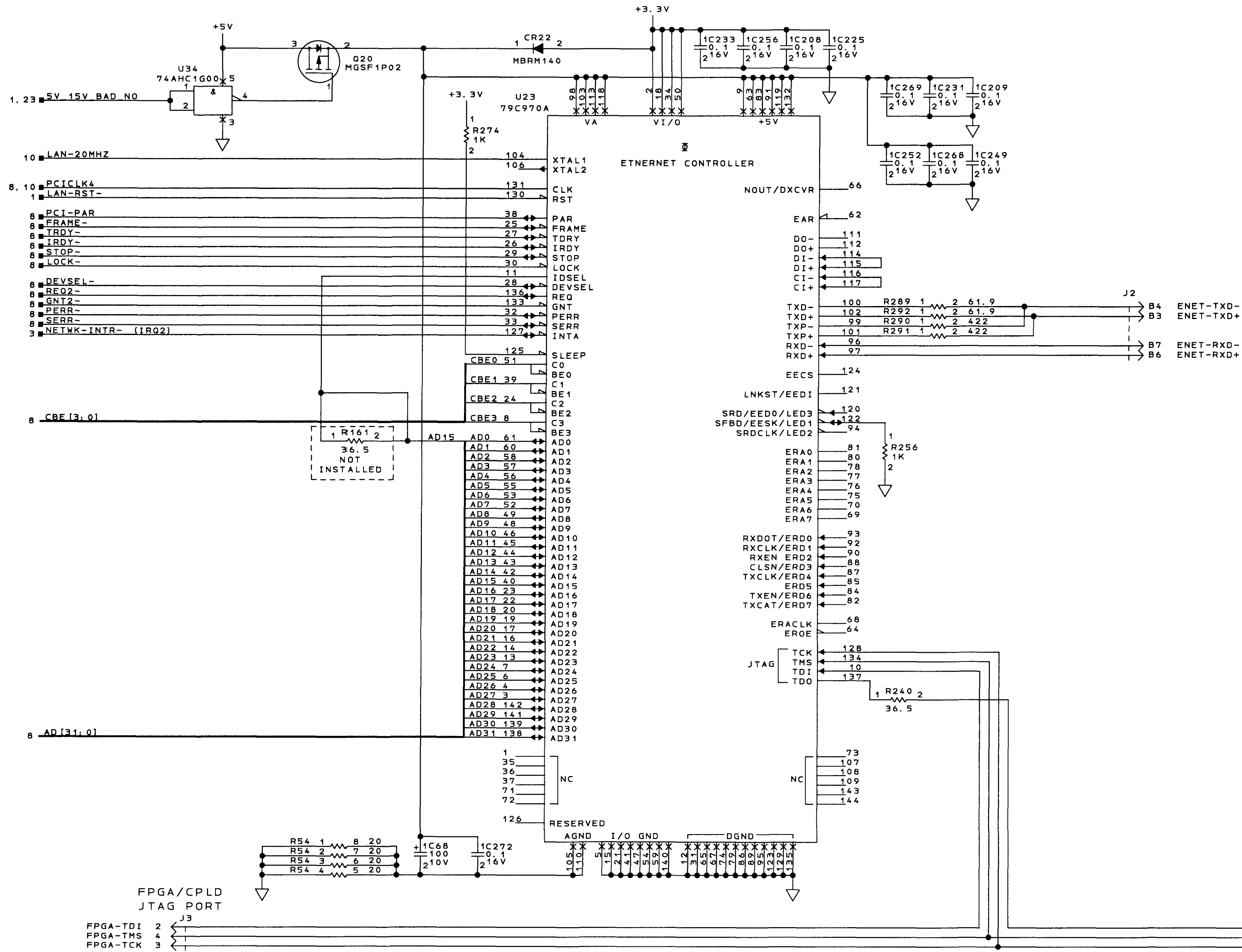


PROCESSOR
 FLASH MEMORY
 SCHEMATIC DIAGRAM SHEET NO. 7

COMPUTER GENERATED

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Honeywell		SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.		SIZE		CAGE CODE		DRAWING NO.	
				E 55939		7026230-902		REV	
				SCALE —				SHEET 10	

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	XX	WAS DWG SHEET 11.		P. COBLENTZ

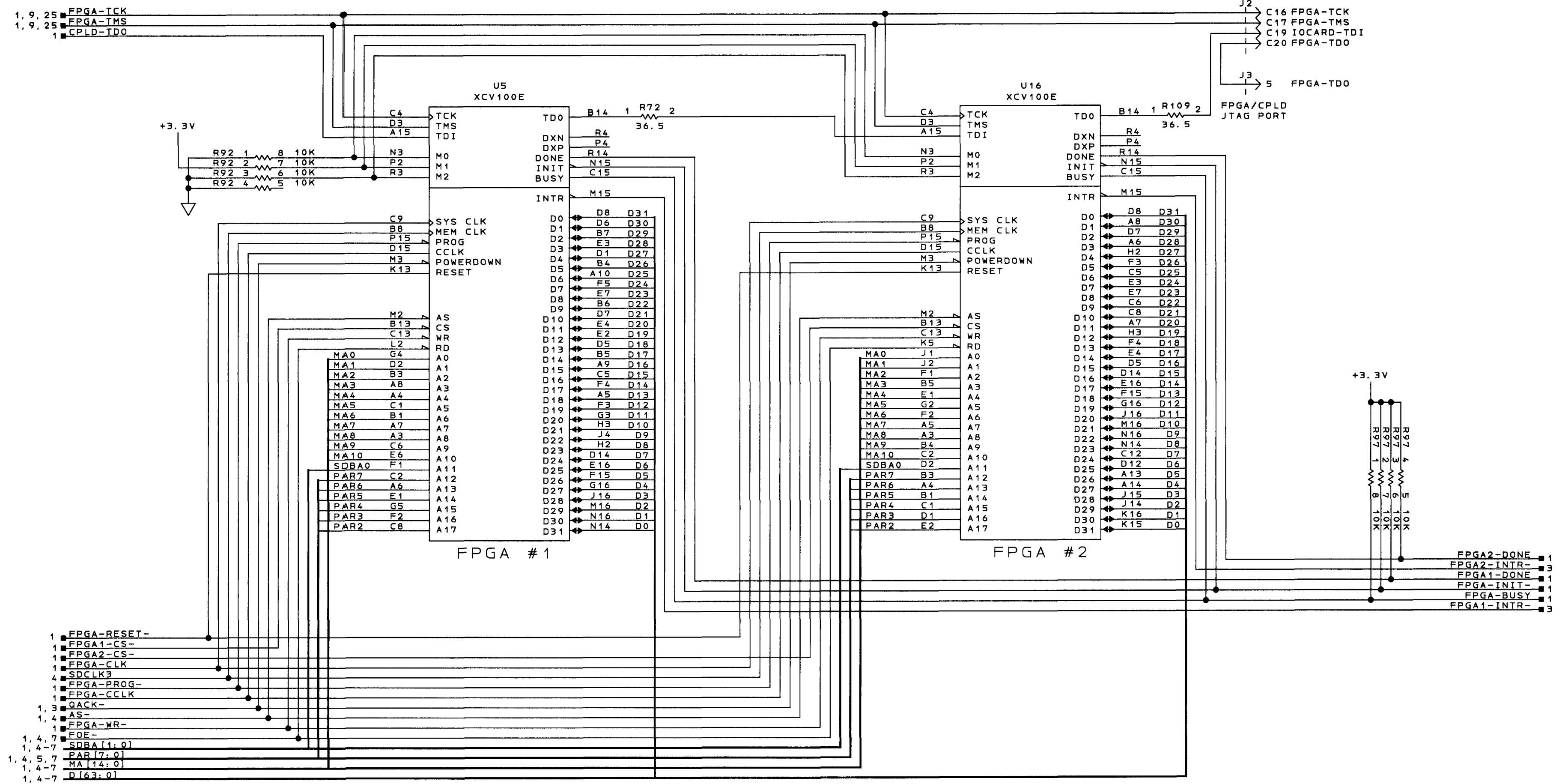


PROCESSOR
NETWORK INTERFACE
SCHEMATIC DIAGRAM SHEET NO. 9

COMPUTER GENERATED

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		E 55939	7026230-902
		SCALE —	SHEET 12

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
XK		WAS DWG SHEET 13.		P. COBLENTZ

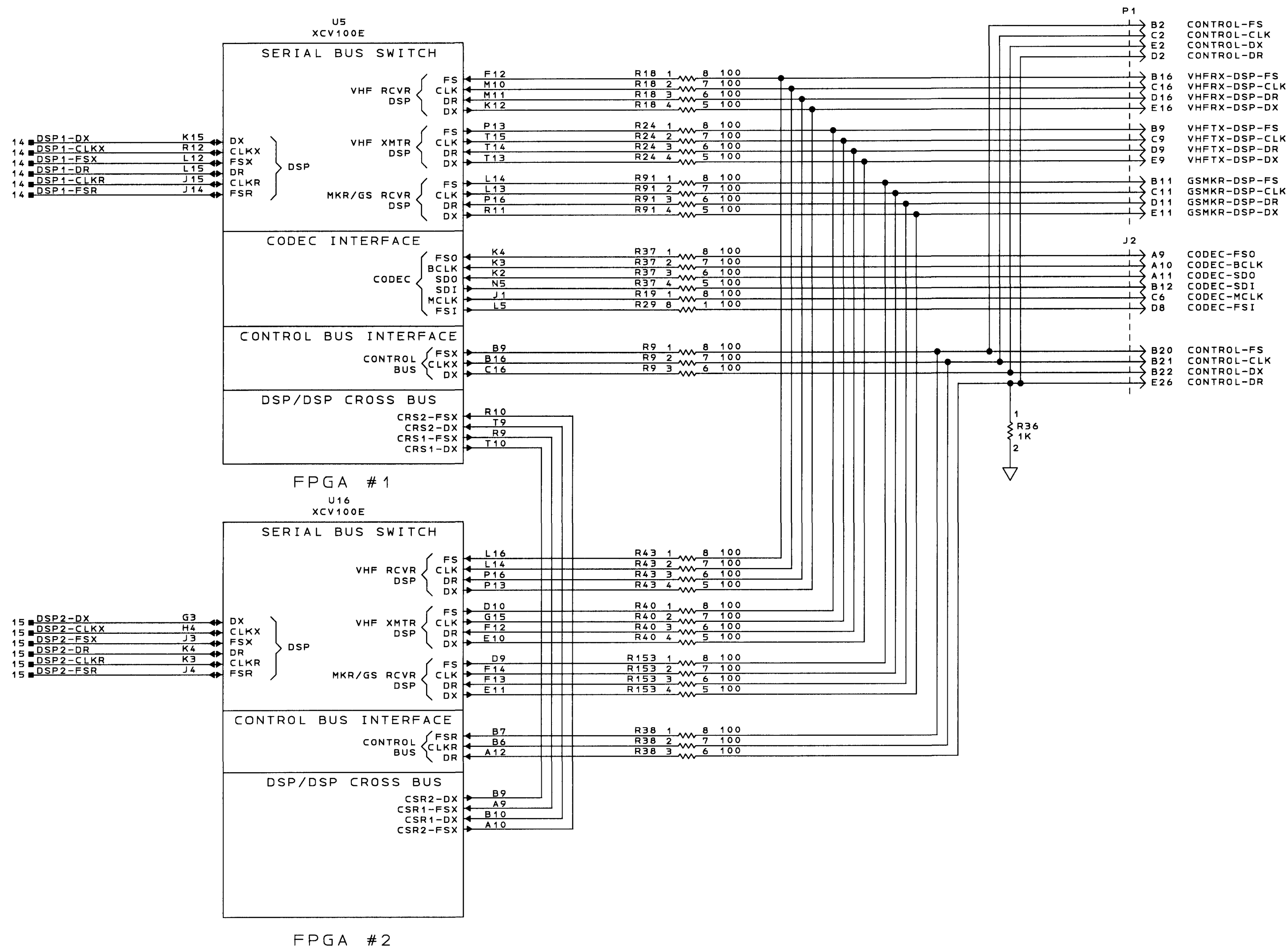


POWERPC FPGA INTERFACE
SCHEMATIC DIAGRAM SHEET NO. 11

COMPUTER GENERATED

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Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	E	55939	7026230-902	XK
		SCALE		SHEET 14	

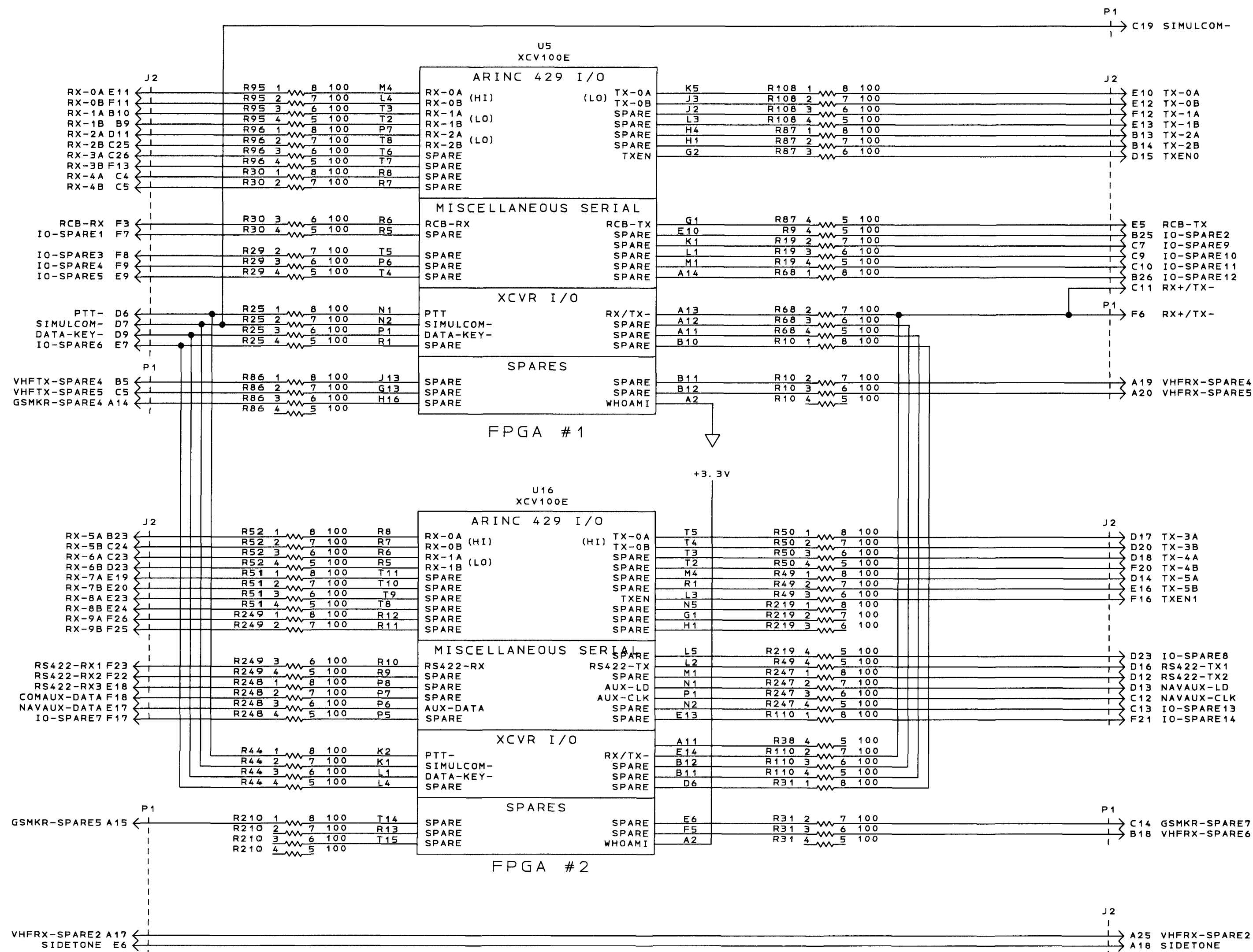
REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 14.	
			P. COBLENTZ



SERIAL BUS INTERFACE
SCHEMATIC DIAGRAM SHEET NO. 12

COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	SIZE	CAGE CODE	DRAWING NO.	REV
Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	E	55939	7026230-902	XK
SCALE		SHEET		15	

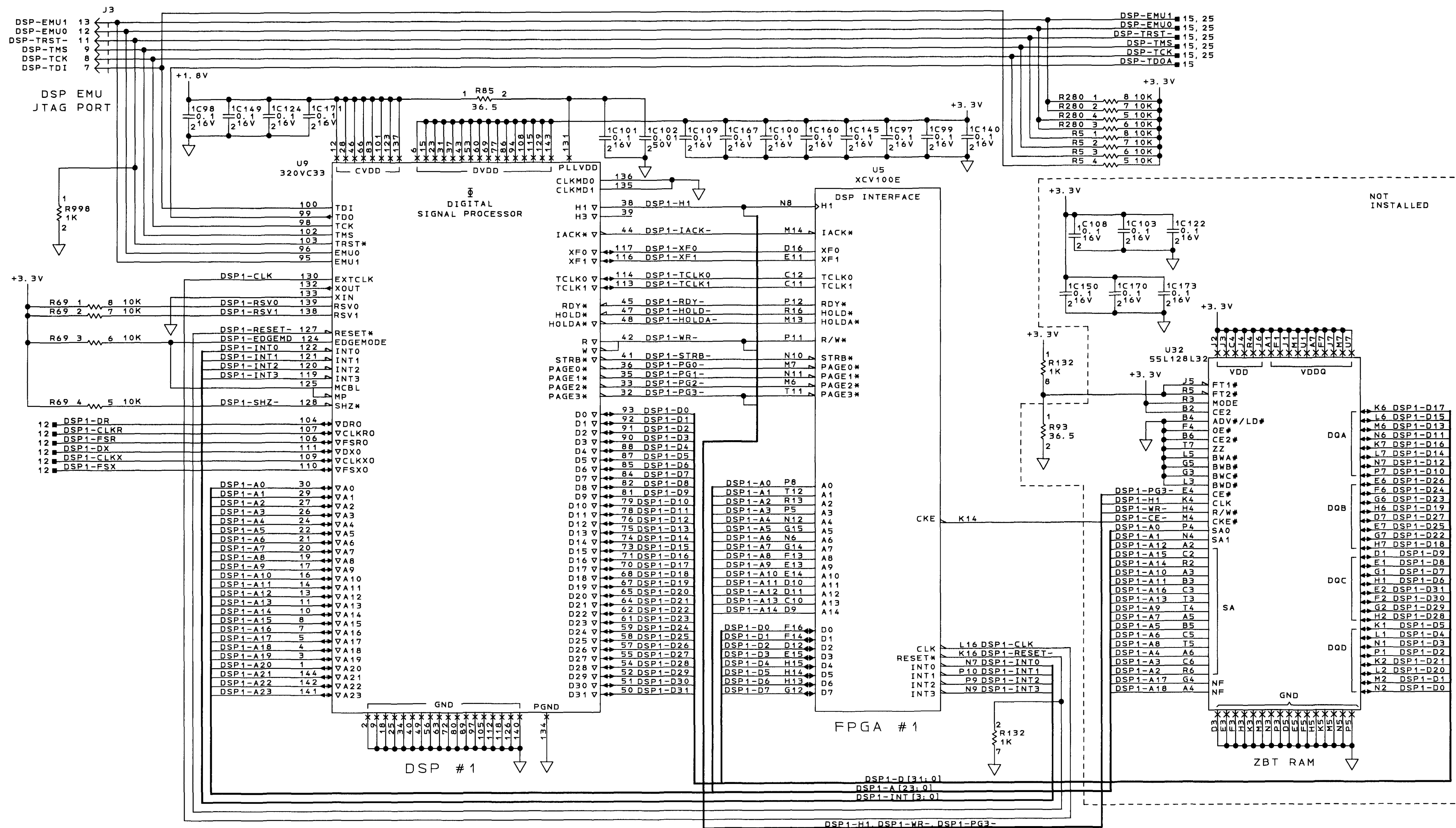


MKR/GS DIGITAL SIGNAL PROC
 MISC I/O AND INTERCONNECT
 SCHEMATIC DIAGRAM SHEET NO. 13

COMPUTER GENERATED

7-25-2002 15:58		PAUL COBLENTZ			
Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OF DISCLOSURE INFORMATION IN THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
		E	55939	7026230-902	XX
		SCALE			SHEET 16

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
XK		WAS DWG SHEET 16.	
			P. COBLENTZ



DSP1 DIGITAL SIGNAL PROCESSOR
SCHEMATIC DIAGRAM SHEET NO. 14

COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	SIZE	E 55939	CAGE CODE	7026230-902	DRAWING NO.	REV
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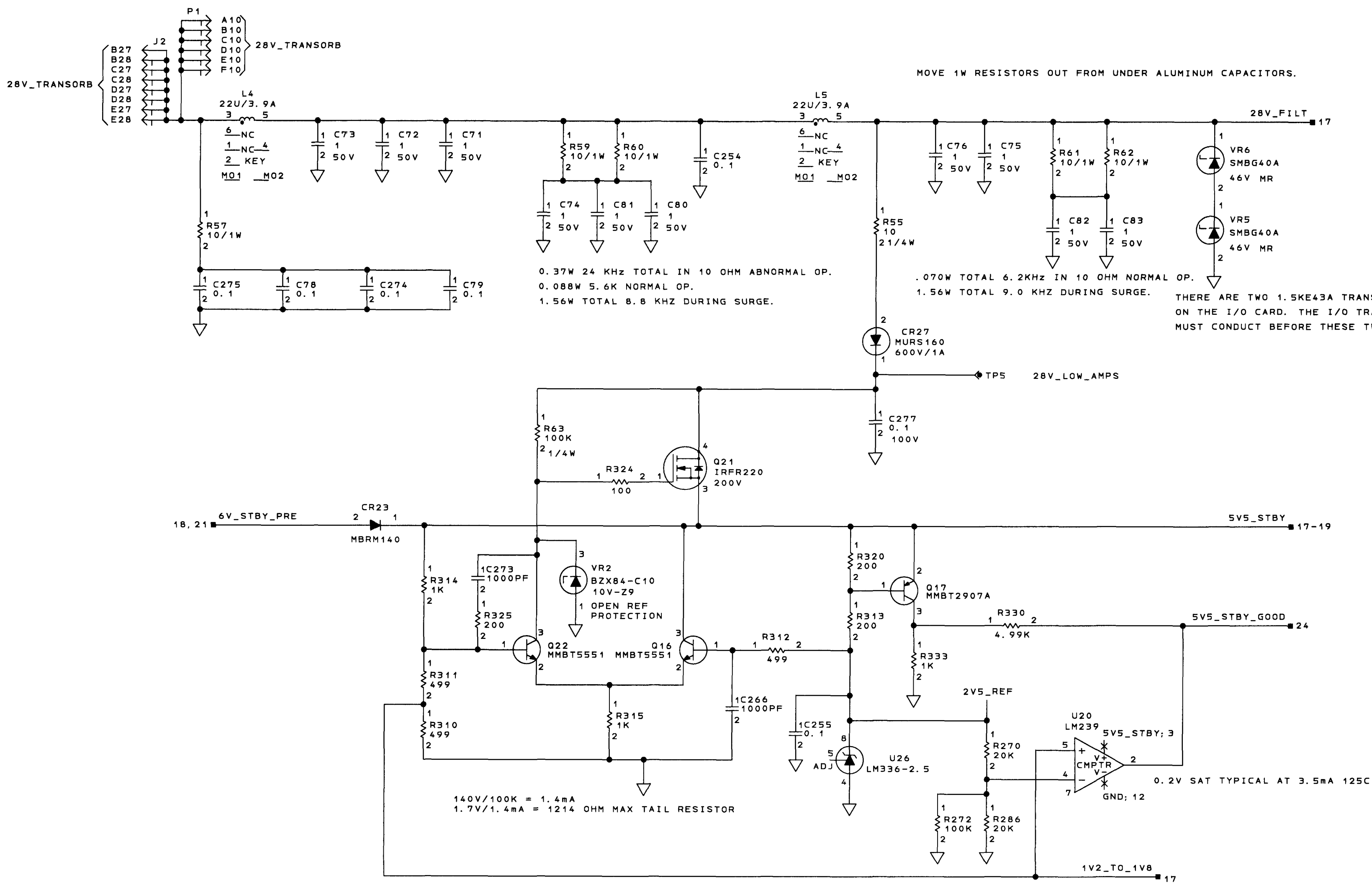
A vertical number line with points labeled A, B, C, D, E, F, G, and H from bottom to top. Horizontal tick marks are present at each point. An arrow points to point D from the left.



COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
		E	55939	7026230-902	XX
		SCALE	SHEET		18

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 18.	
			P. COBLENTZ



20mW 1.8V 5% = 178 OHM, 3.3V 5% = 600 ohm, 2.5V 5% = 344, -5V 10% = 1.51k, -15V 10% = 13.6k, 32V = 51.2k

POWER SUPPLY
EMI FILTER, LIGHTNING SUPPR., 5V STBY
SCHEMATIC DIAGRAM SHEET NO. 16

COMPUTER GENERATED

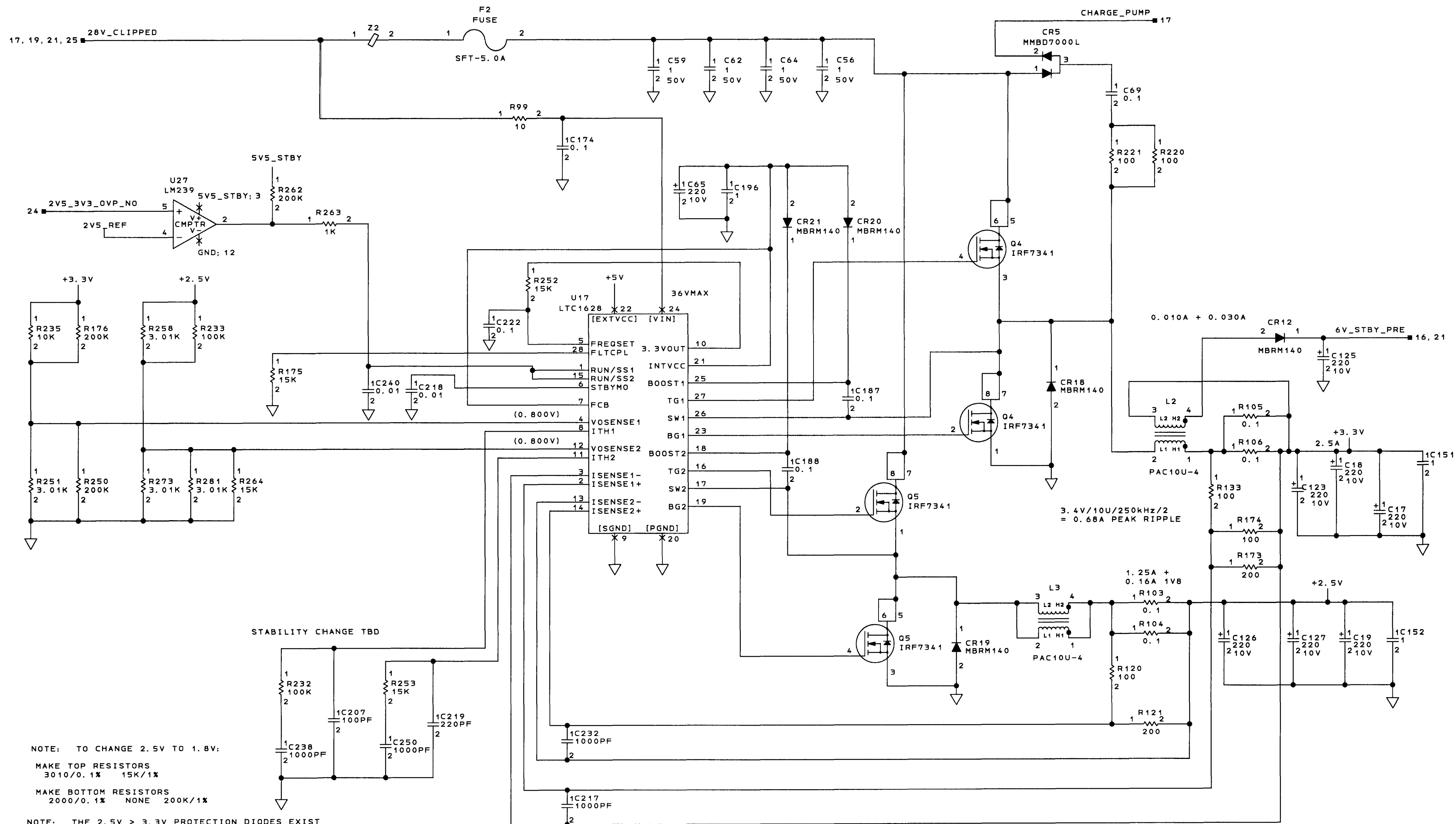
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Honeywell		E	55939	7026230-902	XK
SEE FIRST SHEET FOR CONTRACT NUMBER.		USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.		SCALE	
				SHEET 19	

A vertical timeline diagram showing the sequence of events from A to H. The events are labeled A, B, C, D, E, F, G, and H from bottom to top. A horizontal arrow points to event D, indicating it is the current time step.



8221-641 (REV 4-80) HONEYWELL INC 8
(ANNI 714.1-1980)

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 20.	
			P. COBLENTZ

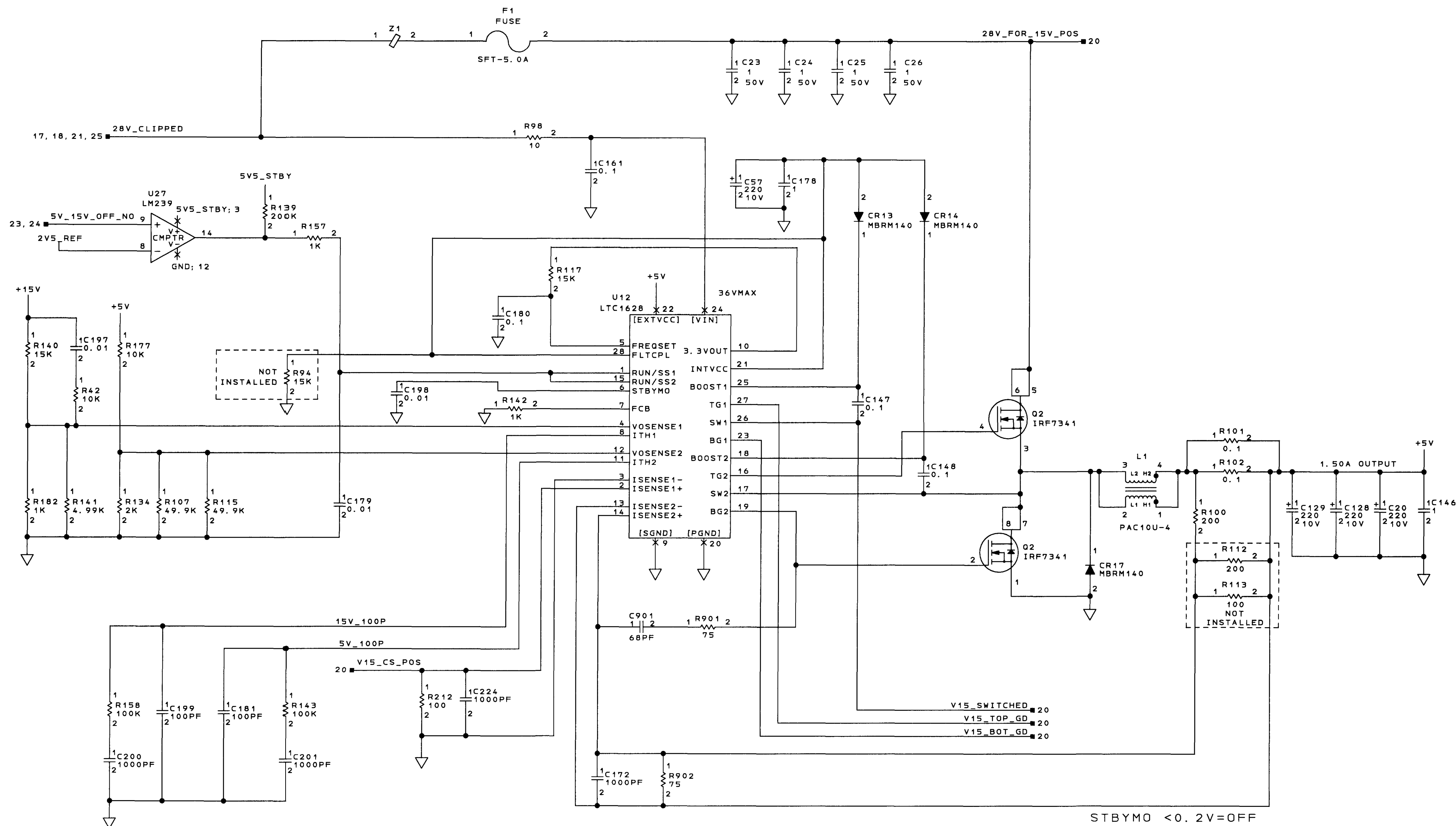


POWER SUPPLY
+3.3V, +2.5V AND V6_STBY OUTPUTS
SCHEMATIC DIAGRAM SHEET NO. 18

COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	SIZE	CAGE CODE	DRAWING NO.	REV
Honeywell		E	55939	7026230-902	XK
		SCALE		SHEET	21

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	XK	WAS DWG SHEET 21.		
				P. COBLENTZ



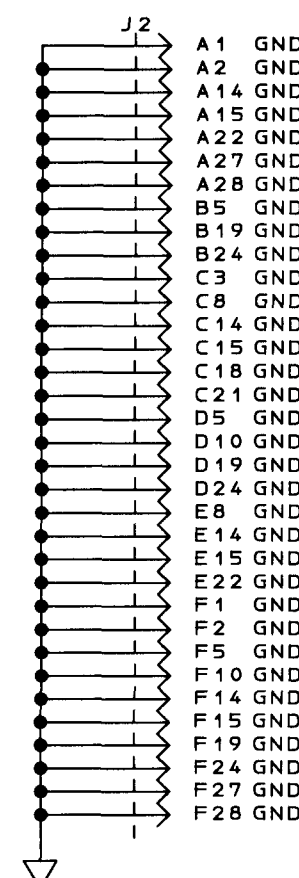
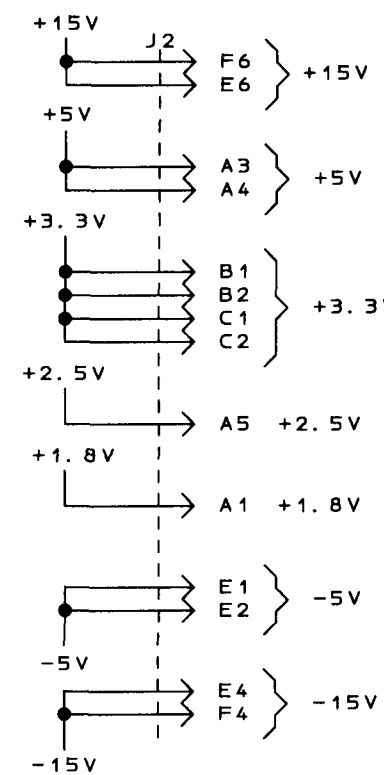
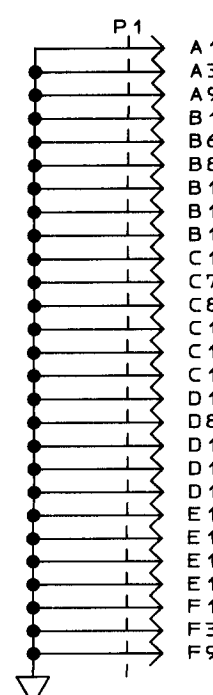
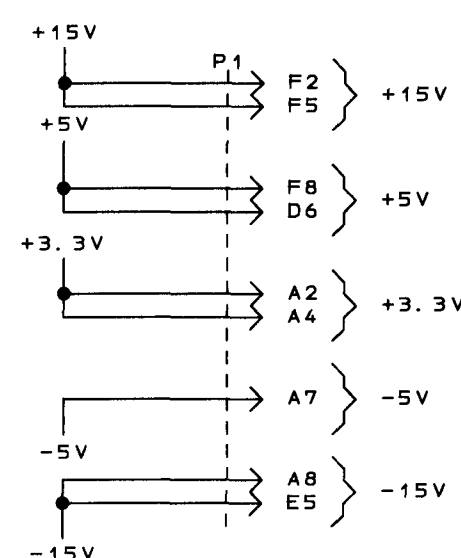
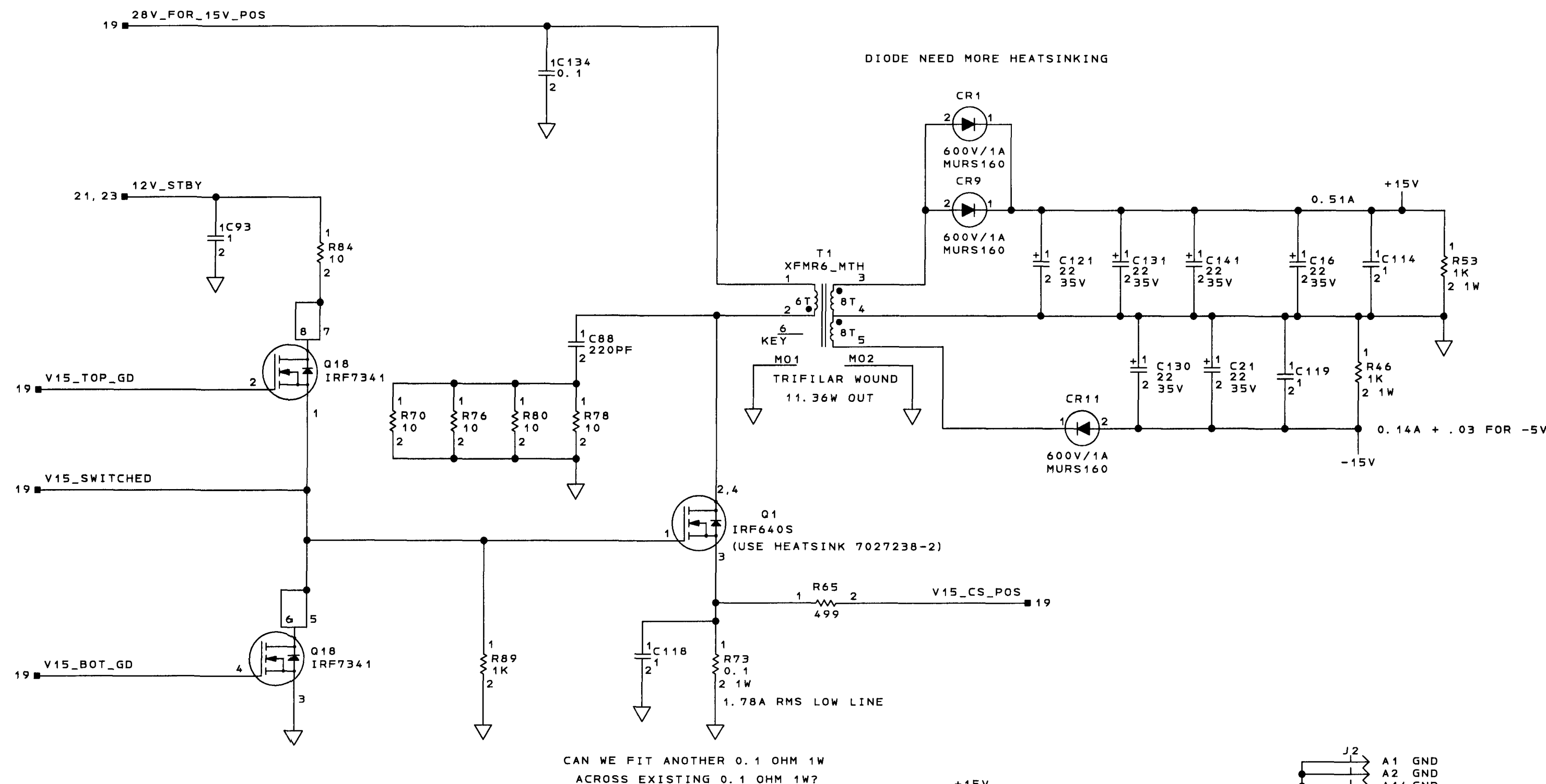
STBYM0 <0.2V=OFF
 RUN/SS 1&2 <1.0V=OFF

POWER SUPPLY
 +5V OUTPUT AND +5V/+15V/-15V CONTROL
 SCHEMATIC DIAGRAM SHEET NO. 19

COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	SIZE	E 55939	DRAWING NO.	7026230-902	REV	XK
Honeywell		SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.		SCALE			SHEET 22

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 22.	
			P. COBLENTZ

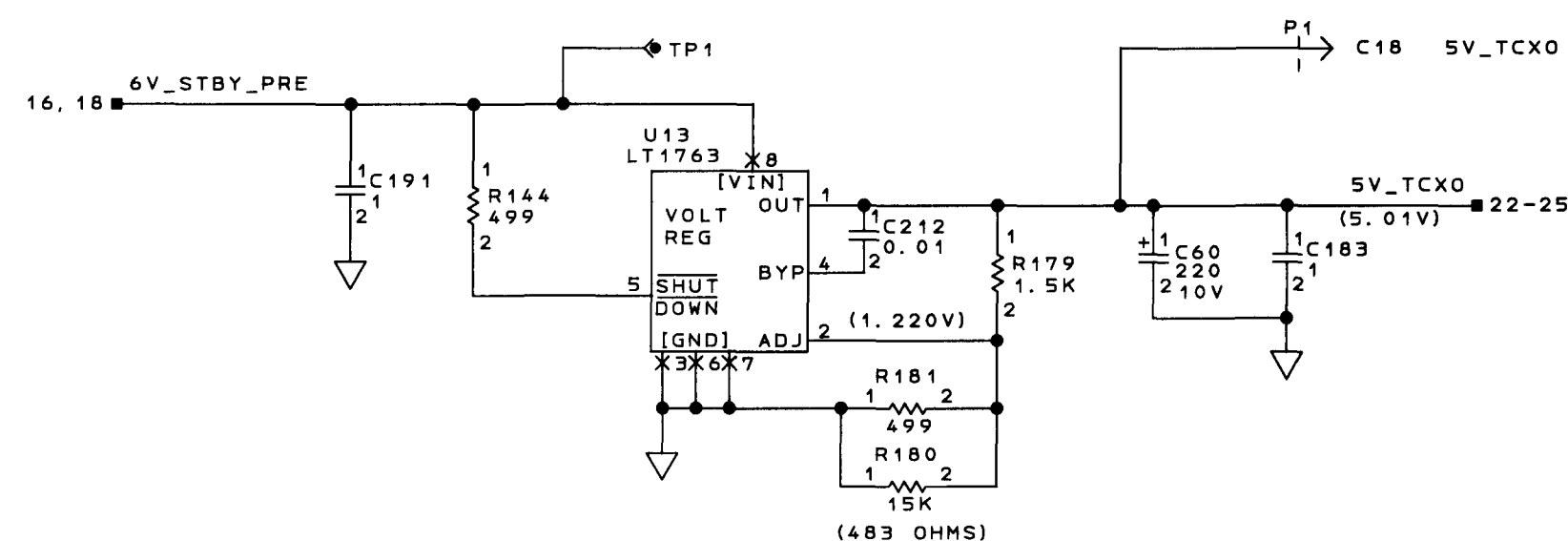
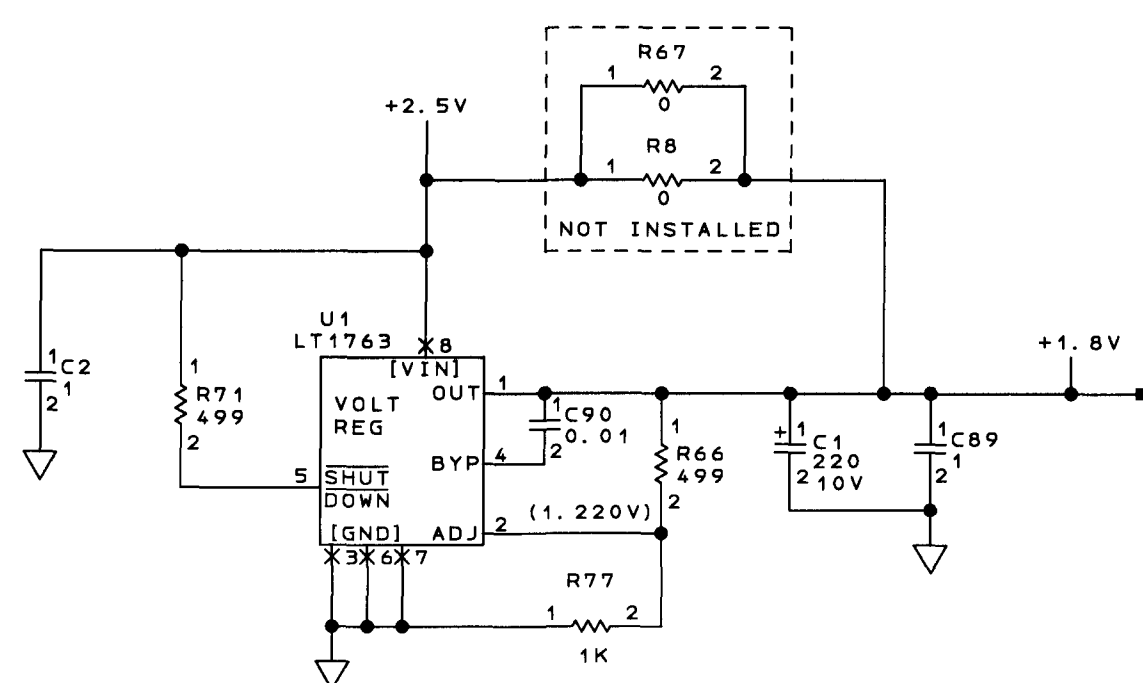
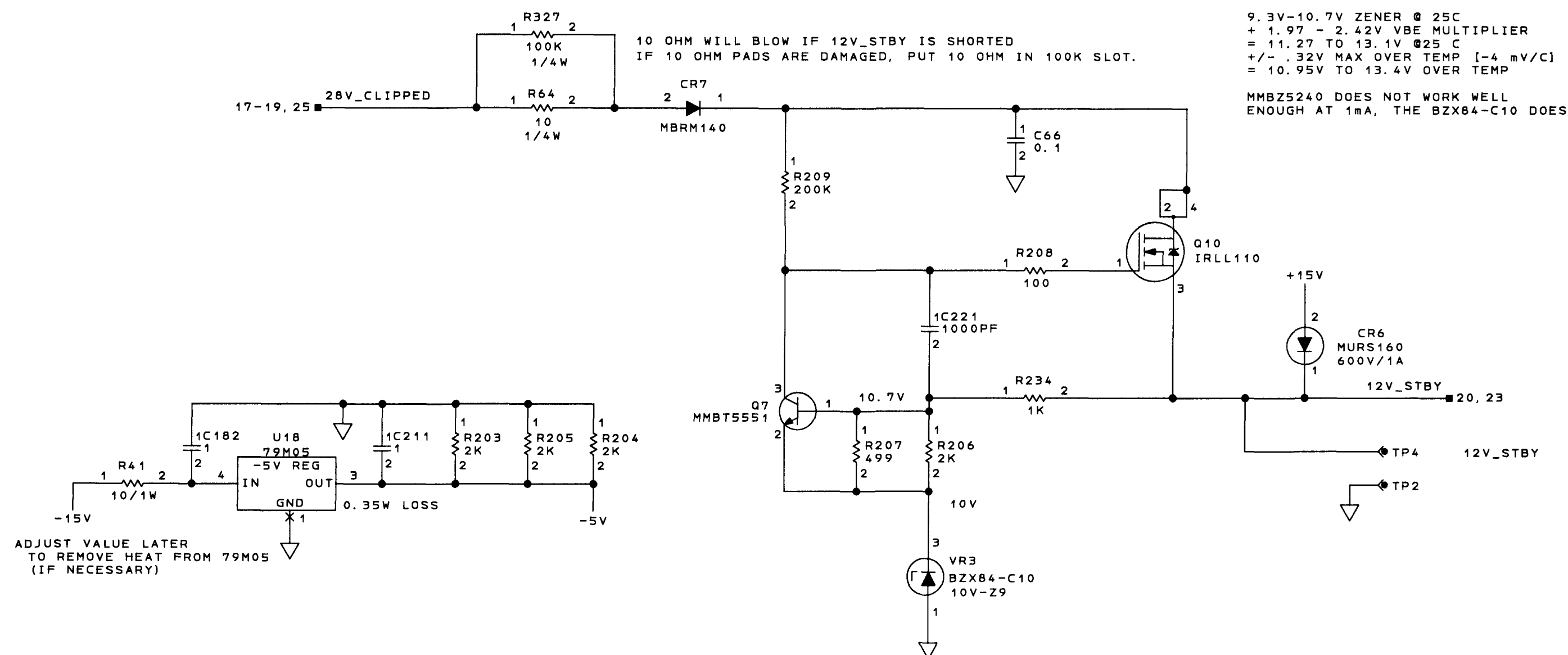


POWER SUPPLY
+15V AND -15V OUTPUTS
SCHEMATIC DIAGRAM SHEET NO. 20

COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	SIZE	E	55939	DRAWING NO.	7026230-902	REV	XK
Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SCALE					SHEET	23

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 23.	
			P. COBLENTZ

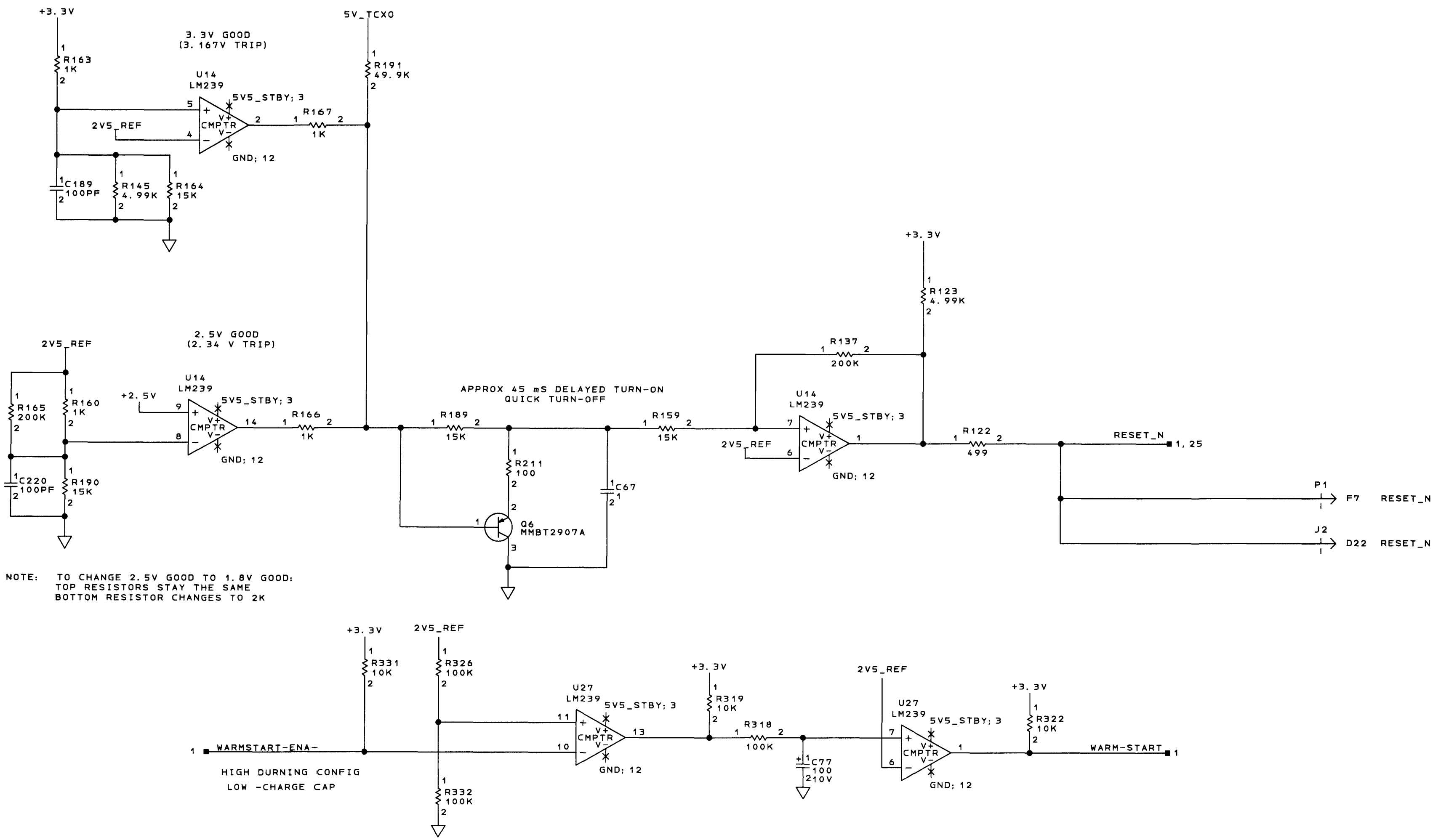


POWER SUPPLY
5V_OSC, 1.8V, -5V AND 12V_STBY OUTPUTS
SCHEMATIC DIAGRAM SHEET NO. 21

COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	SIZE	E 55939	DRAWING NO.	7026230-902	REV	XK
Honeywell		SEE FIRST SHEET FOR CONTRACT NUMBER. THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.		SCALE		SHEET 24	

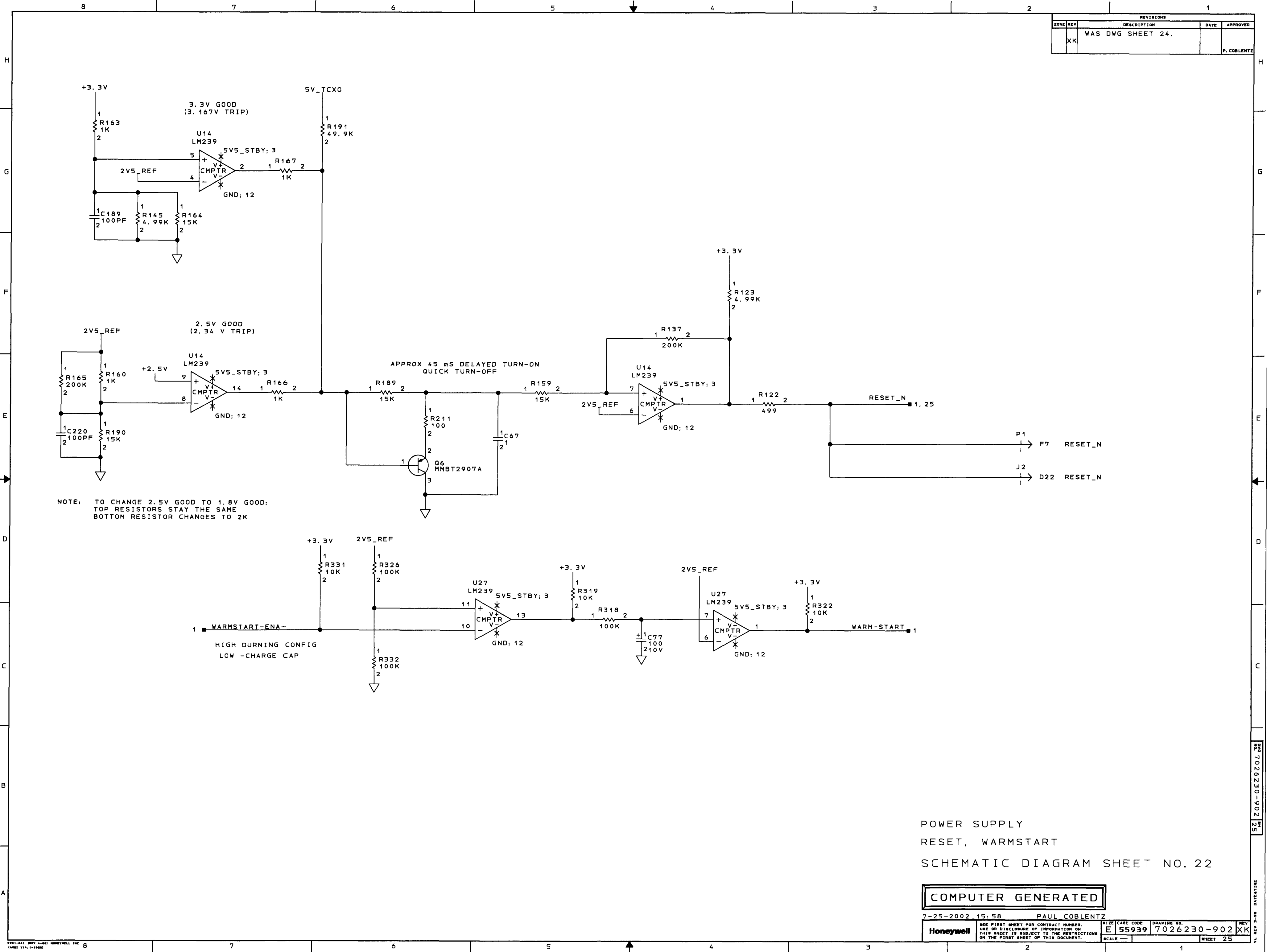
REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	XK	WAS DWG SHEET 24.		P. COBLENTZ



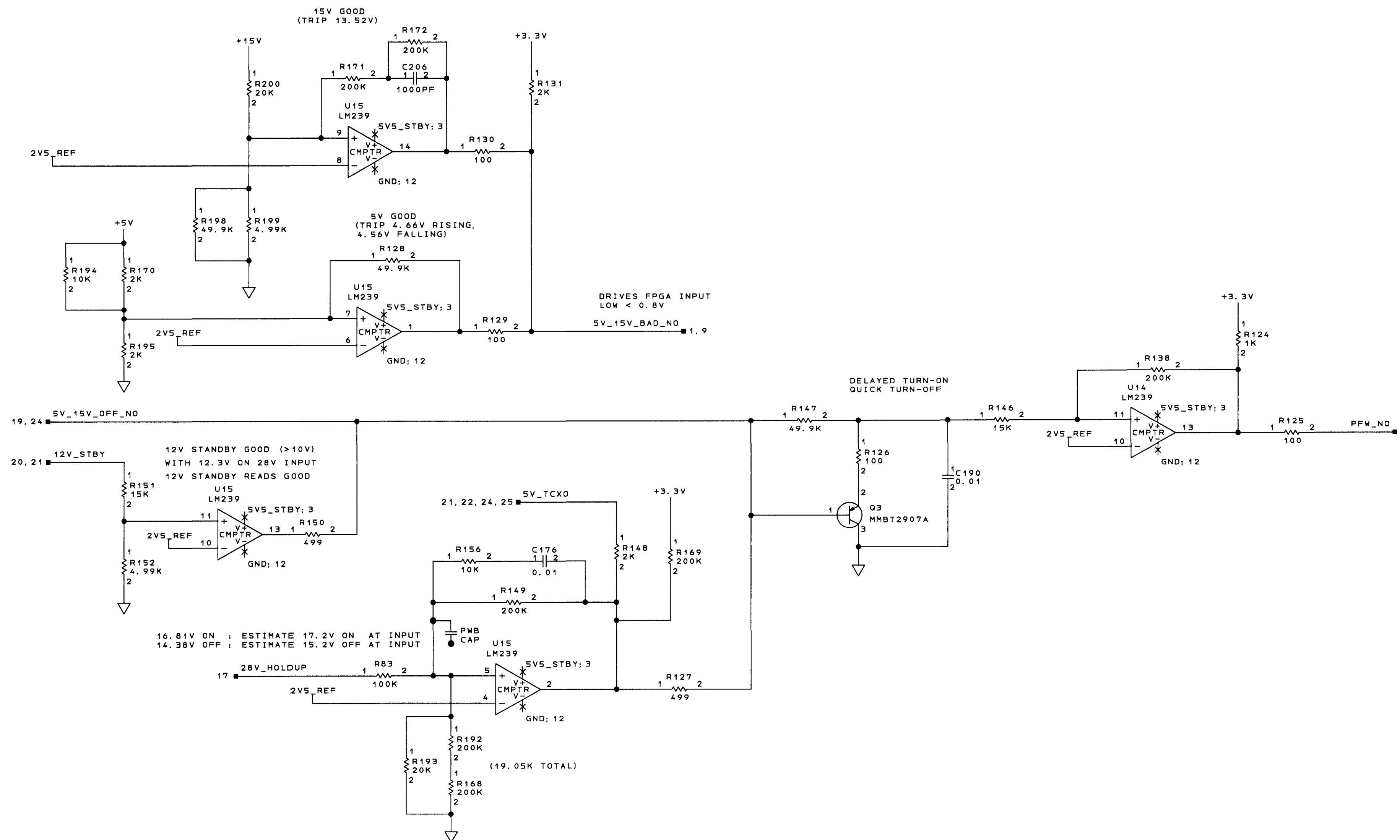
POWER SUPPLY
RESET, WARMSTART
SCHEMATIC DIAGRAM SHEET NO. 22

COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	SIZE	E 55939	DRAWING NO.	7026230-902	REV	XK
Honeywell		SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.		SCALE		SHEET 25	



REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 25.	
			APPROVED
			P. COBLENTZ

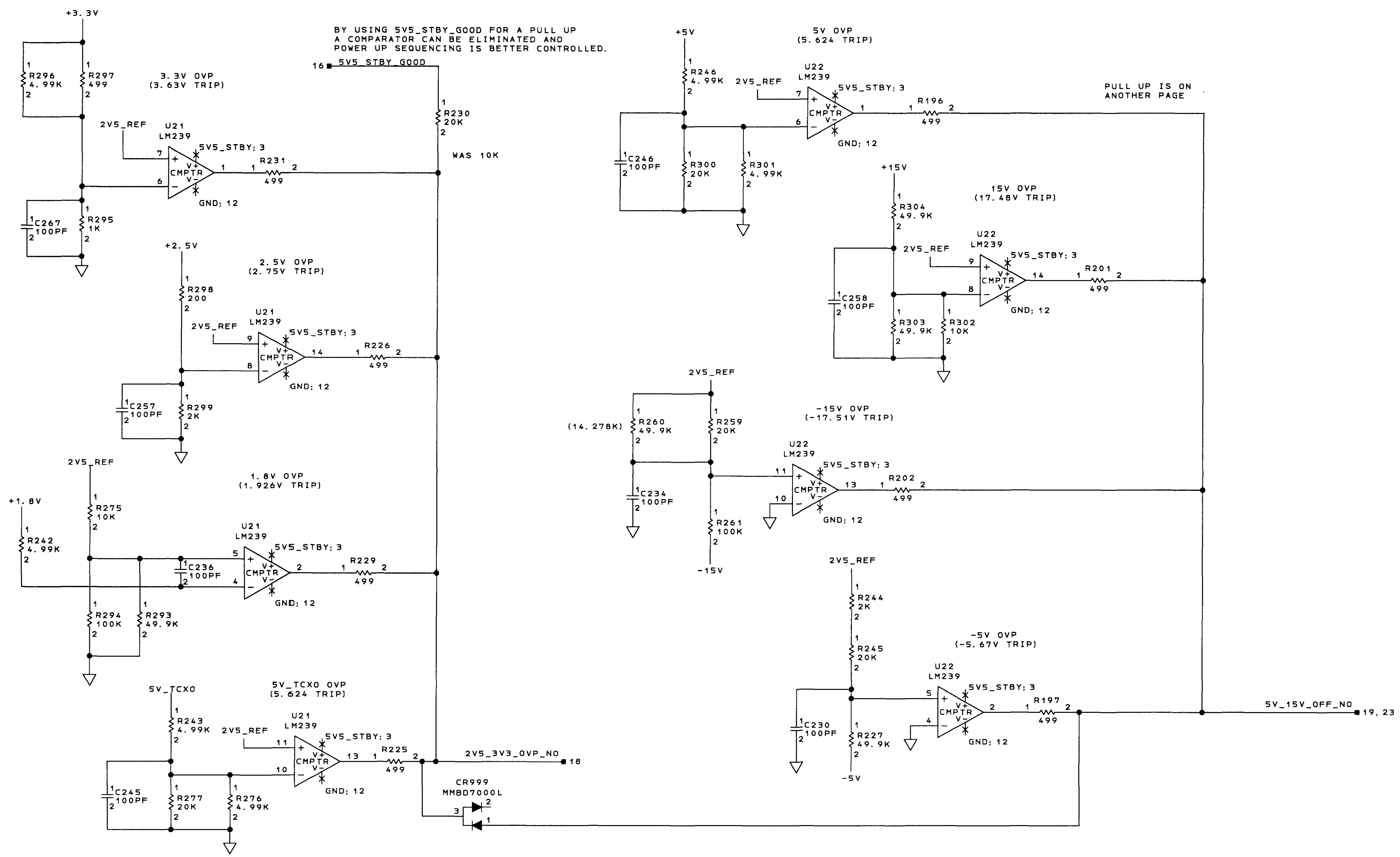


POWER SUPPLY
5V/15V MONITOR AND POWER FAIL WARNING
SCHEMATIC DIAGRAM SHEET NO. 23

COMPUTER GENERATED

7-25-2002 15:56	PAUL COBLENTZ	SIZE	CAGE CODE	DRAWING NO.	REV
Honeywell		E 55939	7026230-902	XK	
		SCALE	SHEET 26		

REVISIONS		DATE	APPROVED
ZONE	REV		
XK			P. COBLENTZ



5V_TCX0 INSTANCE 4 on U6

POWER SUPPLY
STBY GOOD AND OVERVOLTAGE PROTECTION
SCHEMATIC DIAGRAM SHEET NO. 24

COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	SIZE	CAGE CODE	DRAWING NO.	REV
Honeywell		E	55939	7026230-902	XK
SEE FIRST SHEET FOR CONTRACT NUMBER, USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.		SCALE		SHEET	27

REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 27.	
			P. COBLENTZ

J3		CON30	
SH3	GND	1	2
	FPGA-TCK	3	4
	FPGA-TDO	5	6
	DSP-TDI	7	8
	DSP-TMS	9	10
	DSP-TRST-	11	12
	DSP-EMU1	13	14
	UART-TXD	15	16
	HOST-TCK	17	18
	HOST-TDO	19	20
	HOST-HRST-	21	22
	BOOT-DSC0	23	24
	BOOT-DSC1	25	26
	BOOT-DSC2	27	28
	BOOT-DSC3	29	30
			FPGA-TDI
			FPGA-TMS
			WDG-EN
			DSP-TCK
			DSP-TDO
			DSP-EMU0
			POLARIZATION
			HOST-TDI
			HOST-TMS
			HOST-TRST-
			HOST-SRST-
			UART-RXD
			+3.3V
			FPGA-TRST-
			GND
			NC
			SH3

JTAG PORT

P1		CON6X20A	
	GND	A1	
	+3.3V	A2	
	GND	A3	
	+3.3V	A4	
	POLARIZATION	A5	
	-5V	A6	
	-15V	A7	
	GND	A8	
	GND	A9	
	28V TRANSORB	A10	
		A11	
		A12	
		A13	
	GSMKR-SPARE4	A14	
	GSMKR-SPARE5	A15	
		A16	
	VHFRX-SPARE2	A17	
		A18	
	VHFRX-SPARE4	A19	
	VHFRX-SPARE5	A20	

P1		CON6X20A	
	GND	B1	
	CONTROL-FS	B2	
		B3	
		B4	
	VHFTX-SPARE4	B5	
	GND	B6	
	SYS-CLK	B7	
	GND	B8	
	VHFTX-DSP-FS	B9	
	28V TRANSORB	B10	
	GSMKR-DSP-FS	B11	
	GND	B12	
		B13	
		B14	
	GND	B15	
	VHFRX-DSP-FS	B16	
	GND	B17	
	VHFRX-SPARE6	B18	
		B19	
		B20	

P1		CON6X20A	
	GND	C1	
	CONTROL-CLK	C2	
		C3	
		C4	
	VHFTX-SPARE5	C5	
	60V BOOTSTRAP	C6	
	GND	C7	
	GND	C8	
	VHFTX-DSP-CLK	C9	
	28V TRANSORB	C10	
	GSMKR-DSP-CLK	C11	
	GND	C12	
		C13	
	GSMKR-SPARE7	C14	
	GND	C15	
	VHFRX-DSP-CLK	C16	
	GND	C17	
	5V TCXO	C18	
	SIMULCOM-	C19	
		C20	

P1		CON6X20A	
	GND	D1	
	CONTROL-DR	D2	
		D3	
		D4	
	MASTER-VALID	D5	
	+5V	D6	
	GND	D7	
	GND	D8	
	VHFTX-DSP-DR	D9	
	28V TRANSORB	D10	
	GSMKR-DSP-DR	D11	
	GND	D12	
		D13	
		D14	
	GND	D15	
	VHFRX-DSP-DR	D16	
	GND	D17	
		D18	
		D19	
		D20	

P1		CON6X20A	
	GND	E1	
	CONTROL-DX	E2	
		E3	
		E4	
	-15V	E5	
	SIDETONE	E6	
		E7	
	VHFTX-DSP-DX	E8	
	28V TRANSORB	E9	
	GSMKR-DSP-DX	E10	
	GND	E11	
		E12	
		E13	
		E14	
	GND	E15	
	VHFRX-DSP-DX	E16	
	GND	E17	
		E18	
		E19	
		E20	

P1		CON6X20A	
	GND	F1	
	+15V	F2	
	GND	F3	
	POLARIZATION	F4	
	+15V	F5	
	RX+/TX-	F6	
	RESET N	F7	
	+5V	F8	
	GND	F9	
	28V TRANSORB	F10	
		F11	
		F12	
		F13	
		F14	
		F15	
		F16	
		F17	
		F18	
		F19	
		F20	

CONNECTOR TO VHF RCVR CARD

J2		CON6X28	
	GND	A1	
	GND	A2	
	+5V	A3	
	+5V	A4	
	+2.5V	A5	
	SDA	A6	
	SCL	A7	
	GND	A8	
	CODEC-FS0	A9	
	CODEC-BCLK	A10	
	CODEC-SDO	A11	
	+1.8V	A12	
		A13	
	GND	A14	
	GND	A15	
		A16	
		A17	
	SIDETONE	A18	
	28V CLIPPED	A19	
	28V CLIPPED	A20	
		A21	
	GND	A22	
		A23	
		A24	
	VHFRX-SPARE2	A25	
	MASTER-VALID	A26	
	GND	A27	
	GND	A28	

J2		CON6X28	
	+3.3V	B1	
	+3.3V	B2	
	ENET-TXD+	B3	
	ENET-TXD-	B4	
	GND	B5	
	ENET-RXD+	B6	
	ENET-RXD-	B7	
		B8	
	RX-1B	B9	
	RX-1A	B10	
		B11	
	CODEC-SDI	B12	
	TX-2A	B13	
	TX-2B	B14	
	REMOTE OFF NO	B15	
	28V CLIPPED	B16	
	28V CLIPPED	B17	
		B18	
	GND	B19	
	CONTROL-FS	B20	
	CONTROL-CLK	B21	
	CONTROL-DX	B22	
	RX-5A	B23	
	GND	B24	
	IO-SPARE2	B25	
	IO-SPARE12	B26	
	28V TRANSORB	B27	
	28V TRANSORB	B28	

J2		CON6X28	
	+3.3V	C1	
	+3.3V	C2	
	GND	C3	
	RX-4A	C4	
	RX-4B	C5	
	CODEC-MCLK	C6	
	IO-SPARE9	C7	
	GND	C8	
	IO-SPARE10	C9	
	IO-SPARE11	C10	
	RX+/TX-	C11	
	NAVAUX-CLK	C12	
	IO-SPARE13	C13	
	GND	C14	
	GND	C15	
	FPGA-TCK	C16	
	FPGA-TMS	C17	
	GND	C18	
	LOCARD-TDI	C19	
	FPGA-TDO	C20	
	GND	C21	
		C22	
	RX-6A	C23	
	RX-5B	C24	
	RX-2B	C25	
	RX-3A	C26	
	28V TRANSORB	C27	
	28V TRANSORB	C28	

J2		CON6X28	
	BOOT-DSC0	D1	
	BOOT-DSC1	D2	
	BOOT-DSC2	D3	
	BOOT-DSC3	D4	
	GND	D5	
	PIT-	D6	
	SIMULCOM-	D7	
	CODEC-FSI	D8	
	DATA-KEY-	D9	
	GND	D10	
	RX-2A	D11	
	RS422-TX2	D12	
	NAVAUX-LD	D13	
	TX-5A	D14	
	TXEN0	D15	
	RS422-TX1	D16	
	TX-3A	D17	
	TX-4A	D18	
	GND	D19	
	TX-3B	D20	
	POLARIZATION	D21	
	RESET N	D22	
	RX-6B	D23	
	GND	D24	
		D25	
	IO-SPARE12	D26	
	28V TRANSORB	D27	
	28V TRANSORB	D28	

J2		CON6X28	
	-5V	E1	
	-5V	E2	
		E3	
	-15V	E4	
	RCB-TX	E5	
	+15V	E6	
	IO-SPARE6	E7	
	GND	E8	
	IO-SPARE5	E9	
	TX-0A	E10	
	RX-0A	E11	
	TX-0B	E12	
	TX-1B	E13	
	GND	E14	
	GND	E15	
	TX-5B	E16	
	NAVAUX-DATA	E17	
	RS422-RX3	E18	
	RX-7A	E19	
	RX-7B	E20	
		E21	
	GND	E22	
	RX-8A	E23	
	RX-8B	E24	
		E25	
	CONTROL-DR	E26	
	28V TRANSORB	E27	
	28V TRANSORB	E28	

J2		CON6X28	
	GND	F1	
	GND	F2	
	RCB-RX	F3	
	-15V	F4	
	GND	F5	
	+15V	F6	
	IO-SPARE1	F7	
	IO-SPARE3	F8	
	IO-SPARE4	F9	
	GND	F10	
	RX-0B	F11	
	TX-1A	F12	
	RX-3B	F13	
	GND	F14	
	GND	F15	
	TXEN1	F16	
	IO-SPARE7	F17	
	COMAUX-DATA	F18	
	GND	F19	
	TX-4B	F20	
	IO-SPARE14	F21	
	RS422-RX2	F22	
	RS422-RX1	F23	
	GND	F24	
	RX-9B	F25	
	RX-9A	F26	
	GND	F27	
	GND	F28	

CONNECTOR TO I/O CARD

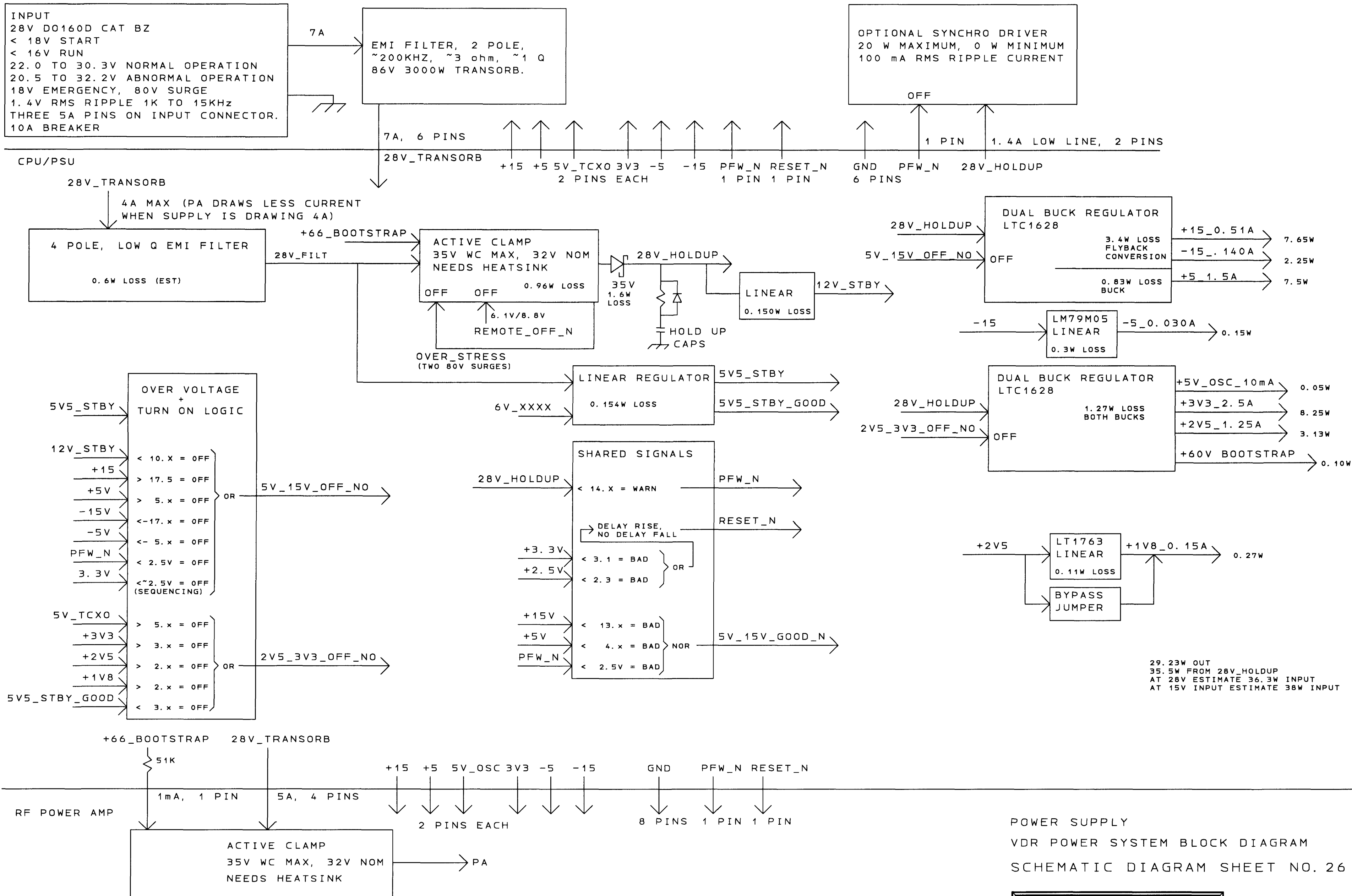
CONNECTOR PIN SUMMARY
CAD REFERENCE/REQUIREMENT
SCHEMATIC DIAGRAM SHEET NO. 25

COMPUTER GENERATED

7-25-2002 15:56	PAUL COBLENTZ	SIZE	E	55939	DRAWING NO.	7026230-902	REV	XK
Honeywell				SCALE	1	SHEET	28	

REV		REVISIONS		DATE	APPROVED
ZONE	REV	DESCRIPTION			
XK		WAS DWG SHEET 28.			P. COBLENTZ

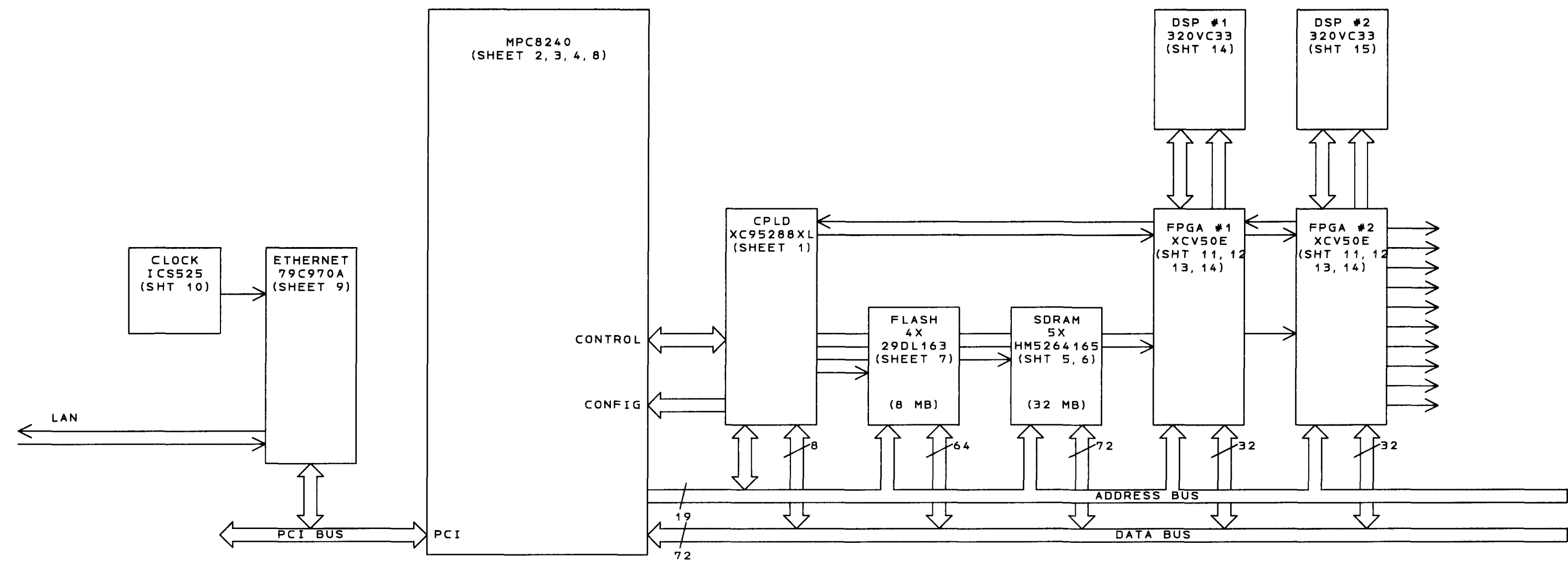
I/O (MEZZANINE) CCA



COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	SEE FIRST SHEET FOR CONTRACT NUMBER, USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SCALE	7026230-902	REV 29
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REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	XK	WAS DWG SHEET 29.	
			P. COBLENTZ



PROCESSOR
BLOCK DIAGRAM
SCHEMATIC DIAGRAM SHEET NO. 27

COMPUTER GENERATED

7-25-2002 15:58	PAUL COBLENTZ	SIZE	E 55939	DRAWING NO.	7026230-902	REV	XK
Honeywell		SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.		SCALE		SHEET 30	