

Figure 1. Control Processor Card Top

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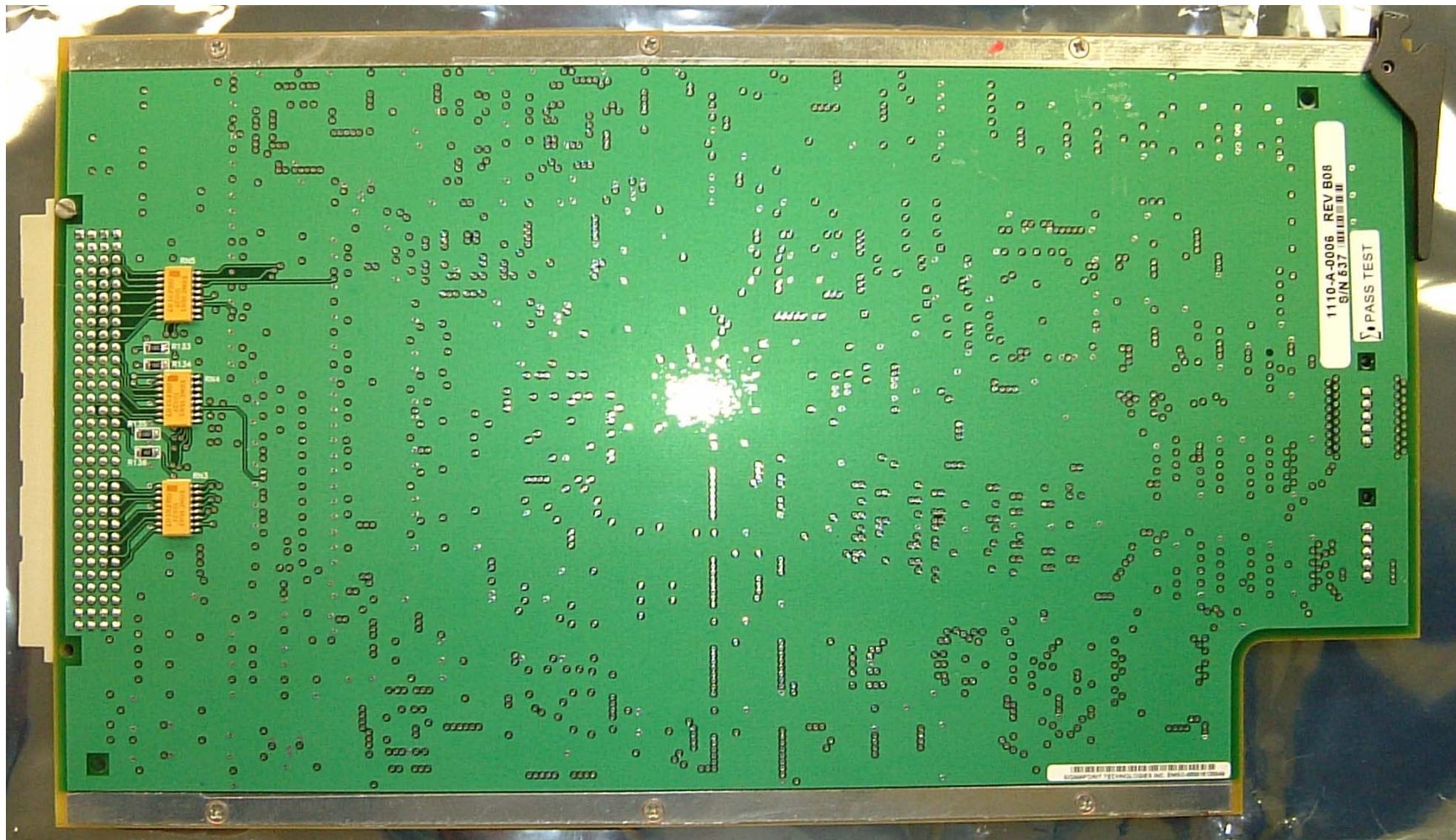


Figure 2. Control Processor Card Bottom

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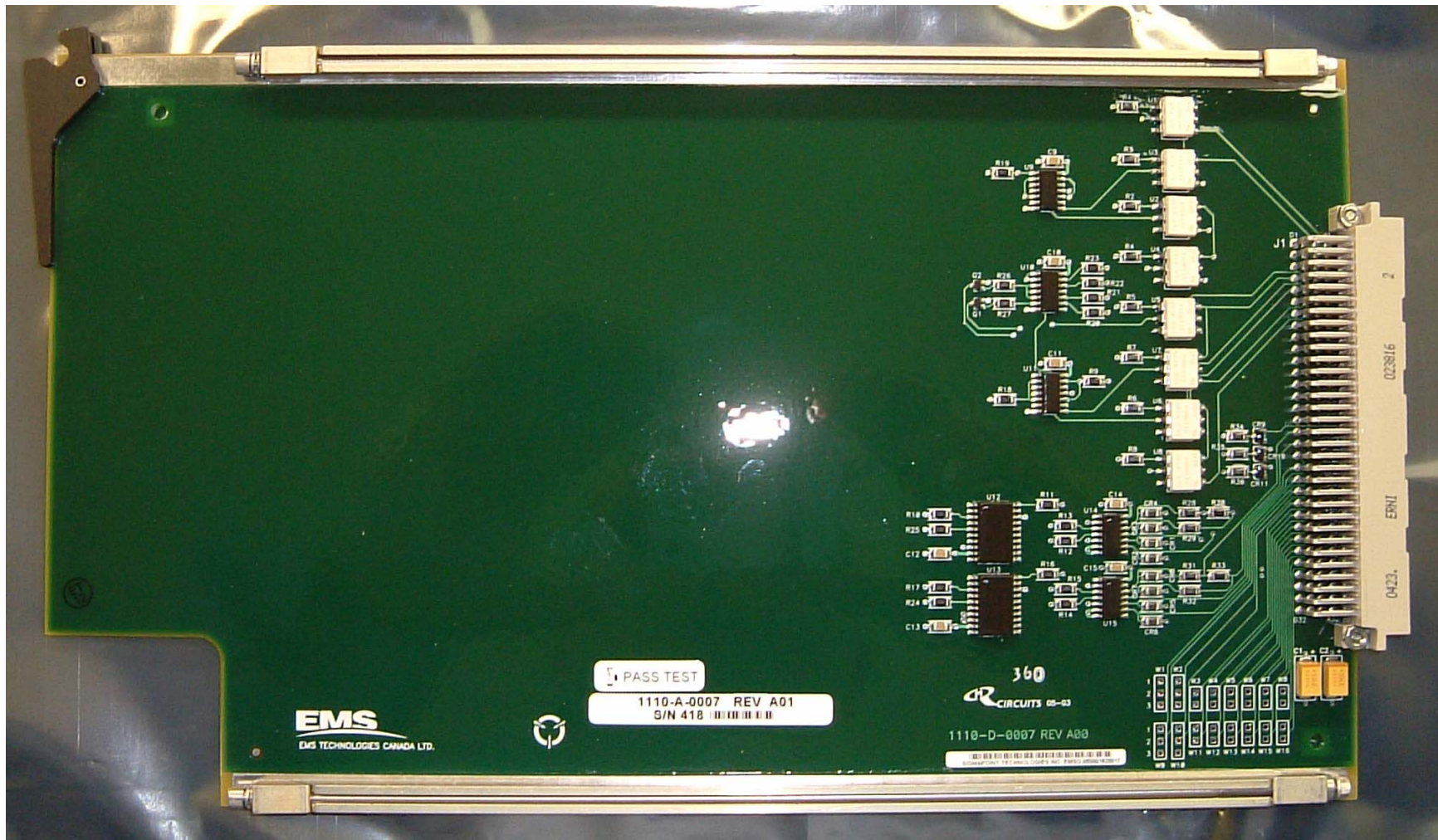


Figure 3. Data I/O Card Top

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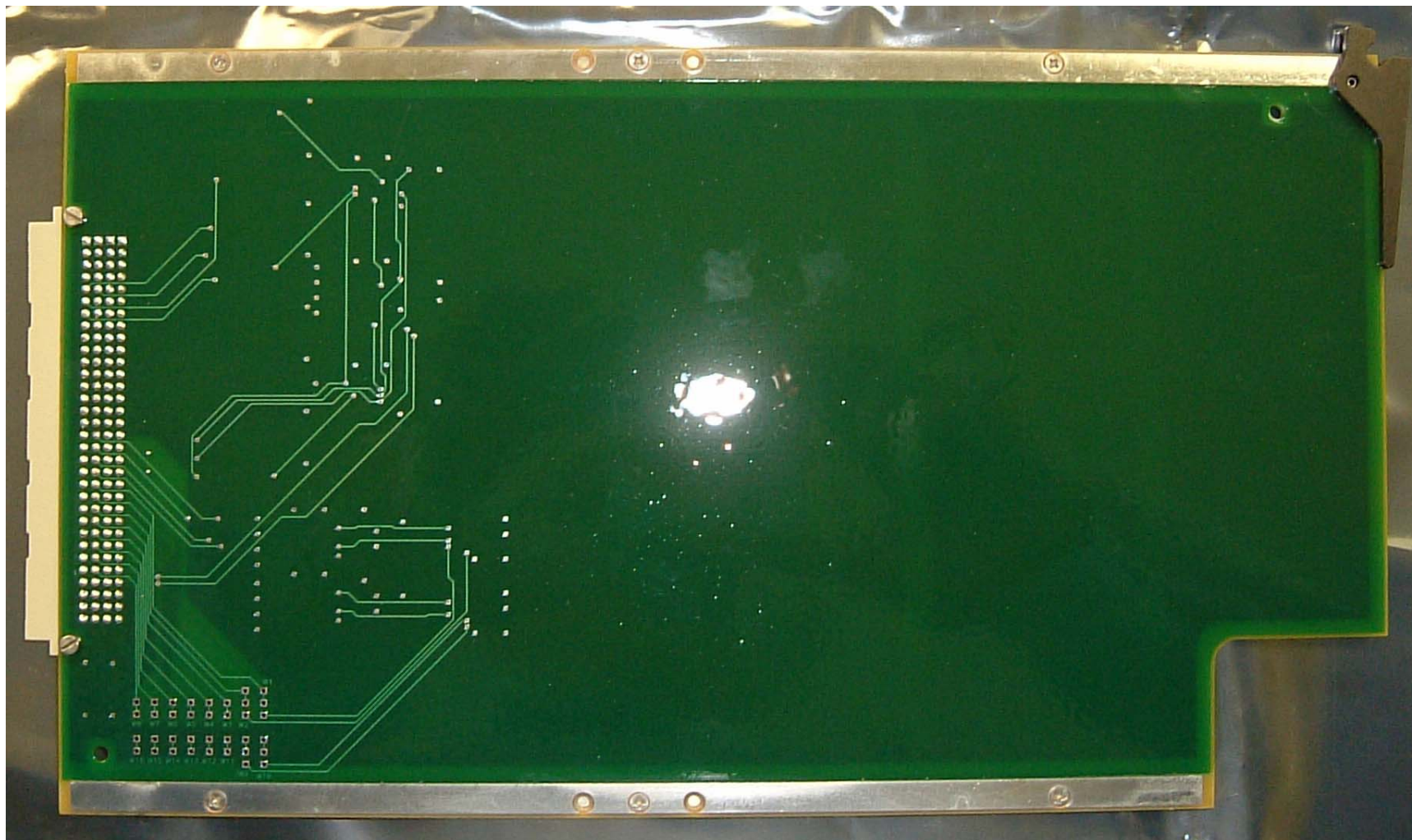


Figure 4. Data I/O Card Bottom

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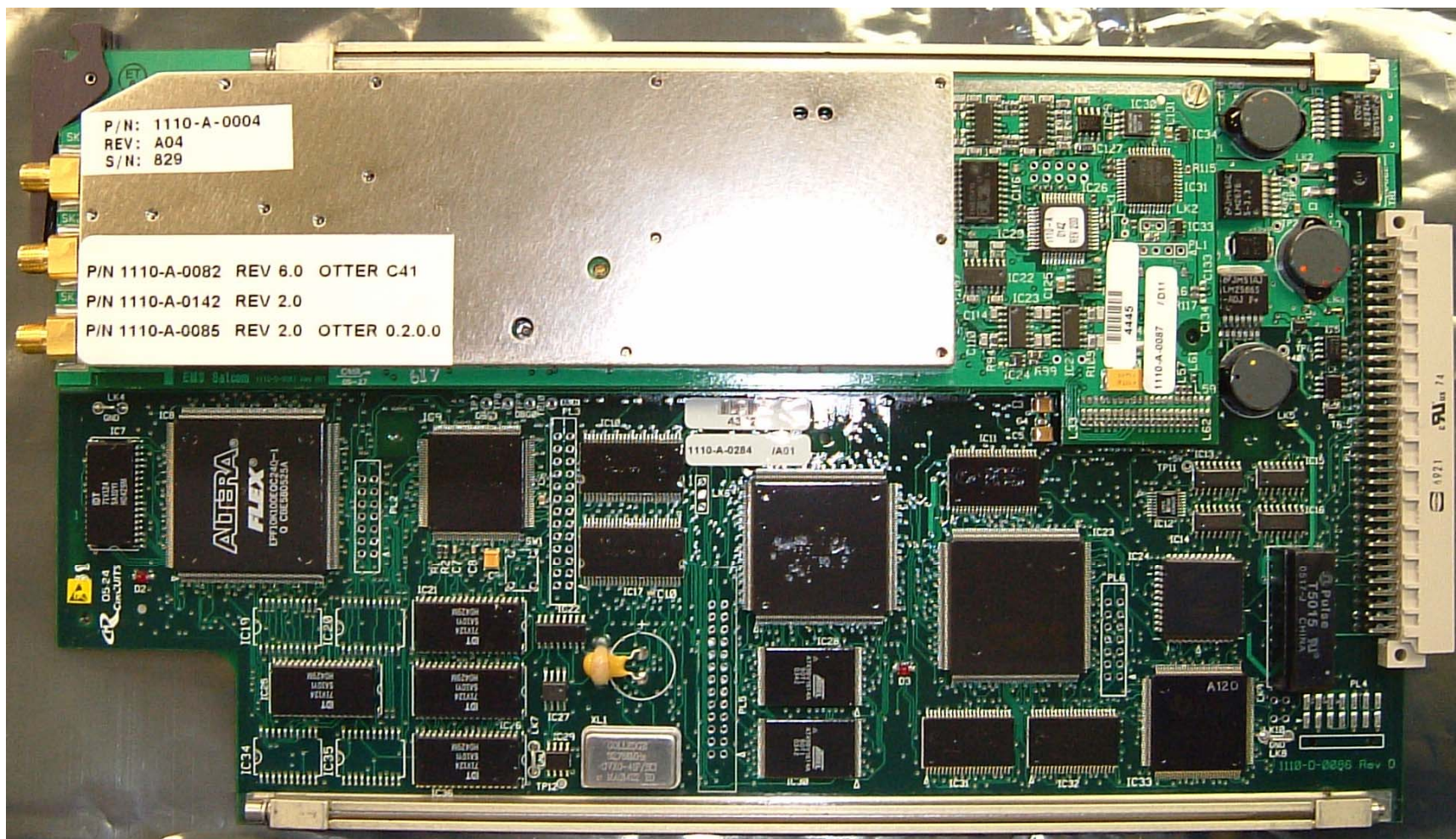


Figure 5. Channel Card Top (Shield Cover On)

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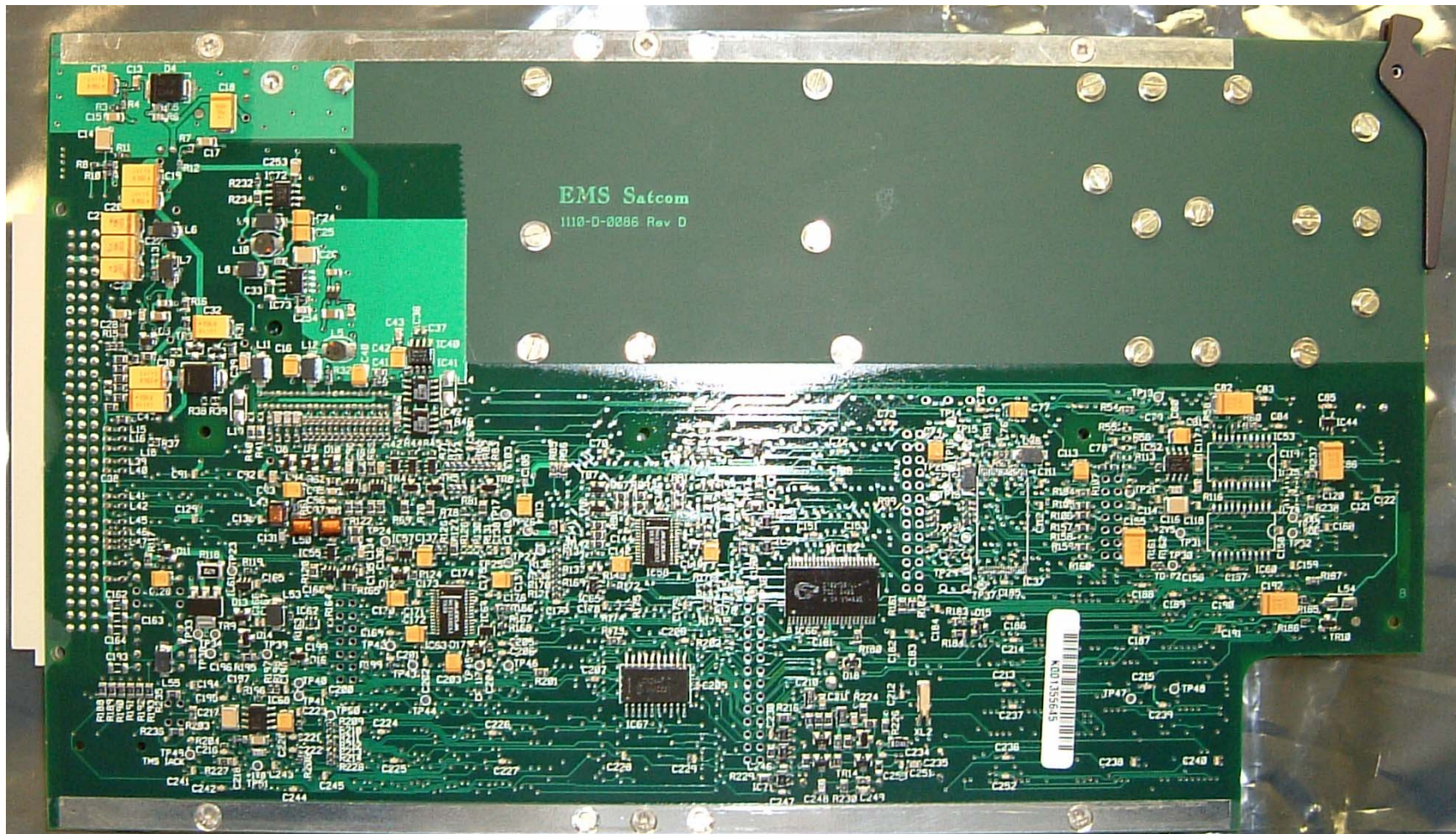


Figure 7. Channle Card Bottom

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