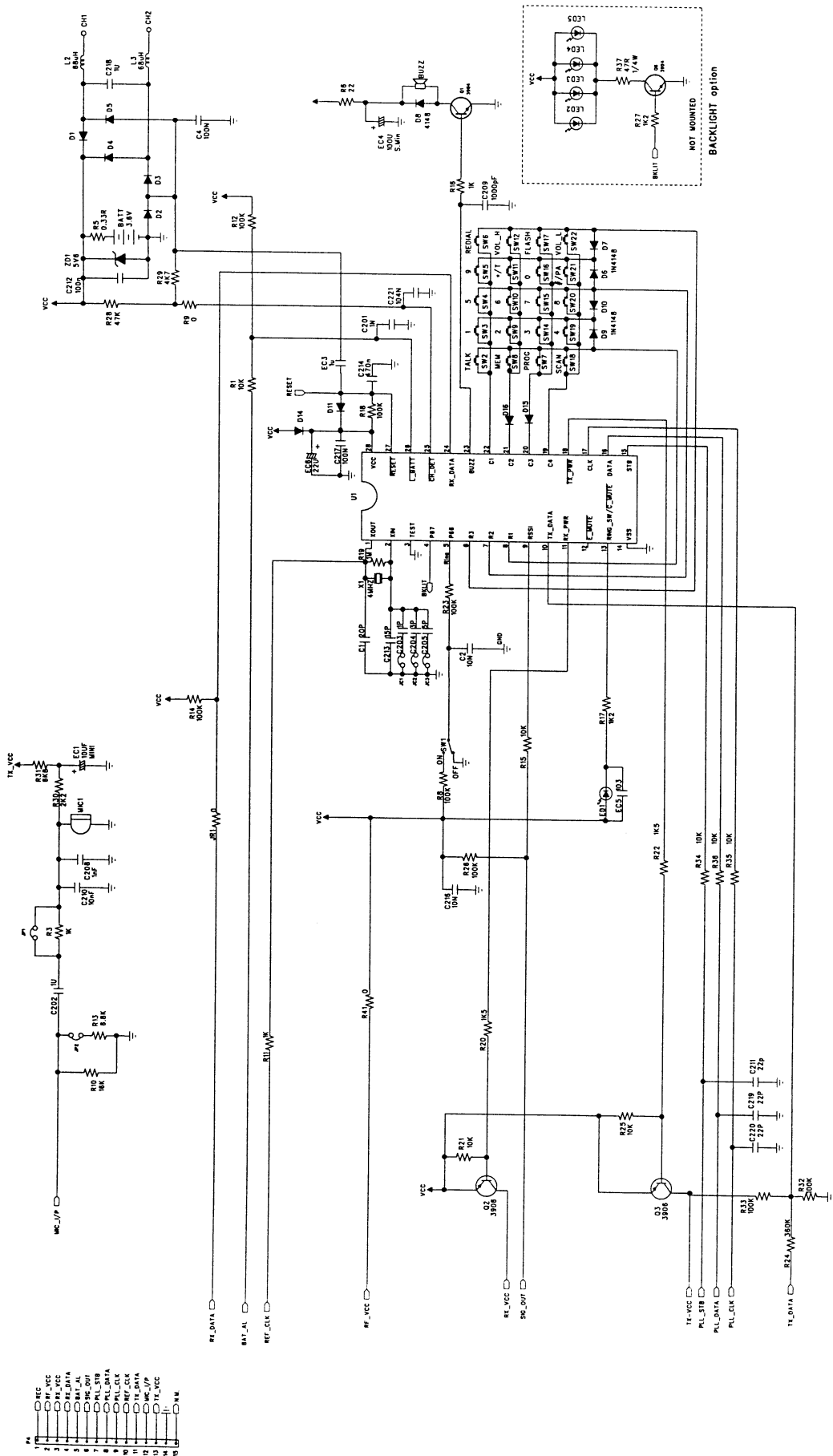
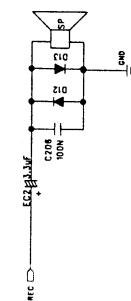


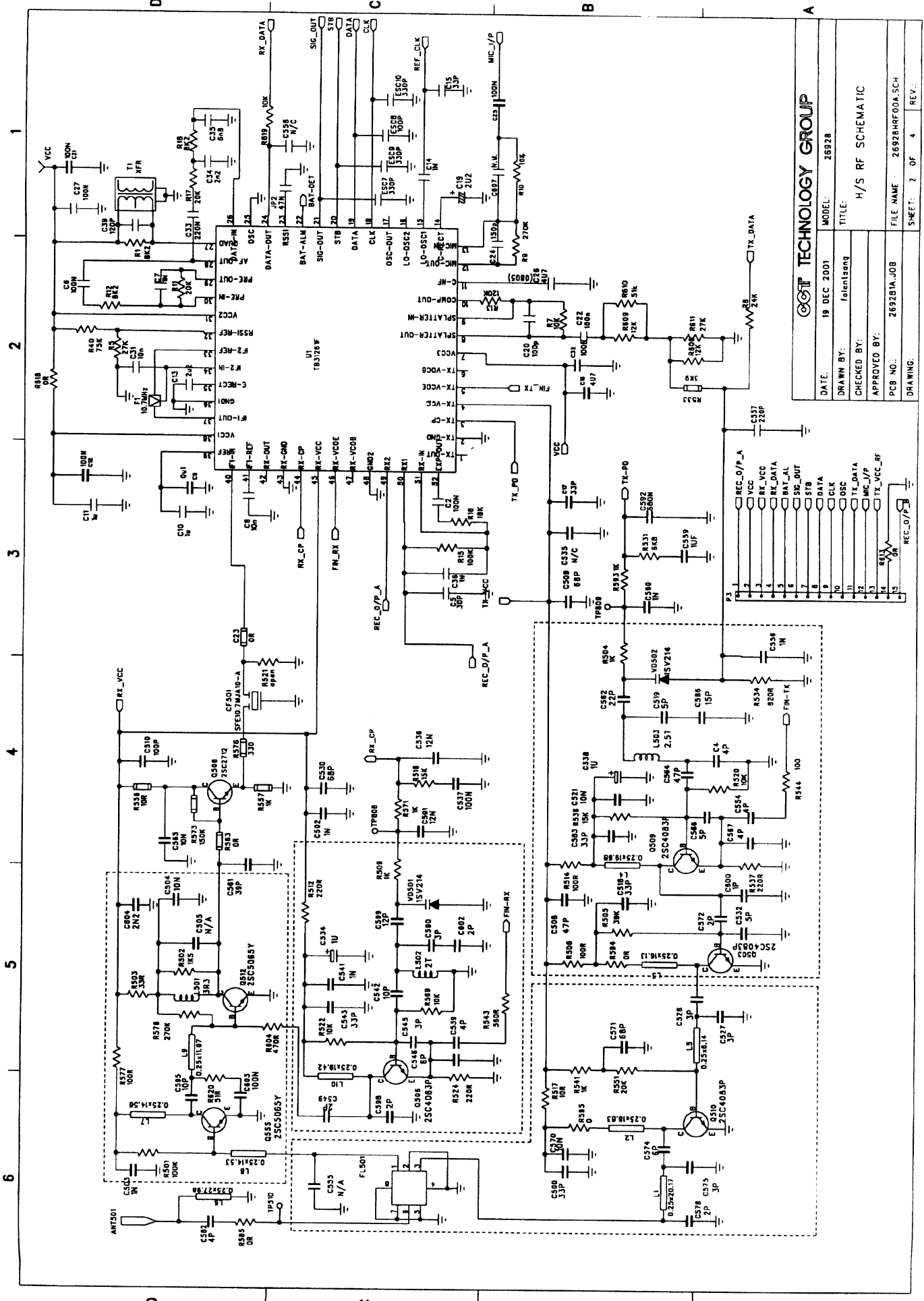


Remark---->
 JR1--decrease MIC level
 JR2--increase MIC level
 Jc1/Jc2/Jc3--adjust Tx frequency



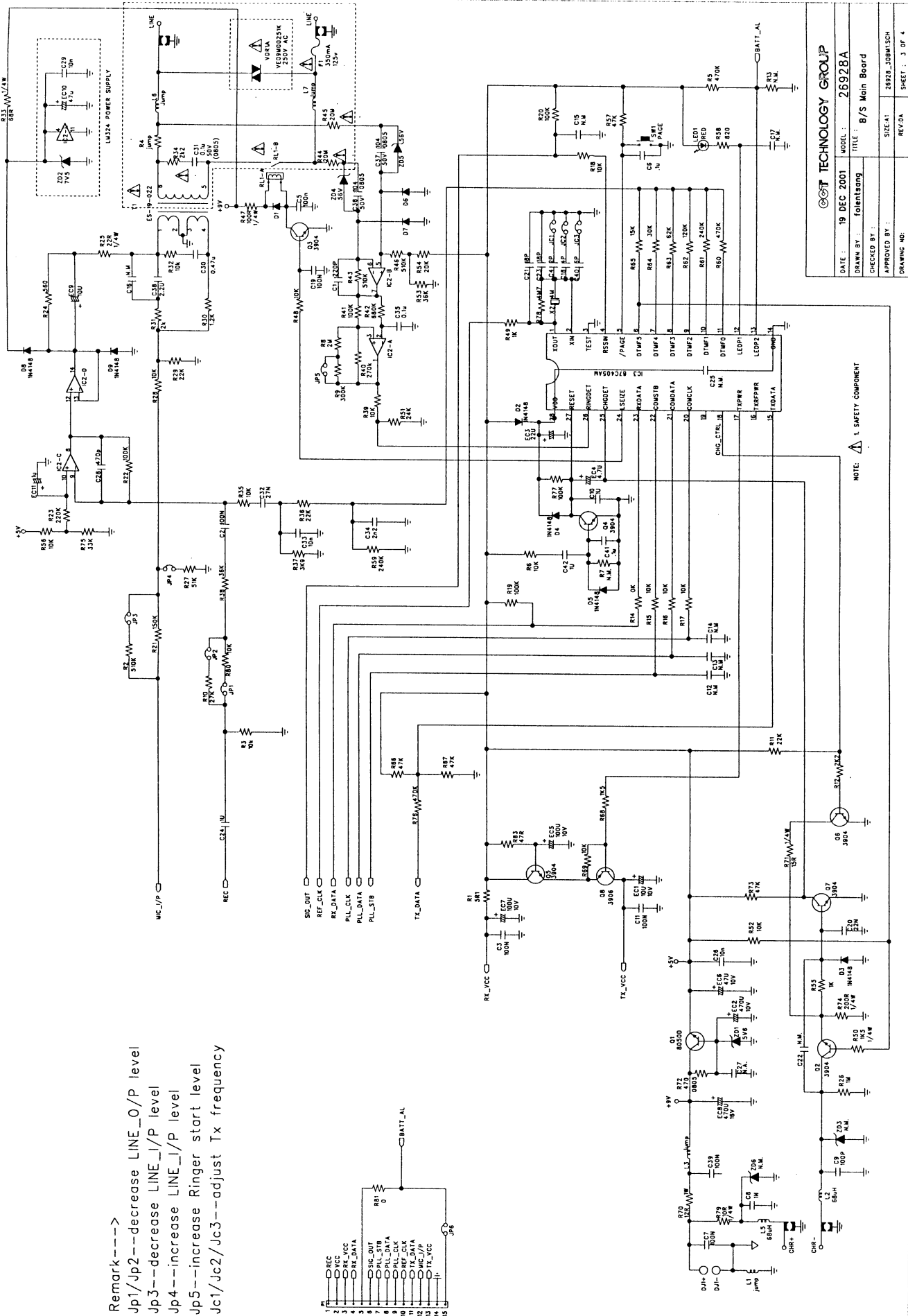
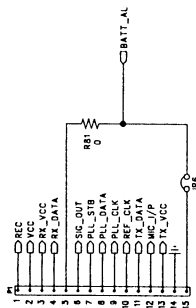
Remark---->
 JR1--decrease MIC level
 JR2--increase MIC level
 Jc1/Jc2/Jc3--adjust Tx frequency

GOT TECHNOLOGY GROUP			
DATE :	19 DEC 2001	REV: 1	MODEL : 26928A
DRAWN BY:	folentsong	FILE NAME:	26928A_20011224.SCH
CHECKED BY:		DRAWING NO:	H/S Key Board
APPROVED BY:		SHEET :	1 OF 4



26928A

Remark--->
 Jp1/Jp2---decrease LINE_O/P level
 Jp3---decrease LINE_I/P level
 Jp4---increase LINE_I/P level
 Jp5---increase Ringer start level
 Jc1/Jc2/Jc3---adjust Tx frequency



GOT TECHNOLOGY GROUP	
DATE : 19 DEC 2001	MODEL : 25928A
DRAWN BY : Tolentong	TITLE : 8/S Main Board
CHECKED BY :	APPROVED BY :
REVISION :	REVISION :
SHEET : 3	OF 4



DATE:	19 DEC 2001	MODEL:	26528
DRAWN BY:		TITLE:	
CHECKED BY:		B/S RF SCHEMATIC	
APPROVED BY:			
PCB NO.:	26528	FILE NAME:	9528BRF00A.SCH
DRAWING:		SHEET :	4 OF 4 REV