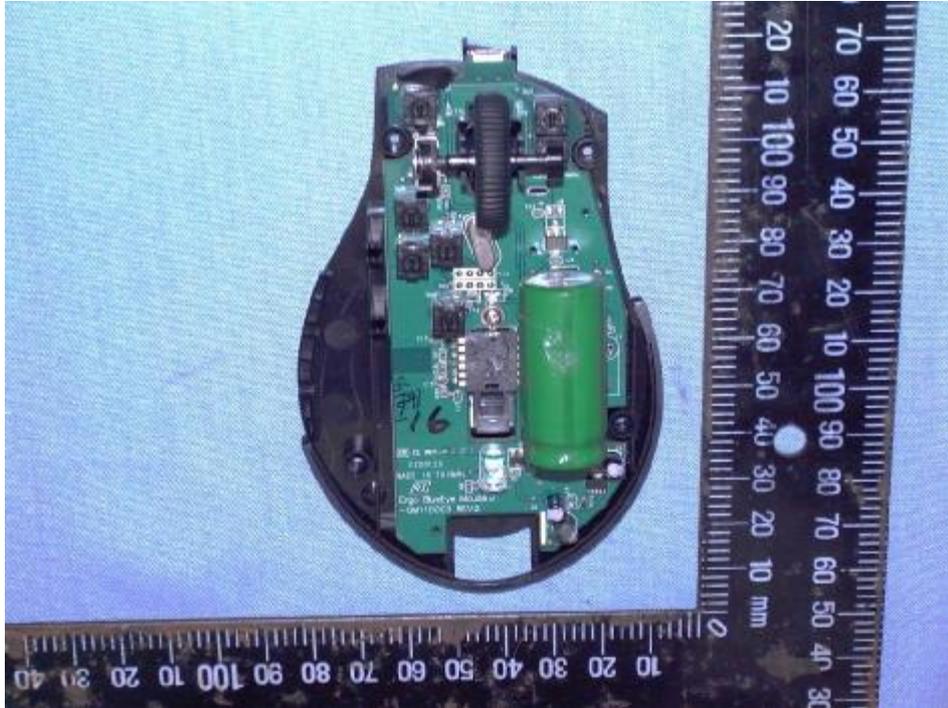




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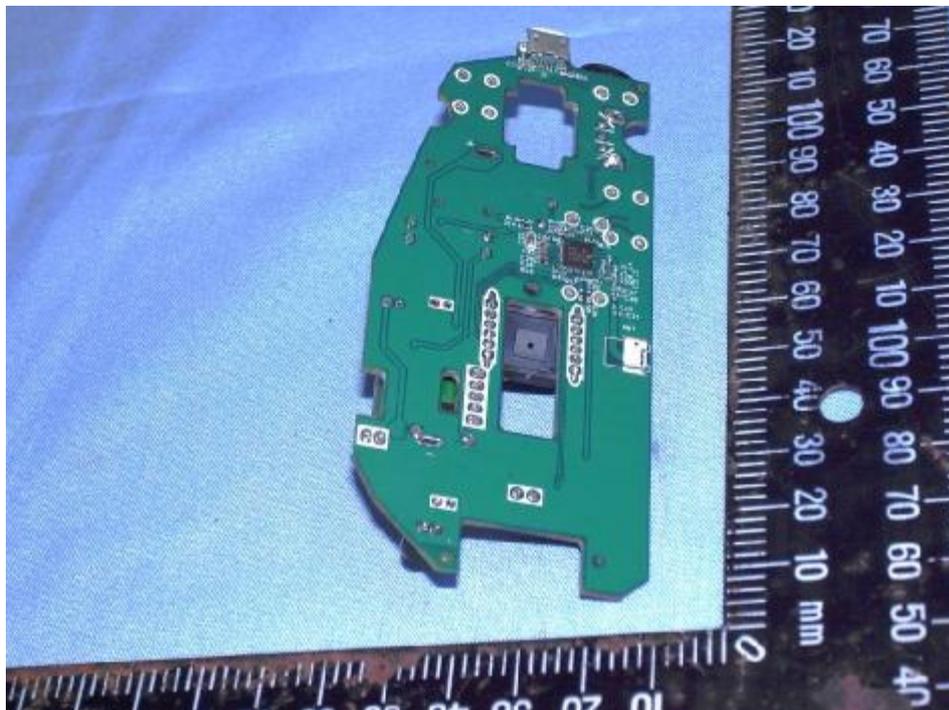
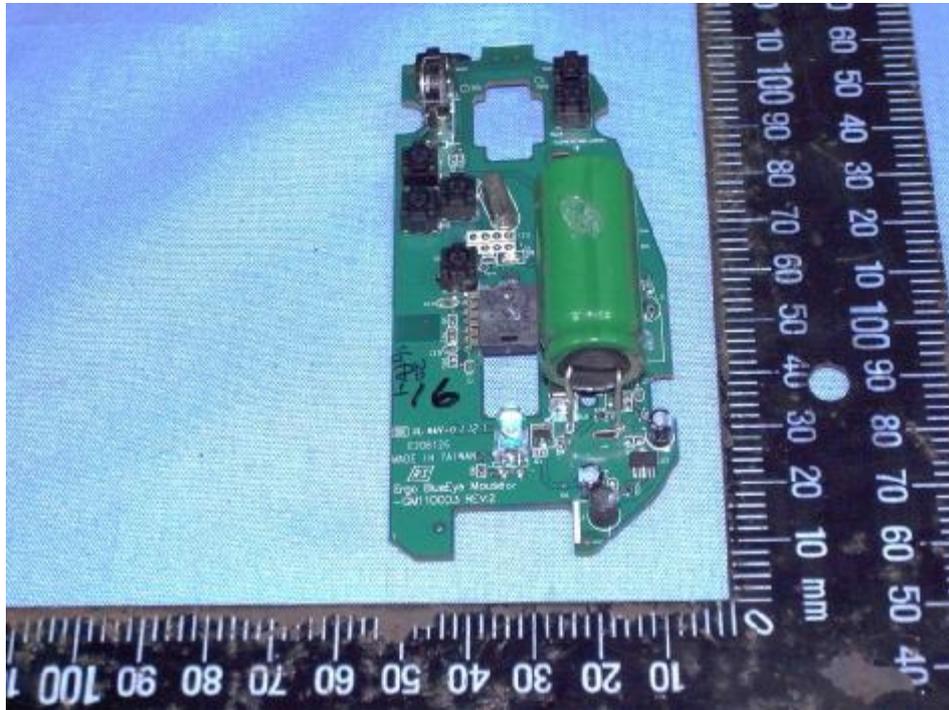
**TOP inside (TX)**





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**PCB (TX)**





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**CASE (TX)**

