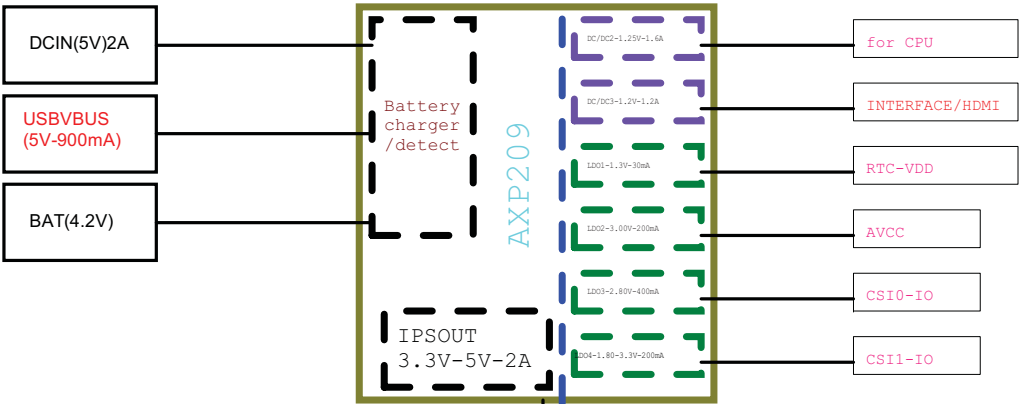
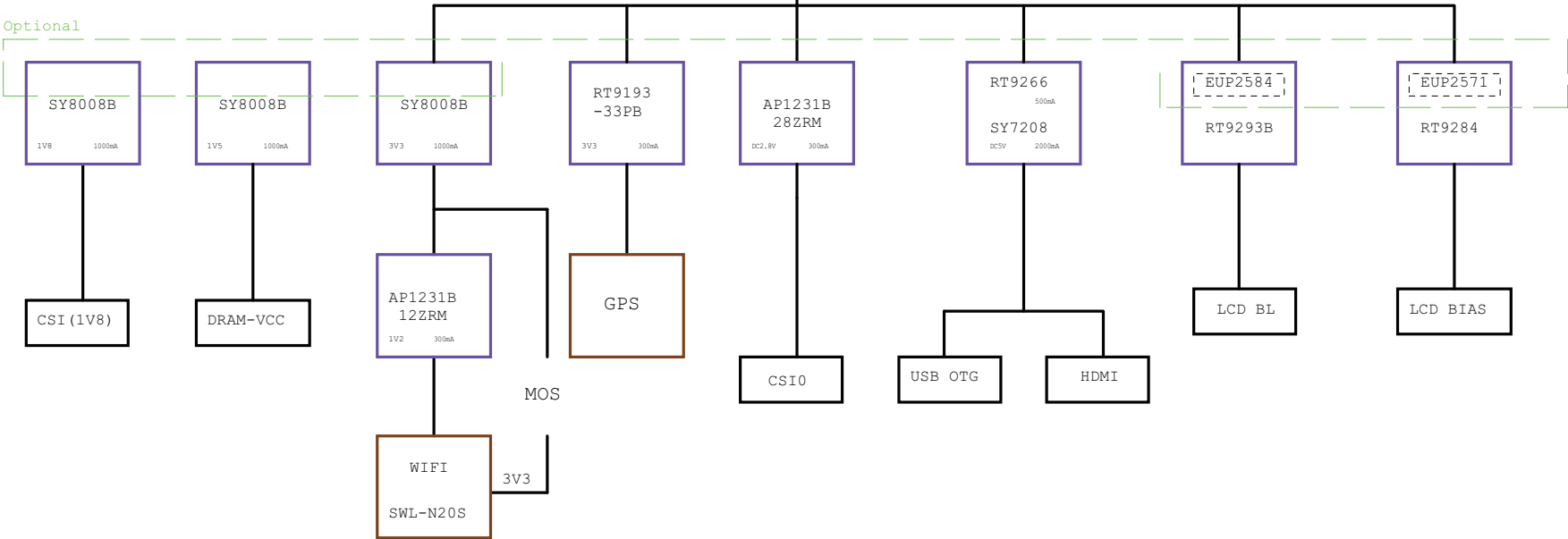


POWER TREE

LAYOUT: ACIN, BATT, IPSOUT input or output cable, must make it is thick enough started from PMU pin

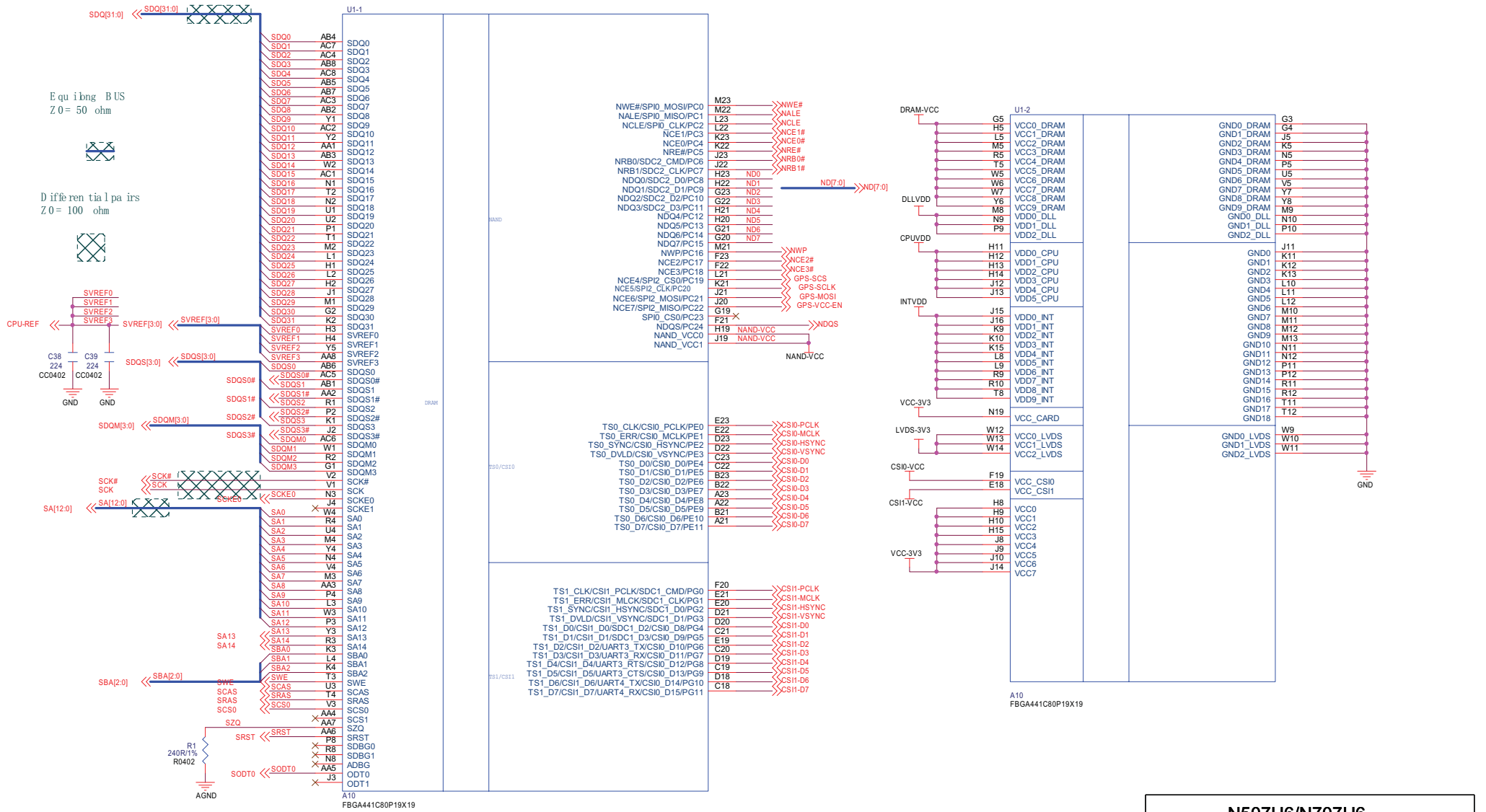


Optional



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File			
POWER TREE			
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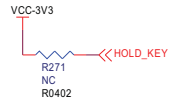
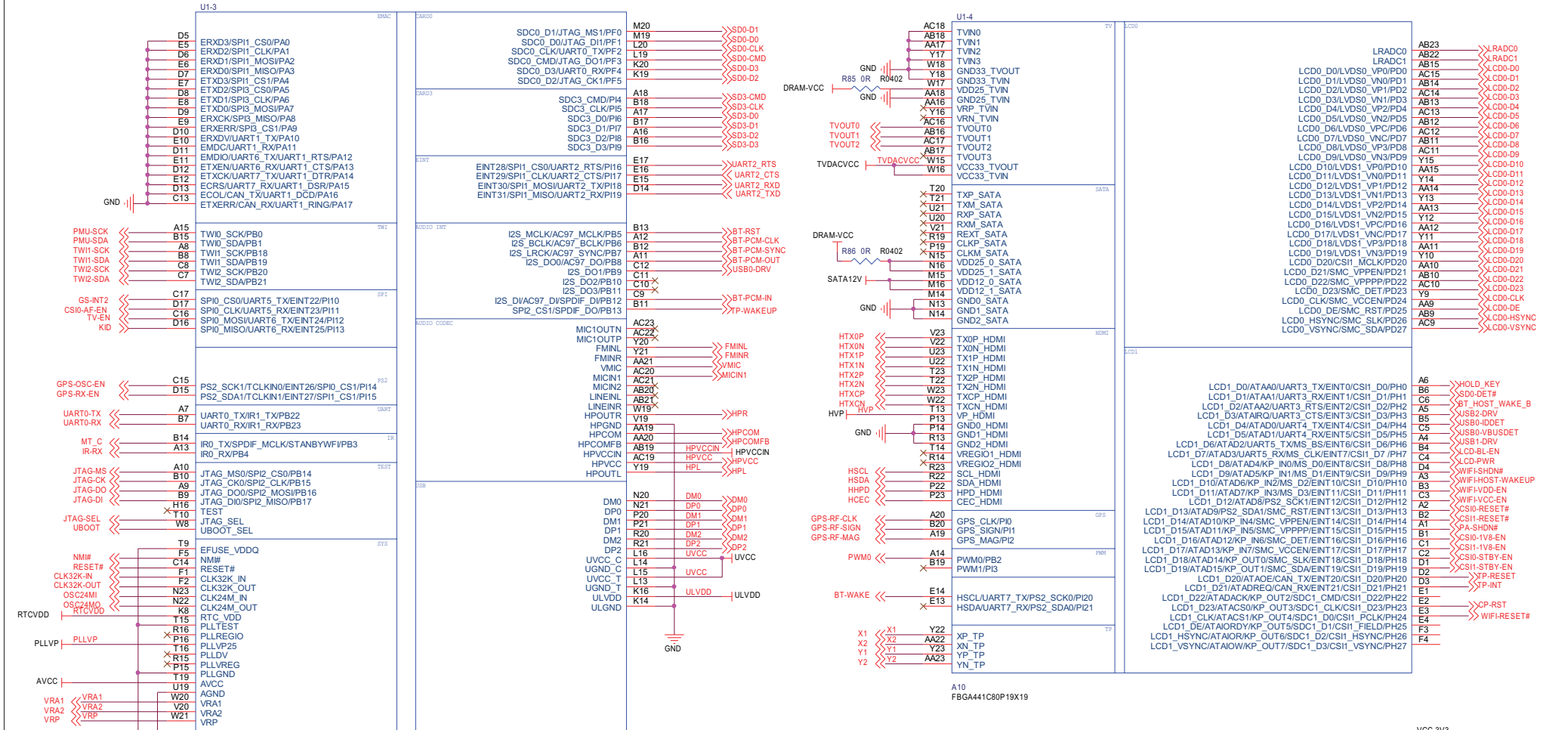
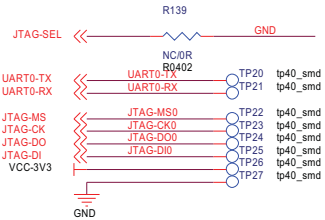
CPU1



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Title					CPU1					
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A3		<V1.0>								
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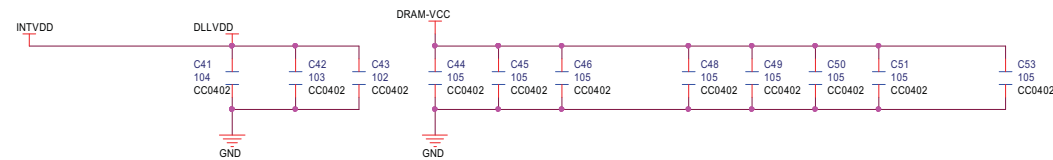
DEBUG Reserve DEBUG test points for debugging



BESIDE CPU

DRAM

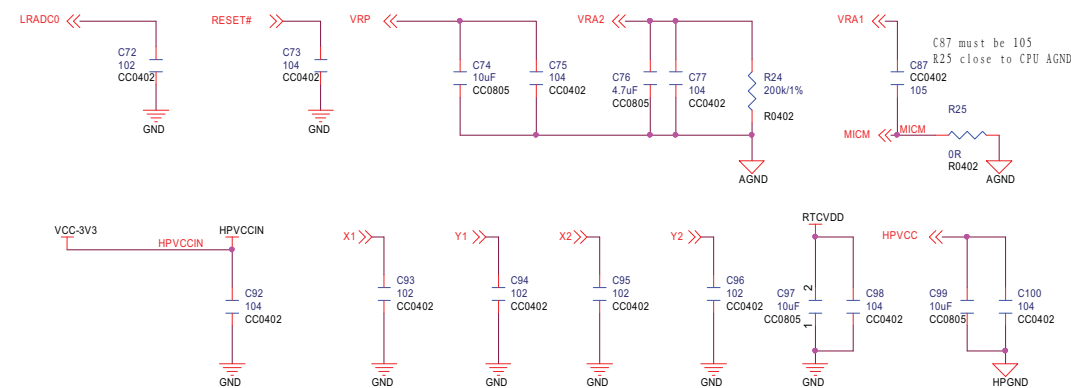
LAYOUT: According to put 1 capacitance in a pin, and put them as close as possible.



Crystal



AUDIO&SYS&TP



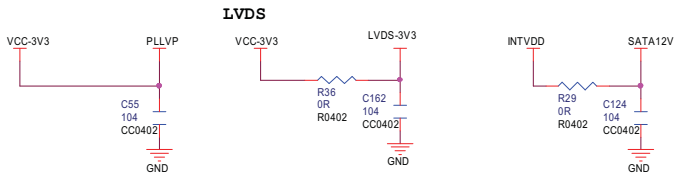
USB



HDMI

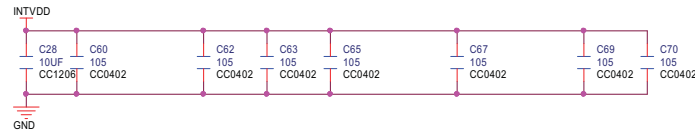


PLL

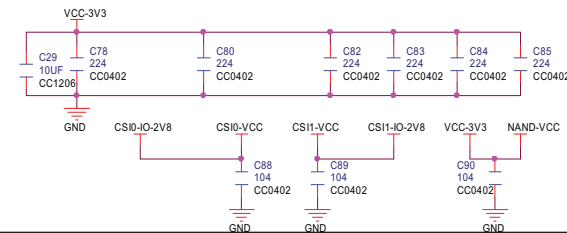


CORE

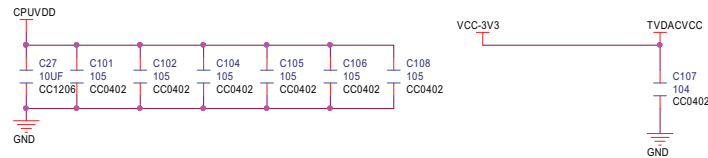
LAYOUT: According to put 1 capacitance in a pin, and put them as close as possible.



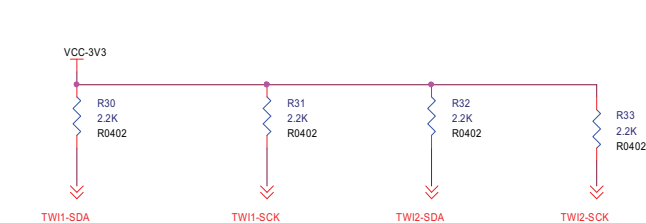
PIO-INTFACE



CPU&TV



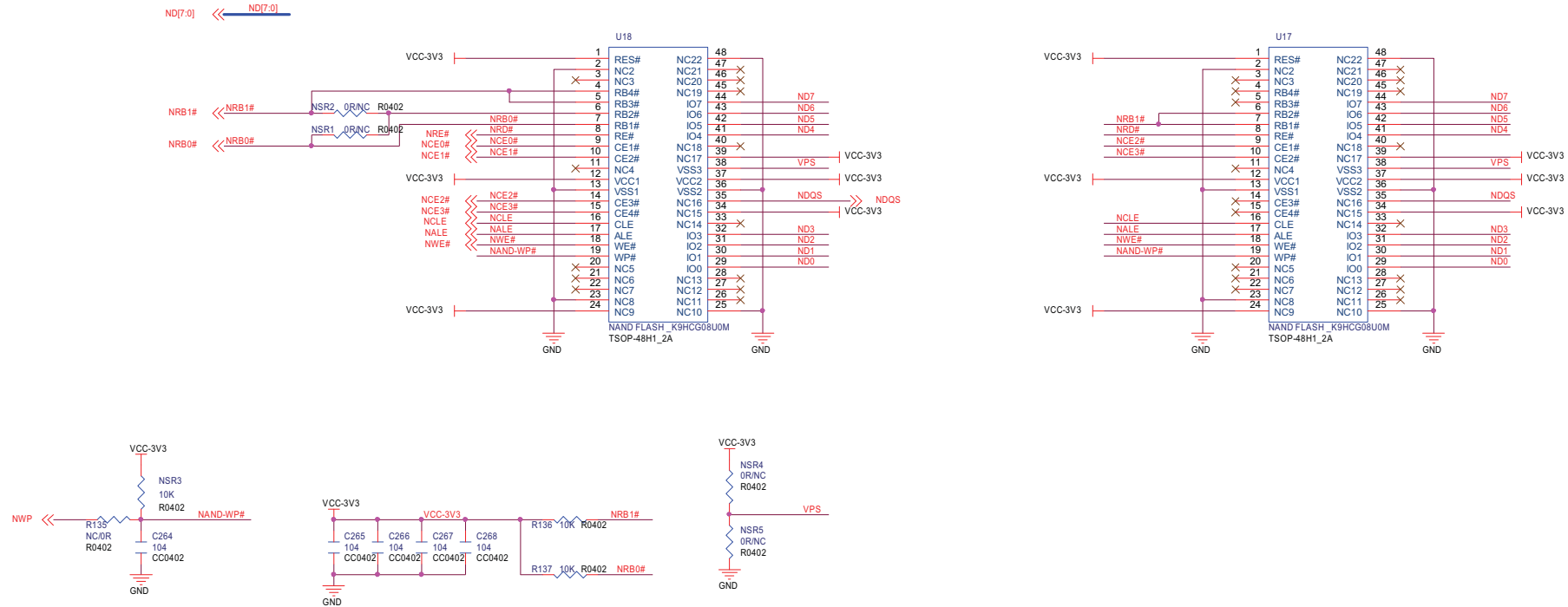
TWI-PULLUP



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File BESIDE CPU			
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NAND Flash

TSOP-48 Nand

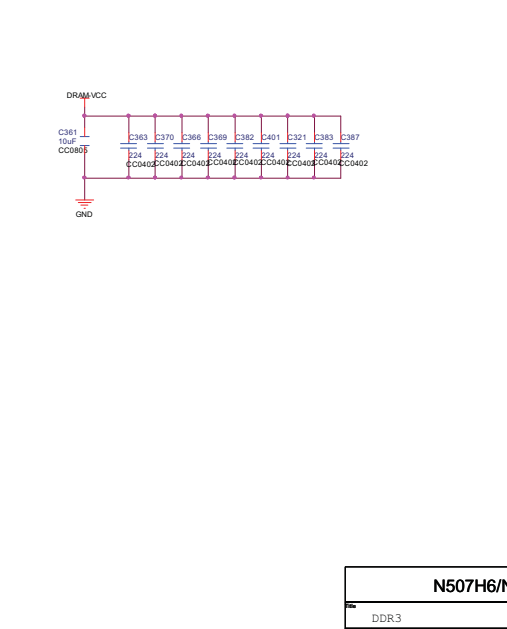
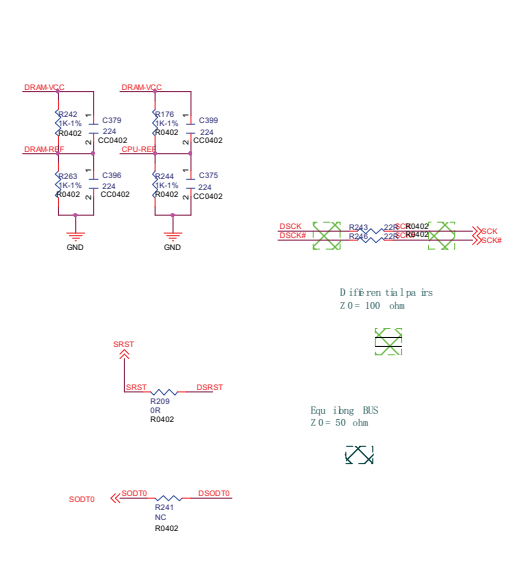
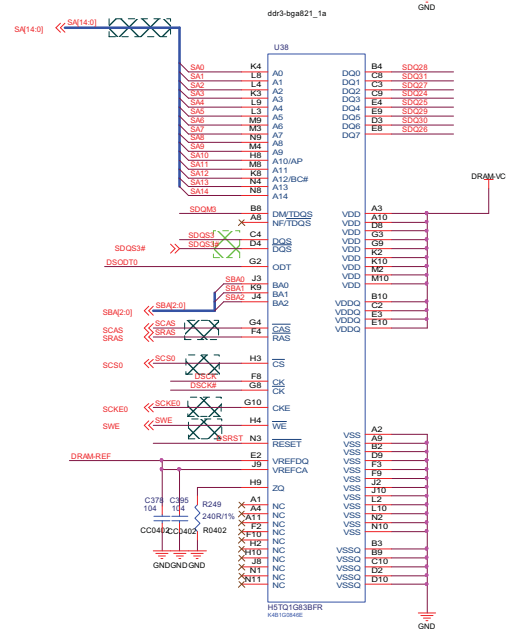
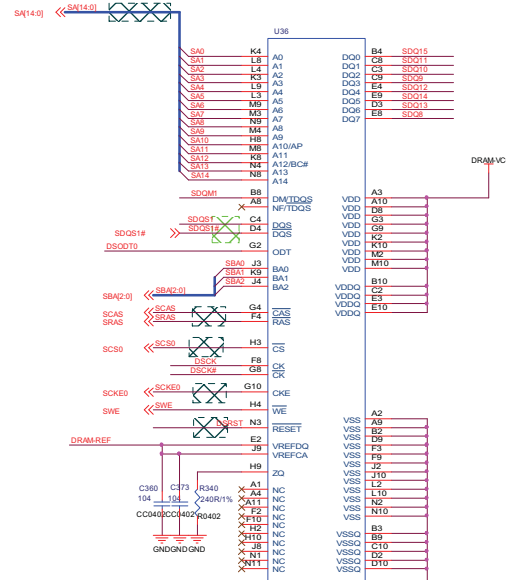
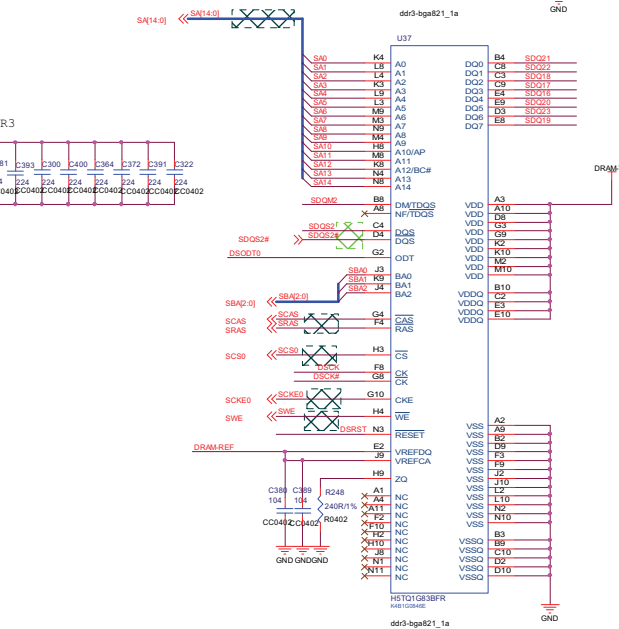
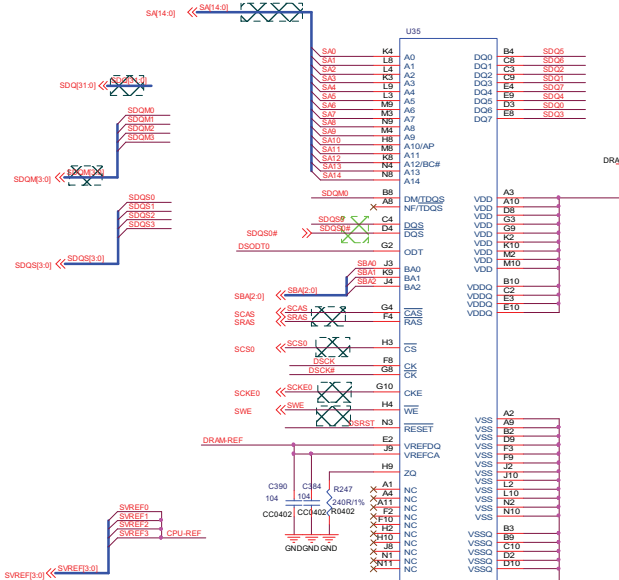


- (1) Disconnect NSR2 and NSR1 while connecting 1pcs of single nand.
- (2) Connect NSR2, disconnect NSR1 while connecting 1pcs of double nand
- (3) Connect NSR1, disconnect NSR2 while connecting 1pcs of four pcs style nand
- (4) Connect NSR1, disconnect NSR2 while connecting 2pcs of single nand or 2pcs of double nand
- (5) While connecting Intel, Toshiba, Samsung 2×nm TSOP nand, connect NSR4, disconnect NSR5, while connecting other nands, disconnect NSR4 and connect NSR5

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File			
NAND Flash			
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DDR3 8bit x 4ea

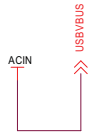
DDR encapsulation is TFBGA82



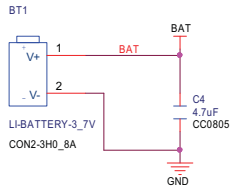
N507H6/N707H6			
Title			
DDR3			
Size	Document Number		Rev
A2	<V1.0>		
Date			
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POWER-PMU

POWER INPUT



BAT INPUT

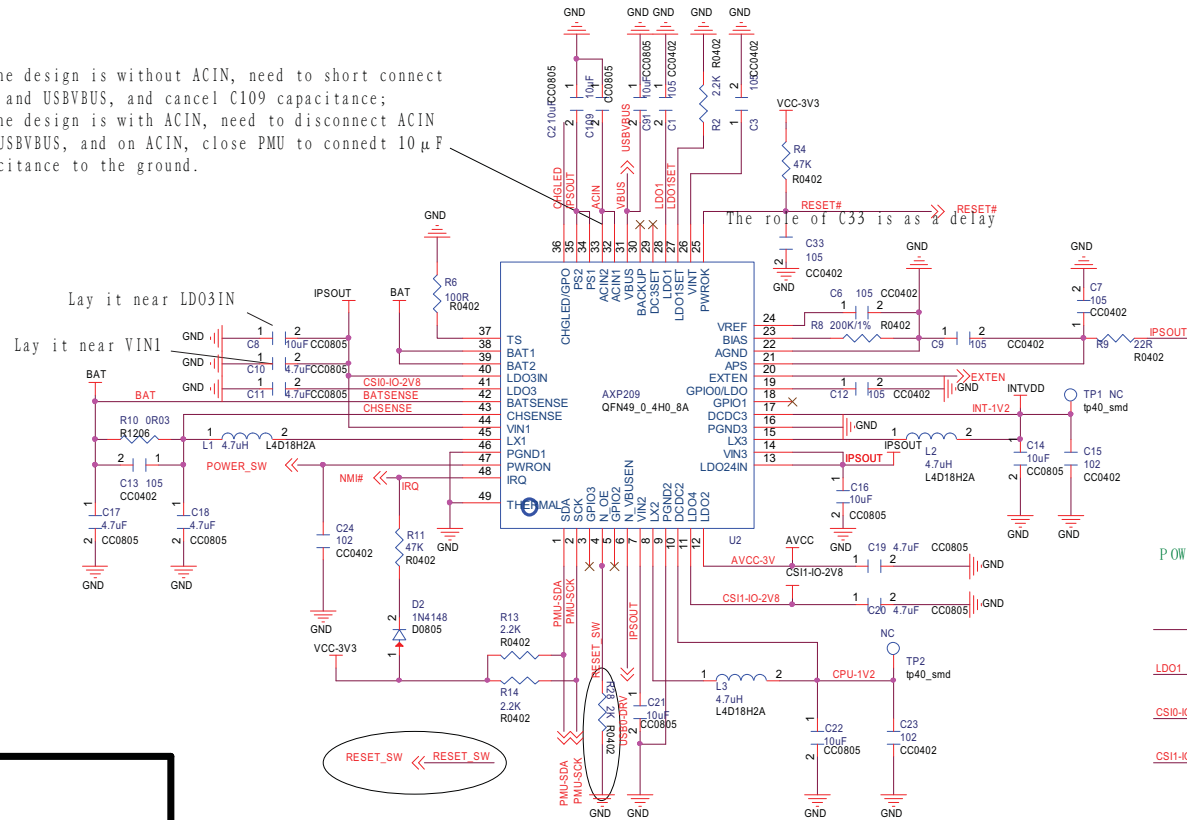


doesn't support to test battery temperature here.

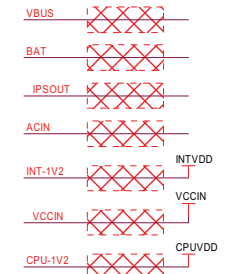
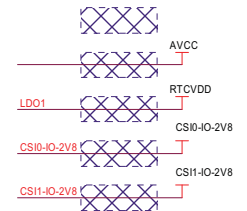
USER POWER FOR OTG/LCD



If the design is without ACIN, need to short connect ACIN and USBVBUS, and cancel C109 capacitance;
If the design is with ACIN, need to disconnect ACIN and USBVBUS, and on ACIN, close PMU to connect 10 μ F capacitance to the ground.



POWER LINE:W id th>= 20m il



POWER LINE:W id th>= 60m il

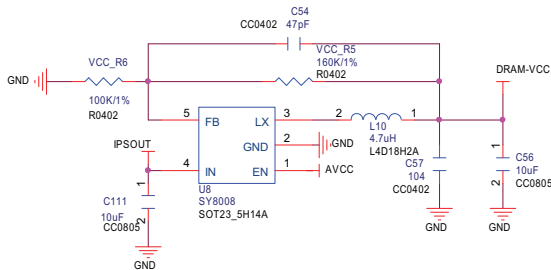
N507H6/N707H6

POWER-PMU			
Size	Document Number	Rev	
A3	<V1.0>		
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POWER-DC/DC

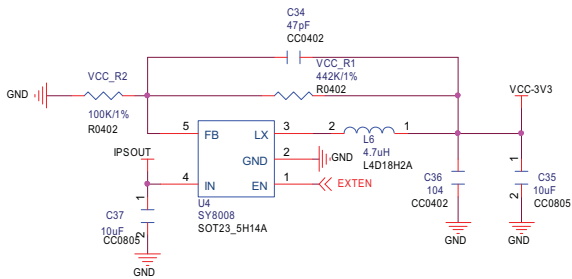
DRAM-VCC 1V5

$V_{out} = 0.6 * (1 + VCC_R5 / VCC_R6)$



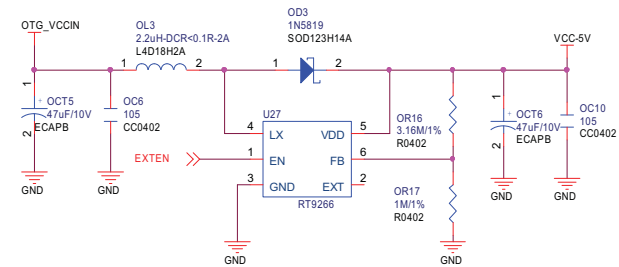
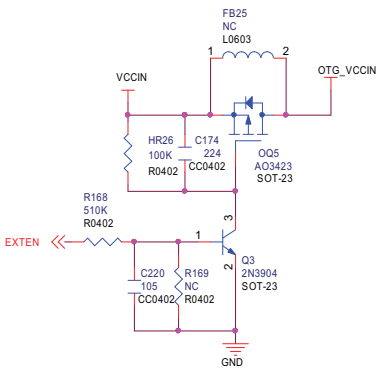
VCC-3V3

$V_{out} = 0.6 * (1 + VCC_R1 / VCC_R2)$

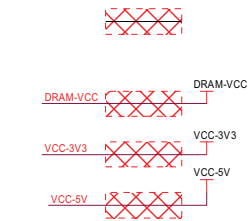


OTG HDMI 5V

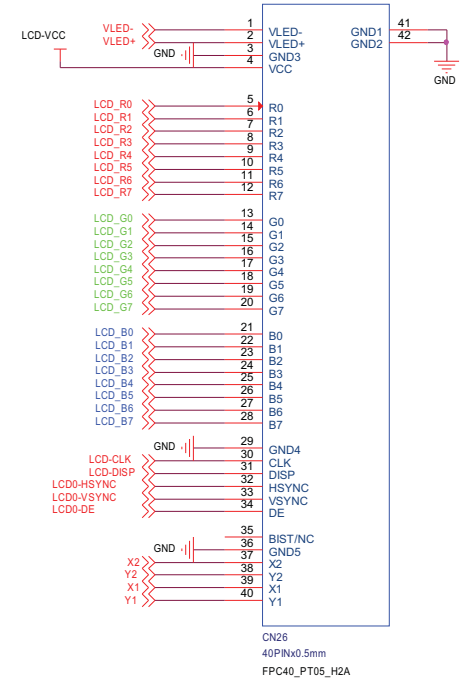
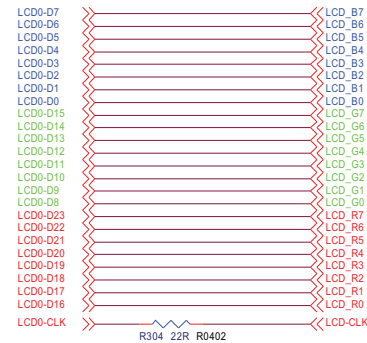
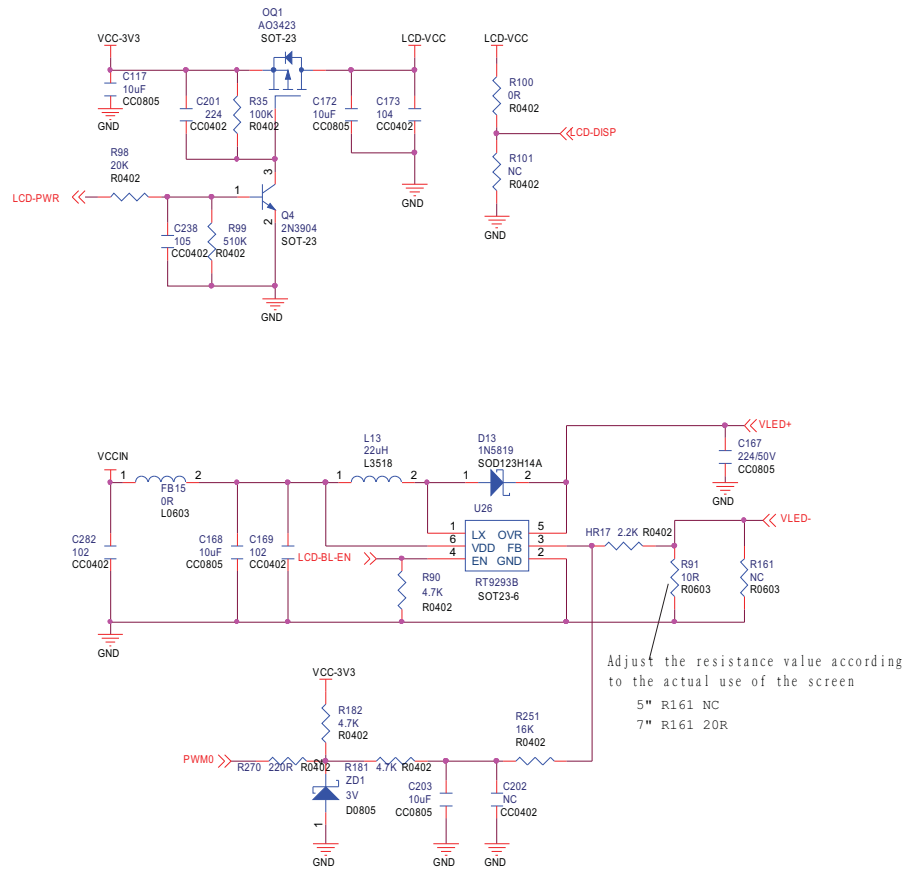
Actual output is controlled on 5.2V



POWER LINE: Width >= 60mil



LCD

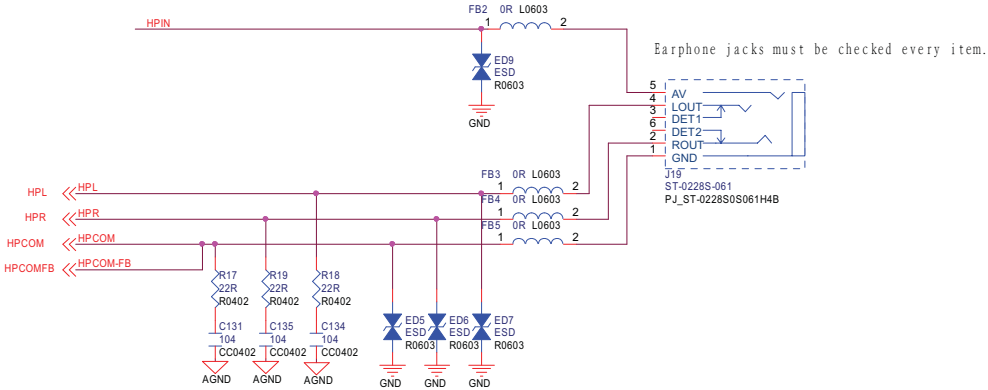
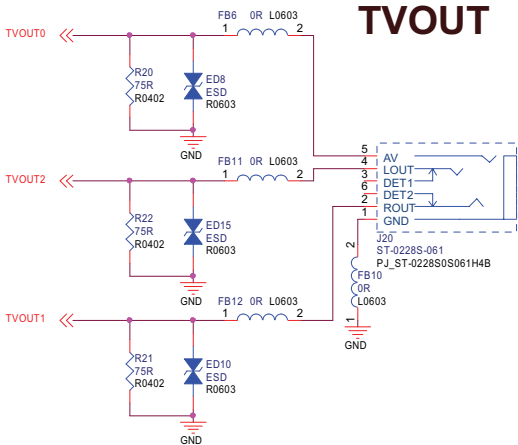


N507H6/N707H6

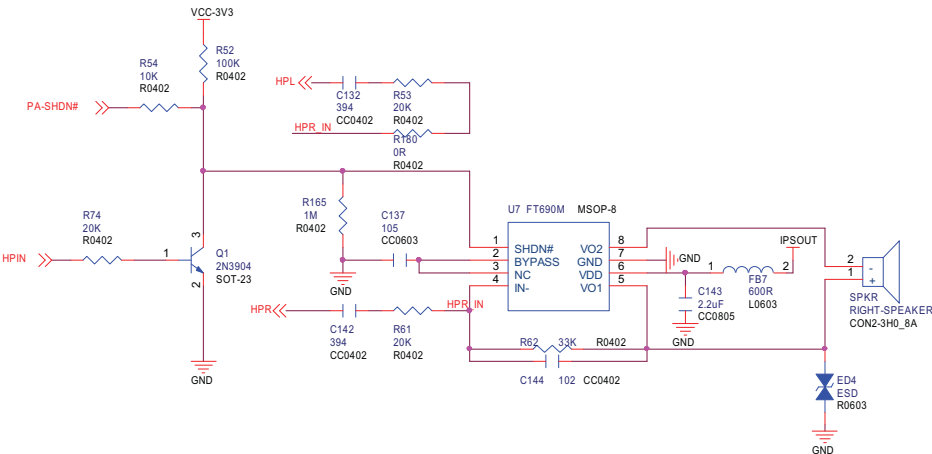
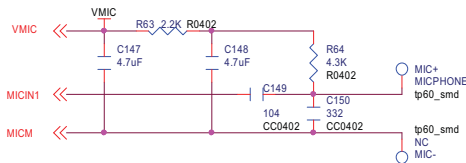
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A3			Rev
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HP-MT-MIC-IR-TVOUT

Head Phone & TVOUT



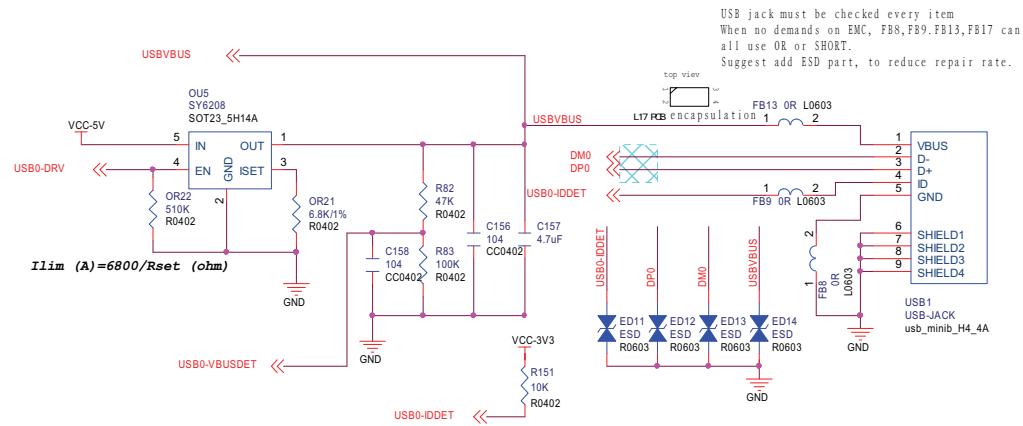
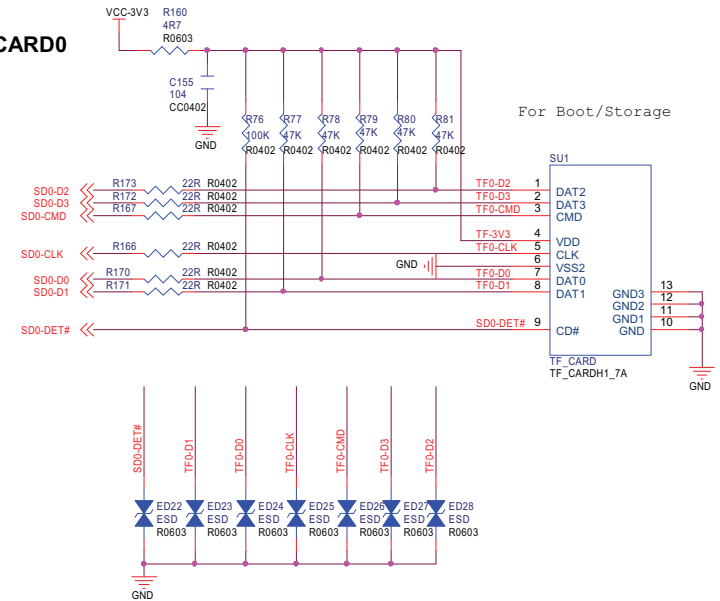
Microphone



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File			
SC3051B			
Size	Document Number	Rev	
A3		<V1.0>	
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USB-CARD

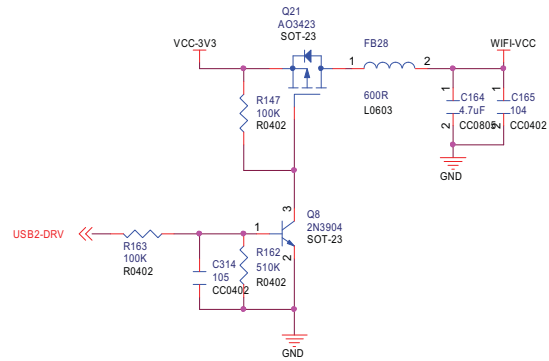
USB

**CARD0**

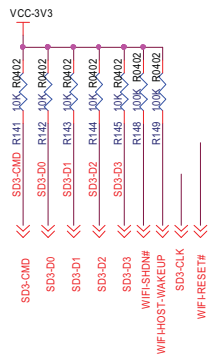
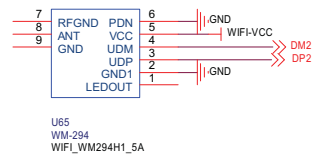
N507H6/N707H6			
Title			
USB-CARD			
Size	Document Number		Rev
A3	<V1.D>		
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W F I 5 6

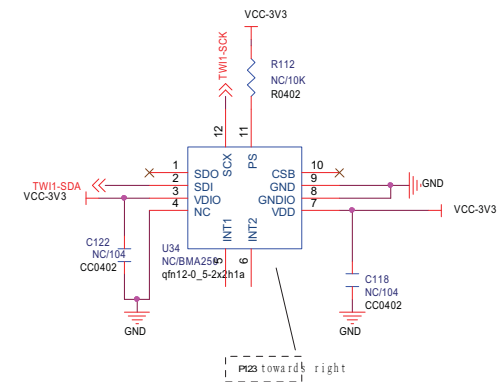
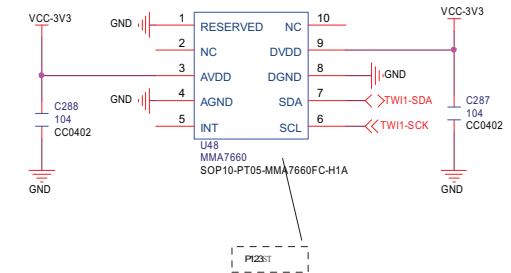
USB WIFI



ANTUV WXYZ < [\ ' 1/2

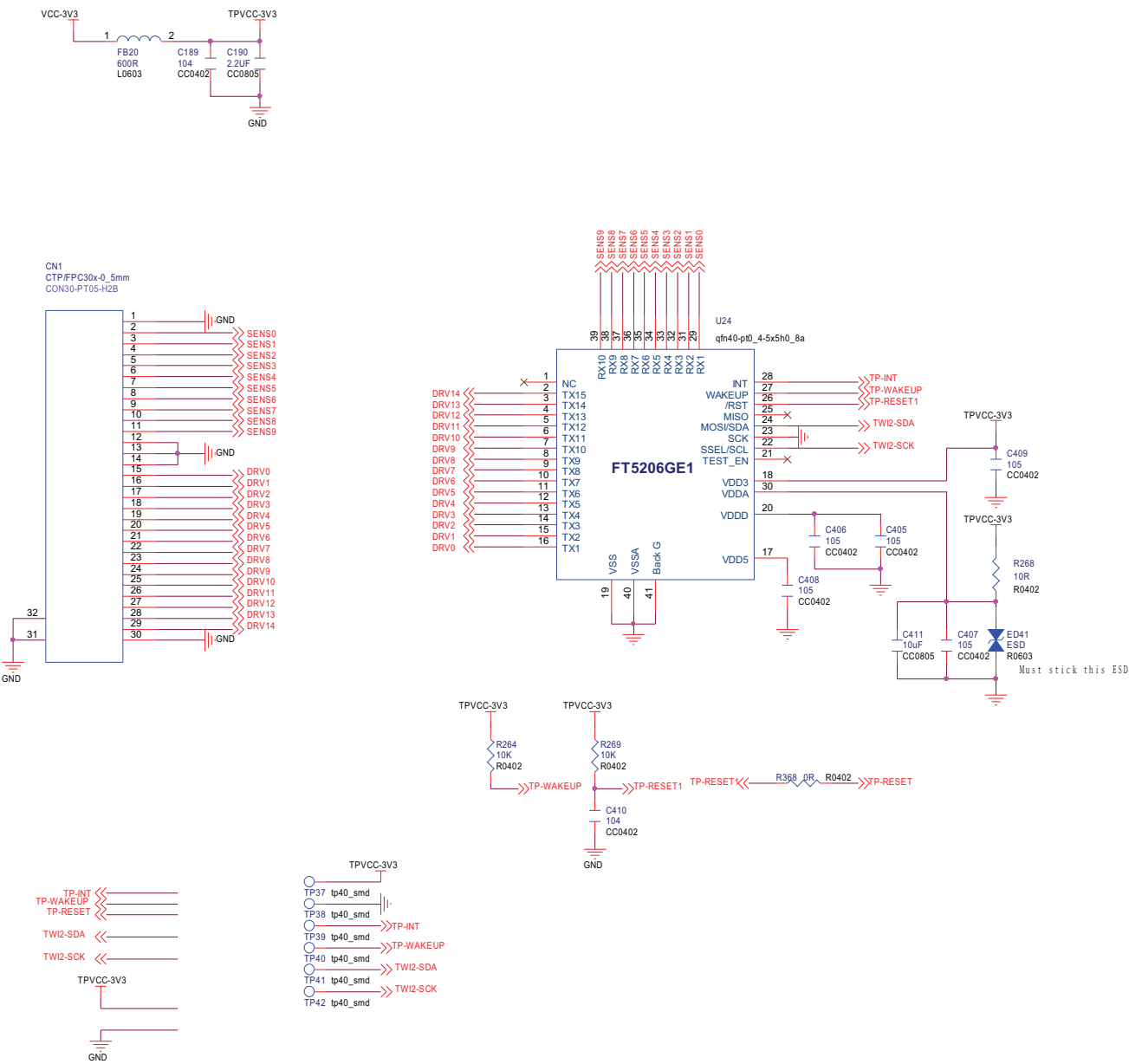


G-SENSOR



N507H6/N707H6			
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WIFI-GSENSOR			
Size	Document Number		Rev
A3	<V1.0>		
Date	Released		
2008.08.16	2008.08.16		16

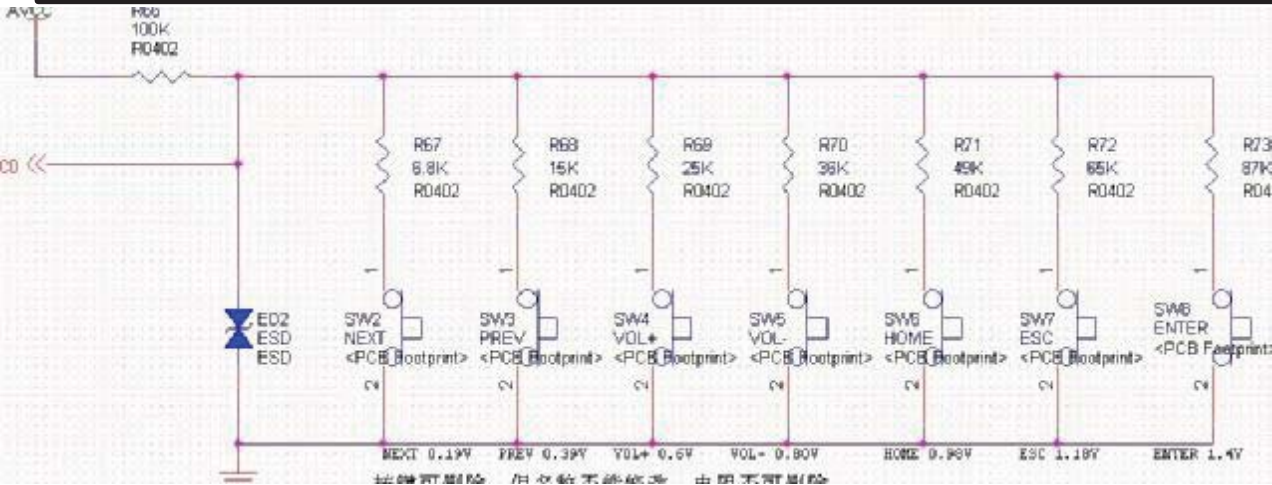
CAP_TP



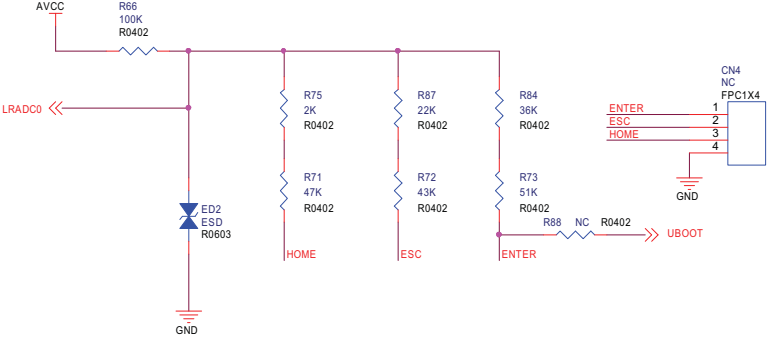
N507H6/N707H6			
Title CAT_TP			
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HDMI

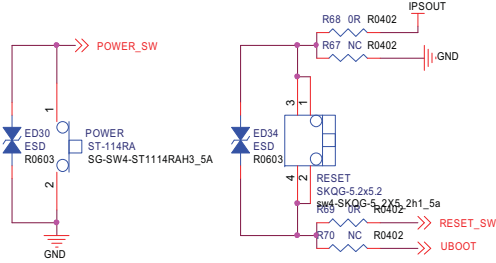
HDMI



KEY



The pin of shielding cover must be layed according to actual demands, the spare pins can be deleted.



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