

Gemini M2 Dongle

The block diagram illustrates the system architecture of the TMS320C49 DSP. The main processing path consists of the following components in sequence:

- UART**: Connected to the **EPF** (External Parallel Flash) via a bidirectional arrow.
- EPF**: Connected to the **Behm** block via a bidirectional arrow.
- Behm**: Connected to the **TX/RX** block via a bidirectional arrow.
- TX/RX**: Connected to the **24C01 RamBo** block via a bidirectional arrow.
- 24C01 RamBo**: A vertical stack of three blocks: **RAMSP** (top), **DSP** (middle), and **MP** (bottom).
- DSP**: Connected to the **IO** block via a bidirectional arrow.
- IO**: Connected to the **Power** block via a bidirectional arrow.
- Power**: Connected to the **USB** block via a bidirectional arrow.
- USB**: Connected to the **Host Computer** via a bidirectional arrow.

Below the main processing path, there is a **16 MHz Crystal** connected to the **24C01 RamBo** block. The **24C01 RamBo** block is also connected to the **Host Memory** block, which is labeled **256K x 16 bit 4Mbit**.