

Appendix B

Circuit (Block) Diagram

1.5/1.5
 0.1/0.1

RESISTOR				
EX: 1K	X	X	X	X
1. CAPACITOR	2. SIZE	3. PERCENTAGE	4. VENDOR	
VALUE				
2. SIZE:	3. PERCENTAGE:	4. VENDOR:		
A 0.1K	A 0.1K	A 0.1K		
B 0.1K	B 0.1K	B 0.1K		
C 0.1K	C 0.1K	C 0.1K		
D 0.1K	D 0.1K	D 0.1K		
E 0.1K	E 0.1K	E 0.1K		
F 0.1K	F 0.1K	F 0.1K		
G 0.1K	G 0.1K	G 0.1K		
H 0.1K	H 0.1K	H 0.1K		
I 0.1K	I 0.1K	I 0.1K		
J 0.1K	J 0.1K	J 0.1K		
K 0.1K	K 0.1K	K 0.1K		
L 0.1K	L 0.1K	L 0.1K		
M 0.1K	M 0.1K	M 0.1K		
N 0.1K	N 0.1K	N 0.1K		
O 0.1K	O 0.1K	O 0.1K		
P 0.1K	P 0.1K	P 0.1K		
Q 0.1K	Q 0.1K	Q 0.1K		
R 0.1K	R 0.1K	R 0.1K		
S 0.1K	S 0.1K	S 0.1K		
T 0.1K	T 0.1K	T 0.1K		
U 0.1K	U 0.1K	U 0.1K		
V 0.1K	V 0.1K	V 0.1K		
W 0.1K	W 0.1K	W 0.1K		
X 0.1K	X 0.1K	X 0.1K		
Y 0.1K	Y 0.1K	Y 0.1K		
Z 0.1K	Z 0.1K	Z 0.1K		

CAPACITOR				
EX: 7P	X	X	X	X
1. CAPACITOR	2. SIZE	3. PERCENTAGE	4. VENDOR	
VALUE				
2. SIZE:	3. PERCENTAGE:	4. VENDOR:		
A 0.1K	A 0.1K	A 0.1K		
B 0.1K	B 0.1K	B 0.1K		
C 0.1K	C 0.1K	C 0.1K		
D 0.1K	D 0.1K	D 0.1K		
E 0.1K	E 0.1K	E 0.1K		
F 0.1K	F 0.1K	F 0.1K		
G 0.1K	G 0.1K	G 0.1K		
H 0.1K	H 0.1K	H 0.1K		
I 0.1K	I 0.1K	I 0.1K		
J 0.1K	J 0.1K	J 0.1K		
K 0.1K	K 0.1K	K 0.1K		
L 0.1K	L 0.1K	L 0.1K		
M 0.1K	M 0.1K	M 0.1K		
N 0.1K	N 0.1K	N 0.1K		
O 0.1K	O 0.1K	O 0.1K		
P 0.1K	P 0.1K	P 0.1K		
Q 0.1K	Q 0.1K	Q 0.1K		
R 0.1K	R 0.1K	R 0.1K		
S 0.1K	S 0.1K	S 0.1K		
T 0.1K	T 0.1K	T 0.1K		
U 0.1K	U 0.1K	U 0.1K		
V 0.1K	V 0.1K	V 0.1K		
W 0.1K	W 0.1K	W 0.1K		
X 0.1K	X 0.1K	X 0.1K		
Y 0.1K	Y 0.1K	Y 0.1K		
Z 0.1K	Z 0.1K	Z 0.1K		

Layout note:

Component side	
ISA	[1,2]
VCC	[2]
PCI	[3]
CLOCK MEMORY, LCD	[4]
GND	[5]
ISA	[6]
Solder side	

ITEM	FILE NAME	DESCRIPTION
01	LINK	AMBER.SCH LINK SHEET
02	IMMO.SCH	NMIO CONNECTOR
03	DRAM-1.SCH	SODIMM CONNECTOR BANK 0.1
04	DRAM-2.SCH	SODIMM CONNECTOR BANK 2.3
05	CLK.SCH	CLOCK GENERATOR
06	PIIX4.SCH	PIIX4E SOUTH BRIDGE
07	PRESET.SCH	FAN CONNECTOR
08	USB.SCH	USB CONNECTOR
09	ITIC.SCH	IIC BUS SELECTOR
10	BIOS.SCH	FLASH ROM (2MB BIOS)
11	RESISTOR.SCH	PULL UP / PULL DOWN RESISTOR
12	DEVICEN.SCH	HDD CORDOM.LS120.FDD/IF
13	ITIC.SCH	RTC BATTERY
14	KB.SCH	KEYBOARD CONTROLLER
15	PS2.SCH	PS2 / GLIDE PAD / INTERNAL KB CONN
16	LED.SCH	LED CONNECTOR
17	PIIC.SCH	MICRO CONTROLLER (PIC 16C62-04 SSOP)
18	PIIC.SCH	MICRO CONTROLLER POWER
19	ISB9.SCH	SUPER I/O SMC 37N769
20	PARALLEL.SCH	PARALLEL PORT
21	SERIAL.SCH	SERIAL PORT
22	ICDM.SCH	VGA CONTROLLER - NMCS
23	INSVGA.SCH	VGA CONTROLLER/LVDS TRANSFER
24	INSPOWER.SCH	LCD CONNECTOR
25	INELCON.SCH	CRT CONNECTOR
26	INSCON.SCH	CARD BUS CONTROLLER (TI1220)
27	1220.SCH	CARD BUS SOLT
28	PCCARD.SCH	CARD BUS ZV PORT
29	12VPORT.SCH	CARD BUS CHIP
30	ALOM2.SCH	AUDIO CODEC
31	IGACODEC.SCH	AUDIO AMP & JACK
32	IGAMPJACK.SCH	DOCKING CONNECTOR
33	DOCKING.SCH	MODEM/LAN CONNECTOR
34	MODCON.SCH	DC-DC REGULATOR
35	DC1IF.SCH	DC-DC CONNECTOR
36	DC2IF.SCH	DC-DC CONNECTOR

BUSMASTER	CHIP
REQ0	TI(1221)
REQ1	VGA
REQ2	AUDIO ESS
REQ3	MODEM/LAN
REQ4	
REQ5	

REQ	CHIP
REQ0	N.C.
REQ1	ESS
REQ2	
REQ3	
REQ4	
REQ5	

PC/INT	CHIP
INTA	TI(1221)
INTB	LAN/MODEM
INTC	VGA/ESS
INTD	USB

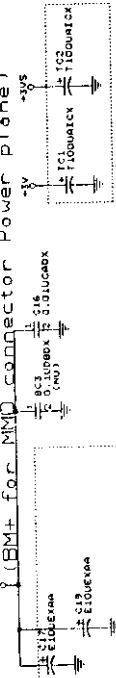
DMA Channel	Device
DMA0	FIR (1221)
DMA1	FIR (1221)
DMA2	FLOPPY DISK
DMA3	AUDIO
DMA4	(Cassade)
DMA5	Unused
DMA6	Unused
DMA7	Unused

IRQ Channel	Description
IRQ0	System timer
IRQ1	Keyboard
IRQ2	(Cassade)
IRQ3	LAN/MODEM
IRQ4	Serial port
IRQ5	AUDIO/VGA/USB
IRQ6	FLOPPY DISK
IRQ7	PCI
IRQ8	PCI
IRQ9	FIR (1221) (MODEM/LAN)
IRQ10	Cardbus
IRQ11	ps/2 mouse
IRQ12	FPU
IRQ13	HDD
IRQ14	COROM
IRQ15	

REV. 0.4 (8/1) REV. 0.5
8/2 B03 CHANGE TO NU PART

E10UEXPB is OSCON PART NO: 10-30075-01

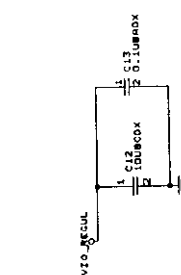
(BM+ for MMD connector Power plane)



Note4: Close to CN14 connector.



SW4 port1&2 ON port3&4 OFF for Tiliamook CPU
SW4 port1&2 OFF port3&4 ON for MPII CPU



NU PARTS :
R21,C14,R28,C15,B03

MMD CONNECTOR



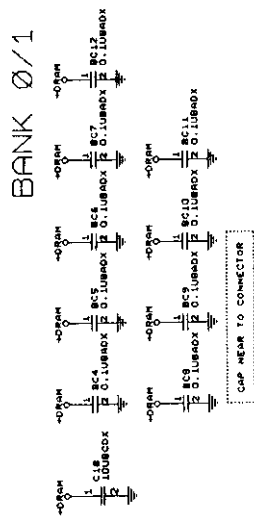
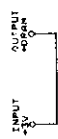
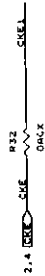
CHANGE L18 PIN NAME

CHG TRACE2N/PIN

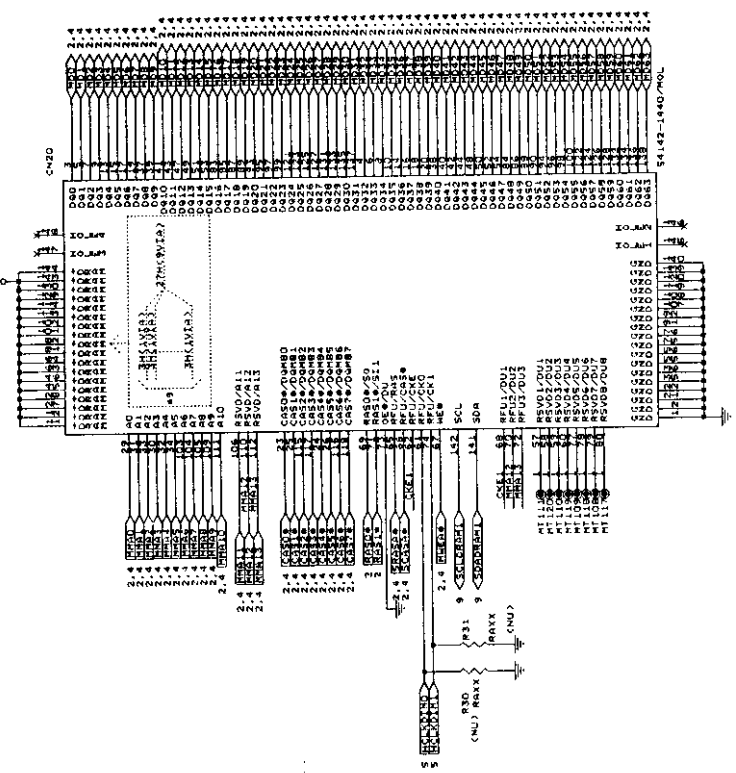
TOTAL 32 VIA

INTEL MMD MODULE DESIGN SPEC: XI, OIUP XI
5V FILTER CAP = 100UF XI, 1UF XI
3/5 FILTER CAP = 100UF XI, 1UF XI

REV	DATE	BY	CHK	APP	DESCRIPTION
0.1	8/1	8/1	8/1	8/1	8/1
0.2	8/2	8/2	8/2	8/2	8/2
0.3	8/3	8/3	8/3	8/3	8/3
0.4	8/4	8/4	8/4	8/4	8/4
0.5	8/5	8/5	8/5	8/5	8/5

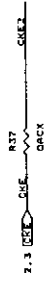


NU PARTS :
R30, R31

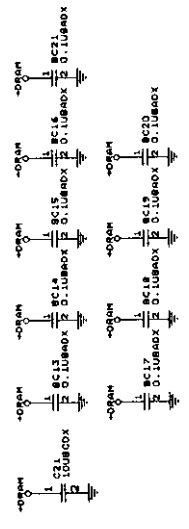


BANKS 0/1

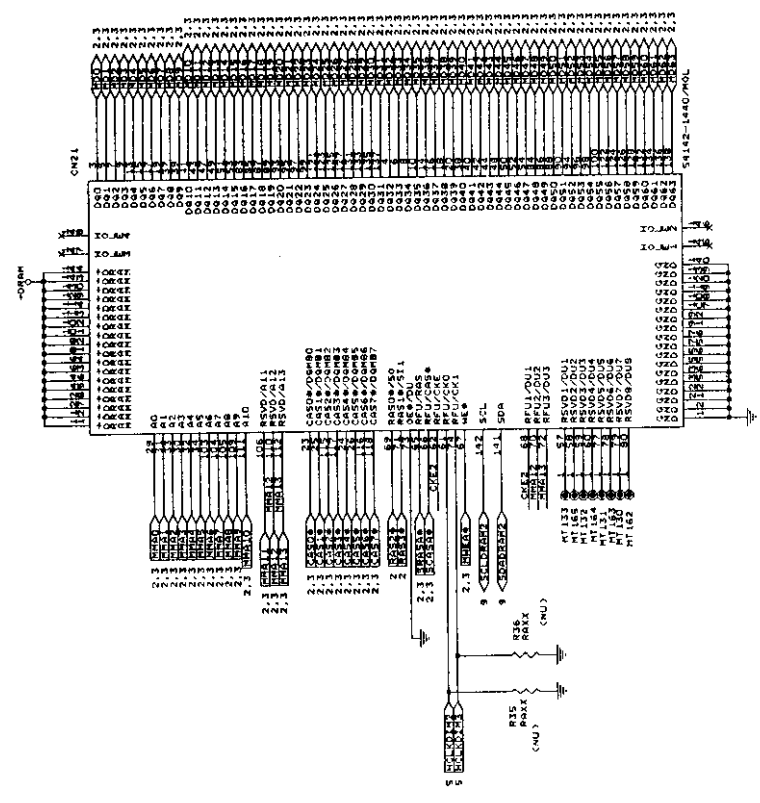
FILE	FIC AMBER
STEP	S001M144
DOCUMENT NUMBER	DRAM-1.SCH (PCB VER.0.8)
REV	0
DATE	2008-12-15



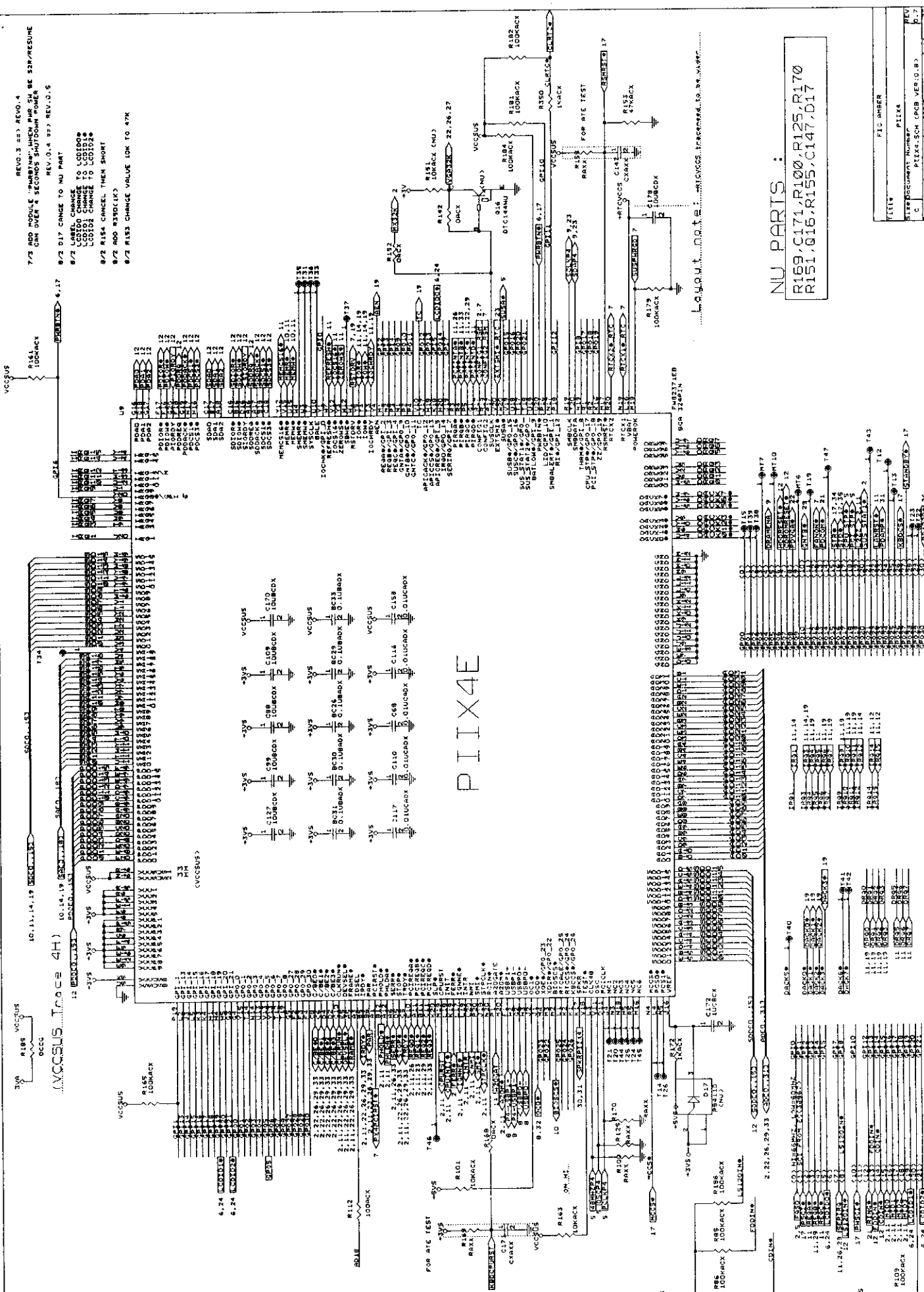
BANK 2/3



NU PARTS :
R35, R36



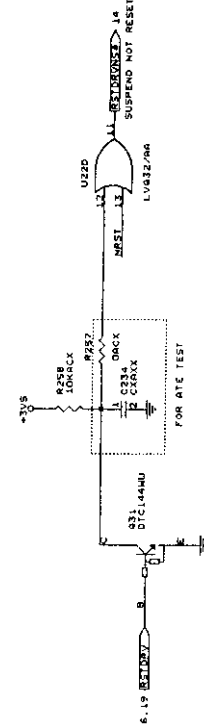
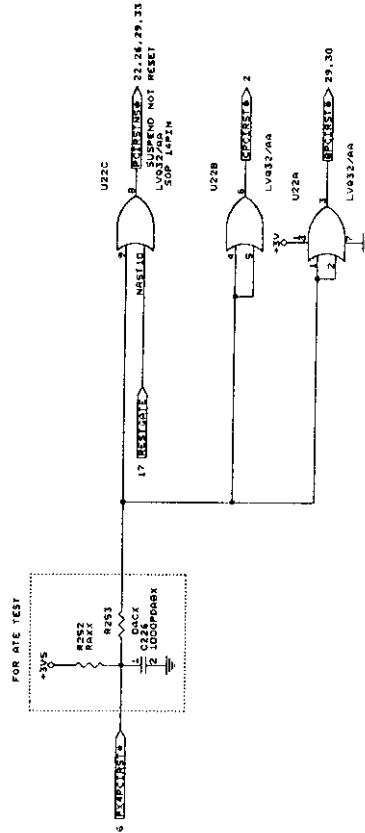
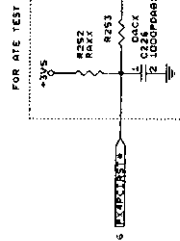
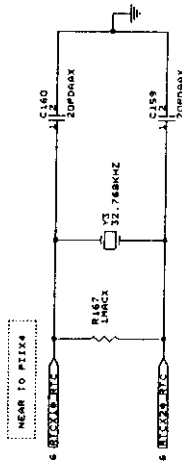
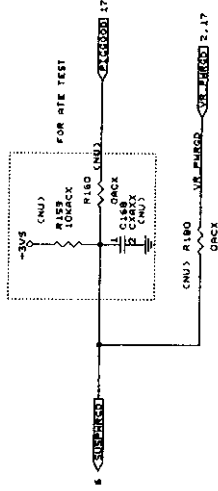
REV. 3.23 REV. 4
7/7 ADD MODULE "PIIX4" WHEN SUPPLY BE 32P-PRESUME
ON OVER 5 SECONDS SHUTDOWN POWER
REV. 0.4 223 REV. 0.5
8/2 DIT CHANGE TO NU PART
8/7 LABEL CHANGE
L01000 CHANGE TO L01000
L01001 CHANGE TO L01001
L01002 CHANGE TO L01002
8/2 R154 CANCEL THEN SHORT
8/2 ADD R150(1K)
8/2 R153 CHANGE VALUE 10K TO 47K



NU PARTS :
R159, C174, R100, R125, R170
R151, Q16, R155, C147, D17

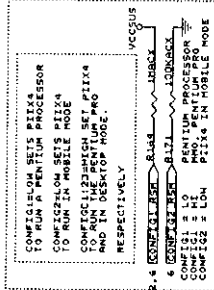
FIG. 000000
PIIX4E
PIIX4, SCH. (PCB VER. 0.8)
P. 2
DATE: 01/01/91
REV. 0.8

POWER GOOD circuit

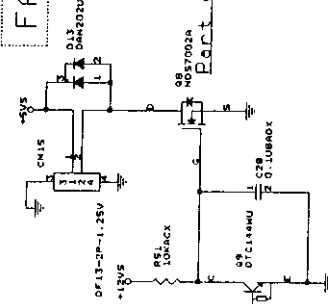


NU PARTS :

R252, C234, R159, C168
R160



FAN



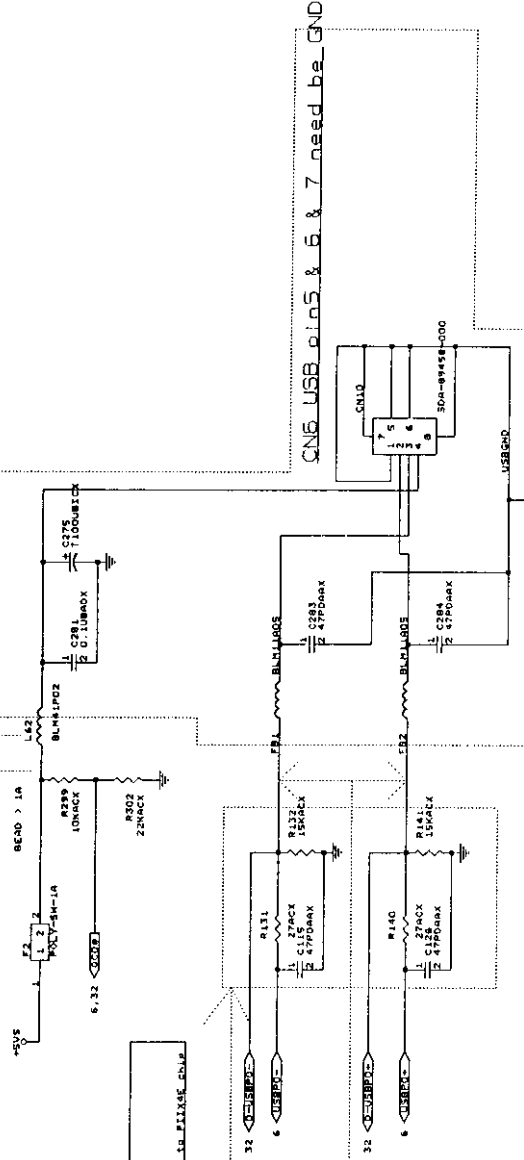
Part no: 05-20181-00

FILE	PIC ARMER
FILE	PICX CONTROL LOGIC
FILE	RESET.SCH (PCB VER10.8)
FILE	0.7
FILE	0.7

BLM41P02 3A	DC resistance	Size
BLM11A05 0.2A	0.05	4.5x1.6
	0.2	1.6x0.8

USB

USB power trace as wide as possible
CLOSE USB CONNECTOR



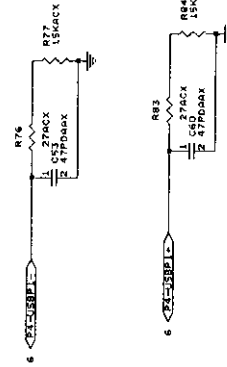
Layout note:

This area capacitor & connection need close to PLIC chip

Layout note: Same length
USBP0-
USBP0+

NU PARTS :

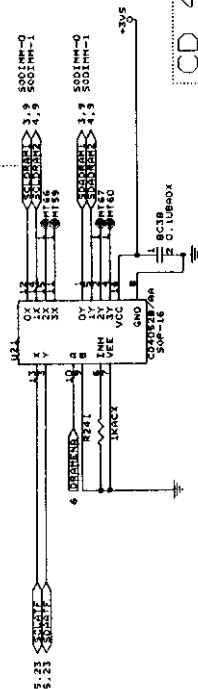
C283,C284



File	PCB DHEER
Size	USB
Current Number	1
USB SON (PCB VER 0.0)	0.0
File	PCB DHEER 15/10/2015

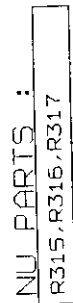
R255, R240, R242, R251

Read	SDRAM-2 slot	EPROM need	DRAMENA: H
Read <th>SDRAM-1 slot</th> <th>EPROM need</th> <th>DRAMENA: L</th>	SDRAM-1 slot	EPROM need	DRAMENA: L



CD 4052 True table

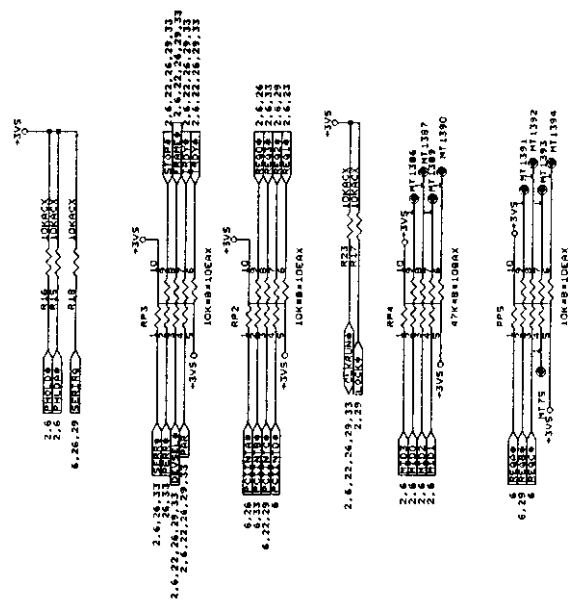
[illegible]



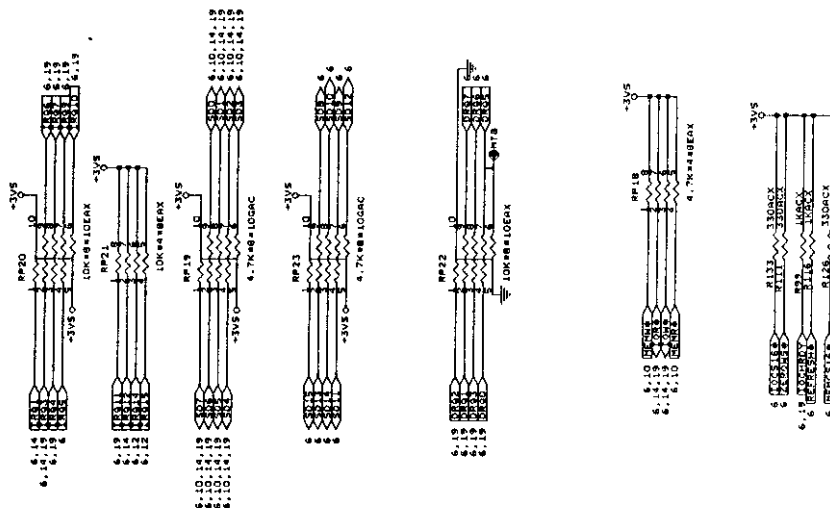
NOET: BIOS Socket 1002-B32T1R

File	Title	Size	Document Number	PLV
C	BIOS.SCH (PCB VER:0.9)	0.7		
Date:	October 14, 1998	13 of 15		

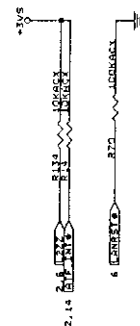
PCI BUS PULL RESISTOR



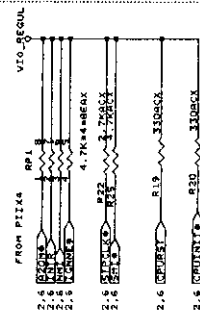
ISA BUS PULL RESISTOR



GPIO PULL RESISTOR



CPU BUS PULL RESISTOR



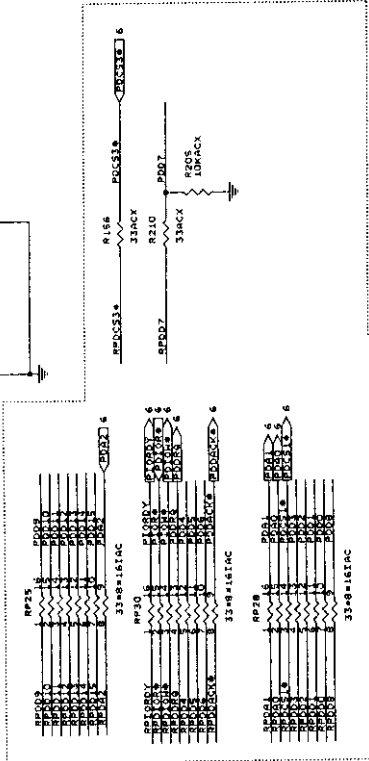
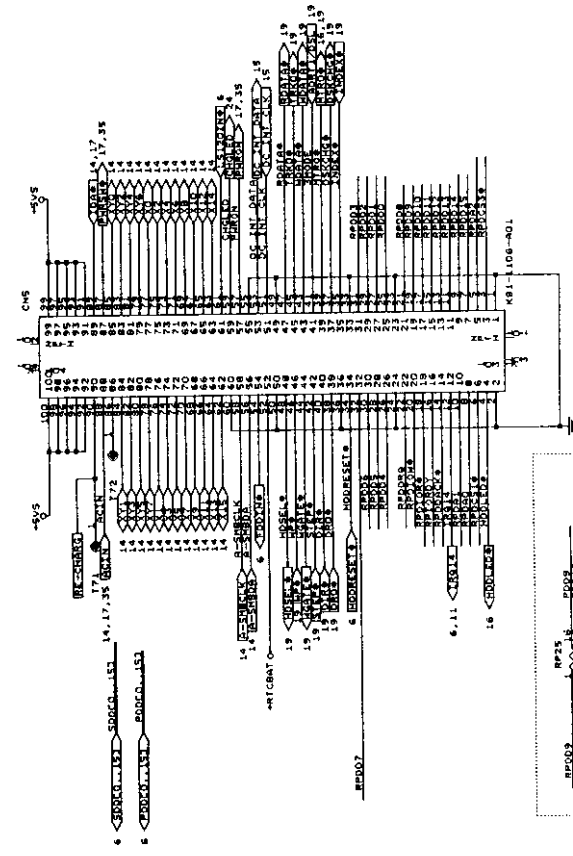
REV. 3.22) REV. 4
 7/3 CHG292 CANCEL T70 THEN CHANGE
 TO 1046
 REV. 4.21) REV. 7
 9/11 CHG P1490 ADD MODULE "RE-CHARGE"

HDD connector
 max 1.5A max 0.32

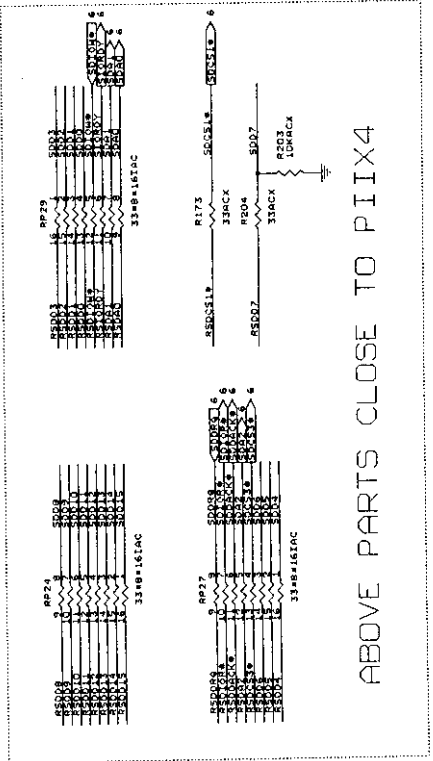
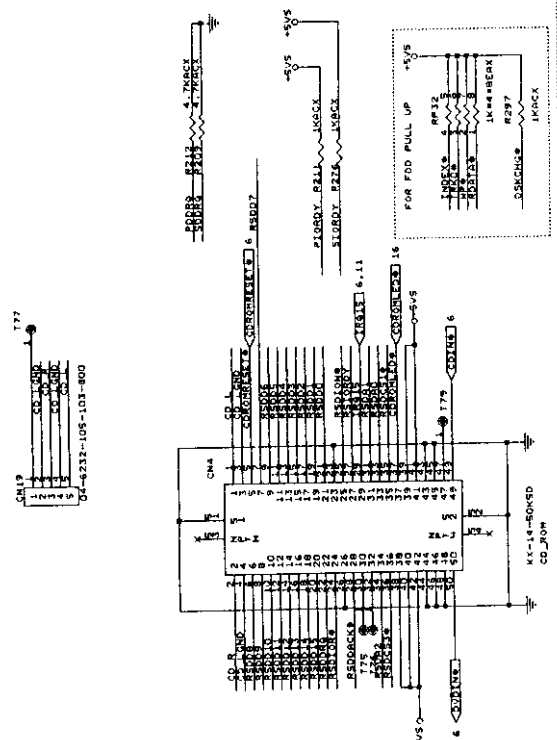
POWER
 max 1.5A
 max 0.32
 max 0.32
 max 0.32
 max 0.32

FDD connector
 max 0.75A
 max 0.32

DEVICE connector circuit to I/O Board

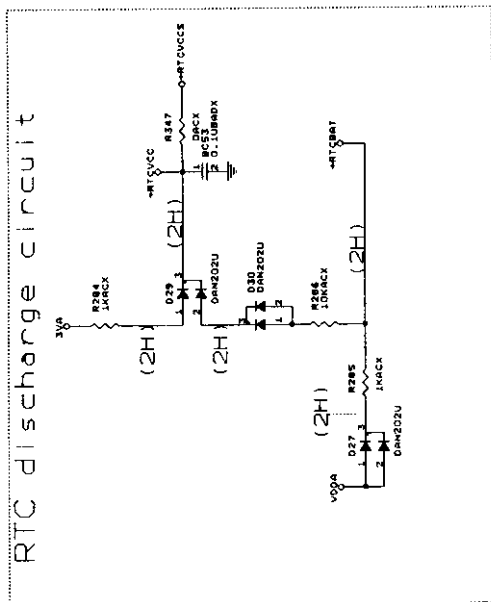


ABOVE PARTS CLOSE TO PIIX4



ABOVE PARTS CLOSE TO PIIX4

REV.0.4 33) REV.0.5
 8/22 ADD R347(D) THEN CONNECT TO +RTCVCDS



Under AC Adapter			
Power ON	Suspend	Power Off	
Charge	Charge	Charge	

Under Battery			
Power ON	Suspend	Power Off	
Charge	Charge	Charge	

5/28 U23 #1N79 R270 10K CHANGE TO 47K
CONNECT FROM +5V TO GND
THEN ADD MODULE WAKEUP TO PIC #1N16

5/28 U23 #1N74 174 CHANGE
CHANGE TO MODULE LYN_R31#

REVO. 2 --> REVO. 3

6/17 U23 PIN20.2L PULL-UP 10K TO -5V
6/17 U23 PIN75 ADD 0.01UF TO GND

REVQ.3 => REVQ.4

7/3 8139.81398 PULL UP TO +5VS

REV. C. J. F. 3

0/2 MODIFY RTC CLEAN FUNCTION

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SM3(5) CHANGE TO GND
SM3(4) ADD TWO PTH AND RESISTOR(R348,R349)

```

THEY #348 25 NO PART

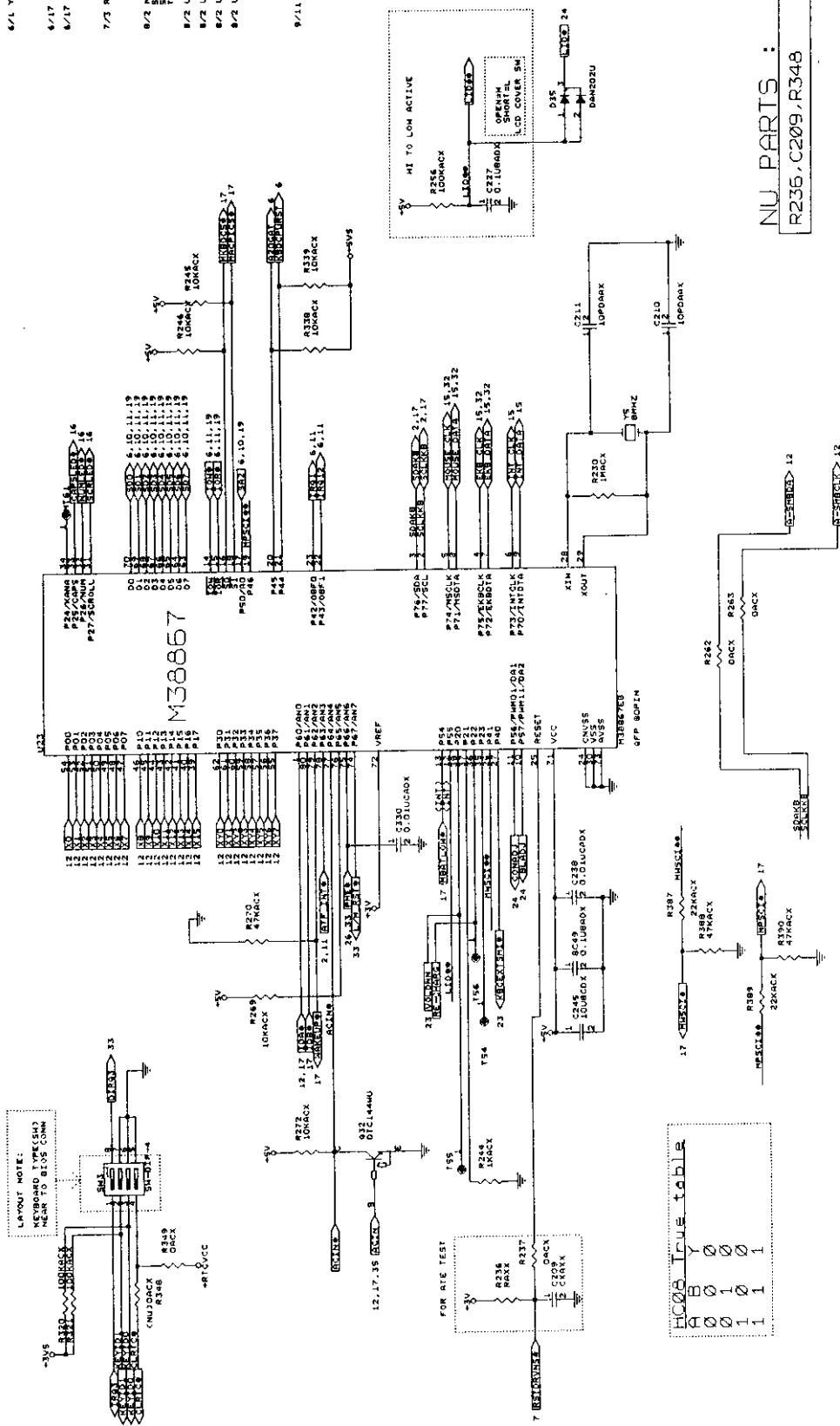
1/2 U23C123 ADD BIOECONOMY

07Z 023030Z ADD A0000000000000000
07Z 113402Z 000 0303 0700

[illegible]

REV-0.6 ==> REV.0.7

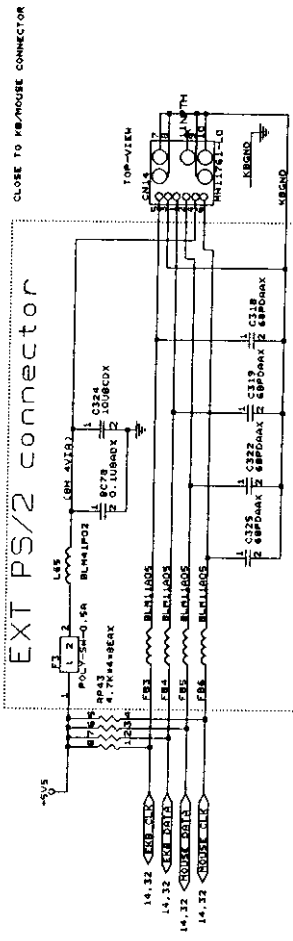
9/11 U23 PIN36 ADD MODULE "RE-CHARG"



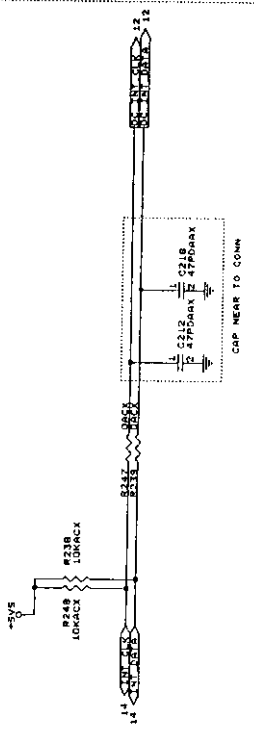
NU PARTS :
R236,C209,R348

HC08 True table	
A	B
0	0
0	0
0	1
1	0
1	1

Glide Pod TM41PDG-220-2 Synaptics

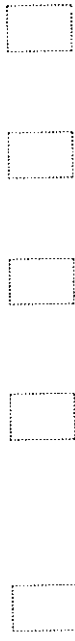


GLIDE PAD pull resistor

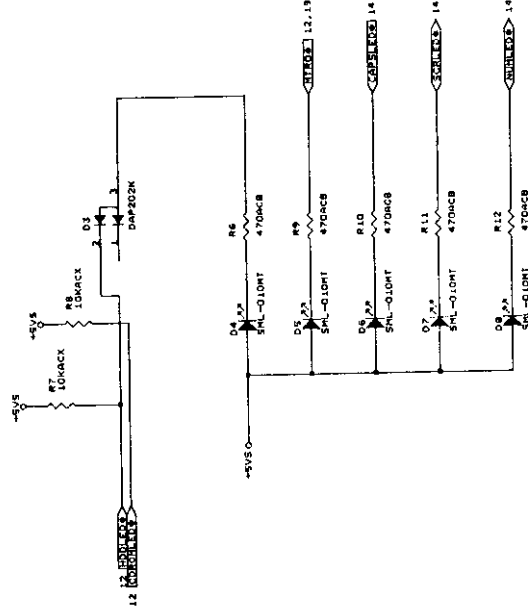


FILE	PTC NUMBER
PTC114	PTC114
PTC115	PTC115
PTC116	PTC116
PTC117	PTC117
PTC118	PTC118
PTC119	PTC119
PTC120	PTC120
PTC121	PTC121
PTC122	PTC122
PTC123	PTC123
PTC124	PTC124
PTC125	PTC125
PTC126	PTC126
PTC127	PTC127
PTC128	PTC128
PTC129	PTC129
PTC130	PTC130
PTC131	PTC131
PTC132	PTC132
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PTC135	PTC135
PTC136	PTC136
PTC137	PTC137
PTC138	PTC138
PTC139	PTC139
PTC140	PTC140
PTC141	PTC141
PTC142	PTC142
PTC143	PTC143
PTC144	PTC144
PTC145	PTC145
PTC146	PTC146
PTC147	PTC147
PTC148	PTC148
PTC149	PTC149
PTC150	PTC150
PTC151	PTC151
PTC152	PTC152
PTC153	PTC153
PTC154	PTC154
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PTC156	PTC156
PTC157	PTC157
PTC158	PTC158
PTC159	PTC159
PTC160	PTC160
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PTC193	PTC193
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PTC196	PTC196
PTC197	PTC197
PTC198	PTC198
PTC199	PTC199
PTC200	PTC200

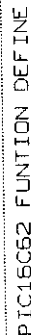
LED STATUS



HDD&CDROM FDD Caps Scr Num



3V transfer 5v
Include DIODE HT 1359



1. POWER ON/OFF
2. POWER GOOD SIGNAL GENERATE
3. INDICATOR LED ON/OFF
4. PANEL ON/OFF TIMING CONTROL
5. SUSPEND/RESUME CONTROL
7. SUSPEND NOT RESET GATE

RA4: OPEN DRAIN OUTPUT, SCHMITT TRIGGER INPUT

STAT 2

R353, R354

PTC 16072

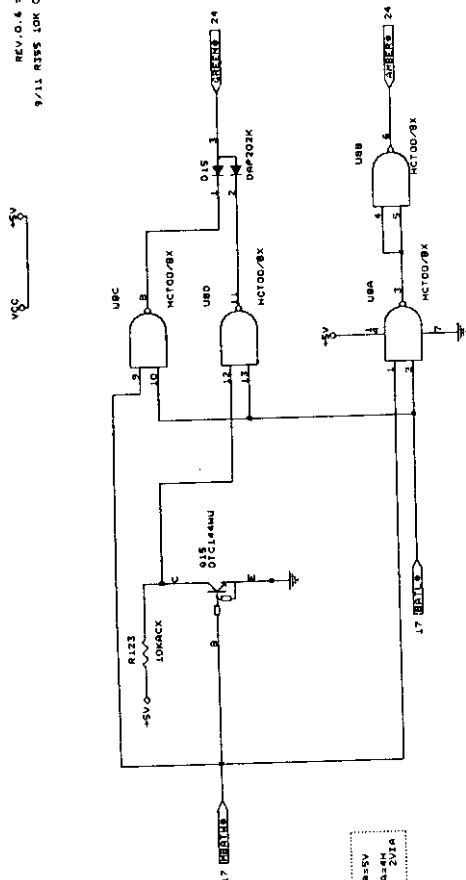
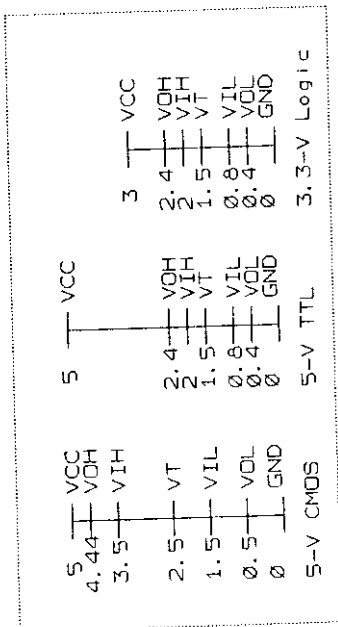
```
PIC16C72 IIC ADDRESS 04  
38A13 IIC BUS TO SMART BATTERY  
  
OTHER IIC BUS TYPE TO PIX4  
AFTER BOOT SAVE SUPEND TO RAM  
AND RECALL FROM TOSTO TIME  
THEN SAVE TO DISK+MER  
SUB-BATTERY DESIGN GUIDE  
1.5 X 20mm X 10mm = 1 X 3P  
1.5 WATT CONSUMPTION DURING  
STANDBY MODE  
TO AGIL SUB-BATTERY WORKING FOR  
1. CHARGE SUB-BATTERY CURRENT
```

REV.0.3 22> REV.0.4
7/3 U16 CHANGE PART TO L22951

REV.0.4 22> REV.0.5
8/2 D24 CHANGE TO NU PART
8/2 D23 PART CHANGE TO 88411D
8/2 840(B,E) ADD R355
8/2 U16(9) ADD MODULE M2
8/2 U16(3) CONNECT TO 840(C)

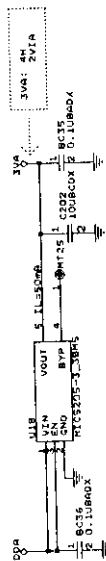
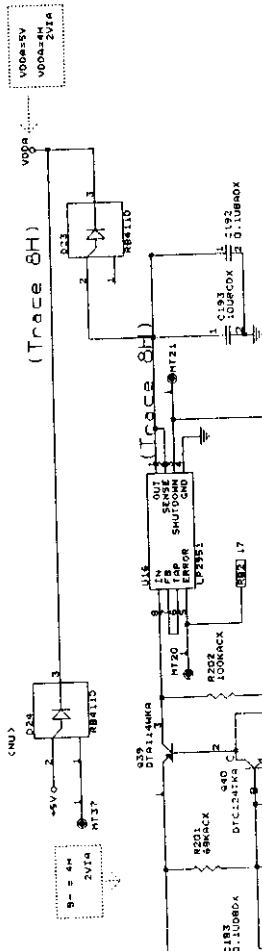
REV.0.6 22> REV.0.7
9/11 R355 10K CHANGE TO 18K

RB411D	Forward voltage
	V110-24V/0.3V(I=10mA)
	V1250-25V/0.3V(I=10mA)



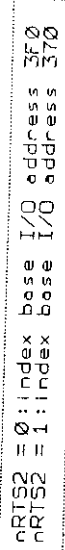
POWER STATUS LED TRUTH TABLE :

	UNDER AC		GREEN#	AMBER#
	MBATW#	BATL#		
NORMAL	1	1	0	1
BATTERY WARNING (8% OR LESS)	1	1	0	1
BATTERY LOW (3% OR LESS)	1	1	0	1
NORMAL	UNDER AC		GREEN#	AMBER#
	MBATW#	BATL#		
	1	1	0	1
BATTERY WARNING (8% OR LESS)	0	1	0	0
BATTERY LOW (3% OR LESS)	1	0	1	0



NU PARTS:

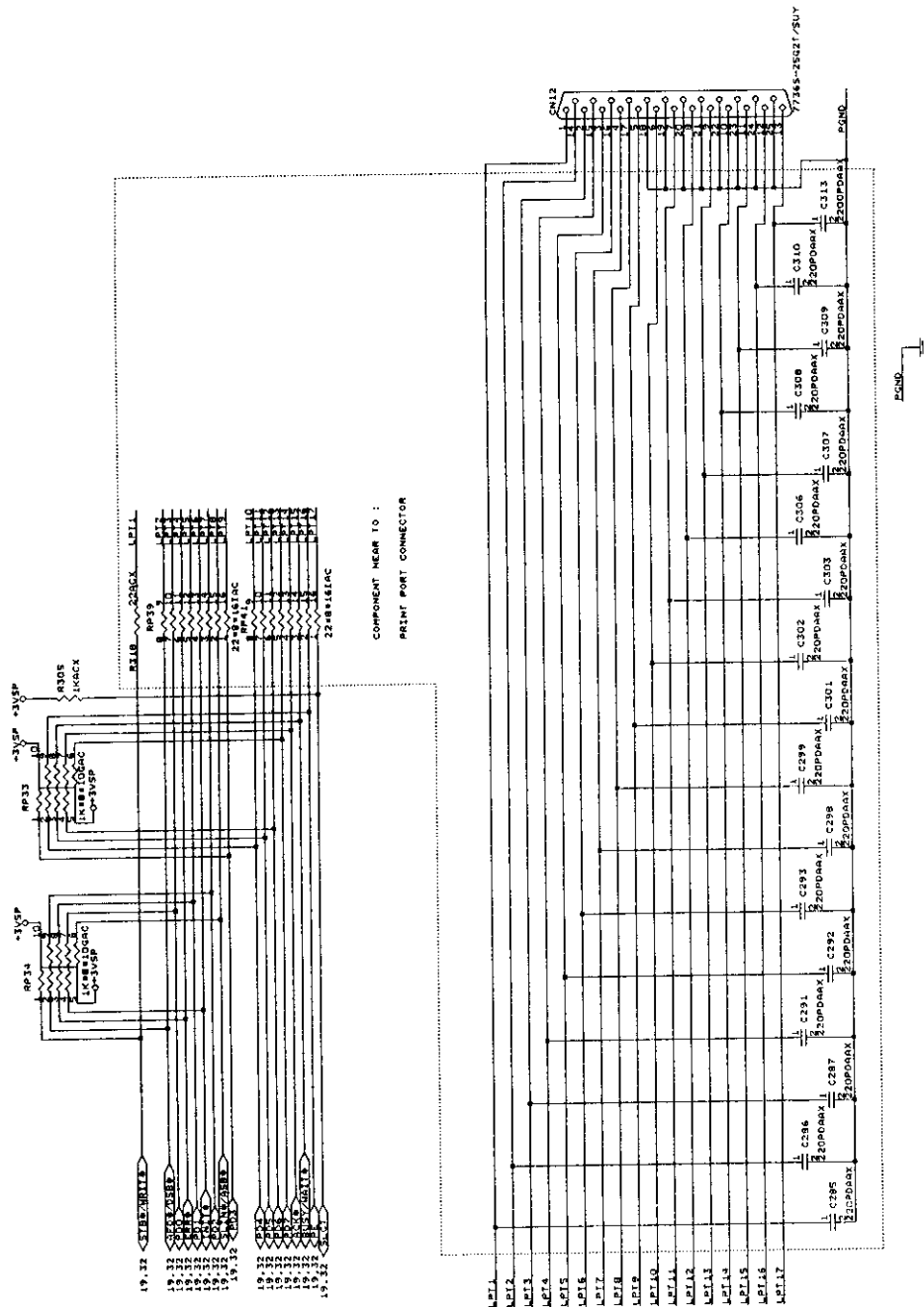
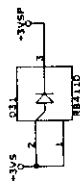
D24



R289, R288, R287, C258, R303, C265
R218, R219, R225

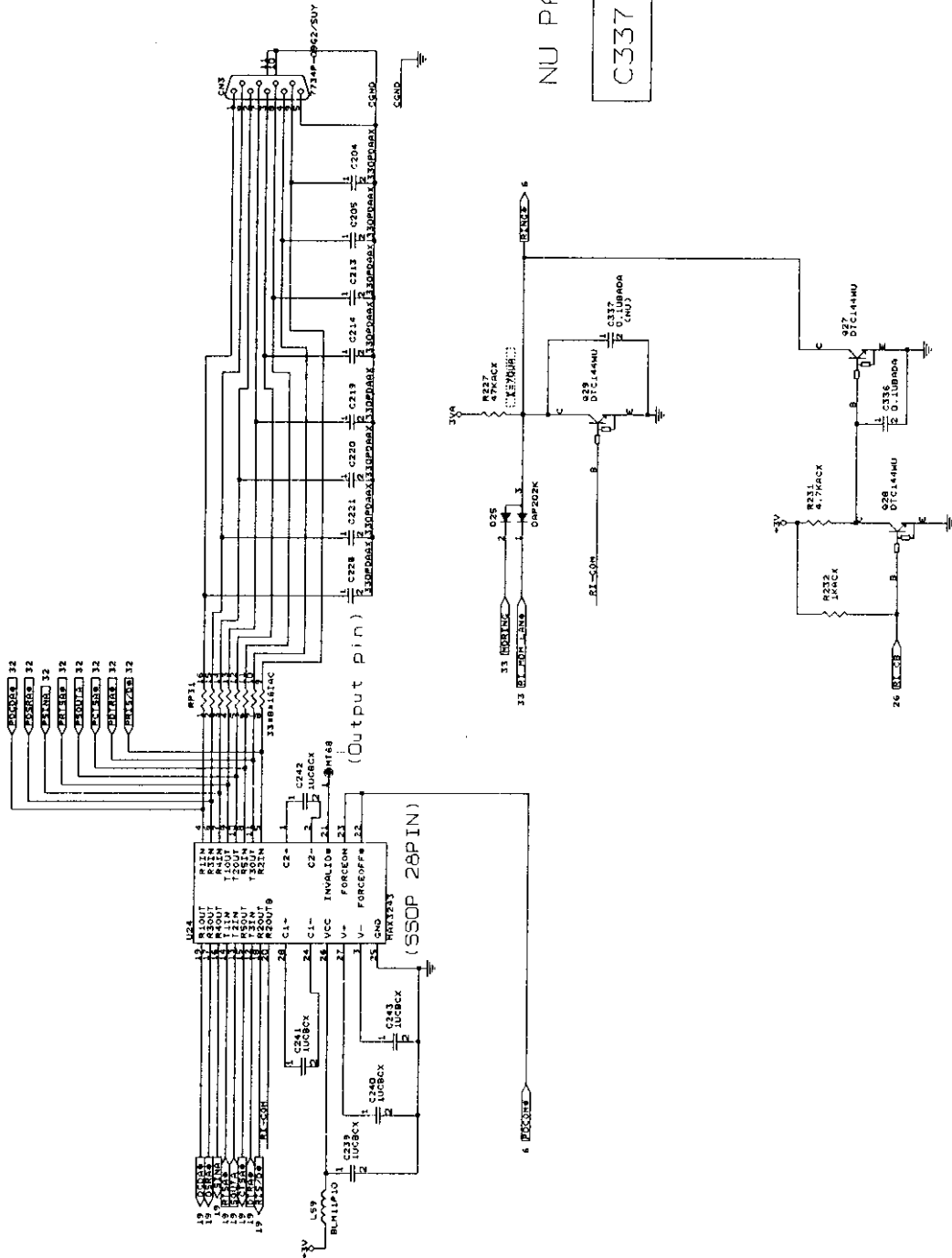
FILE	SUPER I/O SMC37C669FALV	REV
Size Document Number		0.7
C	669.5CH (PCB VER:0.8)	
Date:	October-1 1998	

PRINT PORT STANDARD CIRCUIT



COMPONENT NEAR TO :
PRINT PORT CONNECTOR

REV. 0.4 => REV. 0.5
 8/2 ADD C337, C338 THEN C337 IS NU PART
 8/2 R232 VALUE CHANGE TO 1K
 8/2 R231 VALUE CHANGE TO 4.7K



NU PARTS:

C337

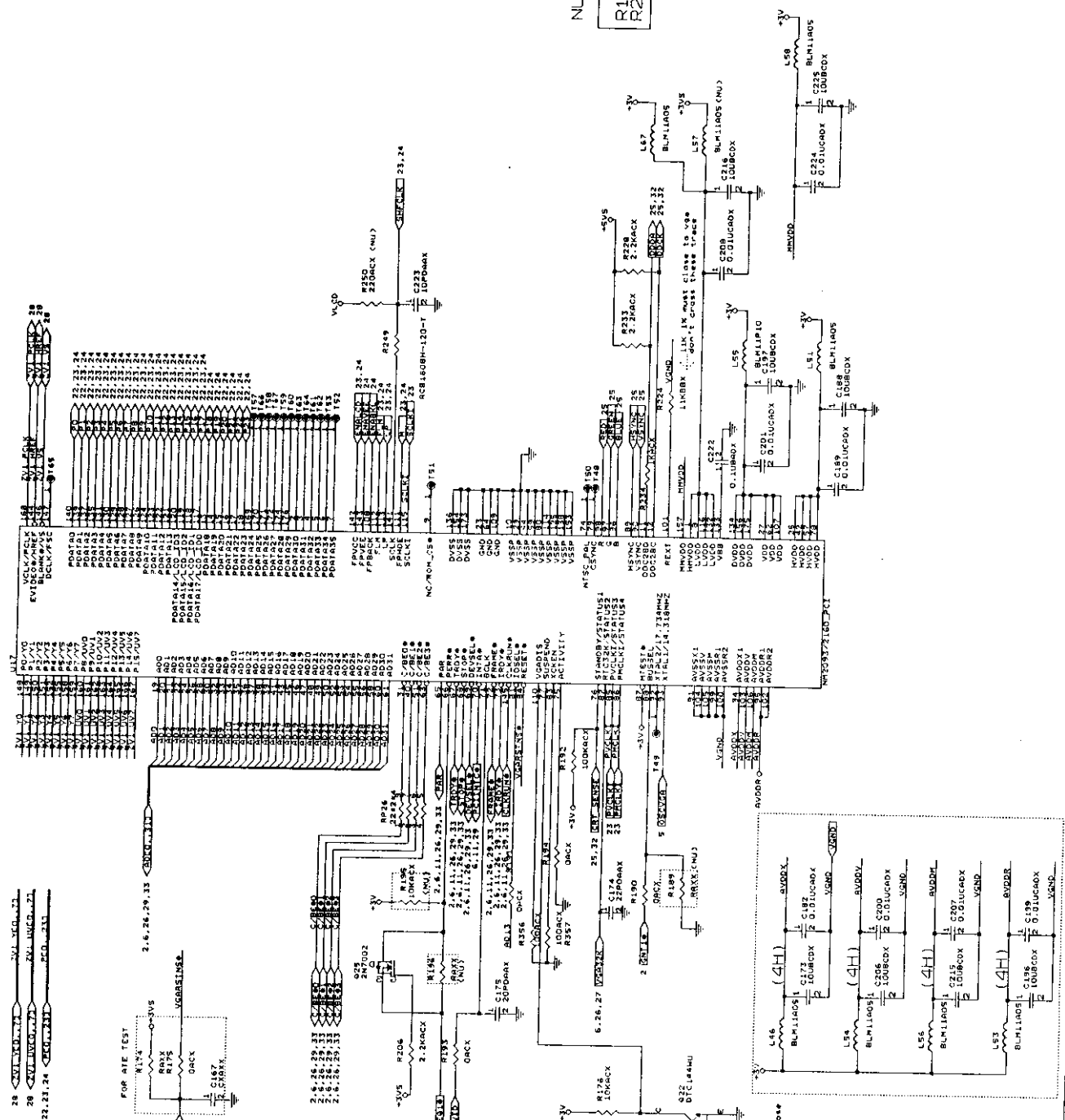
FILE	PIC AMBER
SHEET	1
SERIAL PORT	
DOCUMENT NUMBER	100
COMP. SCH. (PCB VER. 0.8)	0.7
DATE	1999-04-11

REV.0.4 203 REV.0.5
 8/7 L57 CHANGE TO NU PART
 8/7 C216 AND L67 TO +3V
 8/7 U17(L10,83) MOD R356,R357 TO GND

REV.0.6 203 REV.0.7
 9/11 R249 0 CHANGE TO BEAD FOR EMI
 9/11 C223 FROM NU CHANGE TO 100P

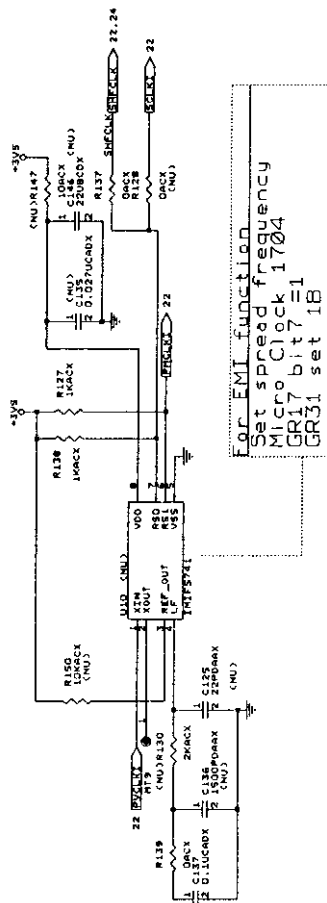
NU PARTS :

R174,C167,R196,R195,R189,C175
 R250,L57

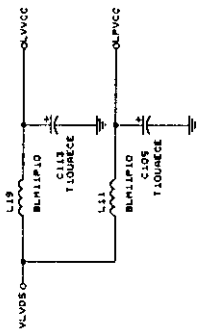
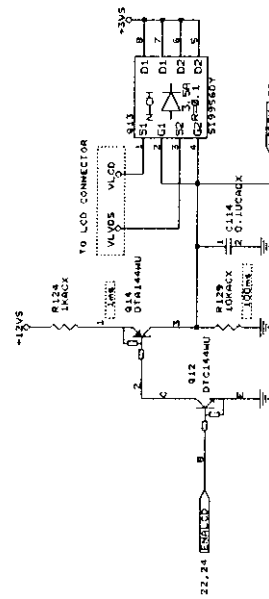


FILE	PCB AMBER
SHEET NUMBER	VGA CONTROLLER
REV	REV.0.7 (PCB VER.0.8)
DATE	05/09/93 13:59

VGA clock generator circuit



VGA & LCD power circuit



```

REV0.2  -> REV0.3
6/17 Q13 PIN4 ADD MODULE LCD5V

REV0.4 => REV0.5
8/2  R12B CHANGE TO M0 PART
8/2  U19C113 ADD R18M,R18A
8/2  U19C113 ADD MODULE VOL0MM
8/2  U19C113 ADD MODULE VOL0M

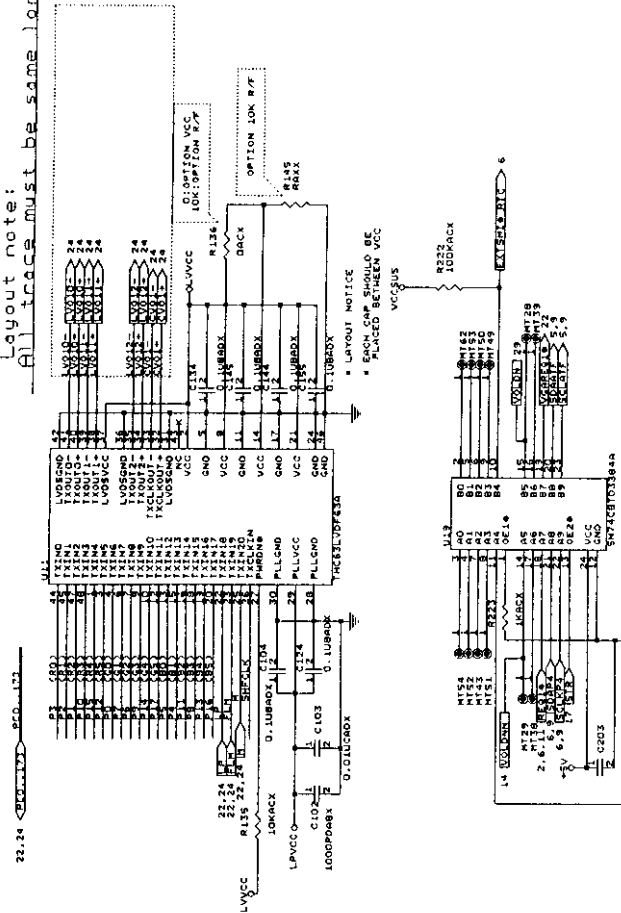
REV0.6 => REV0.7
9/11  U10D CHANGE SOME CAPS TO: C136,C146,R146
9/11  U10D,C136,C138,C146,R146
9/11  W232 FROM 32K CHANGE TO 10K

```

THC63LVDF63A01n14 NC

18bit LVDS Panel (65MHz)

Layout note:
All traces must



NU PARTS :

R137, R136, R145, U10, C125, C135, C146
C137, R139, R128, R150, R130, R147

File	VGA POWER	Size	Document Number	REV
FIC AMBER		C	NSPOWER.SCH (PCB VER:0.9)	0.7
Date:	October 14, 1998	Sheet	23	of

REV0.2 --> REV0.3

```

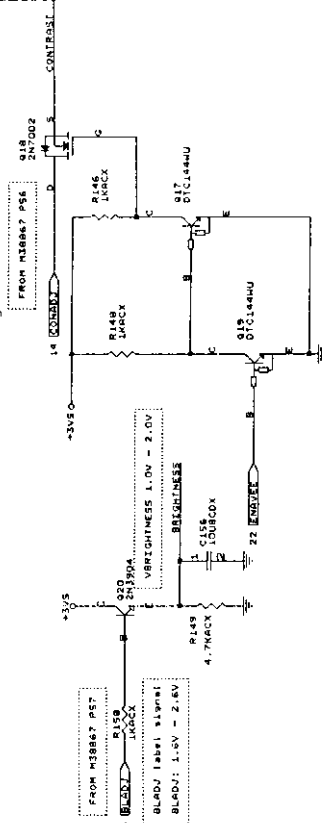
REV.O.4 ==> REV.O.5
8/2 C7 CHANGE TO NU PART
8/2 R122,R121,R120 CHANGE VALUE 1
THEN ADD R391,R392,R393 TO LC
LCDDIO10,LCDDIO20

```

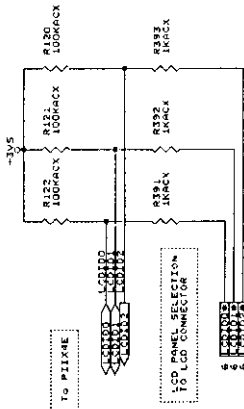
NU PARTS ::

J12, BC34, C7

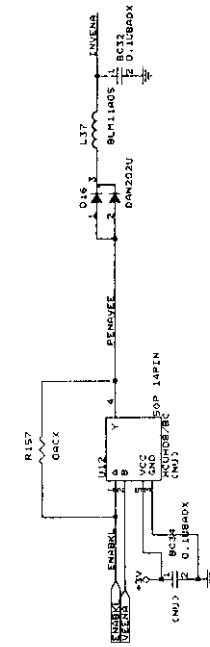
LCD contrast & brightness control



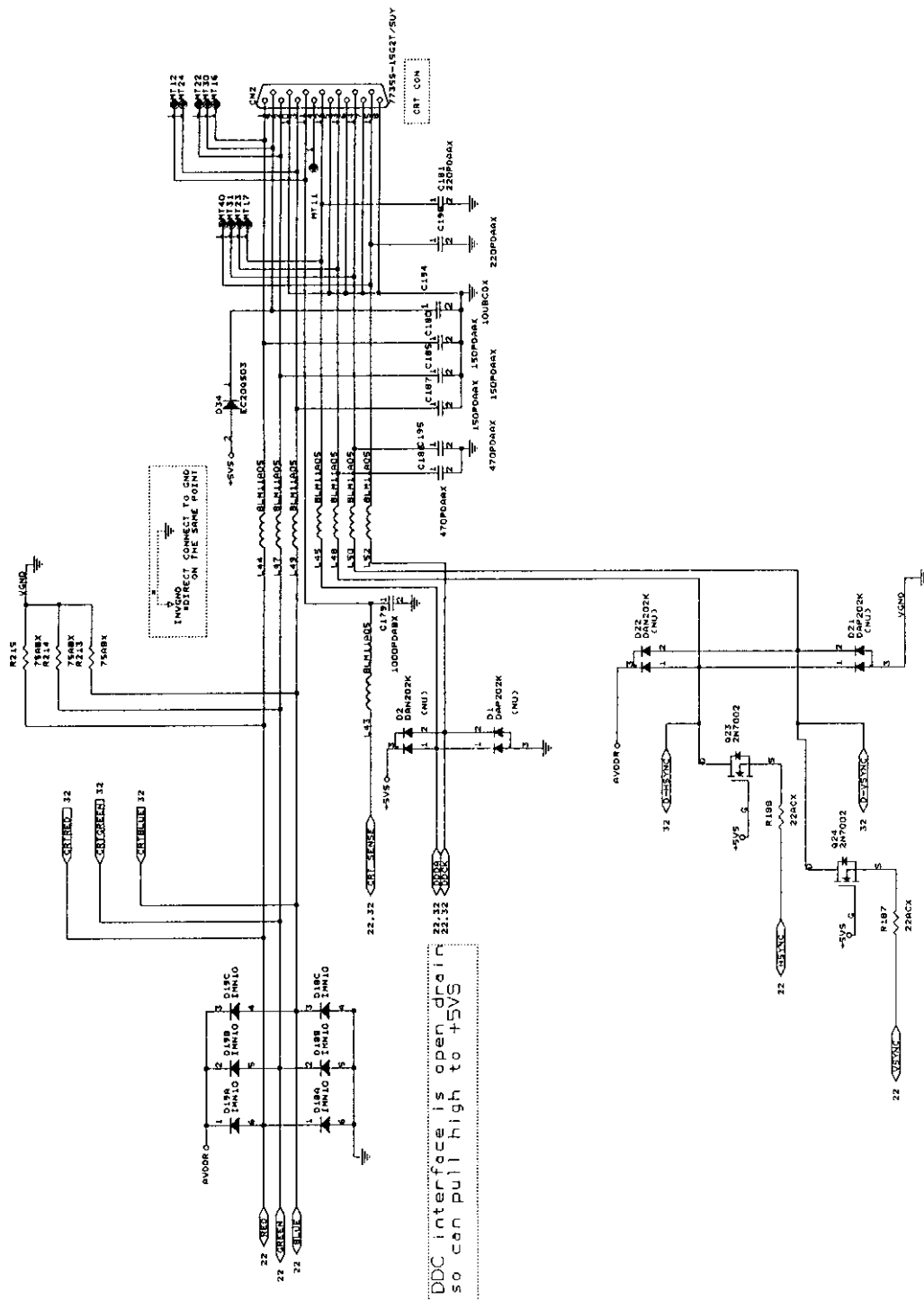
CD type selection



To LCD CN.)



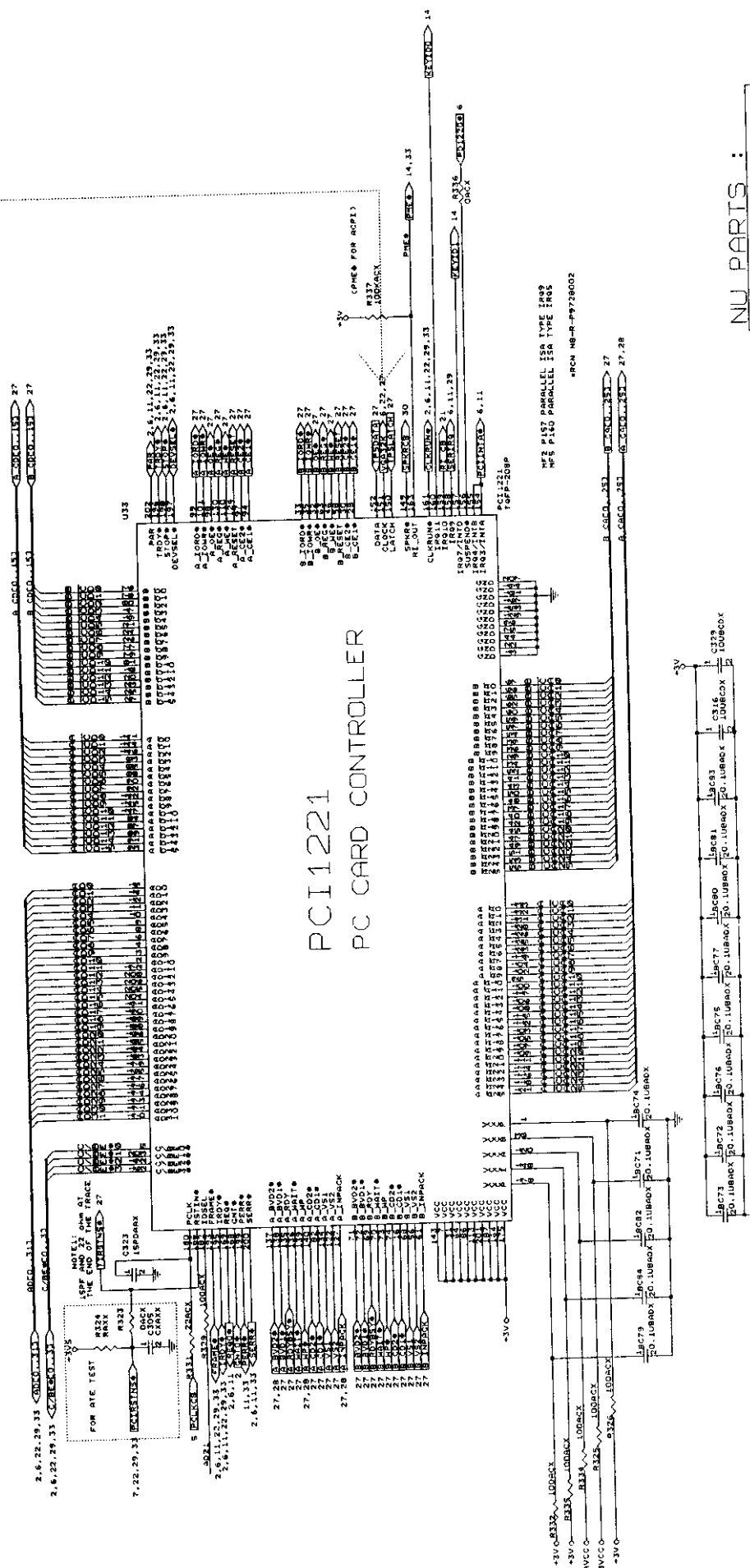
REV	0.7
DESCRIPTION	MSLCPN SCH (PCB VER:0.9)
SIZE	
Document Number	
LCID	LCID
LCID CORR	
FIC AMBER	
DATE	

REV. 0.4 22 REV. 0.5
0/2 F1 CHANGE TO 034 (EC20Q503)

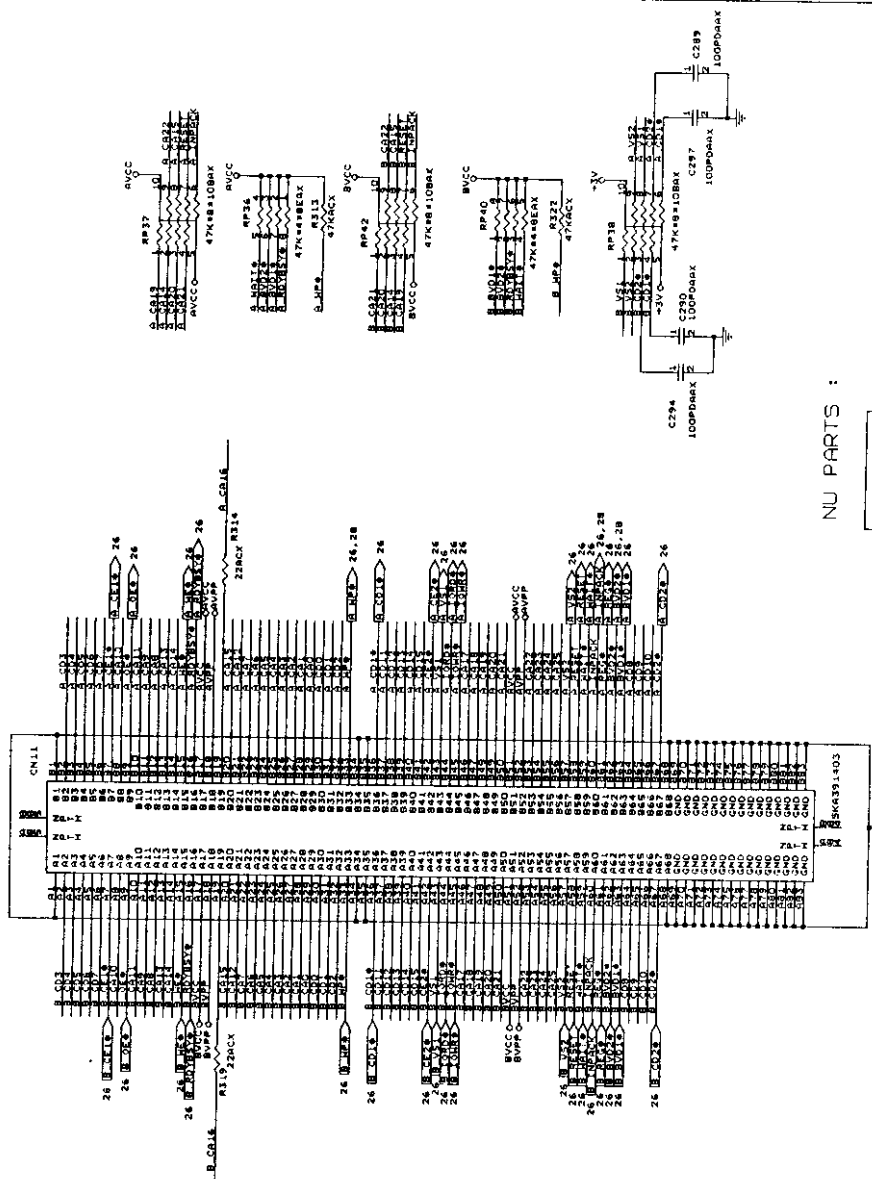
Title	Size	Document Number	REV
FIC AMBER	C	NSC07CN.SCH (PCB VER:0.3)	0.7
VGA CRY CONNECTOR			1.5

Date: October 14, 1998 File: 2300135

BIOS program : 83 BIT3 1 change to 0

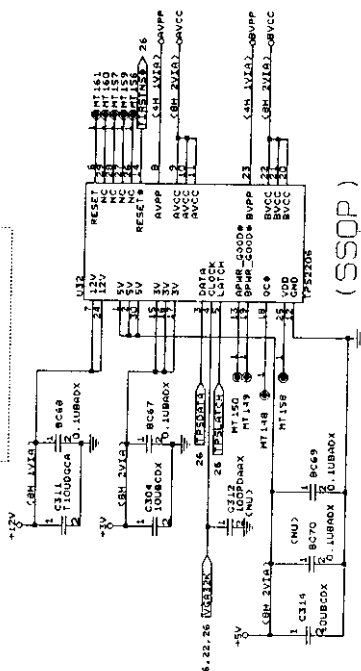
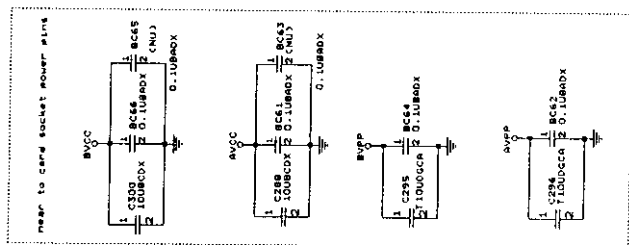


NU PARTS :
R324,C305,C323



NU PARTS :

C312



U21 use SSOP package

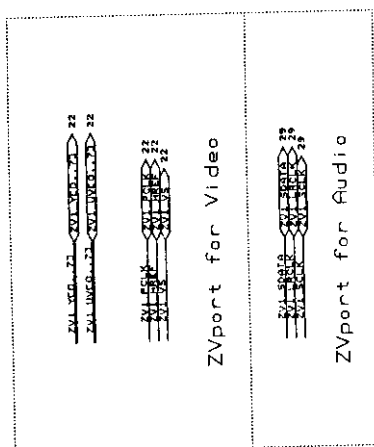
26.29 ACAC 253 ACAC 253

A COR 15 07 A COR 07 15 26

26 B.C. 0. 25 B.C. 0. 757

80015-07

REV	DATE	DESCRIPTION	FIG. NUMBER	TITLE
0.7	October 1, 1955	PC CARD, SCH. (PCB VER: 0.8)		CARD BUS SOCKET
				Document Number
				Size
				C



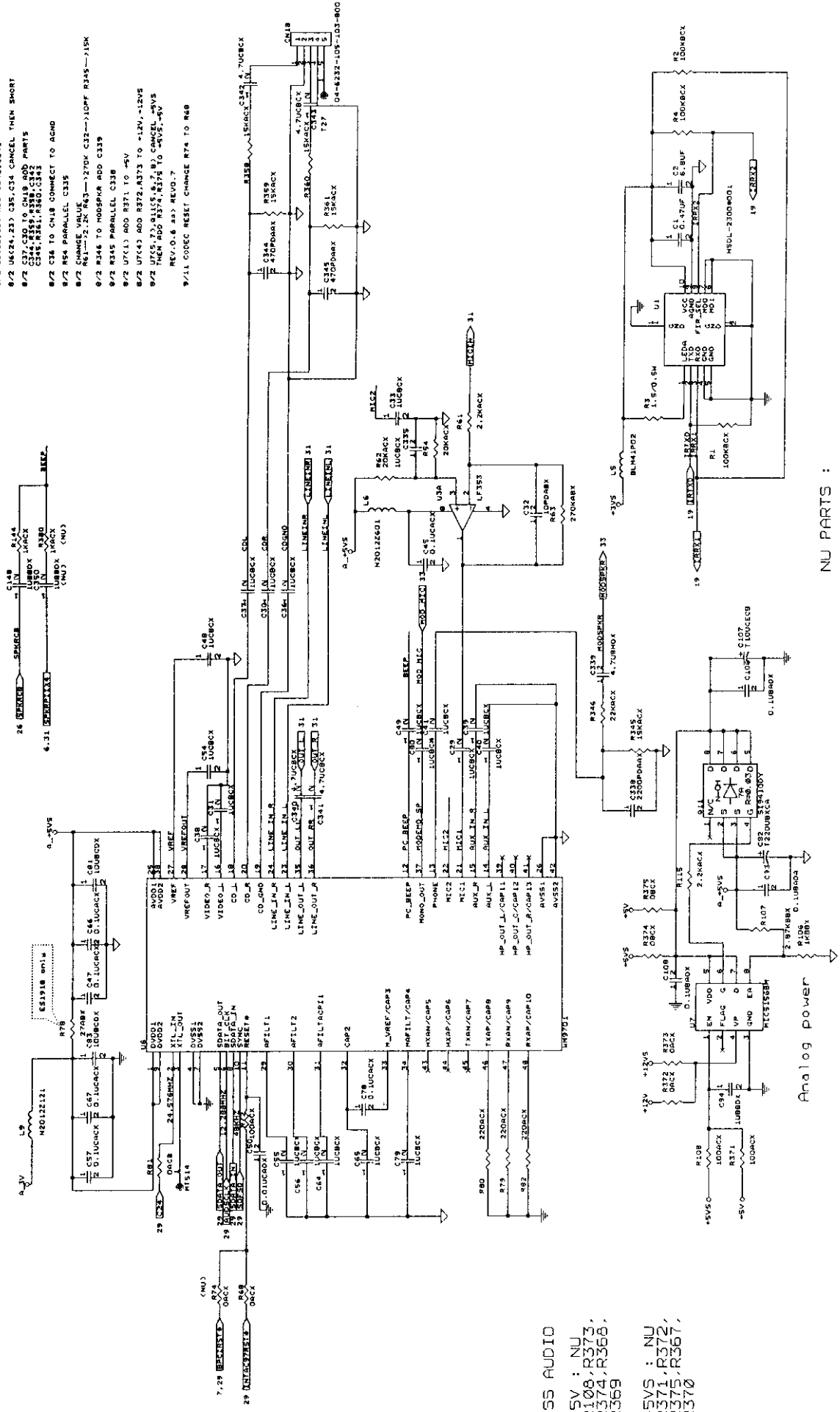
File	FIC AMBER	REV
ZUPORT		0.7
Document Number	ZUPORT.SCH (PCB VER:D.9)	0.7
C		0.7
	October 19, 1989 Sheet	23 of 25

[illegible]

NU PARTS :
R97,C86,R57

Title		FIC AMBER	
SIZE	Document Number	MAESTRO 2	
C	AUDH2.SCH (PCB VER:0.8)		
Date:	October 14, 1998	REV	0.7

REV. 2 --> REV. 3
6/17 MODS PKR CHANGE TO R346, R345 22K
REV. 0.4 --> REV. 0.5
8/2 L9 CONNECT TO A1V
8/2 C59, C58, R31, R1 CANCEL
8/2 UC38, R4, R30 C340, C341
8/2 UC24, R23 C35, C34 CANCEL THEN SHORT
8/2 C37, C36 TO CH18 ADD PARTS
C35, R341, R340, C343
8/2 C34 TO CH18 CONNECT TO AGND
8/2 R54 PARALLEL C335
8/2 CHANGE VALUE
R61 --> 2.2K R63 --> 100K R345 --> 15K
8/2 R346 TO MODS PKR ADD C339
8/2 R345 PARALLEL C338
8/2 U7(1) R371 TO +5V
8/2 U7(4) R372, R373 TO +12V, +12V5
8/2 U7(3) R374, R375 TO +5V
8/2 U7(2) R376 TO +5V
REV. 0.4 --> REV. 0.7
8/11 CODEC RESET CHANGE R74 TO R49



NU PARTS :

R78, C59, C58, Y1, R73
R74, C78, C79, R80
R79, R82, C350, R380

11111 Gerni audio board AC97 Codec
11111 Gerni audio board AC97 Codec
11111 Gerni audio board AC97 Codec

REV. 0.4 --> REV. 0.5
8/2 L9 CONNECT TO A1V
8/2 C59, C58, R31, R1 CANCEL
8/2 UC38, R4, R30 C340, C341
8/2 UC24, R23 C35, C34 CANCEL THEN SHORT
8/2 C37, C36 TO CH18 ADD PARTS
C35, R341, R340, C343
8/2 C34 TO CH18 CONNECT TO AGND
8/2 R54 PARALLEL C335
8/2 CHANGE VALUE
R61 --> 2.2K R63 --> 100K R345 --> 15K
8/2 R346 TO MODS PKR ADD C339
8/2 R345 PARALLEL C338
8/2 U7(1) R371 TO +5V
8/2 U7(4) R372, R373 TO +12V, +12V5
8/2 U7(3) R374, R375 TO +5V
8/2 U7(2) R376 TO +5V
REV. 0.4 --> REV. 0.7
8/11 CODEC RESET CHANGE R74 TO R49

ESS AUDIO

+5V : NU
R100, R373,
R374, R368,
R369

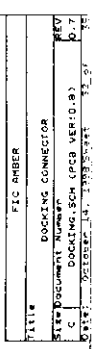
+5V5 : NU
R371, R372,
R375, R367,
R370

Analog power

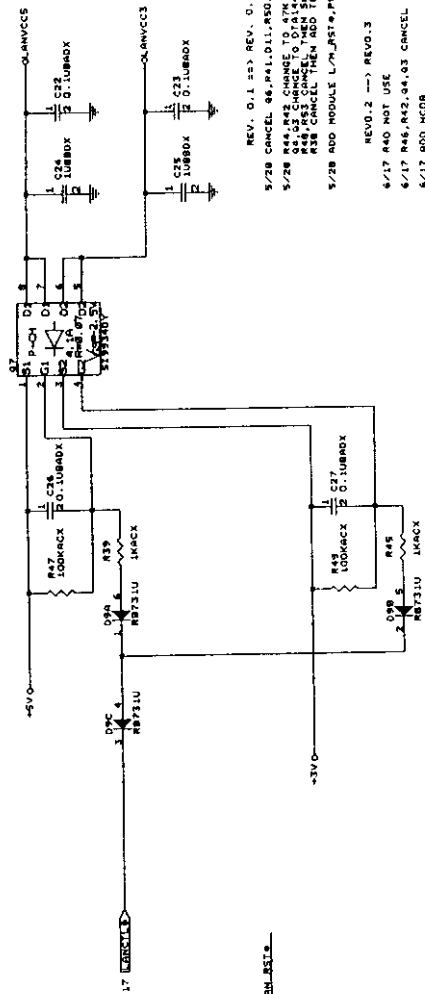

```

REVO.2 --> REVO.3
6/17 U25 PIN2 ADD 0.1UF TO GND
6/17 U25 PIN3 CHANGE TO VLCD5V
U25 PIN4 CHANGE TO LCD5V
6/17 C96 PIN1A,73,75,77,79 SHORT

```

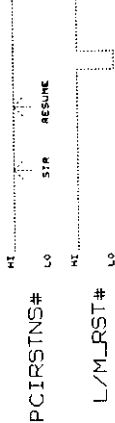


MODEM/LAN CONNECTOR



REV. 0.1 => REV. 0.2 MODIFICATION
 5/28 CANCEL R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100
 5/28 R44, R45 CHANGE TO 47K
 5/28 R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100
 5/28 R58 CANCEL THEN ADD TEST POINT MT170, MT171
 5/28 ADD MODULE L/M_JRST.PHE
 REV. 0.2 => REV. 0.3
 6/17 R40 NOT USE
 6/17 R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100
 6/17 ADD MC08

STR RESET TIMING



LAN/MODEM POWER CONTROL TRUTH TABLE

POWER	STR#	MODEM#	ACTN	LAN_TYPE	LANCTL#
1	1	0	1	1	0
1	0	0	1	1	0
0	1	1	1	1	1
1	0	1	1	1	1
1	1	0	0	1	0
1	0	1	0	1	1
0	1	1	0	1	1
1	1	0	1	0	0
1	0	0	1	0	0
0	1	1	0	0	1
1	0	1	0	0	1
0	1	0	0	0	0
1	0	0	0	0	0
0	1	1	0	0	1
1	0	1	0	0	1

LAN
 ADAPTOR_IN
 LAN
 BATTERY ONLY
 MODEM
 ADAPTOR_IN
 MODEM
 BATTERY ONLY

REV.0.4 => REV.0.5
 8/2 036(4) R20 R376.C349 THEN C349 IS NU PART
 10/15 REV.0.7 => REV.0.8
 R376 NU (PCB SHORT)

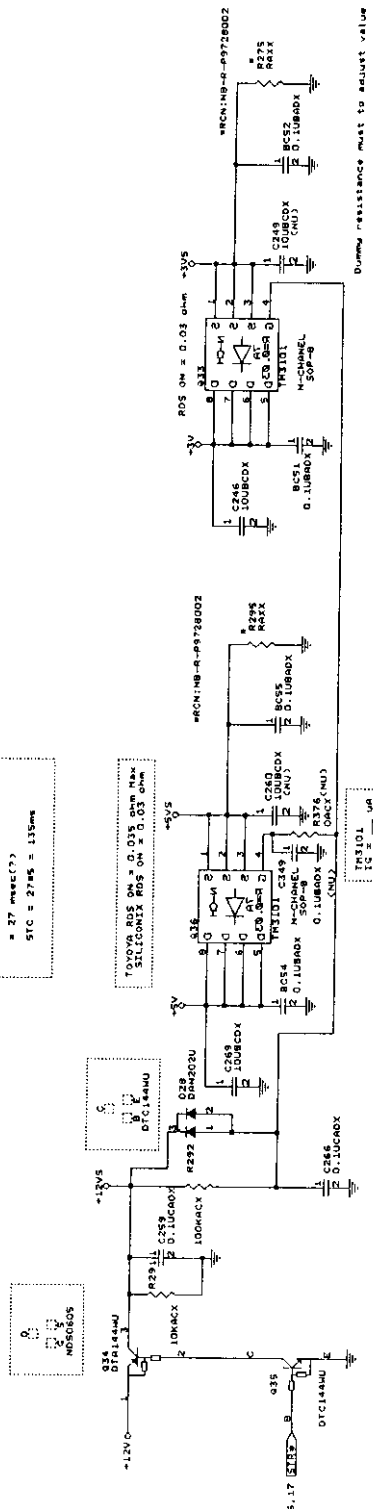
NU PARTS :

R295,R275,C349

MODE transfer

	ON	SIR	STD	ME	OFF
+5V	5	5	0	0	0
+5VS	5	0	0	0	0
+12V	12	12	0	0	0
+12VS	12	0	0	0	0
+3V	3.3	3.3	0	0	0
+3VS	3.3	0	0	0	0
VDDA	5	5	0	0	0
BM+	10~20	10~20	0	0	0
B+	10~20	10~20	0	0	0
VIO_REGUL	2.5	0	0	0	0
+DRAM	3.3	3.3	0	0	0
3VA	3.3	3.3	3.3	0	0
VCCSUS	3.3	3.3	3.3	0	0
+R10VCCS	3.3	3.3	3.3	3.3	3.3

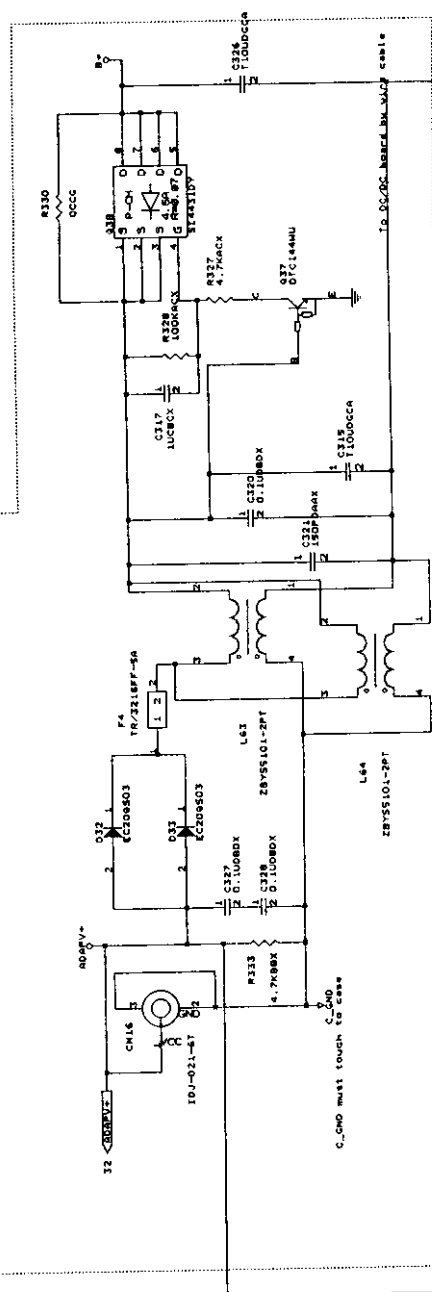
POWER PLANE



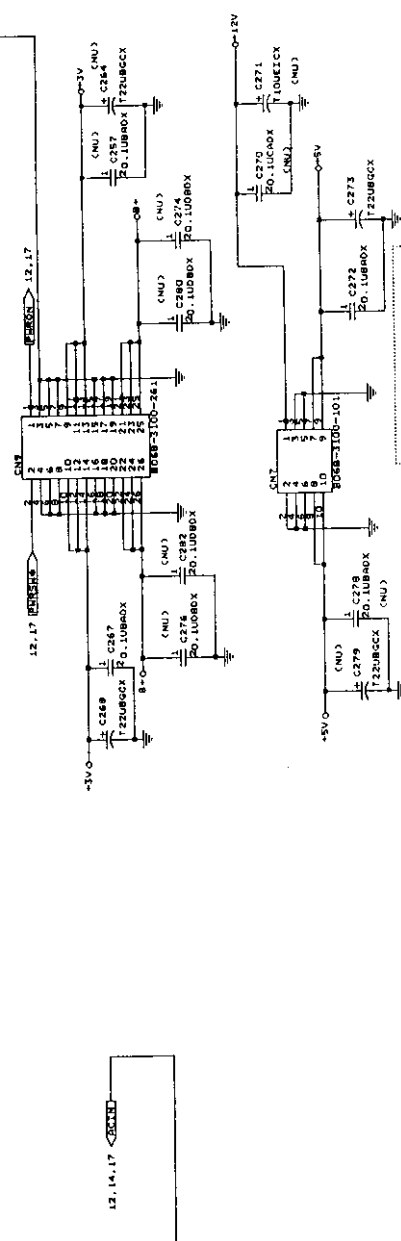
6/17 CN16 PIN2.3 SHORT
6/17 C318,C326 CHANGE TO T1XJGCA

REV 0.6 => REV 0.7
9/11 R333 LOOK CHANGE TO 4.7X

INRUSH current circuit



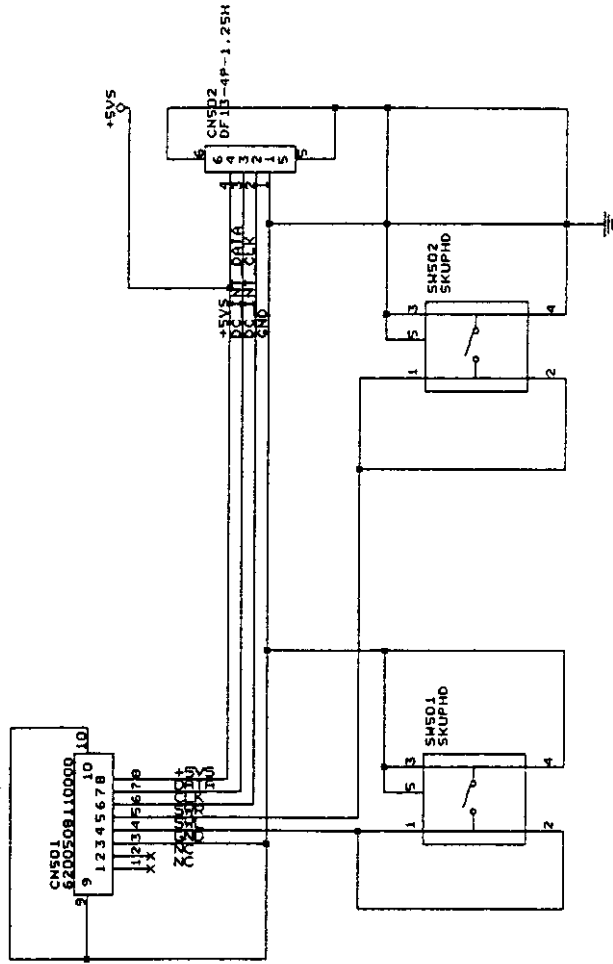
DC/B to M/B connector



NU PARTS :

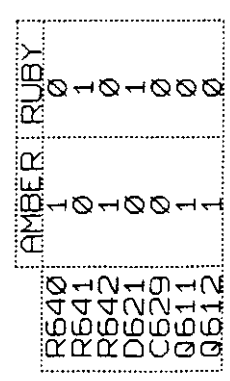
0321, 0320, 0315, 0317
0328, R327, 037, 038
0326, 020, R33, R34, 02, 01
03274, 0280, 0282, 0275

FILE	DC-DC /IF	REV
SIZE	Document Number	0.7
C	DC21F.SCH (PCB VER:0.8)	1.7
DATE	October 13, 1998 Sheet	16 of

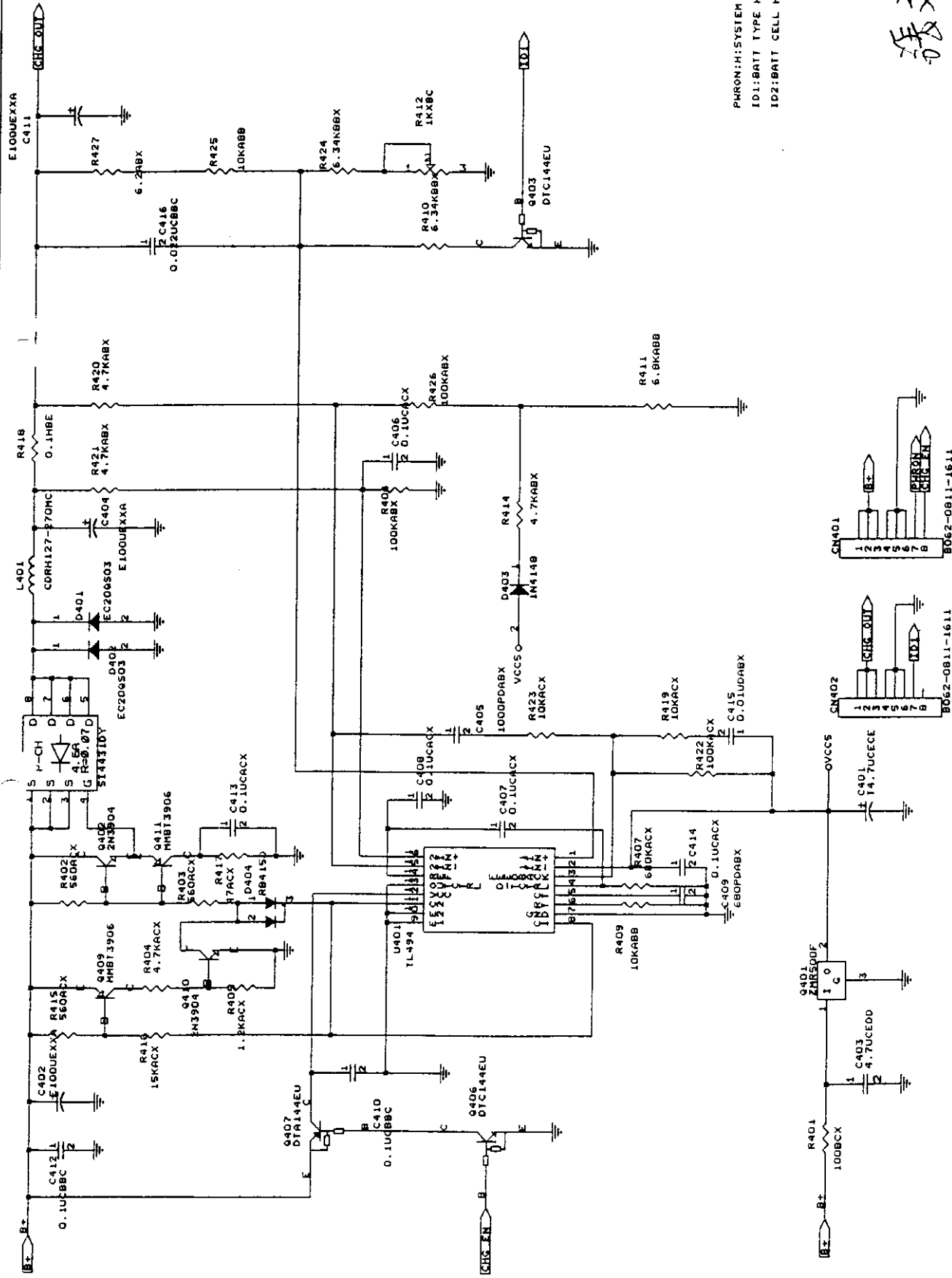


2010/11

Title		AMBER 2.0
Size		Document Number
B		ACD SCH (PCB VERSION 0.8)
Date:		October 13, 1998 Sheet 1 of 1



Size	Document Number	REV
B	AIO1.5CH (PCB VER:0.8)	0.7
Date:	October 15, 1998	Sheet 2



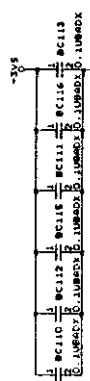
PHARON H: SYSTEM POWER ON CURRENT 1.0A
 ID1: BATT TYPE MINI-MH L: LI-ION
 ID2: BATT CELL N: 4 CELLS L: 8 CELLS

張文湧 10/9

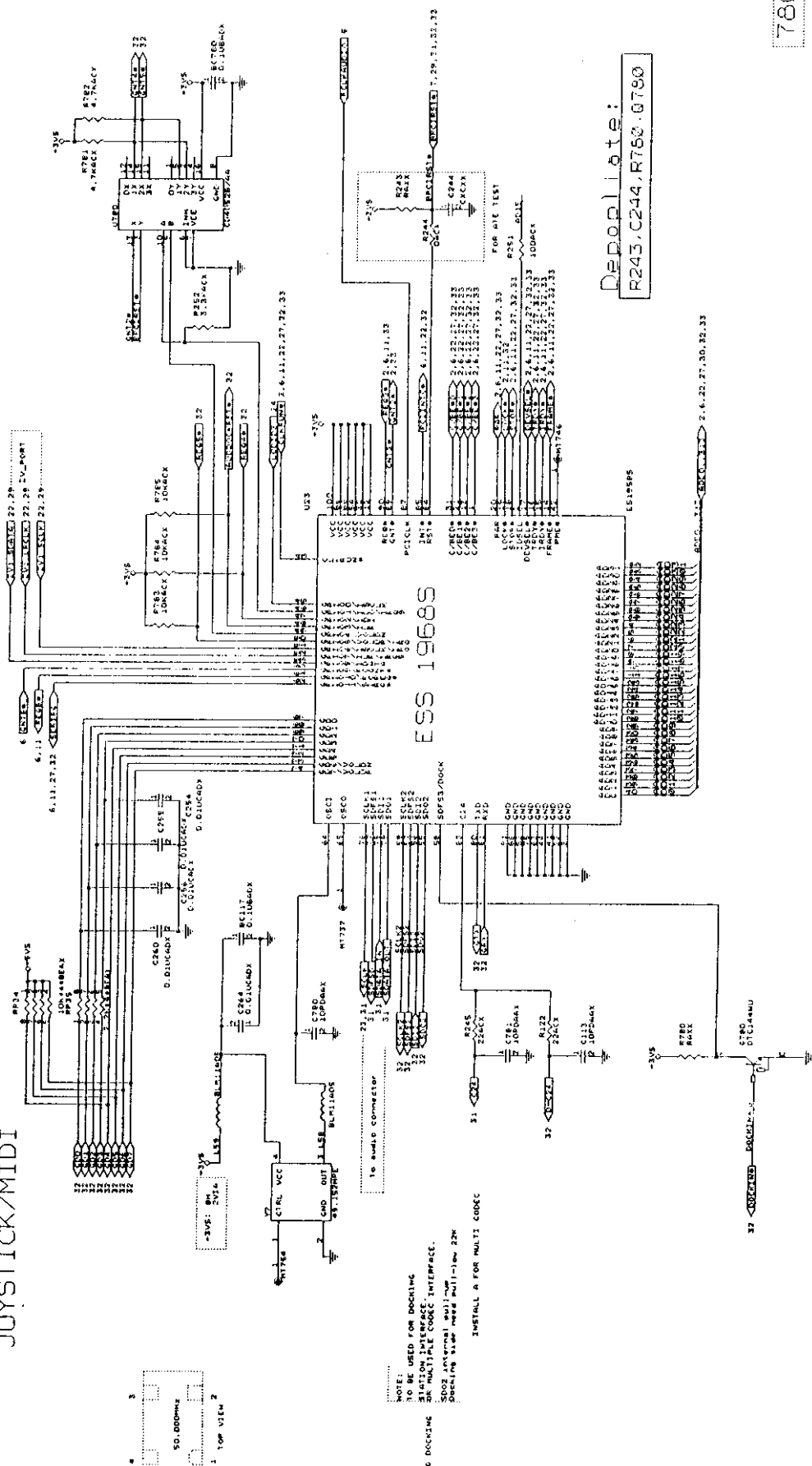
FIRST INTERNATIONAL COMPUTER, INC.	
BY JASON CHANG	
Title	
ANBER 2.0 CHARGER CIRCUIT	
Size	Document Number
B	BATTERY CHARGER (PCB VER: D.8)
Date	October 15, 1998
	Page 1 of 1

REV: 0.2

- 1) U23 pin 20 change to R1028 from R1008
- 2) U23 pin 20 change to GND28 from GND18
- 3) Cancel R1018
- 4) U23 pin 20 add resistor for docking
- 5) Cancel C251, C250, C249, C248
- 6) Cancel R246, R247, C247, C244
- 7) Cancel R242, C245



JOYSTICK/MIDI



ESS 1968S

Depollate:
R243, C244, R750, Q750

780

FIG	1	Rev	0.2
DATE	10-1-1968	DESIGNED BY	W. J. B. 100
REV		CHECKED BY	W. J. B. 100
DATE		DATE	
REV		DATE	