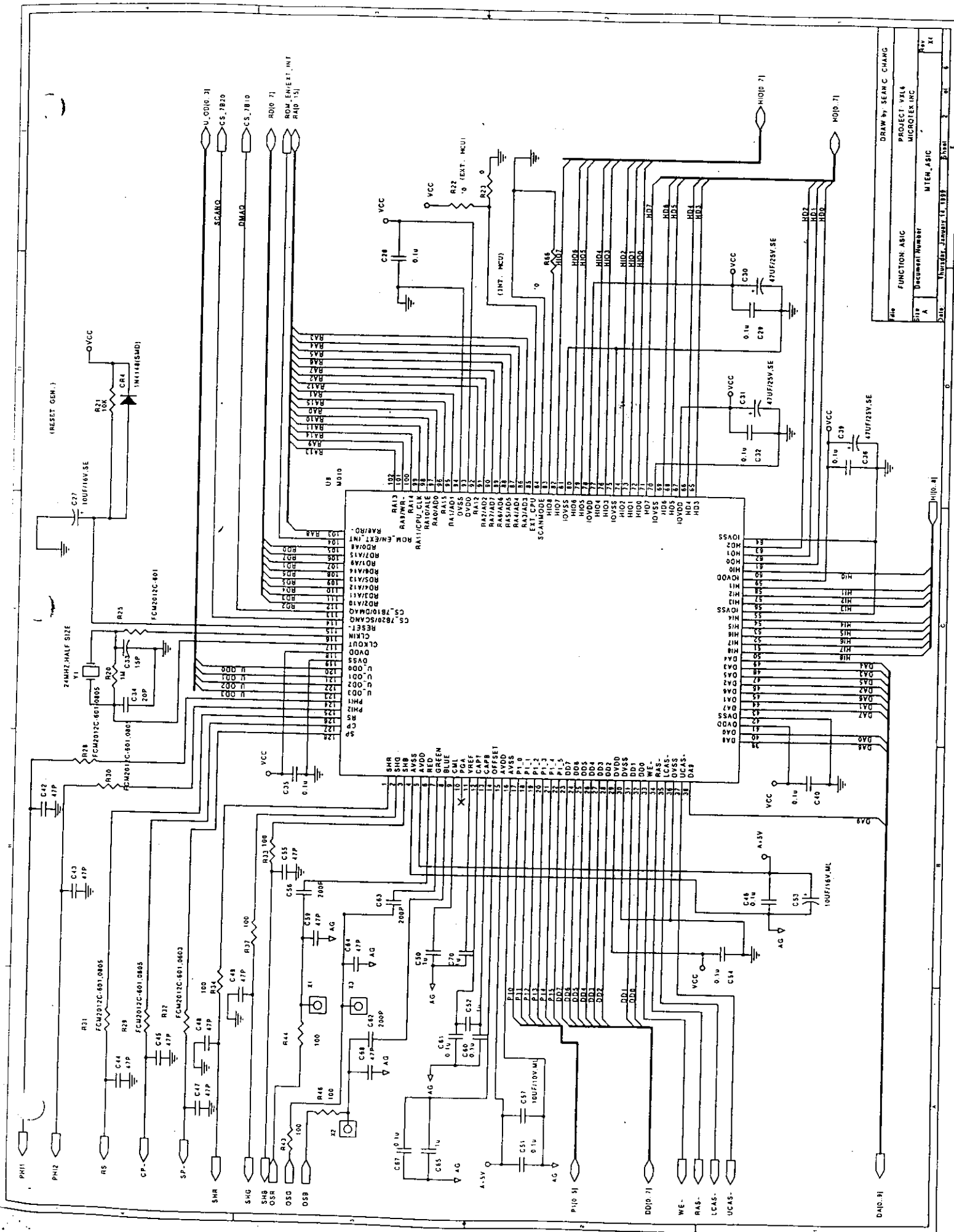
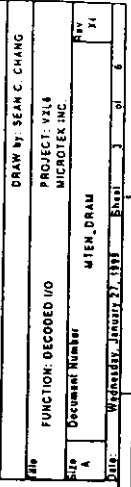
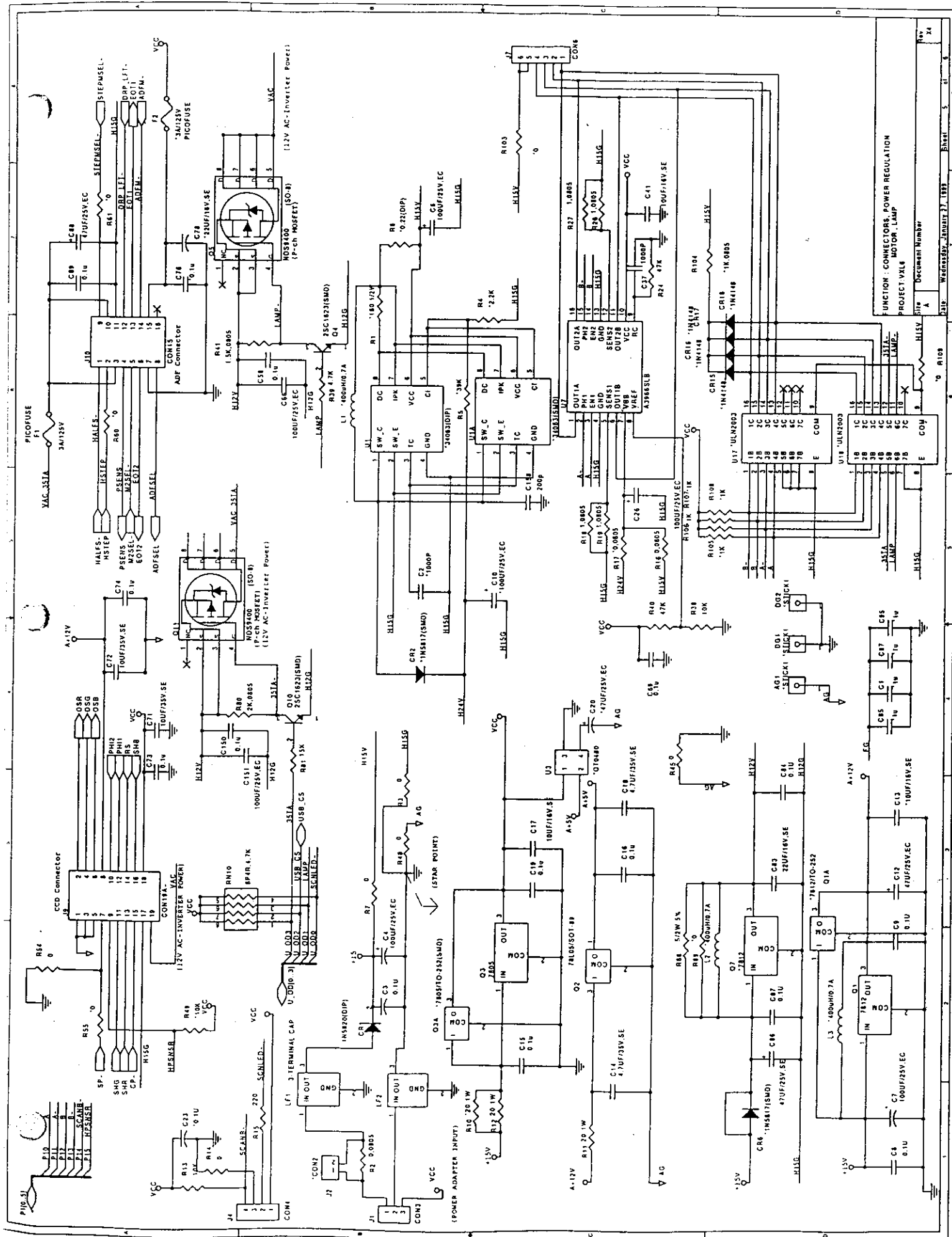
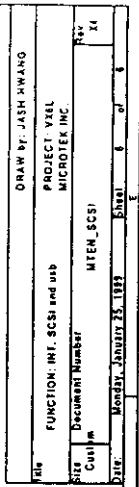




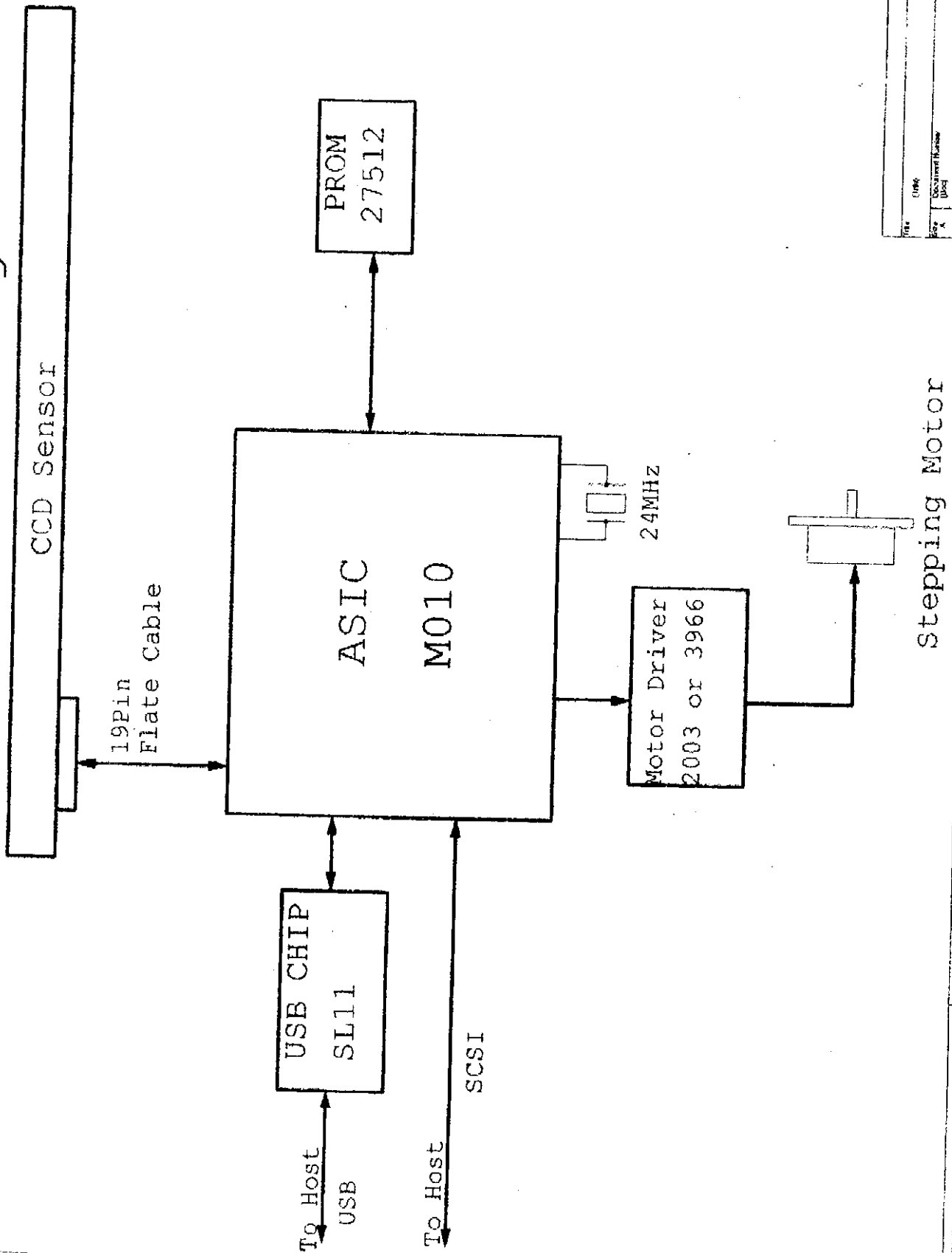




FUNCTION: CONNECTORS, POWER REGULATION
 PROJECT VXL6
 Document Number

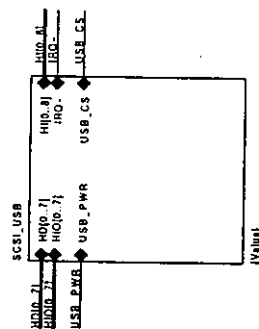
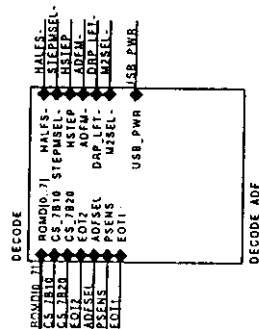
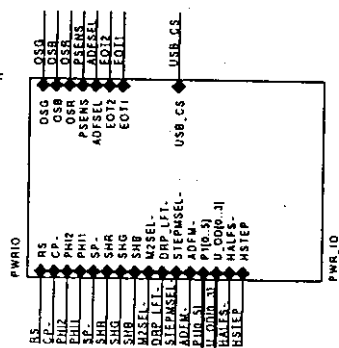
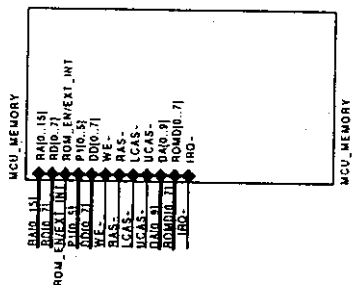


MRS-1200V6US System Block Diagram



File	0106
Doc Name	Doc Name
Doc No	Doc No
Doc Rev	Doc Rev
Doc Date	Doc Date
Doc Author	Doc Author
Doc Checker	Doc Checker
Doc Engineer	Doc Engineer
Doc Manager	Doc Manager
Doc Release	Doc Release
Doc Status	Doc Status
Doc Version	Doc Version
Doc History	Doc History
Doc Comments	Doc Comments
Doc Notes	Doc Notes
Doc Footer	Doc Footer

Timing diagram for ROM and RAM access. The diagram shows signals for ROM Enable (ROM_EN), ROM Address (ROM_A[10:0]), ROM Data (ROM_D[7:0]), RAM Address (RAM_A[10:0]), RAM Data (RAM_D[7:0]), and RAM Enable (RAM_EN). The signals are shown as waveforms over time, with labels indicating the state of each signal (e.g., ROM_A[10:0] = 0000, ROM_A[10:0] = 0001, etc.).



P1_0-P1_3	MOTOR
P1_4	SCANNER LAMP
P1_5	HOME SENSOR
P1_6-P1_7	35A LAMP
U_000	READY LED
U_001	SCAM BUTTON
U_002	5V1 3V POWER, I=ON
U_003	USB 3.3V POWER, I=ON
SGS1 ID1	I:SGS1 DISCONNECT
	O:SGS1 CONNECT