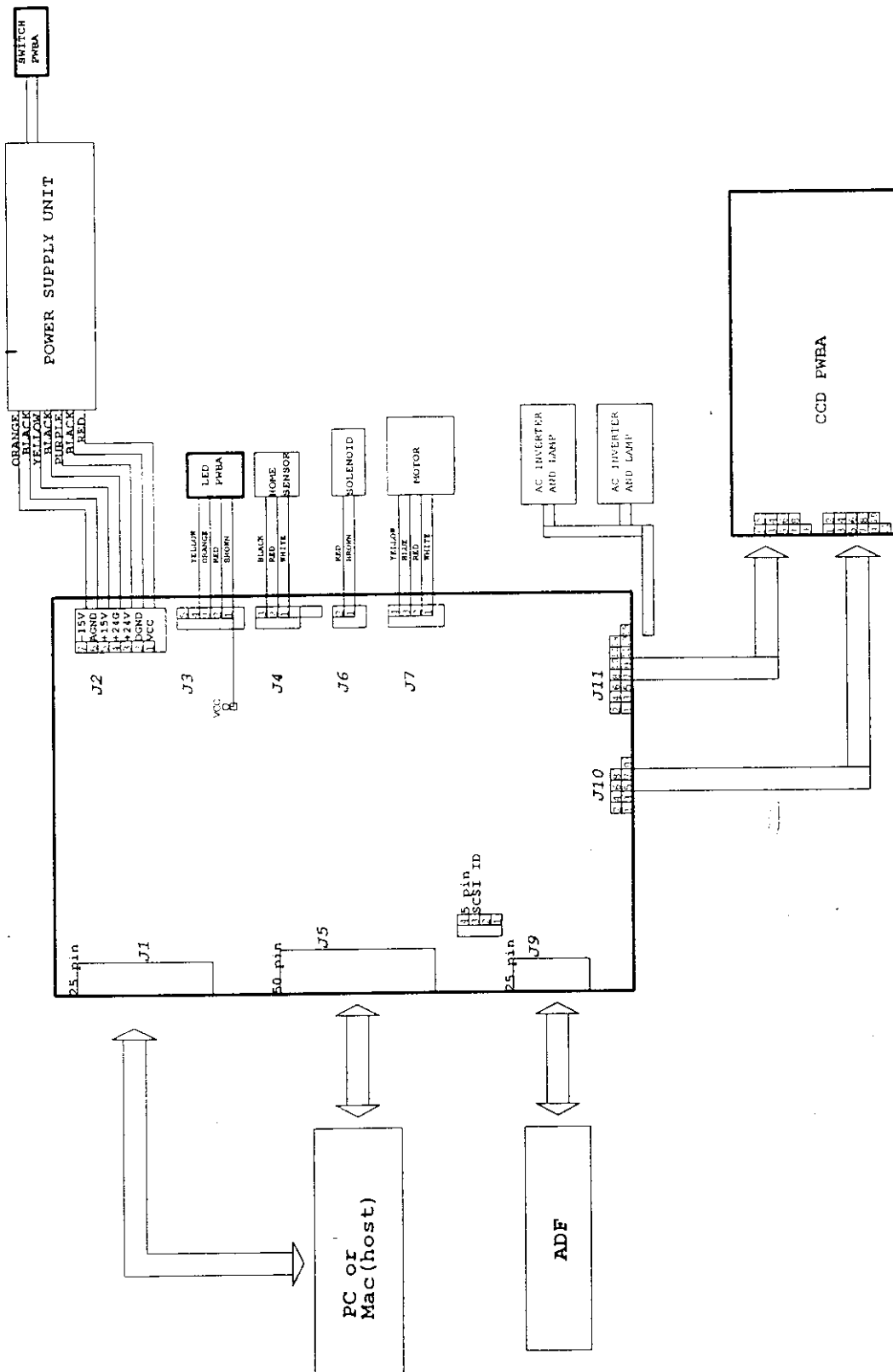
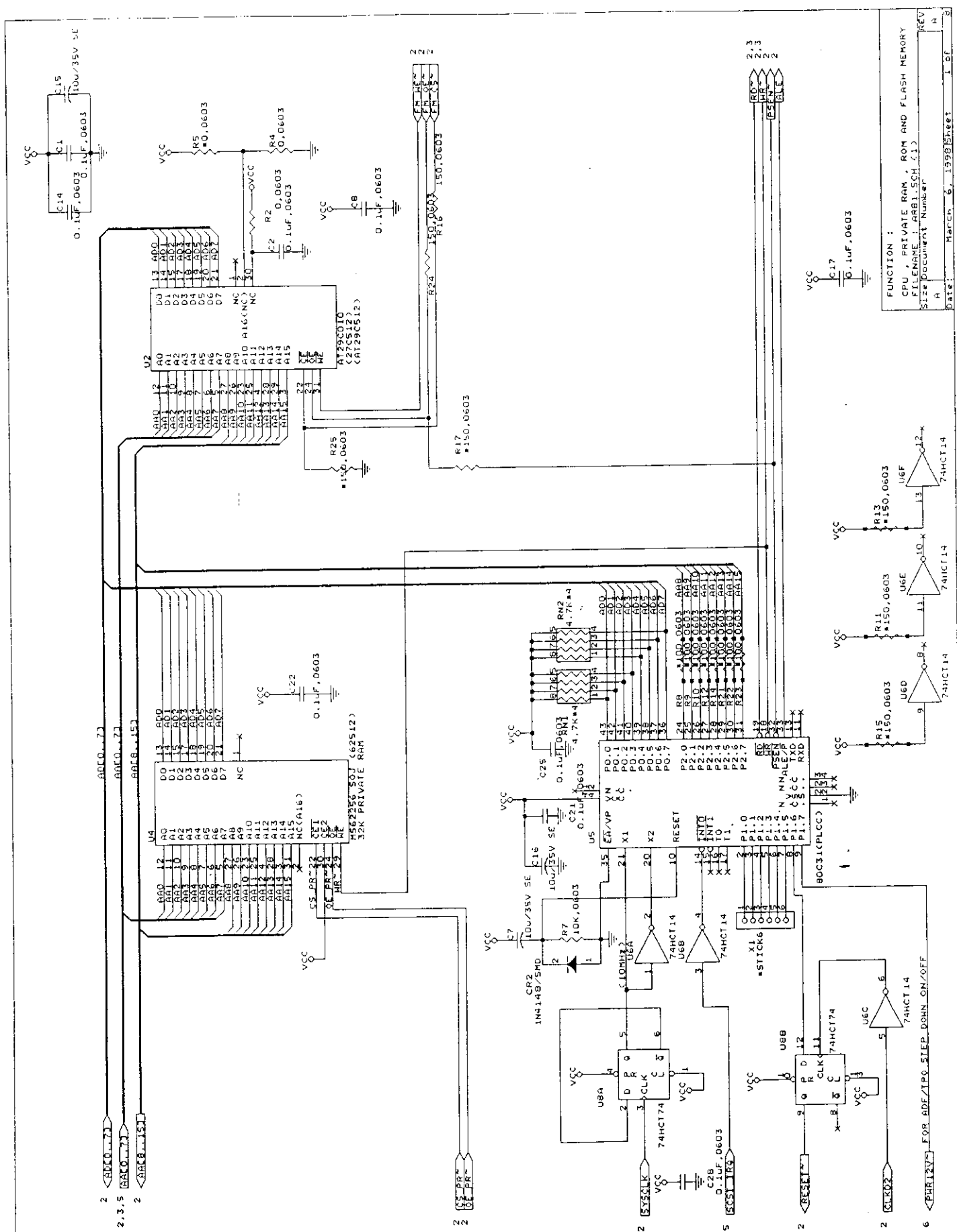


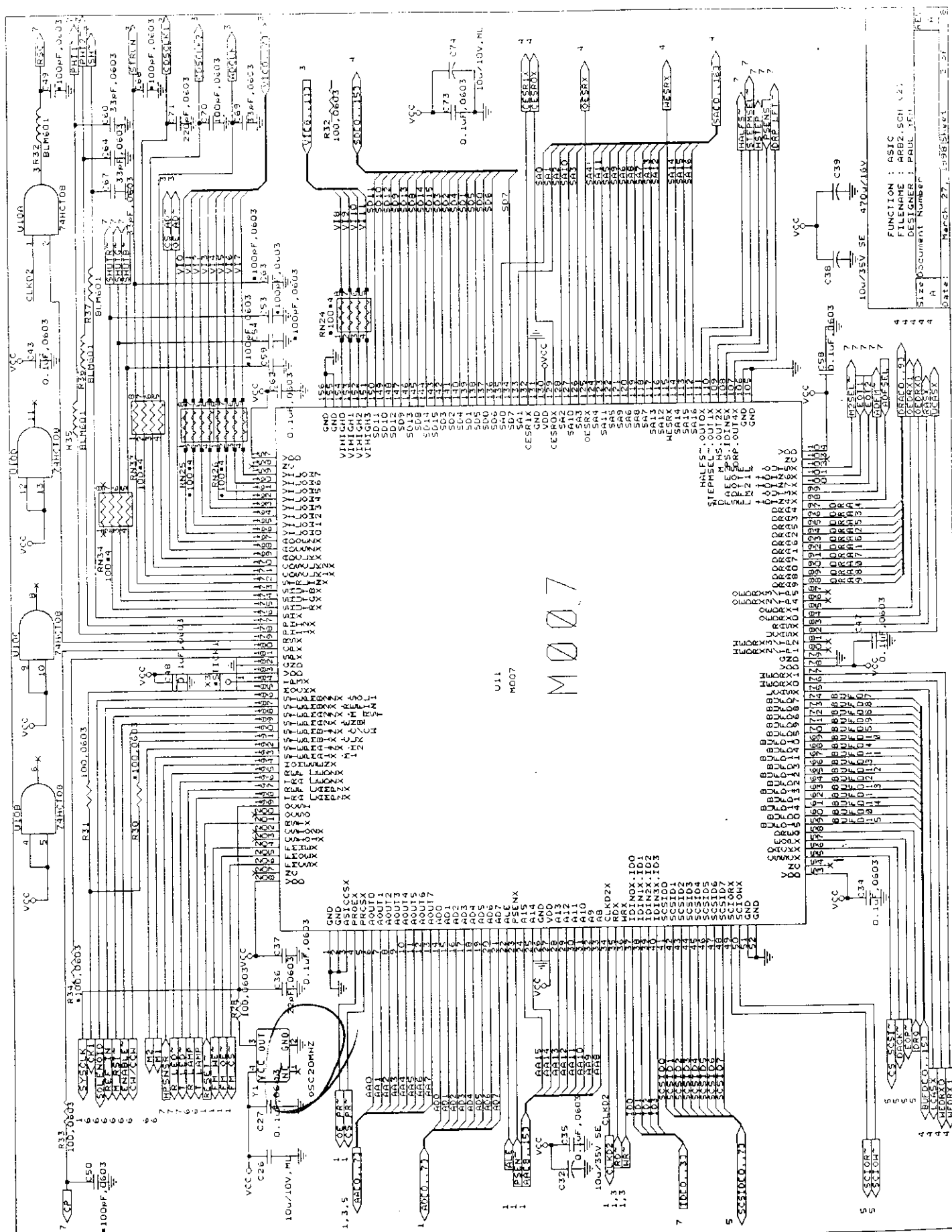
Aruba for FCC

ARUBA WIRING DIAGRAM



ARUBA WIRING DIAGRAM			
Design by Paul J. Yeh			
Size	Document Number	Date	Sheet
A	1001	10/26/98	1 of 1
			Rev. X

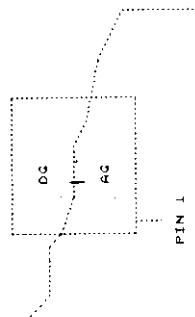




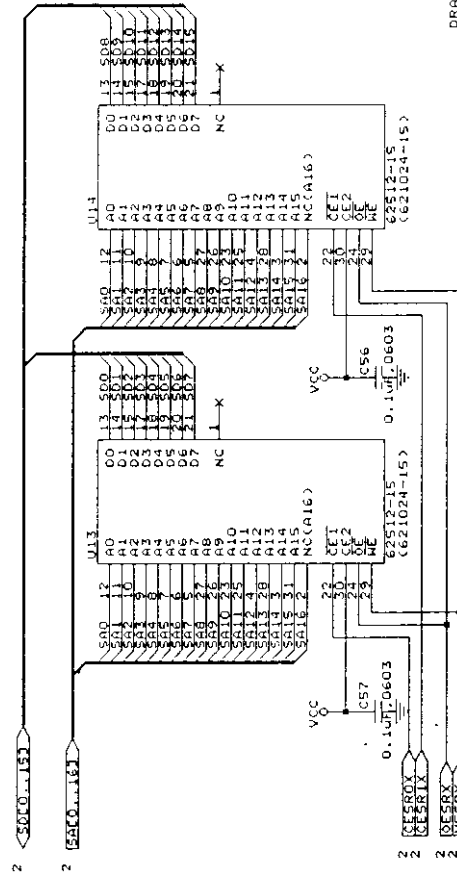
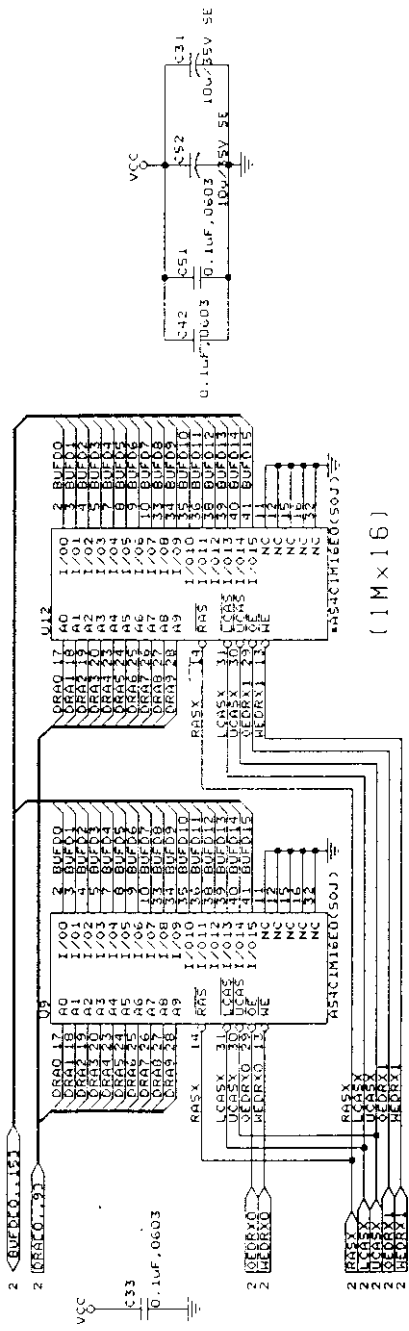
FUNCTION : ASIC
DESIGNED : PAUL J. A.
DATE : March 27, 1985

Size Document Number

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..... AGND
..... AGND
..... SVA



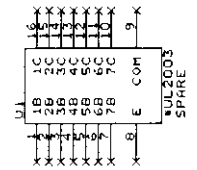
SYSTEM RAM (LOOK-UP TABLE)

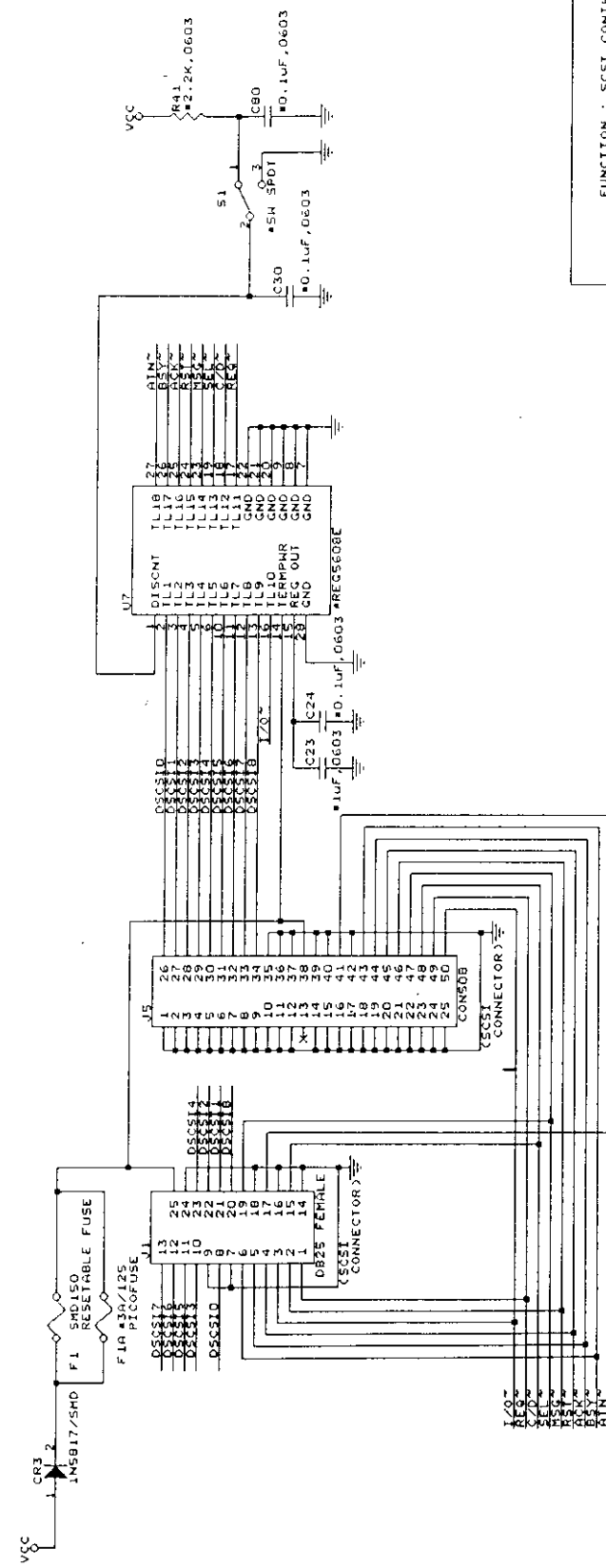
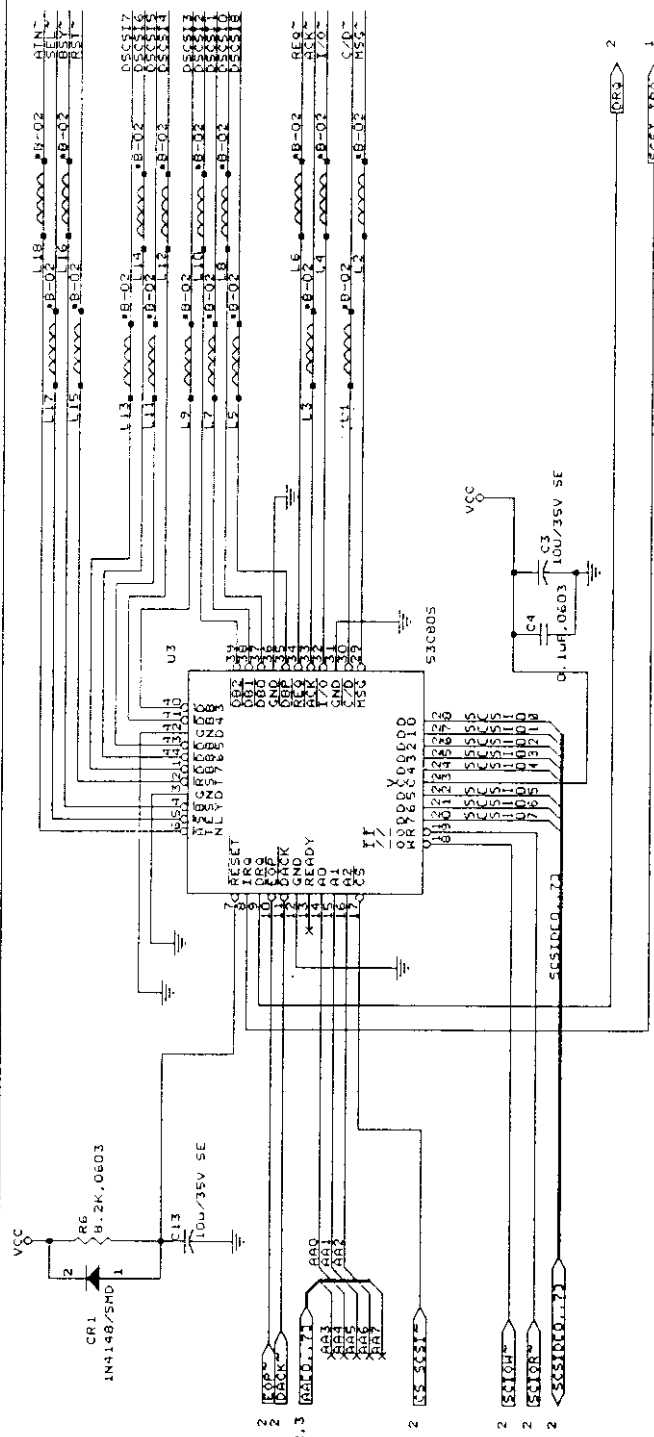
DRAM : 1M WORD X 2 = 2M WORD = 4M BYTE (2Mx2)
 OR 1M WORD X 1 = 1M WORD = 2M BYTE (1Mx2)
 DEFAULT: FA02C3..03 = Q110

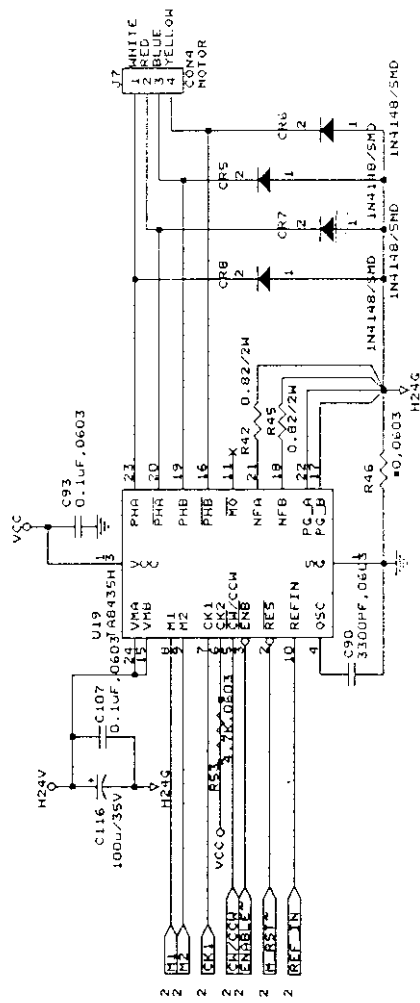
SRAM : 128K BYTE X 2 = 128K WORD (CH1 + LO BYTE)
 OR 64K BYTE X 2 = 64K WORD (CH1 + LO BYTE)
 R39=open

FUNCTION : IMAGE BUFFER & LOOK UP TABLE
 FILENAME : ARB4.SCH (4)
 Size: Document Number

Date: March 6, 1998 Sheet 4 of 3

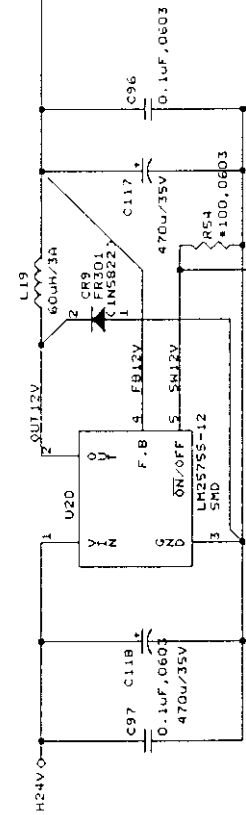




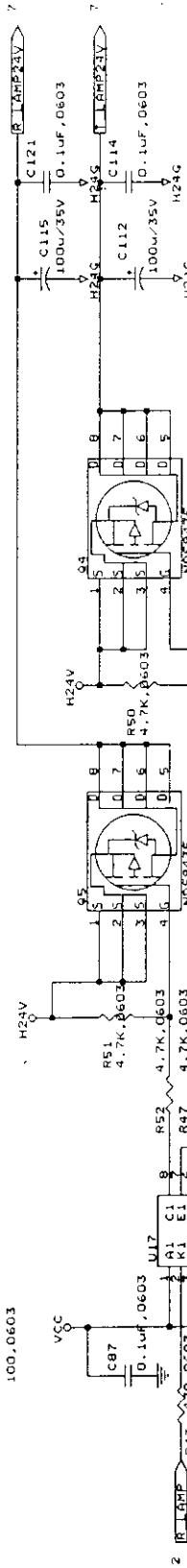


M1	M2	PHASE	STEP
0	0	1	PULL
1	0	2	1/4
1	1	3	1/8

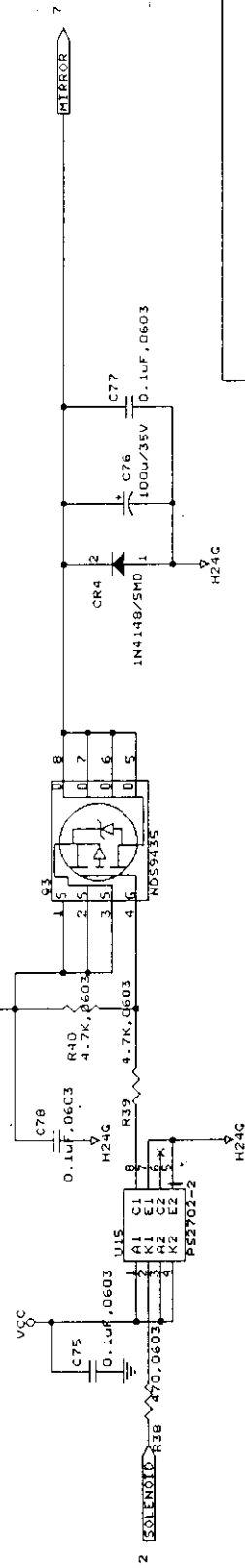
(1.20) (Power to 80F/1PQ) H12V 7



2 PER12V 100.0603

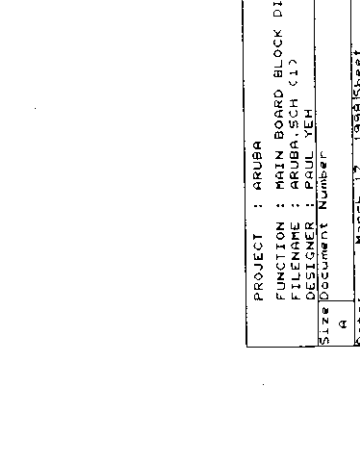
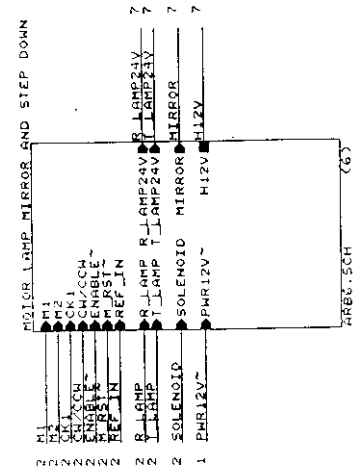
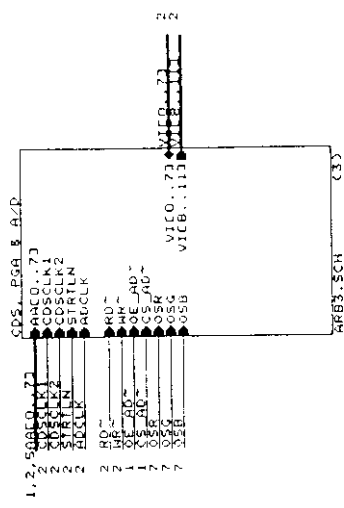


2 LAMP 470.0603



2 SOL12V 470.0603

FUNCTION : MOTOR , LAMP , MIRROR DRIVER
 FILENAME : ARB6SCH.C67
 Size : 1000 Bytes
 Date : March 6, 1998 Sheet 6 of 6



PROJECT : ARUBA
 FUNCTION : MAIN BOARD BLOCK DIAGRAM
 FILENAME : ARUBA.SCH (1)
 DESIGNER : PAUL YEH
 Size Document Number
 Date : March 17, 1998 Sheet 8 of 8

