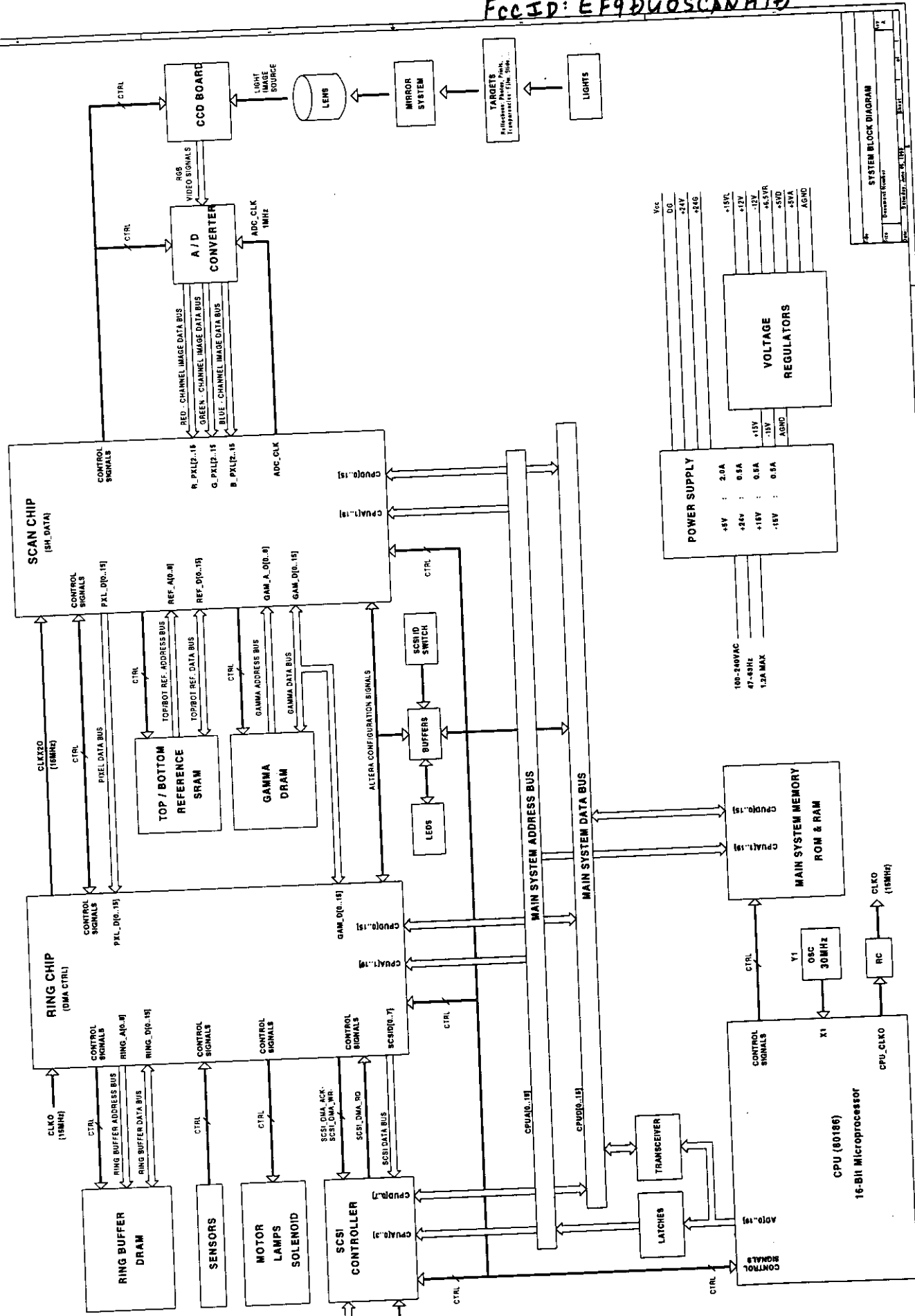


FCC ID: EF9DUOSCANHID

SYSTEM BLOCK DIAGRAM

DuoScan HiD (AGFA)



SYSTEM BLOCK DIAGRAM

Block	Signal	Direction
CPU (80186)	CLK0 (15MHz)	Input
	CTRL	Input
MAIN SYSTEM MEMORY	CPUDQ_15	Output
	CPUAL_15	Output
SCSI CONTROLLER	CPUAL_15	Output
	CPUDQ_15	Output
LATCHES	DATA	Output
	CTRL	Output
TRANSCIVER	DATA	Output
	CTRL	Output