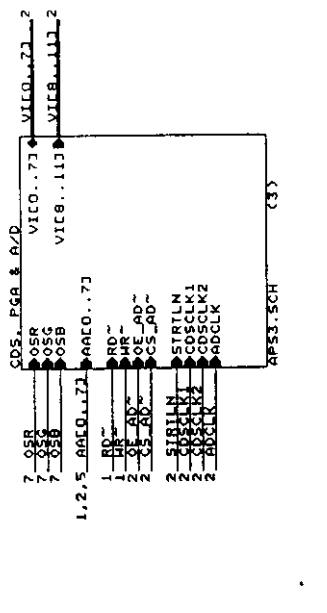
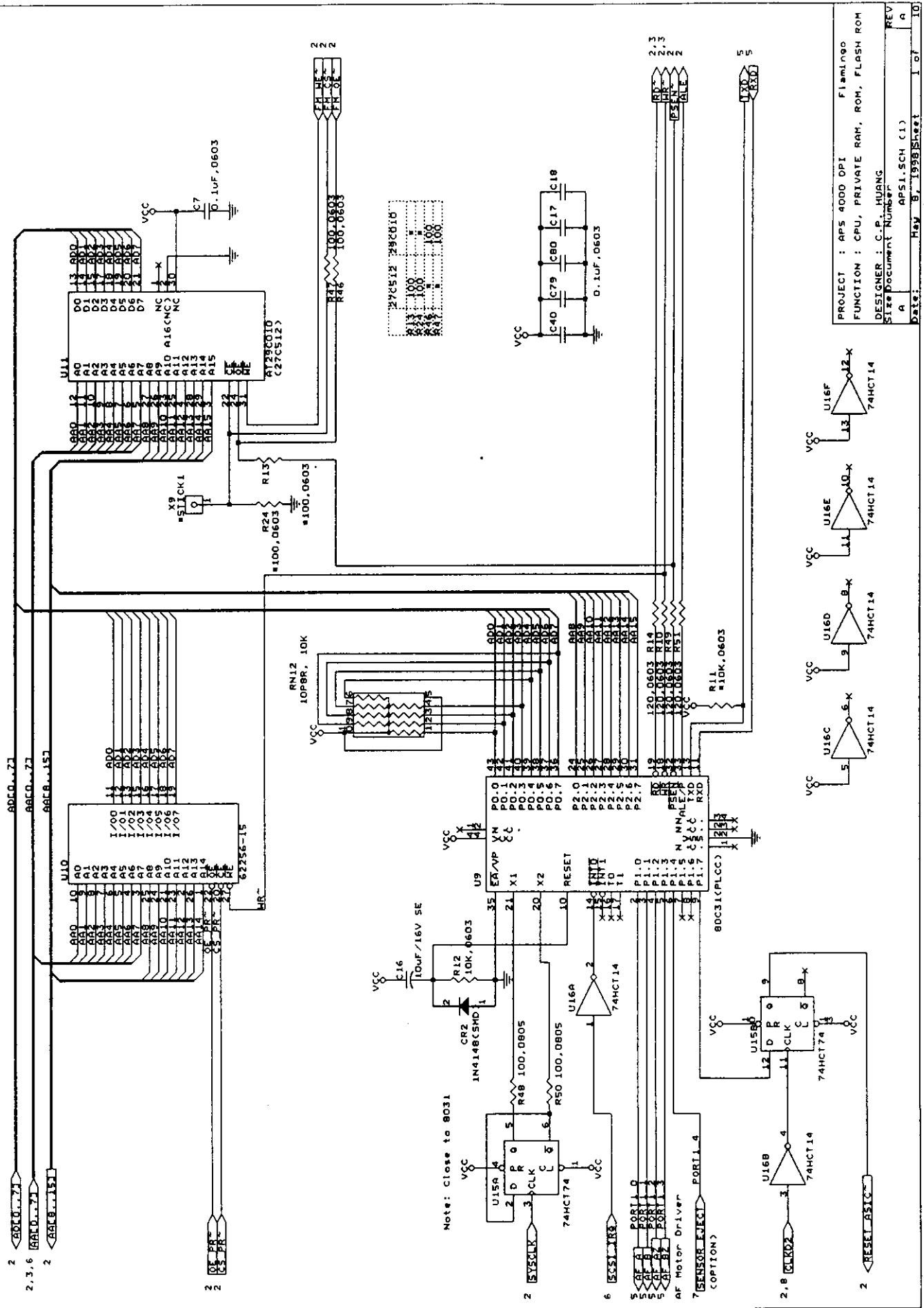
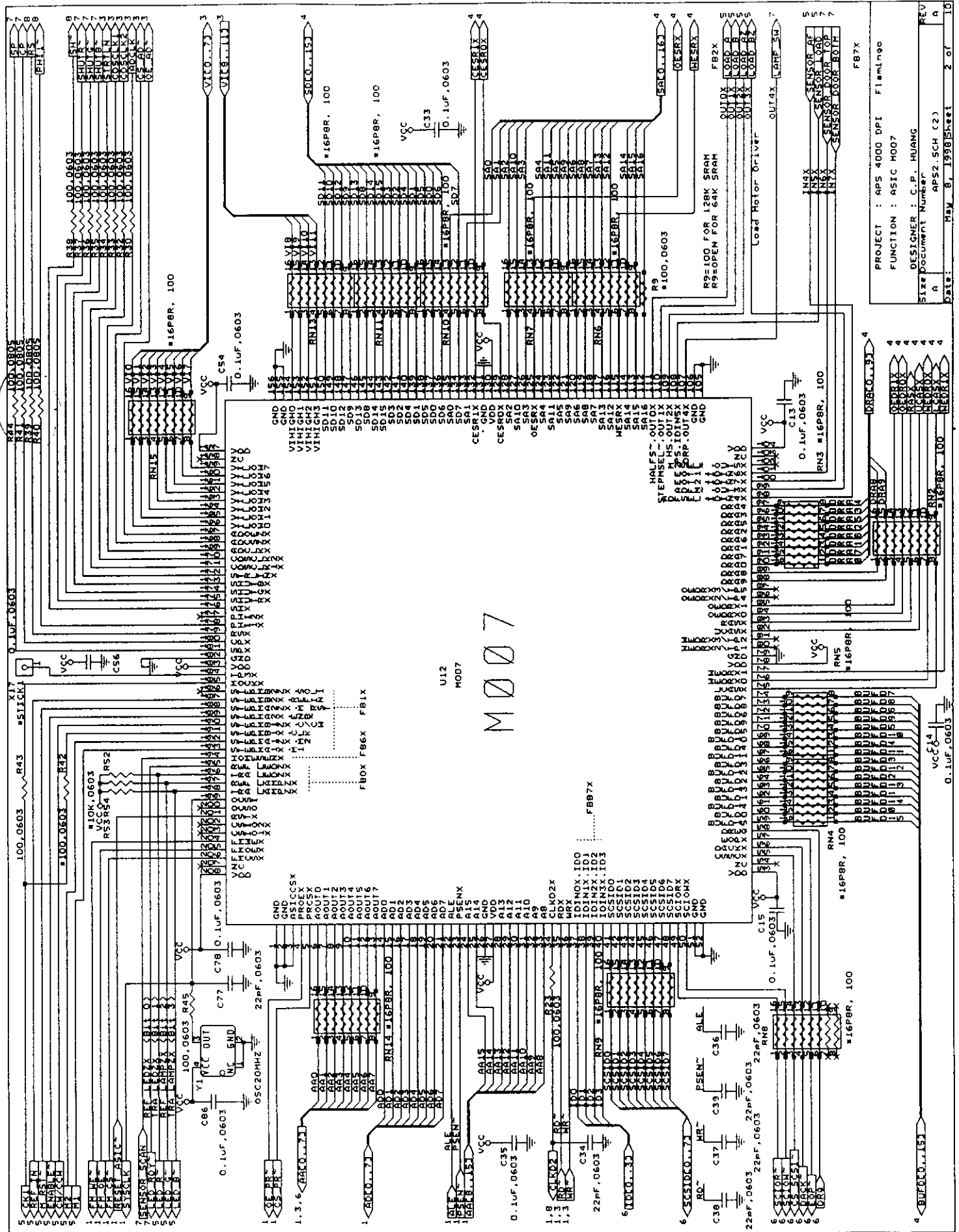






PROJECT : APS 4000 DPI FlameLogo
 FUNCTION : ASIC MOD7
 DESIGNER : C.P. HUANG
 Size Document Number
 A APS2.5CH (2)
 Date: May 8, 1998 Sheet 2 of 10

M007

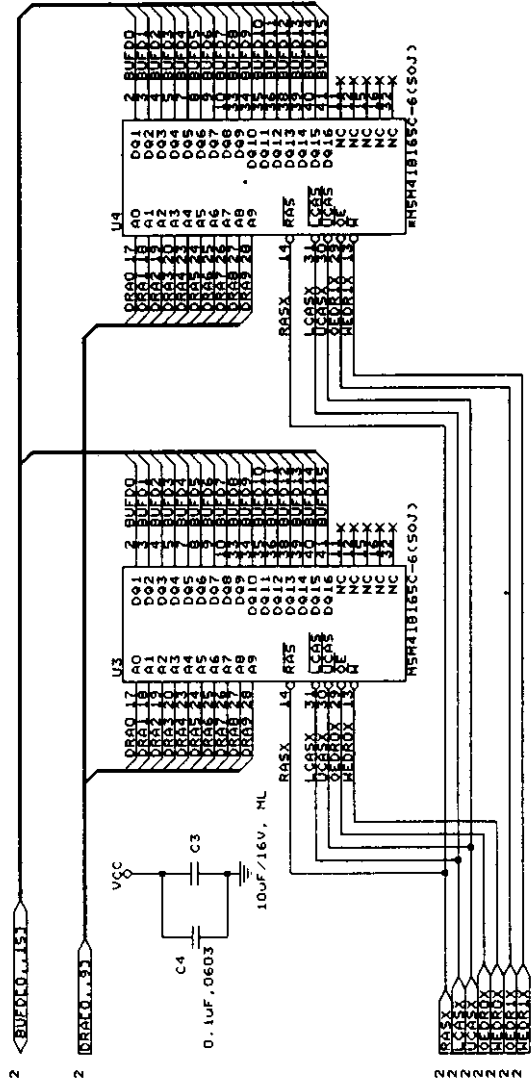
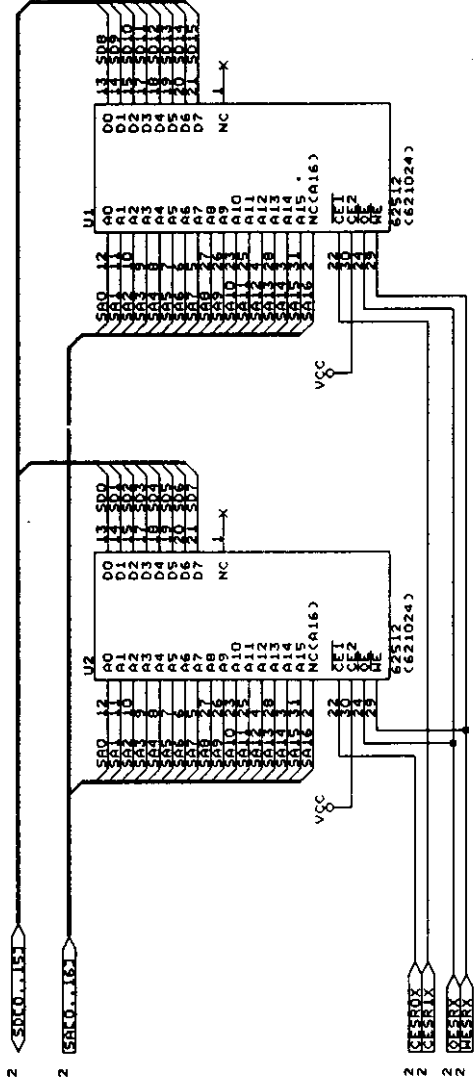


IMAGE BUFFER
2MB (+ 2MB OPTION)

1M X 16

1M X 16 (OPTION FORT 4MB BUFFER)



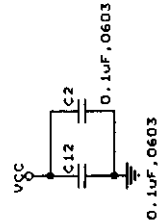
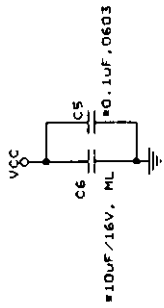
SYSTEM RAM (LOOK-UP TABLE)

64KB X 2 -- 62512 X2

OPTION FOR 128KB X 2 -- 621024 X 2

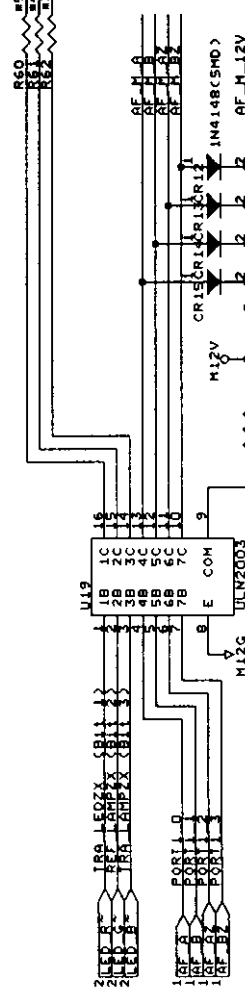
OPTION FOR 128K WORD (H1 + L0 BYTE)

SRAM: 64K BYTE X 2 = 64K WORD (H1 + L0 BYTE)



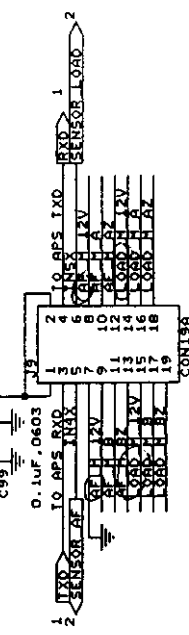
(12V 1A)
(Power to 2 Motors & LED light source)

M12V
C94
C95
M12G
#0.1uF.0603
#47uF/25V, SE

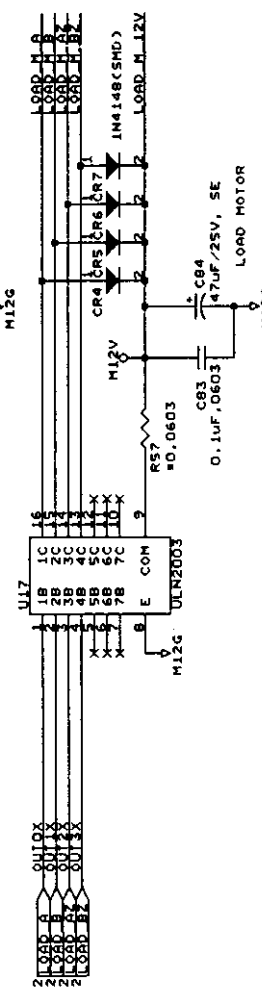


(OPTION FOR LED LIGHT SOURCE)

VCC
C93
C97
0.1uF.0603
10uF/10V, ML

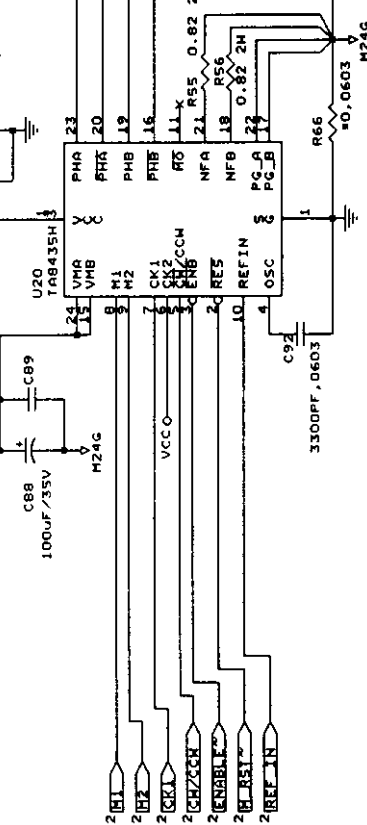


(TO SCAN CARRIAGE)



VCC
C91
C90
0.1uF.0603
10uF/16V SE

M24V
C88
100uF/35V
M24G



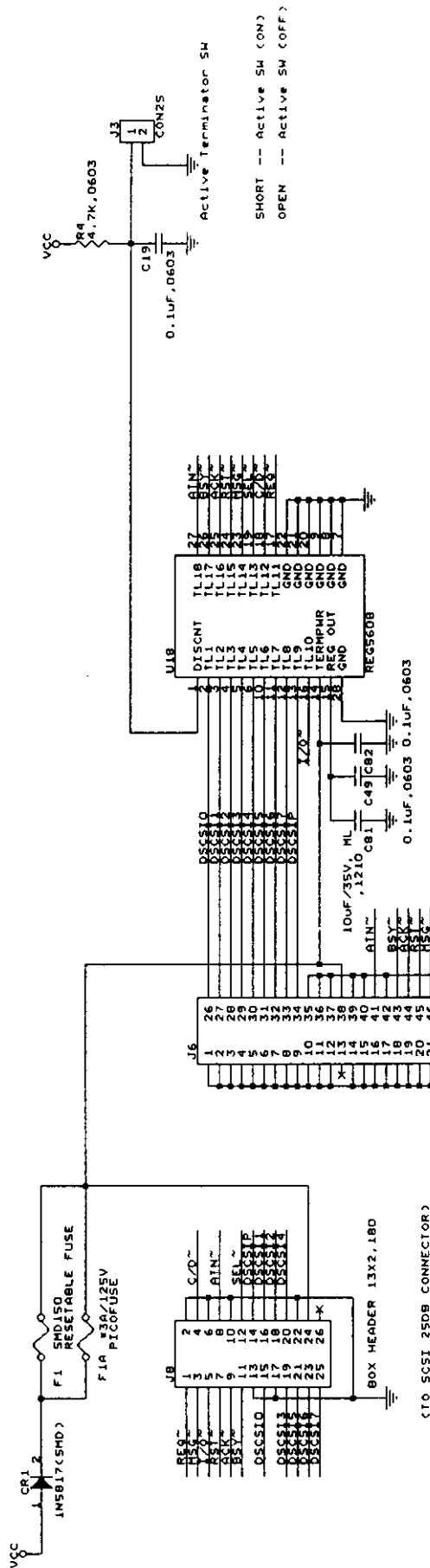
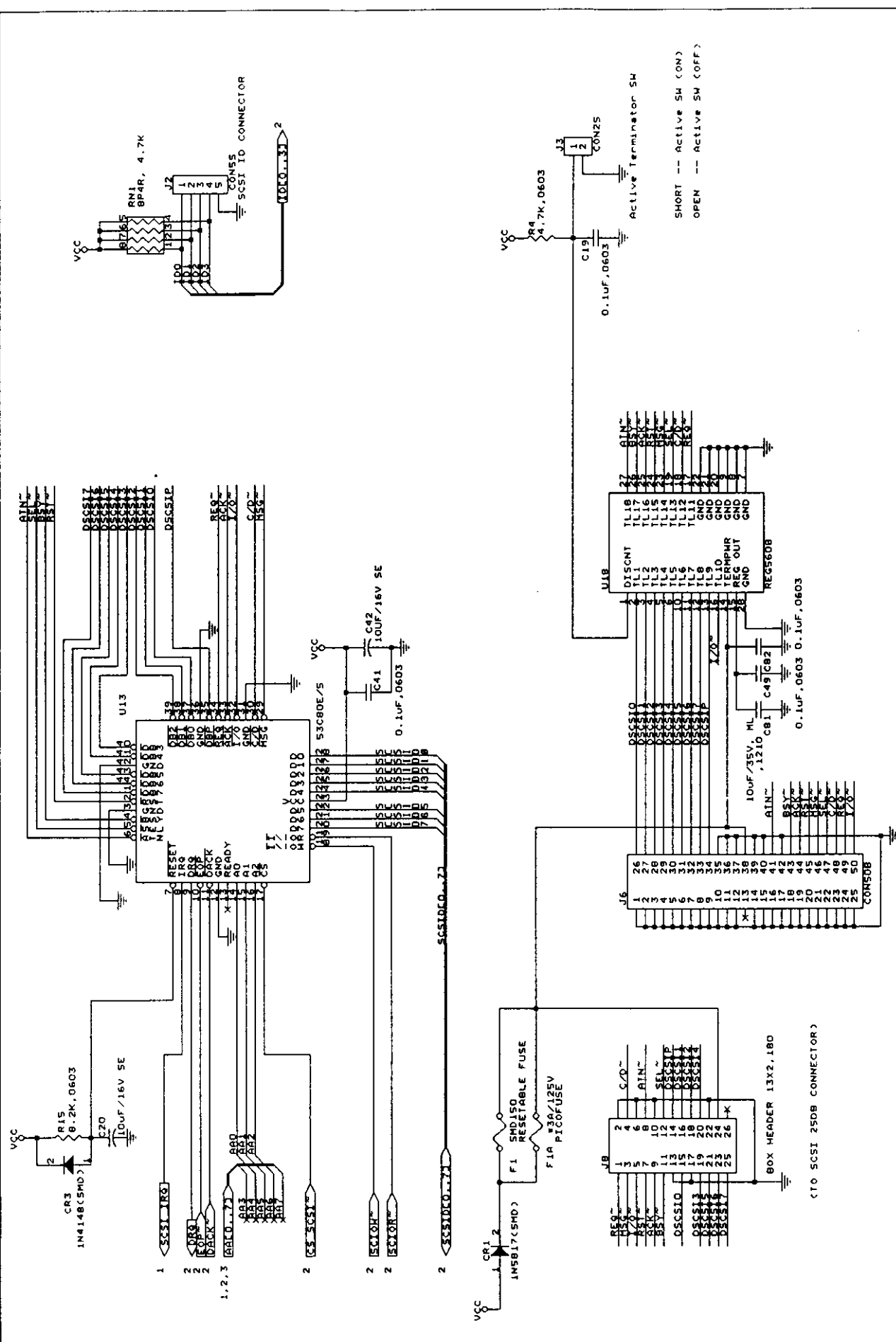
M1	M2	PHASE	STEP
0	0	1-2	FULL
0	1	1-2	HALF
1	0	2H1-2	1/4
1	1	2H1-2	1/8

PROJECT : APS 4000 DPI Flamingo
FUNCTION : 3 MOTOR, LED DRIVER

DESIGNER : C.P. HUANG

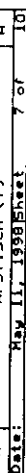
Size Document Number
A
APSS.SCH (S)

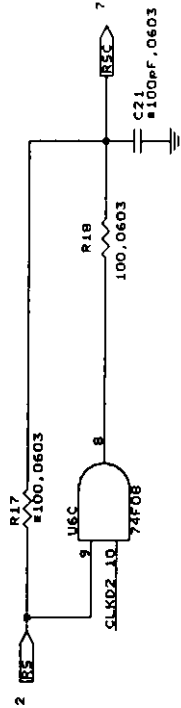
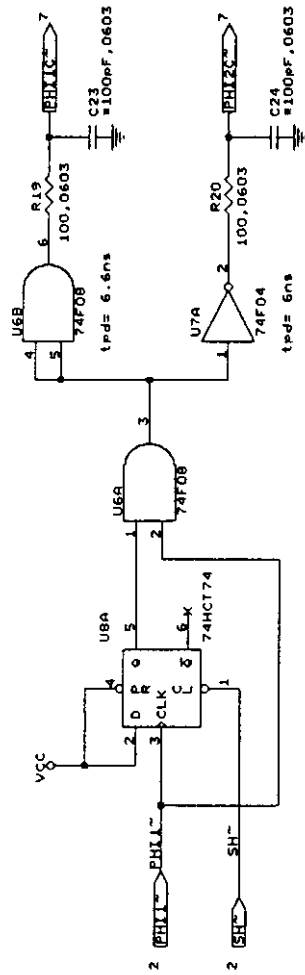
Date: Apr-11-27, 1998 Sheet 5 of 10



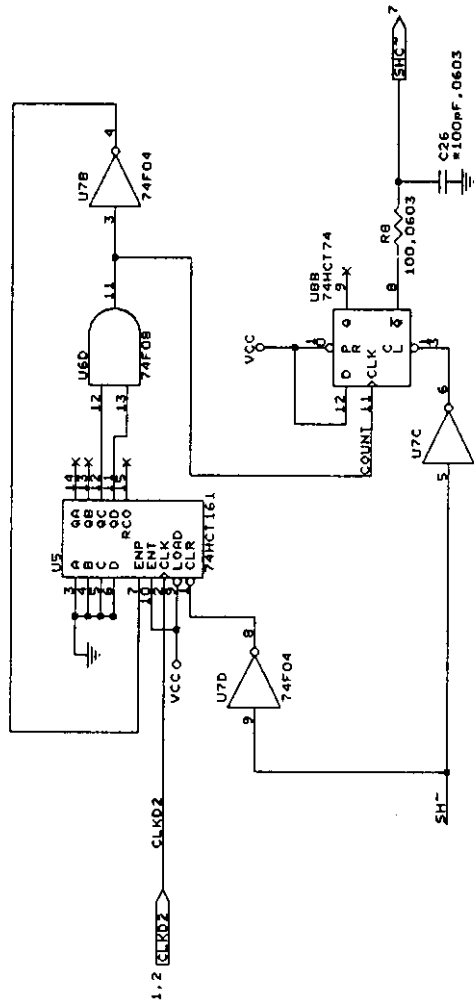
<50 PIN SCSI CONNECTOR>

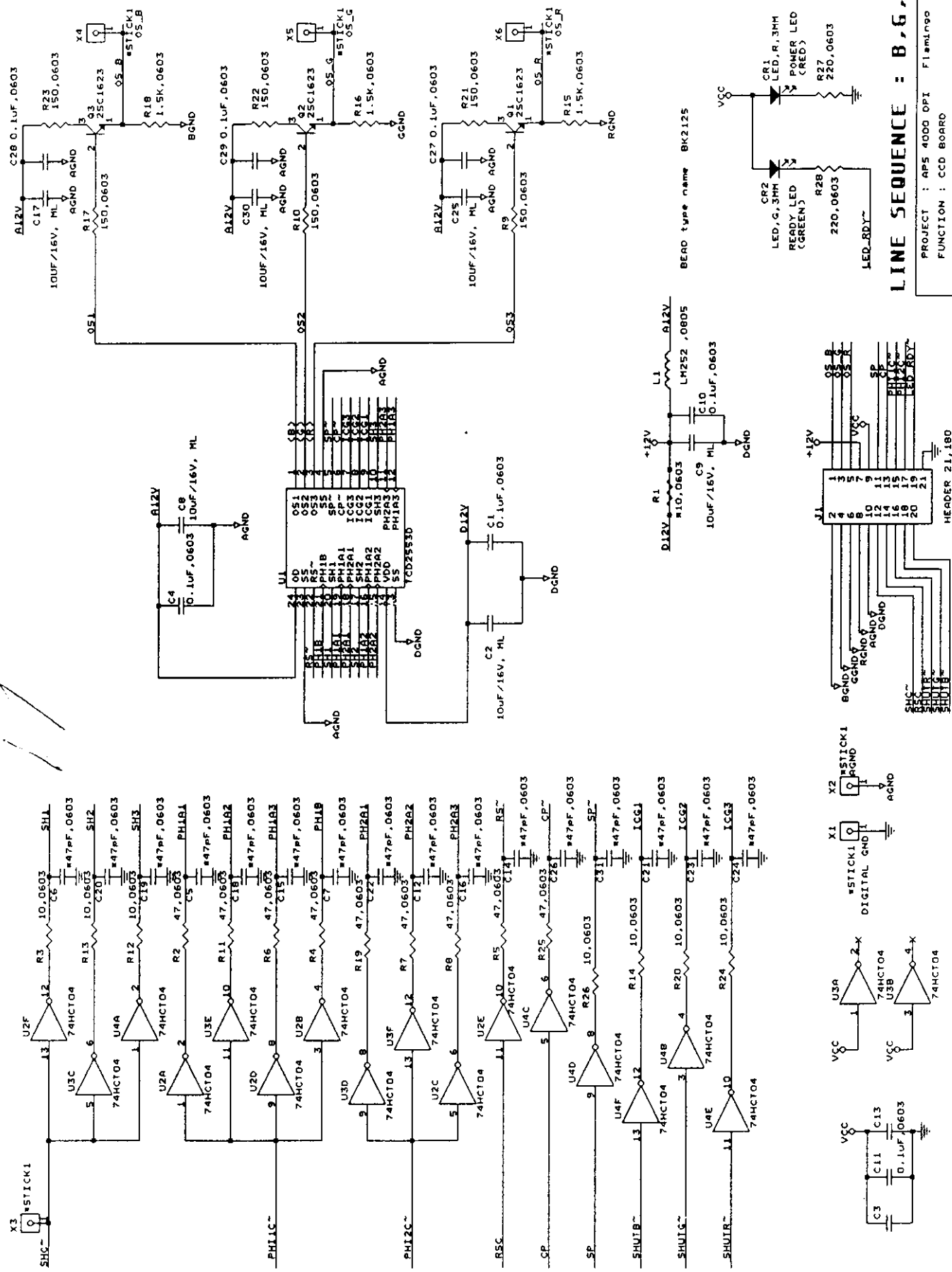
PROJECT : APS 4000 DPI Flamingo
FUNCTION : SCSI CONTROLLER
DESIGNER : C.P. HUANG
Size
Document Number
A
APSS6.SCH (c)
Date: April 28, 1998 Sheet 6 of 6





R21	100	OPEN	100
R22	100	OPEN	100





LINE SEQUENCE = B,6,R

PROJECT :	APS 4000 DPI	F1am1ng0
FUNCTION :	CCD BOARD	
DESIGNER :	C.P. HUANG	
Size	Document Number	REV
A	APS2553.SCH	A
Date :	May 8, 1998	Sheet 1 of 1