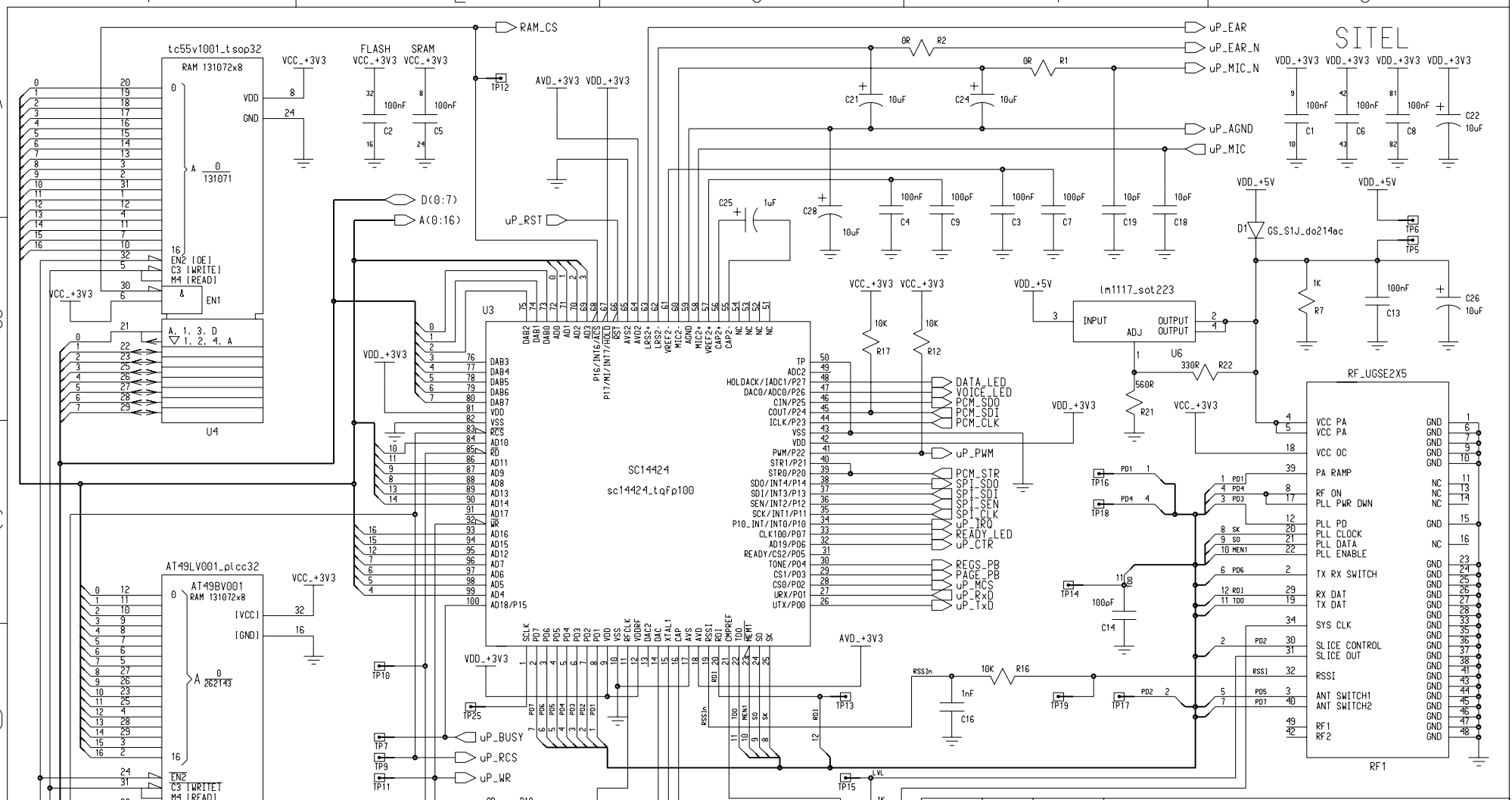
SITEL
tc55v1001_tsop32
RAM 131072x8
FLASH
SRAM
VCC_+3V3
RAM_CS
TP12
AVD_+3V3
VDD_+3V3
D(0:7)
A(0:16)
uP_RST
uP_EAR
uP_EAR_N
uP_MIC_N
uP_AGND
uP_MIC
R2
R1
C21
C24
C25
C28
C4
C9
C3
C7
C19
C18
C1
C6
C8
C22
C2
C5
U4
U3
SC14424
sc14424_tqfp100
DATA_LED
VOICE_LED
PCM_SDO
PCM_SDI
PCM_CLK
uP_PWM
PCM_STR
SPI_SDO
SPI_SDI
SPI_SEN
SPI_CLK
uP_IRQ
READY_LED
uP_CTR
REGS_PB
PAGE_PB
uP_MCS
uP_RxD
uP_TxD
VDD_+5V
D1
GS_S1J_do214ac
TP6
TP5
R7
C13
C26
lm1117_sot223
INPUT
ADJ
OUTPUT
OUTPUT
U6
R22
R21
R17
R12
RF_UGSE2X5
VCC PA
VCC OC
PA RAMP
RF ON
PLL PWR DWN
PLL PD
PLL CLOCK
PLL DATA
PLL ENABLE
TX RX SWITCH
RX DAT
TX DAT
SYS CLK
SLICE CONTROL
SLICE OUT
RSSI
ANT SWITCH1
ANT SWITCH2
RF1
RF2
GND
NC
RF1
AT49LV001_plcc32
AT49BV001
RAM 131072x8
[VCC]
[GND]
EN2
C3 [WRITE]
M4 [READ]
ENT
RESET
U2
uP_BUSY
uP_RCS
uP_WR
uP_RD
TP10
TP25
TP7
TP9
TP11
TP24
TP20
TP13
TP14
TP15
TP16
TP18
TP19
TP17
R16
C16
C14
R19
R20
C31
74hc04_tssop14
U1
L1
5.6nH
R18
1M
1nF
C17
10.368MHz
Y1
C15
33pF
C11
C20
C10
R11
1K
BB639
D2
CLK
LVL
INESC
- INSTITUTO DE ENGENHARIA
DE SISTEMAS E COMPUTADORES
SERVICO DE PCB
PROJ
DES
JMG
99-11-16
VERIF
APROV
FORMATO
A4
TITULO
Dect_PADCard_V4.0
MICROCONTROLADOR
FOLHA
1
DE
2
REF
DP.EE/219.1/4.0
OBS