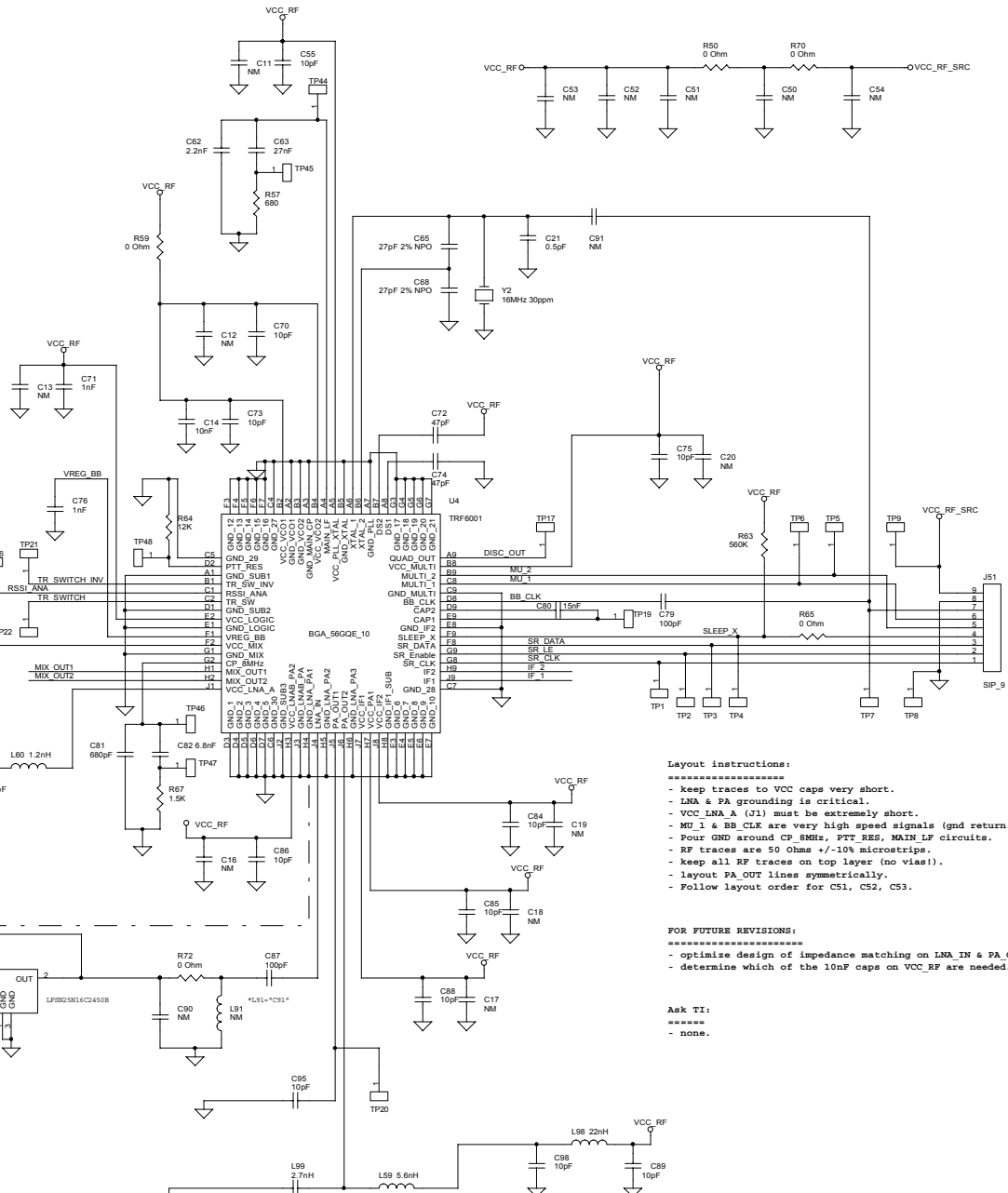
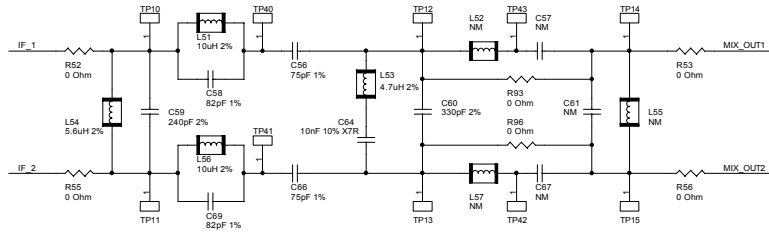
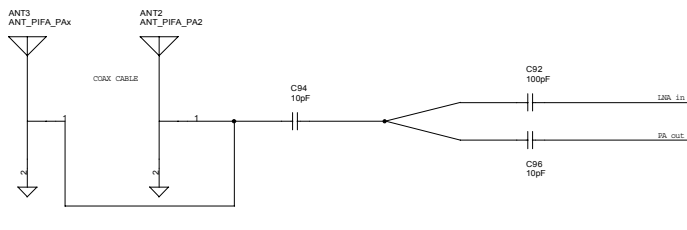


\*\*\* WILL UPDATE COMPLETE IF FILTER, CHOOSE LOWER TOL. COMPONENTS \*\*\*  
 Note: All inductors are specified 5% tol, but expected performance is 2% tol. All capacitors are NPO type.



RF microstrip: 50 Ohm +/- 10% @ 2.44GHz.



#### Layout instructions:

- keep traces to VCC caps very short.
- LNA & PA grounding is critical.
- VCC\_LNA\_A (J1) must be extremely short.
- MU\_1 & BB\_CLK are very high speed signals (gnd return!).
- Pour GND around CP\_8MHz, PTT\_RES, MAIN\_LF circuits.
- RF traces are 50 Ohms +/-10% microstrips.
- keep all RF traces on top layer (no vias!).
- layout PA\_OUT lines symmetrically.
- Follow layout order for C51, C52, C53.

#### FOR FUTURE REVISIONS:

- optimize design of impedance matching on LNA\_IN & PA\_OUT.
- determine which of the 10nF caps on VCC\_RF are needed.

#### Ask TI:

- none.

R59 value is for 3.0V VCC\_RF product version.

LOGITECH CONFIDENTIAL

|             |                            |                   |        |
|-------------|----------------------------|-------------------|--------|
| File        | RF Module_Hagrid - TRF6001 | Project Name      | HAGRID |
| Designed by | ES, PL                     | Project Name      | HAGRID |
| Size        | Document Number            | PCB / part number | Rev    |
| B           | 221625-0000                | 201625-0000       | PA5.2  |
| Date        | Tuesday, June 04 2002      | Sheet             | 1 of 1 |

