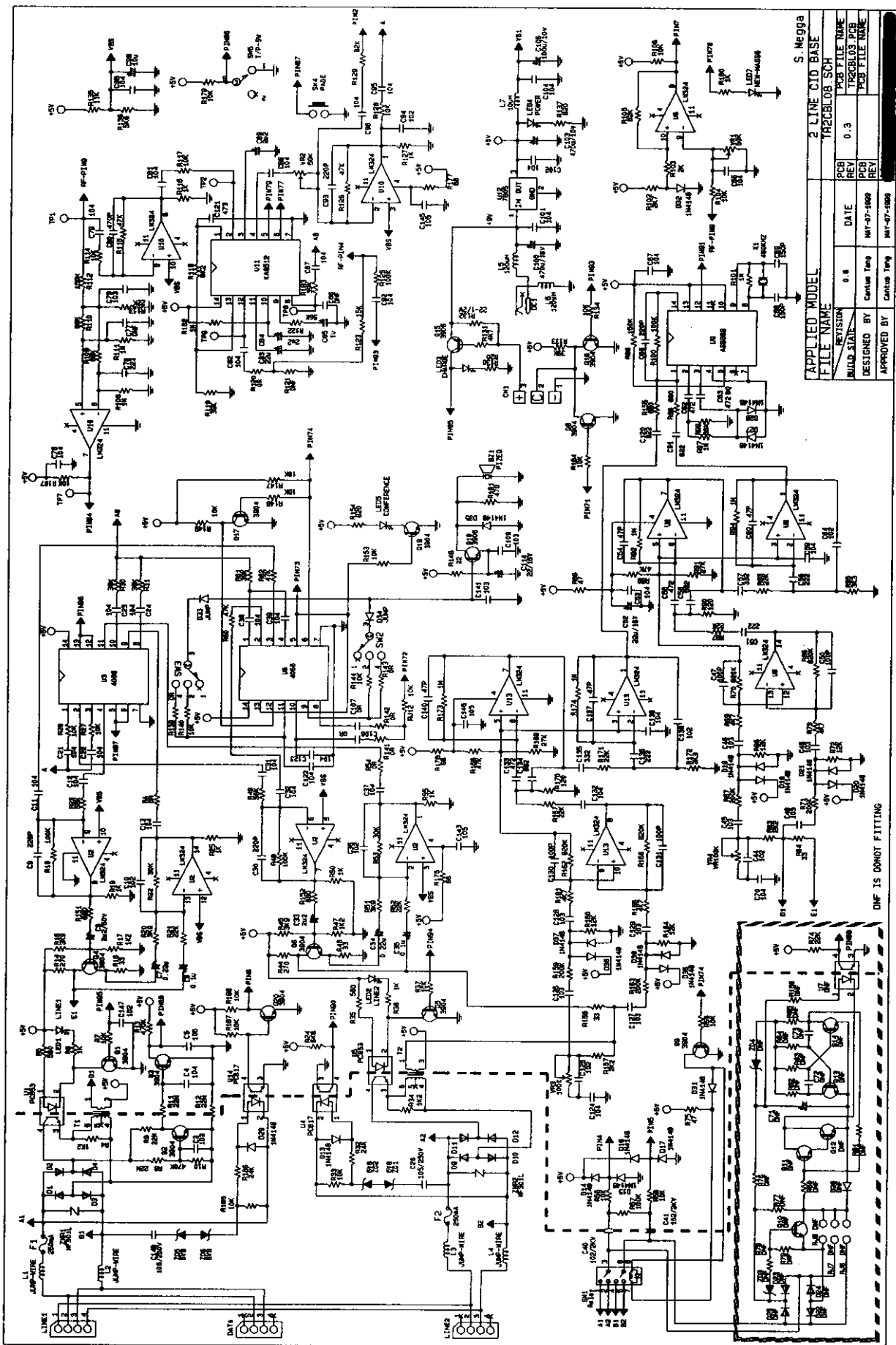
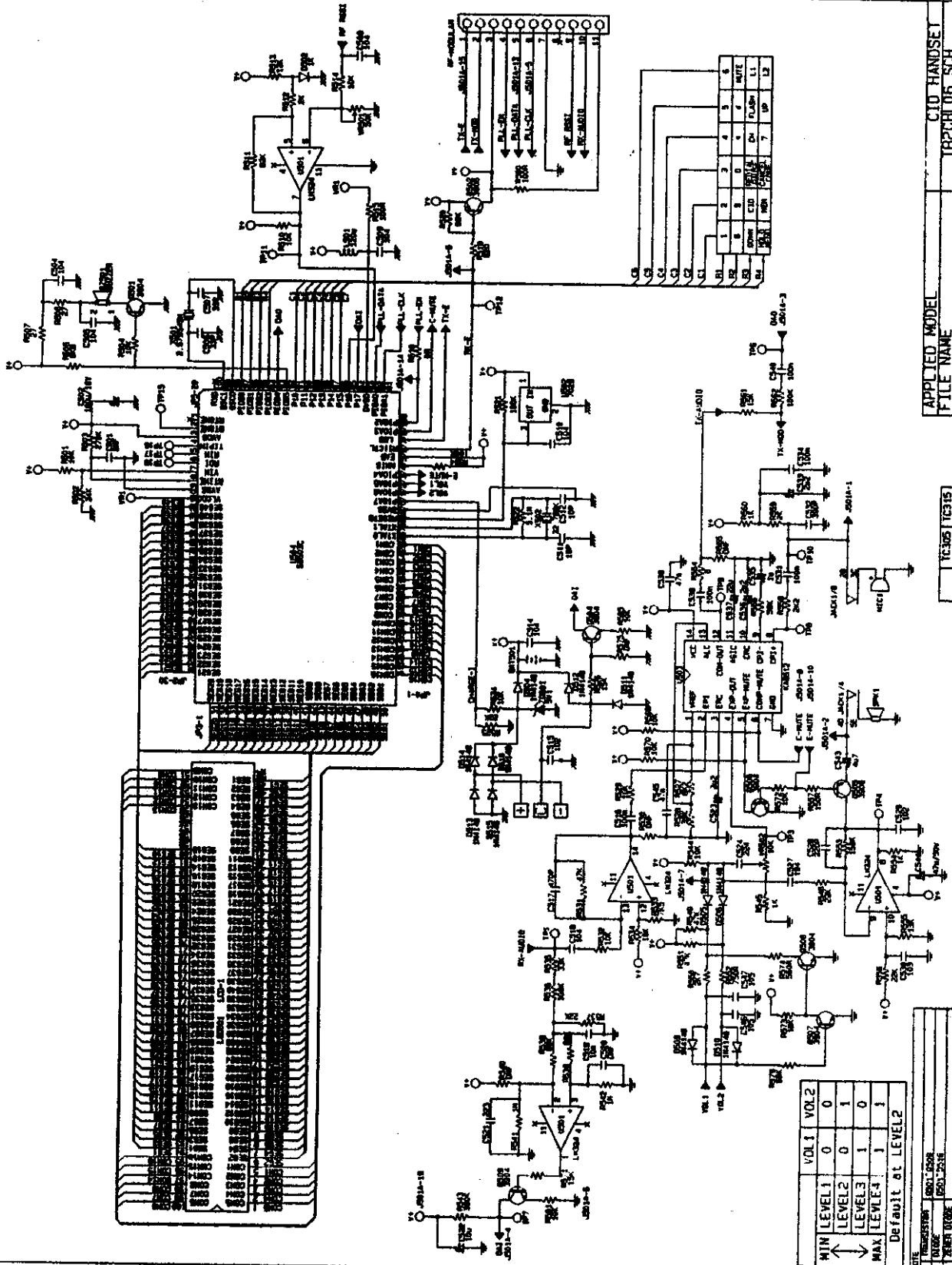




APPLIED MODEL		2 LINE CID BASE	
FILE NAME		TR2CBL03 SCH	
REVISION		0.3	
BUILD STATE		PCB	
DESIGNED BY		MAY-27-2000	
APPROVED BY		MAY-27-2000	

DWG IS DONOT FITTING



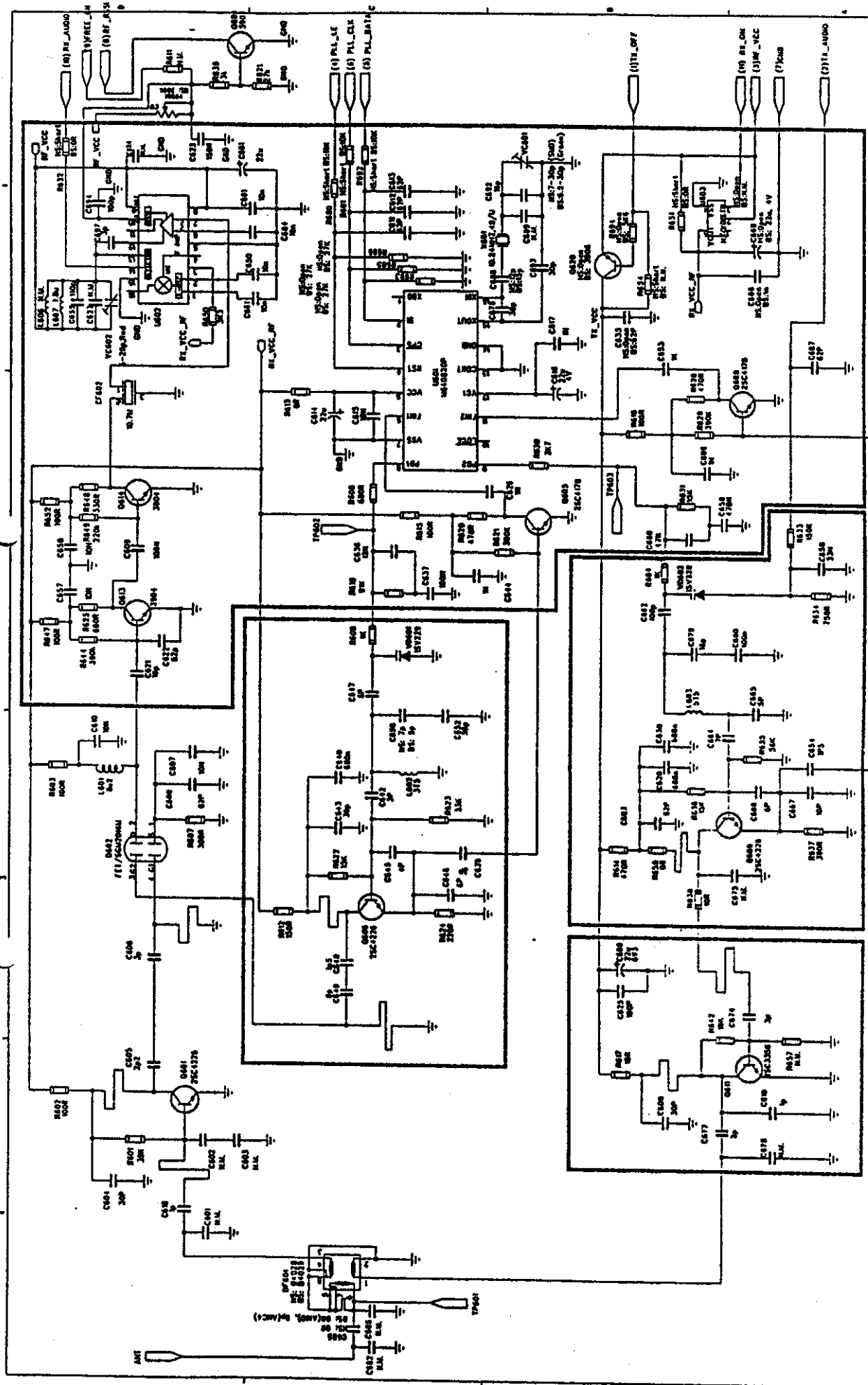
APPLIED MODEL				C10 HANDSET			
FILE NAME		TR2CH05.SCH		PCB FILE NAME		TR2CH05.PCB	
BUILD SIZE		0.6		DATE		APR-28-1999	
DESIGNED BY		MICHAEL		PCB		PCB	
APPROVED BY		CANTUS		REV		REV	

VC05	VC05	VC05
RS05	ENG	DMF

	VOL1	VOL2
MIN LEVEL1	0	0
LEVEL2	0	1
LEVEL3	1	0
MAX LEVEL4	1	1

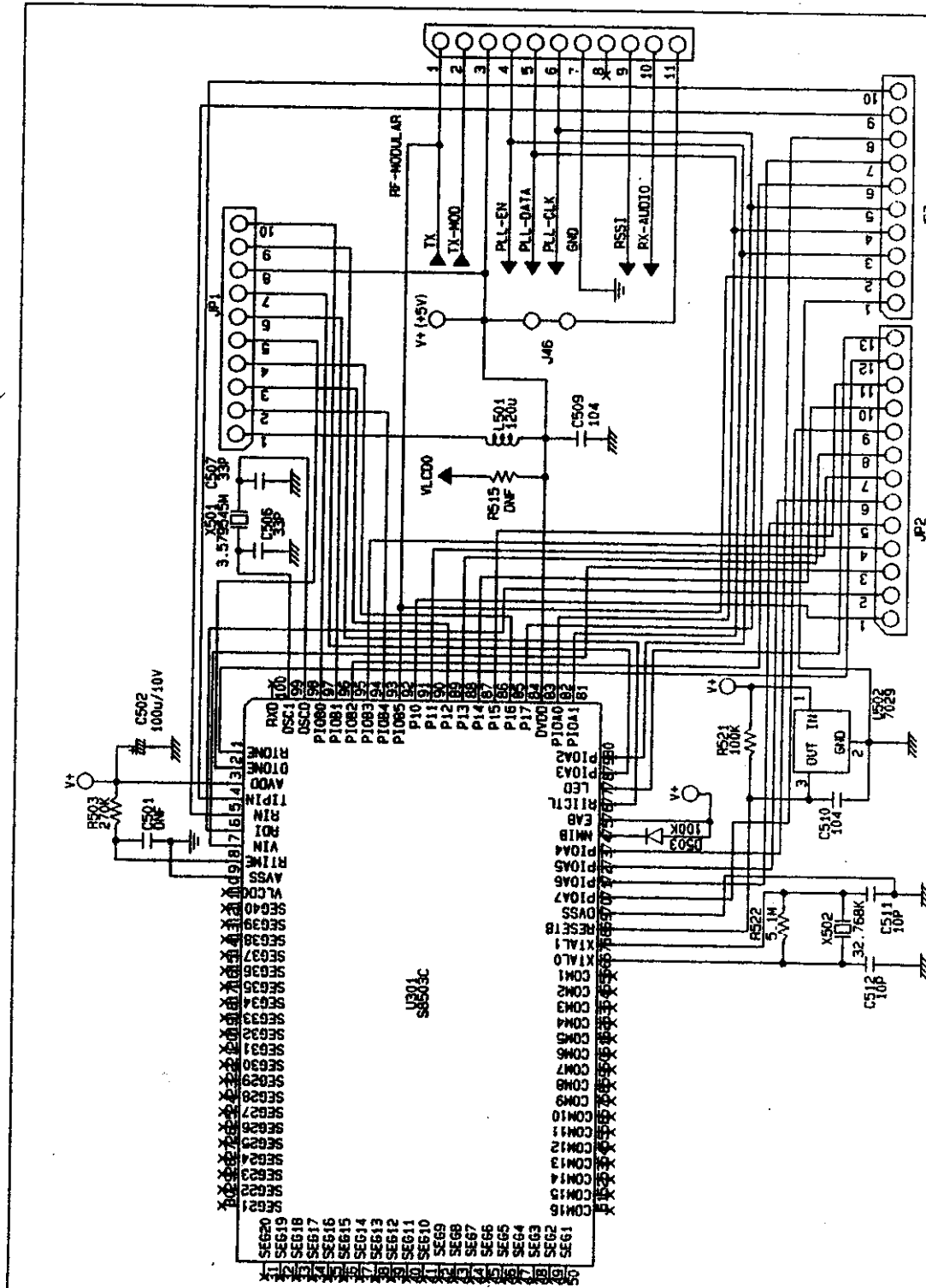
Default at LEVEL2

1	DATE	001-2209
2	TIME	050-2216
3	USER	001-2216
4	REVISION	001-2216
5	REVISION	001-2216
6	REVISION	001-2216
7	REVISION	001-2216
8	REVISION	001-2216



•Remark: ignore parts for the handset -
VR601,C623,R611,R605,R608,0615

Drawn:	Kenneth To	Company:	3 Stage Technology LTD.
Checked:	Kenneth To	Title & Filename:	MF MODULE (USA)
Rev:		Rev:	
Date:	17 March 1999	Sheet:	5.0



NOTE		1 TRANSISTOR	U503
2 DIODE			
3 ZENER DIODE			
4 RESISTOR			
5 CAPACITOR			
6 IC			
7 TFI			
8 INDUCTORS			

APPLIED MODEL		CID BASE CPU	
FILE NAME		TR2CCPU03.SCH	
REVISION	0.4	PCB REV	0.2
BUILD STATE	Frank	DATE	APR-28-1999
DESIGNED BY	Frank	PCB FILE NAME	TR2CCPU03
APPROVED BY	CANTUS	PCB FILE NAME	
		PCB REV	
		DATE	
		APR-28-1999	
		APR-28-1999	