ADVANCED
COMPLIANCE LABORATORY

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ELECTROMAGNETIC EMISSION COMPLIANCE REPORT

of

LOW POWER TRANSMITTER
MODEL: TELISSOL
FCC ID: DWNTELISSOL

April 4, 2000

This report concerns (check one): Original grant __x__ Class II change _____
Equipment type: TRANSMITTER

Deferred grant requested per 47 CF 0.457(d)(1)(ii)? yes _____ no __x__
If yes, defer until: _____ (date)
Company agrees to notify the Commission by _____ (date)
of the intended date of announcement of the product so that the grant can be issued on that date.

Transition Rules Request per 15.37? yes _____ no __x__
If no, assumed Part 15, Subpart B for unintentional radiators - the new 47 CFR [10-1-90 Edition] provision.

Report prepared for:	SOMFY SYSTEM, INC.
Report prepared by:	Advanced Compliance Lab
Report number:	0048-032701-01

NVLAP

The test result in this report IS supported and covered by the NVLAP accreditation

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Figures

1. GENERAL INFORMATION

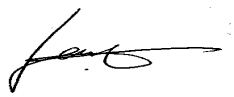
1.1 Verification of Compliance

EUT:	TRANSMITTER
Model:	TELISSOL
Applicant:	SOMFY SYSTEM, INC. 47 Commerce Drive, Cranbury, NJ 08512 Tel: (609) 395-1300
Test Type:	FCC Part 15C CERTIFICATION
Result:	PASS
Tested by:	ADVANCED COMPLIANCE LAB
Test Date:	March 30, 2001
Report Number:	0048-032701-01

The above equipment was tested by Advanced Compliance Lab. Compliance Laboratory for compliance with the requirement set forth in the FCC rules and regulations Part 15, subpart C. This said equipment in the configuration described in the report, shows the maximum emission levels emanating from equipment are within the compliance requirements.

The estimated uncertainty of the test result is given as following. The method of uncertainty calculation is provided in Advanced Compliance Lab. Doc. No. 0048-01-01.

	Prob. Dist.	Uncertainty(dB)	Uncertainty(dB)	Uncertainty(dB)
		30-1000MHz	1-6.5GHz	Conducted
Combined Std. Uncertainty u_c	norm.	±2.36	±2.99	±1.83



Wei Li

Date: April 4, 2001

Lab Manager
Advanced Compliance Lab

1.2 Equipment Modifications

N/A

1.3 Product Information

System Configuration

ITEM	DESCRIPTION	FCC ID	CABLE
Product	TRANSMITTER(1)	DWNTELISSOL	
Housing	PLASTICS		
Power Supply	3V DC BATTERY		
Clock/OSC Freq.	433.5 MHz		
Device Type	Periodic Operation		

(1) EUT submitted for grant.

1.4 Test Methodology

Radiated tests were performed according to the procedures in ANSI C63.4-1992 at an antenna to EUT distance of 3 meters.

1.5 Test Facility

The open area test site and conducted measurement facility used to collect the radiated and conducted data are located at 50 Randolph Road, Somerset, New Jersey. This site has been accepted by FCC to perform measurements under Part 15 or 18 in a letter dated May 19, 1997 (Refer to: 31040/PRV 1300F2). The NVLAP Lab code for accreditation of FCC EMC Test Method is: 200101-0.

1.6 Test Equipment

Manufacture	Model	Serial No.	Description	Last Cal dd/mm/yy	Cal Due dd/mm/yy
Hewlett-Packard	HP8546A	3625A00341	EMI Receiver	08/01/01	08/01/02
Fischer Custom	LISN-2	900-4-008	Line Impedance Stabilization Networks	20/05/00	20/05/01
Fischer Custom	LISN-2	900-4-009	Line Impedance Stabilization Networks	26/04/00	26/04/01
EMCO	3115	4945	Double Ridge Guide Horn Antenna	05/12/99	05/12/00
EMCO	3104C	4396	30-200MHz Biconical Antenna	02/05/00	02/05/01
EMCO	3146	3350	200-1000MHz Log-Periodic Antenna	02/05/00	02/05/01

All Test Equipment Used are Calibrated Traceable to NIST Standards.

1.7 Statement for the Document Use

This report shall not be reproduced except in full, without the written approval of the laboratory. And this report must not be used by the client to claim product endorsement by NVLAP or any agency of the U.S. Government.

2. PRODUCT LABELING

See attachment: fcclabel.jpg

Fig 2.1 FCC ID Label

Fig. 2.2 Location of Label

3. SYSTEM TEST CONFIGURATION

3.1 Justification

The system was configured for testing in a typical fashion (as a customer would normally use it). And its antenna was permanently attached to the EUT (Made on the PCB). There are two LEDs on the front side.

This manually operated transmitter will deactivate immediately after release " button.

Testing was performed in either "UP arrow", " Down arrow" and " O" button. It is the worst case.

3.2 Special Accessories

N/A

3.3 Configuration of Tested System

Figure 3.1 and Figure 3.3 illustrate this system, which is tested standing along.

Figure 3.1 Radiated Test Setup, Position 1

Figure 3.2 Radiated Test Setup, Position 2

Figure 3.3 Radiated Test Setup, Position 3

4. SYSTEM SCHEMATICS

See attachment: schematic1.jpg & schematic2.jpg

Figure 4.1(a) (b) System Schematics

5. RADIATED EMISSION DATA

5.1 Field Strength Calculation

The corrected field strength is automatically calculated by EMI Receiver using following:

$$RA = FS + AF + CF + AG$$

where FS: Corrected Field Strength in dBμV/m
RA: Amplitude of EMI Receiver before correction in dBμV
AF: Antenna Factor in dB/m
CF: Cable Attenuation Factor in dB
AG: Built-in Preamplifier Gain in dB (Stored in receiver as part of the calibration data)

The pulse train timing plots are showed in Figure 5.1.
The pulse train timing plots as follows:
The total time for each pulse train is 139.62 ms. The max pulse length counted is 100ms. The short pulse is 0.640ms.The middle pulse is 2.5 ms. The long pulse is 4.8ms.

Coeff. =(56x0.640+2.5x4+0.38+4.8x1)/100 = 0.5102

The maximum average field strength should be 0.5102 of the peak field strength measured. So we use peak value minus 5.85dB as calculated maximum average field strength.

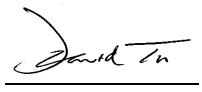
5.2 Test Methods and Conditions

The EUT exercise program was loaded during the radiated emission test. The initial step in collecting radiated data is a EMI Receiver scan of the measurement range 30MHz - 5GHz using peak detector. IF bandwidth is 120kHz and video bandwidth is 300kHz for measuring 30MHz-1GHz. Both bandwidth are 1MHz for above 1GHz measurement.

5.3 Test Data

The following data lists the significant emission frequencies, polarity and position, peak reading of the EMI Receiver, calculated average reading, the FCC limit, and the difference between the peak reading and the limit. Explanation of the correction and calculation are given in section 5.1.

Test Personnel:

Tester Signature 

Typed/Printed Name: David Tu Date: March 30, 2000

Radiated Test Data

Frequency (MHz)	Polarity [H or V], Position (X,Y,Z)	Height (m)	Azimuth (Degree)	Peak Reading (dBμV/m)	Calculated Average Reading (dBμV/m)	FCC 3m Limit (dBμV/m)	Difference from limit (dB)
433.5	H,X	2.4	270	82.3	76.45	80.8(3)	-4.35
1300.4	H,X	1.0	175	54.8	48.95	54.0(2)	-5.05
1733.8	H,X	1.0	090	52.4	46.55	60.8(4)	-14.25
433.5	V,X	2.2	175	79.3	73.45	80.8	-7.35
1300.4	V,X	1.0	105	50.5	44.65	54.0	-9.35
433.5	H,Y	2.4	090	83.3	77.45	80.8	-3.35
1300.4	H,Y	1.0	085	52.8	46.95	54.0	-7.05
1733.8	H,Y	1.0	105	49.6	43.75	60.8	-17.05
433.5	V,Y	1.9	180	78.4	72.55	80.8	-8.25
1300.4	V,Y	1.0	105	53.5	47.65	54.0	-6.35
433.5	H,Z	1.0	255	67.8	61.95	80.8	-18.85
1300.4	H,Z	1.0	045	55.5	49.65	54.0	-4.35
1733.8	H,Z	1.0	225	48.0	42.15	60.8	-18.65
433.5	V,Z	1.3	180	84.7	78.85	80.8	-1.95
1300.4	V,Z	1.0	180	49.4	43.55	54.0	-10.45

(1) See Figure 3.1, 3.2 and 3.3 for definition of position X-1, Y-2, Z-3.
(2) Restricted band.
(3) Fundamental limit is 3750-12500 microvolts/meter linear interpolations.
(4) Spurious limit is 375-1250 microvolts/meter linear interpolations.

5.4 Occupied Bandwidth

The bandwidth of the emission shall be no wider than 0.25% of the center frequency, in this case, 1.084MHz(433.5x0.25%). Bandwidth is determined at the points 20dB down from the modulated carrier. Figure 5.1 shows the occupied bandwidth plot.

See attachment: Pulsetrn.jpg

Figure 5.1 Pulse Train Timing

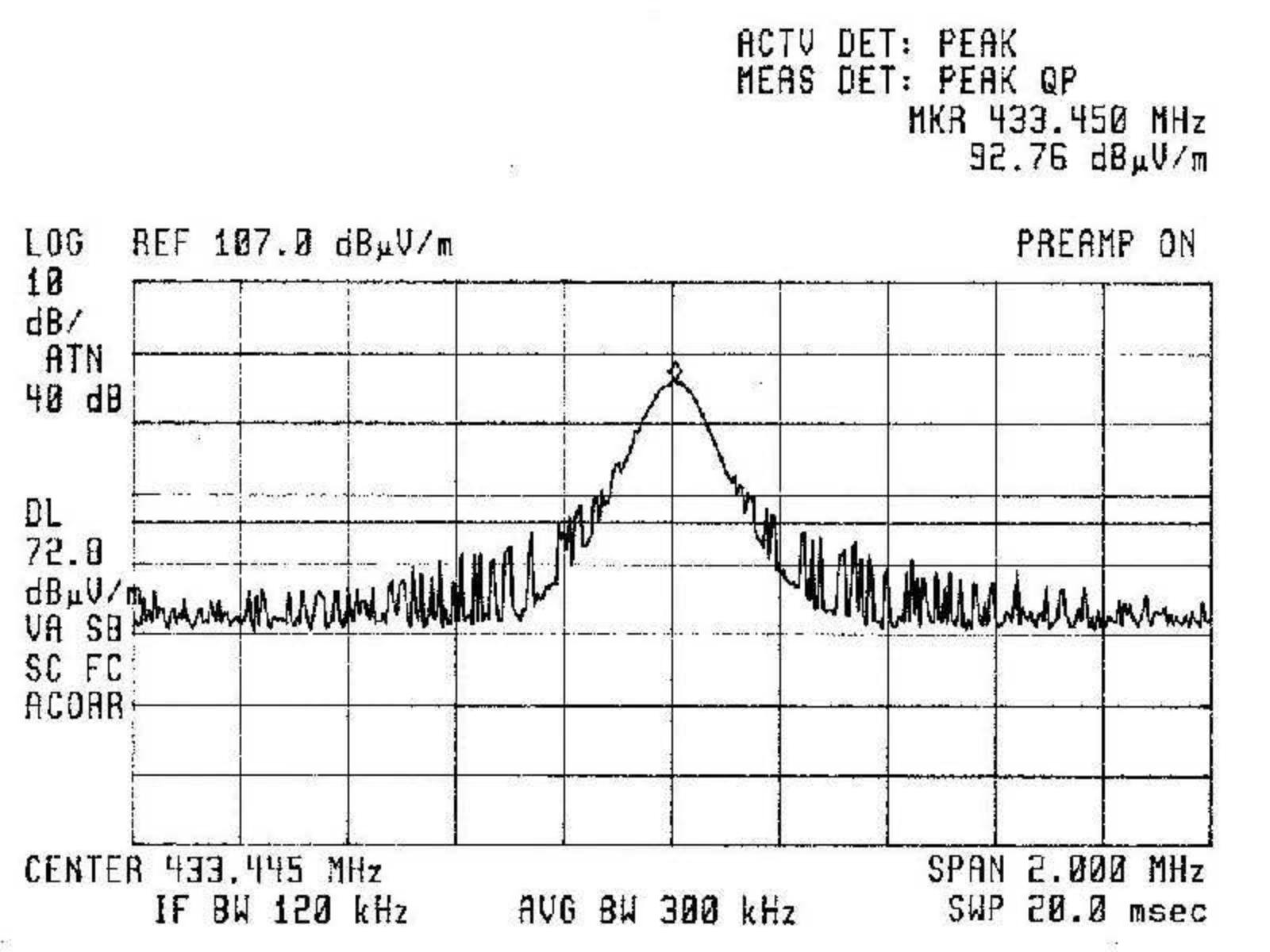
See attachment: ocpband.jpg

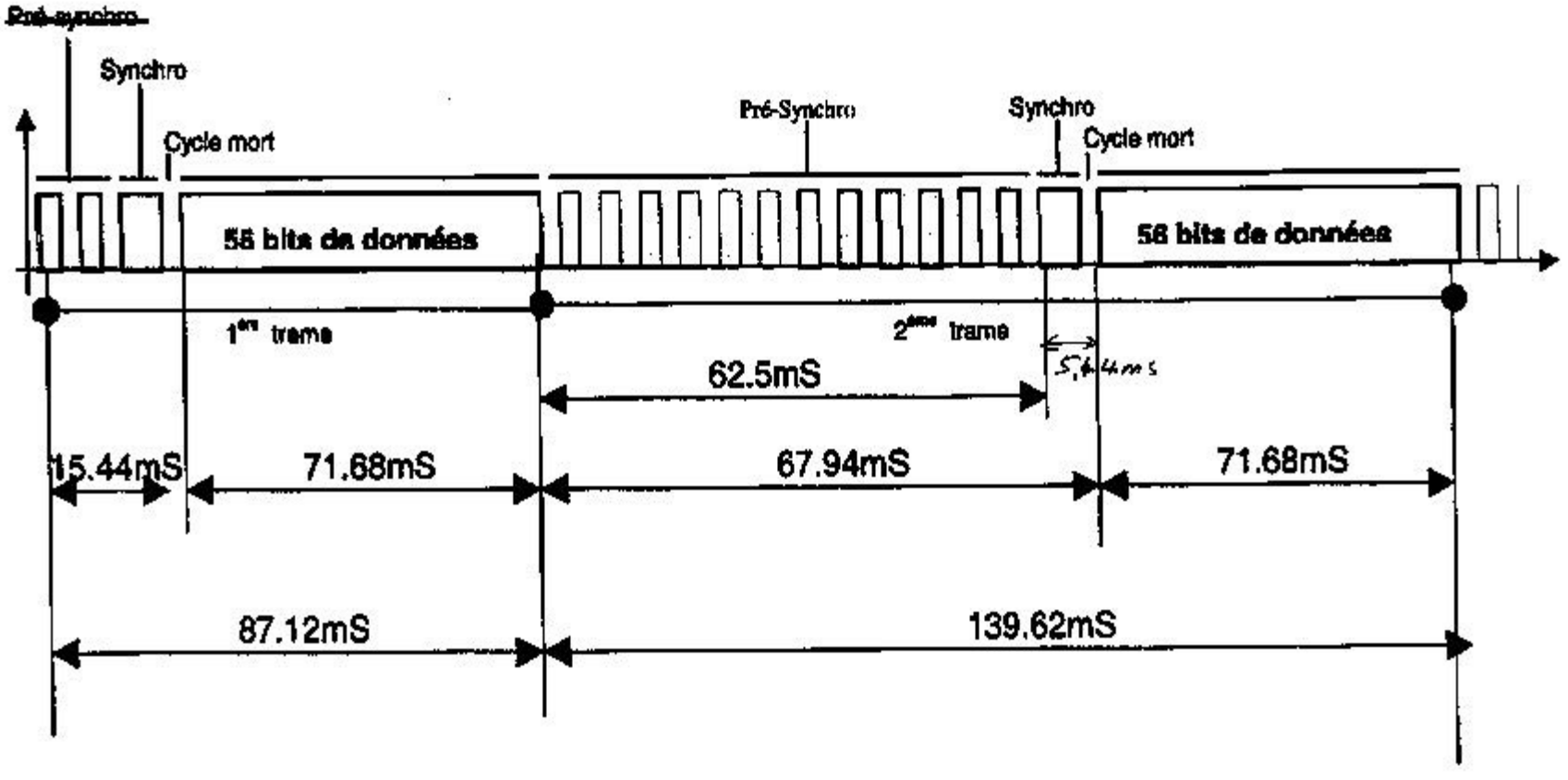
Figure 5.2 Occupied Bandwidth

6. PHOTOS OF TESTED EUT

The following photos show the inside details of the EUT.

See Attachments: front.jpg, rear.jpg, inside.jpg, component.jpg, foil.jpg


ACTV DET: PEAK
MEAS DET: PEAK QP
MKR 433.450 MHz
92.76 dBµV/m
LOG
10
dB/
ATN
40 dB
REF 107.0 dBµV/m
PREAMP ON
DL
72.0
dBµV/m
VA SB
SC FC
ACORR
CENTER 433.445 MHz
IF BW 120 kHz
AVG BW 300 kHz
SPAN 2.000 MHz
SWP 20.0 msec


Pré-synchro
Synchro
Cycle mort
Pré-Synchro
Synchro
Cycle mort
56 bits de données
56 bits de données
1ère trame
2ème trame
62.5mS
5,44mS
15.44mS
71.68mS
67.94mS
71.68mS
87.12mS
139.62mS