11 Mbps RF Module

"No more Wires"



Features

- Utilizing Intersil Prism II Technology
- 2.4 ~ 2.5 GHz ISM band operations
- Data rate up to 11 mbps
- · Single heterodyne conversion
- Direct Sequence Spread Spectrum Technology
- +10 to +27 dBm transmitter power out
- Includes additional LNA for receiver front end.
- Diversity ANT for better receiver signal
- System targeting IEEE 802.11b standard except Tx power out. Set Tx power out as required by Application or per Country Regulation.
- Small size : 3.40" x 2.05" x 0.750" (86 x 52 x 19 mm).
- All Spec FCC part 15 ISM Band Non License compliant Radio.



Overview

The GINA-2011 is a small, versatile high performance RF Module that delivers data up to speeds of 11Mbits/s. The Module is designed to provide up to 0.5 watt (27dBm) output power. The GINA-2011 RF Module is available as a plug-in module for those Researche who wish to interface it with there own designed controllers.

GINA-2011 operates at 2.4 GHz Direct Sequence Spread Spectrum (DSSS) Modulation physical layer can be compliant with IEEE 802.11b standard insures interoperability with other Wireless LAN products.

GINA-2011's highly integrated digital modulation transceiver module can also be used for factory automation equipment and barcode readers, embedded systems, and other applications.

APPLICATIONS

- Wireless Network
- Wireless Internet
- Mobile Scanner
- Remote Guidance Vehicles
- Robotic Remote Control
- Last-Mile Telephone
- Camera Surveillance Control (PTZ)
- Industrial Automation
- GPS Data Link
- Power & Gas Utililies
- PLC / Router Connections
- Computer Data Links
- · Traffic Controls
- Remote Networking
- High Speed File Transfer



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"No more Wires"

RECEIVER

Frequency Range:

> 2.4 ~ 2.4835 GHz

Data Rate:

I ~ 11 Mbits/s

Sensitivity 10-6 @:

-96 dBm for 1 Mbit/s
 -93 dBm for 2 Mbits/s
 -89 dBm for 5.5 Mbits/s

Noise Figure:

Less then 10 dB

Supply Current :

► 150 mA without sleep mode 100 mA with sleep mode

-87 dBm for 11 Mbits/s

TRANSMITER

Frequency Range:

> 2.4 ~ 2.4835 GHz

Data Rate:

I ~ 11 Mbits/s

Output Power:

► +10 ~ +27 dBm (adjustable) Note: Tx supply voltage requires at least 6.0V DC for 25 ~ 27 dBM typical power output

Spurious Output:

FCC Part 15

Supply Current:

650 mA at Maximum Out.

UNIT SPECIFICATION

Supply Voltage

3.3v DC required for basic voltage 6.0 v DC TX section to get MAX power 27 dBm 5.0 v DC to opterate for TX section, but TX power will be 24 dBm

Supply Current:

300 mA for 3.3v line required at TX condition 500 mA for 6.0v or 5.0v line when TX is ON

TX Duty Cycle:

30 % for TX 70 % for RX

Temperature Range:

-20°C~ +60°C

Antenna Connector:

2x MMCX connector (for Diversity)

Interface Connector:

50 PIN 1.27 mm standard female connector (.050"x.050" SAMTEC SOCKET SFM Series) PN: SFML-125-T1-S-D-A

LED Indicator:

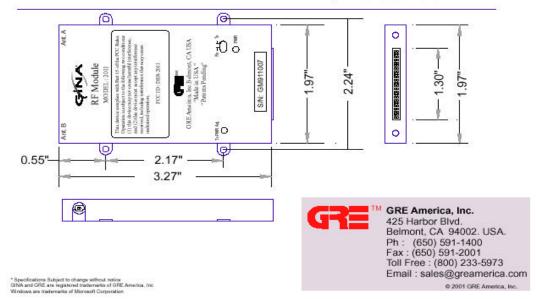
DC PWR, TX ON, RX Receiver

Adjustment:

TX power adjustment by POT volume

Approvals:

▶ FCC Part15. DE8-2011



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Warning

WARNING

RADIATION.

RF energy at microwave frequencies is present with this equipment. Personnel should not be exposed to high RF energies, and should not be in front of the antenna when the equipment is switched on.

All accessible RF connections must be terminated when the radio is powered on.

The radio should not be powered on with any of its covers removed.

INFORMATION TO USERS.

"Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment."



ANTENNA WARNING

EACH INDIVIDUAL ANTENNA USED FOR THIS TRASNMITTER MUST BE IN-STALLED TO PROVIDE A MINIMUM SEPARATION DISTANCE OF 20 CM OR MORE FROM ALL PERSONS AND MUST NOT BE CO-LOCATED WITH ANY OTHER ANTENNA FOR MEETING RF EXPOSURE REQUIREMENTS.

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GINA 2011 RF Module Voltage Requirement

GINA 2011 RF Module Voltage Requirement

The GINA 2011 RF Module requirs a device controller to set Local oscillator, Set BASEBAND processor for actual operating condition. It also requires proper I/O DC level because semiconductor is very thigh margin for I/O level.

The RF Module requires 3.3V and 6 Volt DC. Module has internal regulator to support 3.0V and 2.8 Volt. The device controller must be designed with 3 Volt working Voltage for I/O to RF Module. However it is not suitable if apply beyond 3.5V DC because majority of semiconductor Chip I/O level will be cross to Maximum specification point.

The 5 volt line is used for Transmitter final amplifier chip. The module 2011 has Current source voltage control MOS FET device to protect final amplifier chip. The final transmitter device is not liner output power verses voltage source. More likely the transmitter out is liner out with negative bias to GATE of Final amplifier. Nature of this performance comes from GaAs HFET parameter.

This negative bias to the GATE is regulated from 3.3volt line, so that transmitter out is set for Maximum to 27 dBm level.

However the 6 volt should not exceed more than 6 volt, due to some of the control line using 3 volt line to execute switching device.

Transmitter is not design to operate at 100% duty cycle.

Best performance is less than 30% duty cycle.

GINA 2011 RF Module is not designed or intended for any portable or handheld devices such as telephone set, PCMCIA devices or Portable PC's.

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GINA 2011 RF Module Data Specification Guide

GINA 2011 RF Module

Overview

Introduction

GINA RF Module 2011 is designed with the Prism II 11Mbit Intersil chipset to obtain a maximum performance data rate up to 11Mbit/sec. We are also focusing beyond the IEEE 802.11b application standard. The RF Module can provide an adjustable transmitter output power from 10 dBm to 27dBm (500 mW). The Receiver front end is installed with an extra low noise amplifier in order to perform extended distance wireless communications.

The RF Module size is also as small as a name card so as to cover any typical OEM application to fit this very small size. However this RF Module cannot work without a Controller. The Prism II chipset requires that you configure each registor value while programming from the other controller unit.

2. Requirement

GINA RF Module uses the Prism II chip set. The base band chip, RF up / down chip and the IF chip are required to be programmed from the Controller (similar to a MAC device). Therefore you should understand the basic theory about how it is necessary to program to these chips. The Data Book (included in this PDF downlaod Application Notes from Intersil) also describes a more detailed technical information on "How to Program".

Module are using chip as below.

HFA3861B-----Base band HFA3783N-----IF

HFA3683AIN-----RF up/down

- 2a The RF Module requires the User to basically enable the transmit or receive mode via the controller unit. You must also select the antenna port because the RF Module features a dual antenna port for diversity operations. This selection is executed via the base band chip - HFA3861.
- 2b DC voltage required to module.

3.3 v DC -----max current 300mA 6.0v DC -----max current 650 mA

(5.0 VDC still will operate however the Tx out power will be at 24 dBm maximum)

Programming

2c Please be sure to follow these requirements when you are

programming the RF Module.

Master clock ------44.000 Mhz

IF center frequency -----374 Mhz

Therefor 1st LO frequency need to program to HFA3683AIN RF up/down chip.

LO = Frx/tx - 374 Mhz Frx/tx is receive or transmit frequency between 2400~24835 Mhz.

2d Also IF chip HFA 3783IN need to program.

Main clock ------44.000 Mhz

LO required to program -----2 x 374 Mhz which is 748 Mhz.

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GINA 2011 RF Module Data Specification Guide

2e Base band chip HFA 3861B must also be programmed. However this program will be up to the design of your system. You should be familiar with and understand the HFA 3861B programming requirement. Main clock is 44.000 MHz

PIN Description

$ \begin{array}{ccc} \textbf{RF Module PINOUTS} \\ \textbf{H=3v} & \textbf{L=.5v or less} & \textbf{O} = \textbf{out put from Module} & \textbf{I} = \textbf{input to Module} \end{array} $			
PIN No.	PIN Name	Type	DESCRIPTION
1	TR_SW_N	I	Control antenna switch.H for RX. L for TX on.
2	RADIO_PD	I	Enable VCO. Normally set to H
3	LE_RF	I	Programming HFA3683 (pin21)for PLL latch enable
4	GND		
5	GND		
6	GND		
7	S_DATA	1	Programming HFA3861B(pin 3,64 internally connected).
8	RX_DATA	0	Recovered RX data from HFA3861(pin53)
9	TX_CLK	0	TX clock from HFA3861 (pin 55) to controller
10	TX_RDY	0	Output from HFA3861(pin 59)for other device
-11	RX_PE	1	HFA3861(pin 61)receiver enable active H
12	NOT USE		
13	NOT USE		
14	NOT USE		
15	VCCS	I	HFA3861 digital voltage(pin2,8,37,41,57) 3.3V required
16	NOT USE		
17	3.3v	I	Supply voltage to Module.current will up to 300 ma
18	3.3v	I	Same as above
19	3.3v	I	Same as above
20	NOT USE		
21	NOT USE		
22	CAL_EN	1	Controlled HFA3783(pin42)
23	LE_IF	I	HFA3783(pin20) PLL latch enable

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