

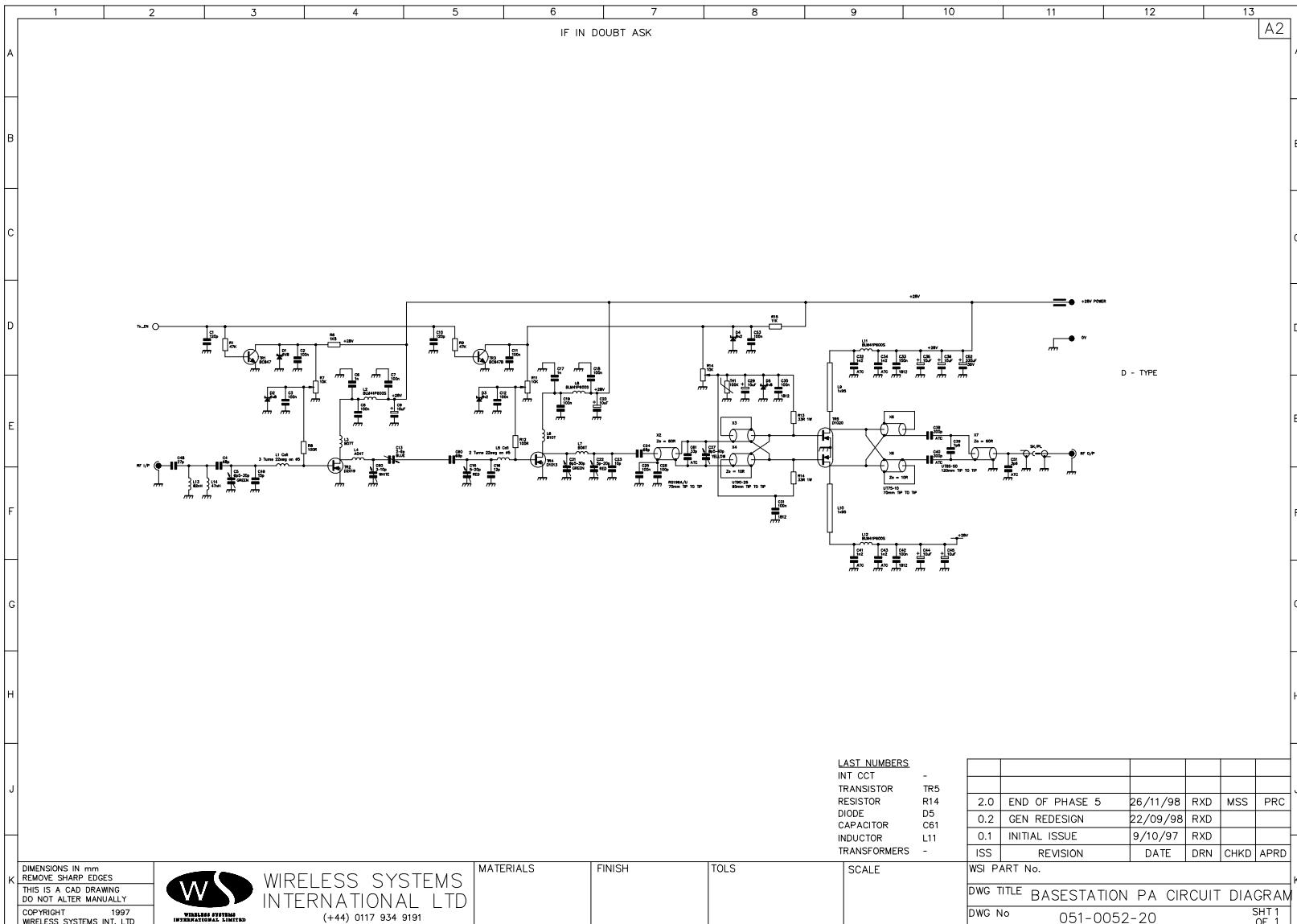
APPENDIX 3

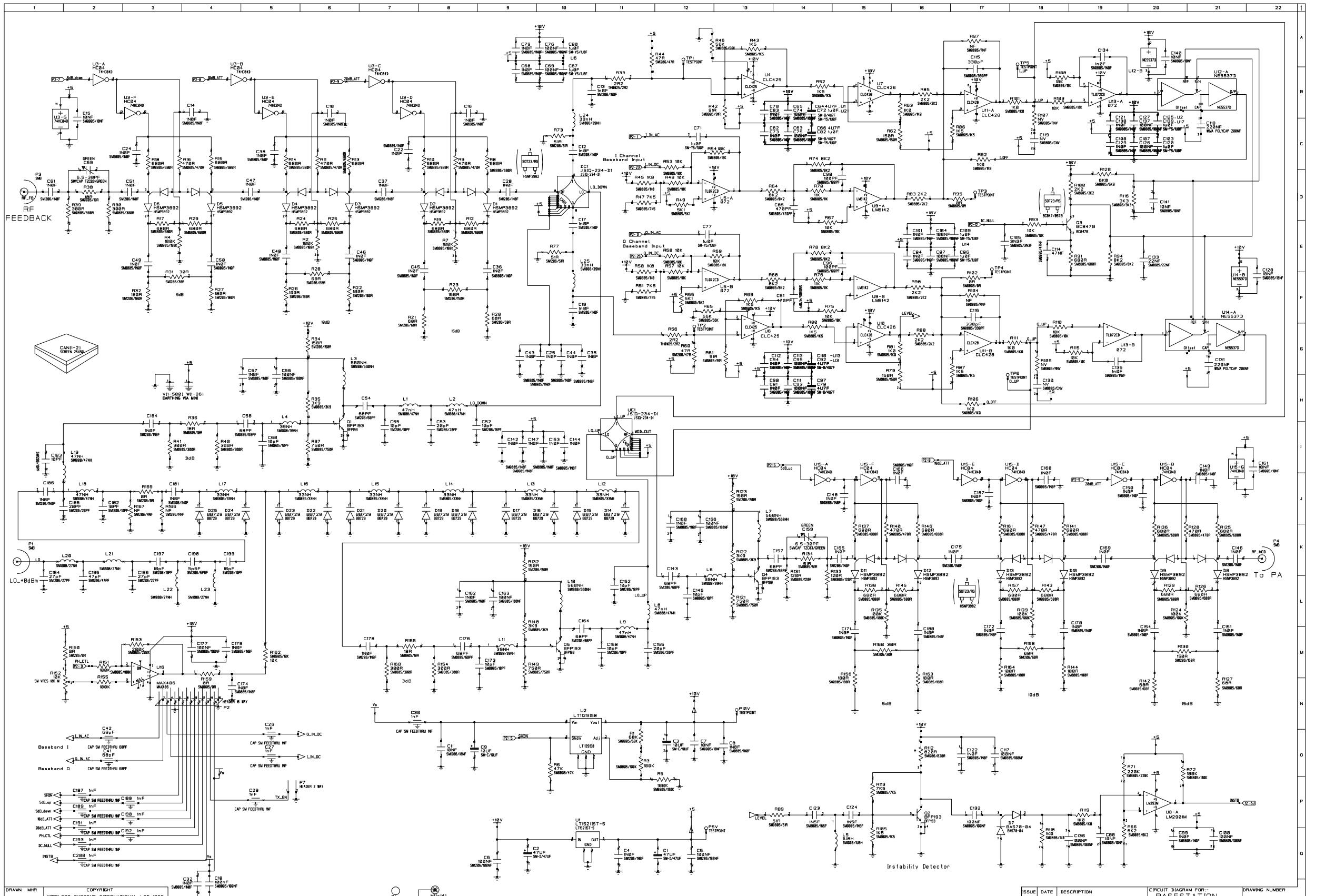
Circuit Diagrams

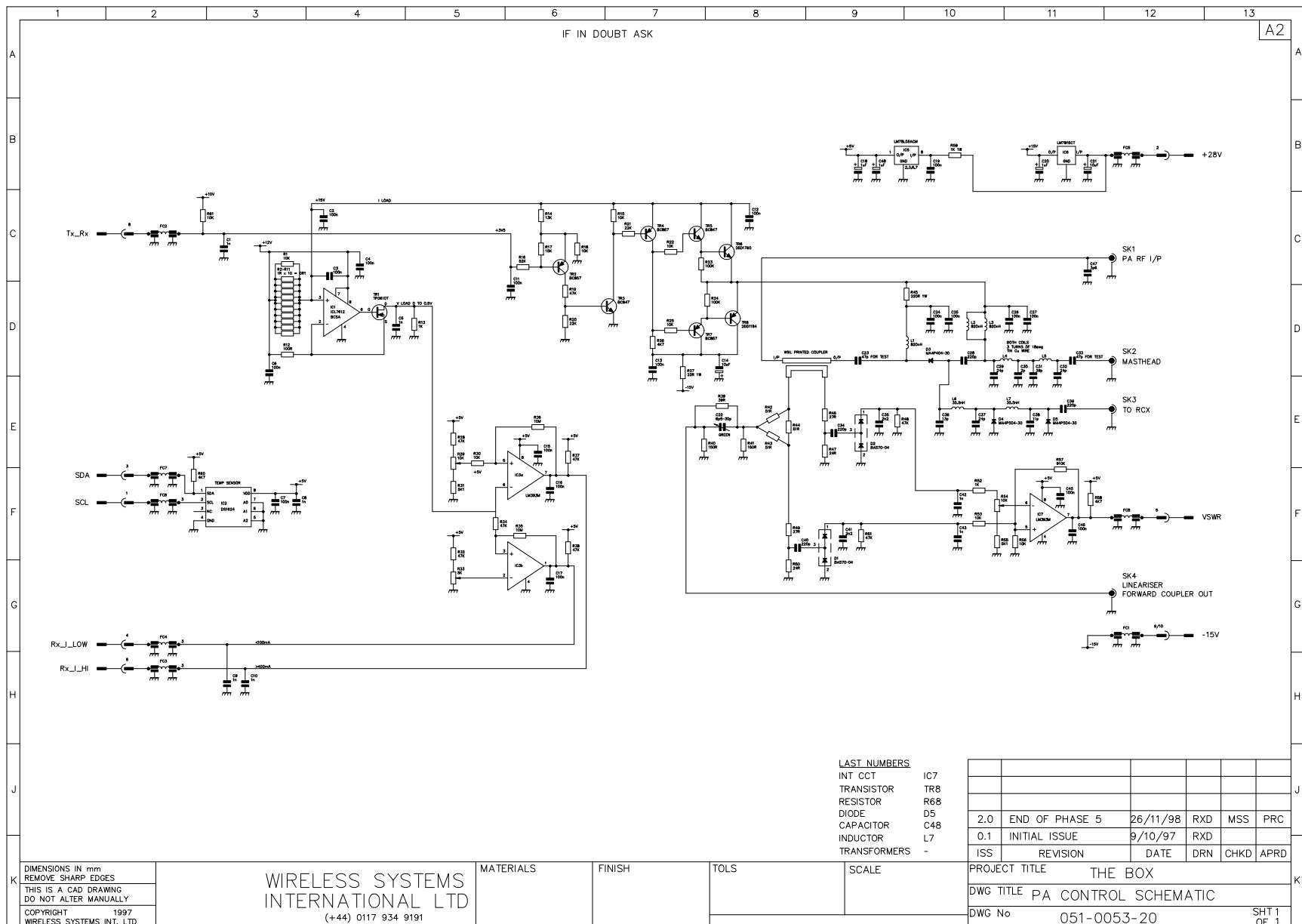
Drawing Number	Number of sheets	Description
101.6509	1	Power Amplifier
101.6510	1	Linearizer
101.6508	1	Power Amplifier Control
1015/511/B	5	Command Unit Baseband Board
106/516/C	2	Rx RF Board
106/513/C	7	Data Rx Baseband Board

RU Transmitter (3 sheets)

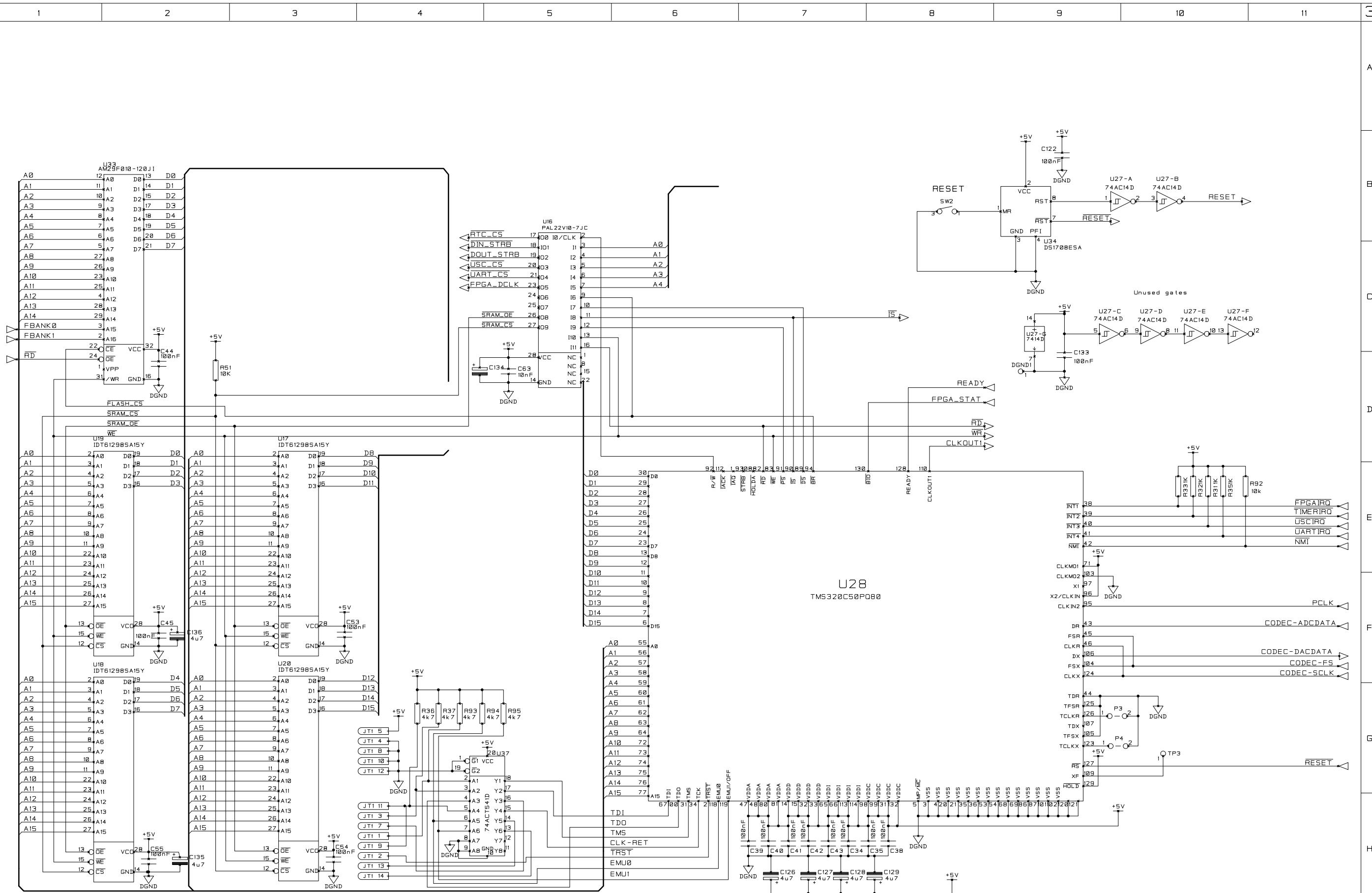
**Power Amplifier
Linearizer
Power Amplifier Control**





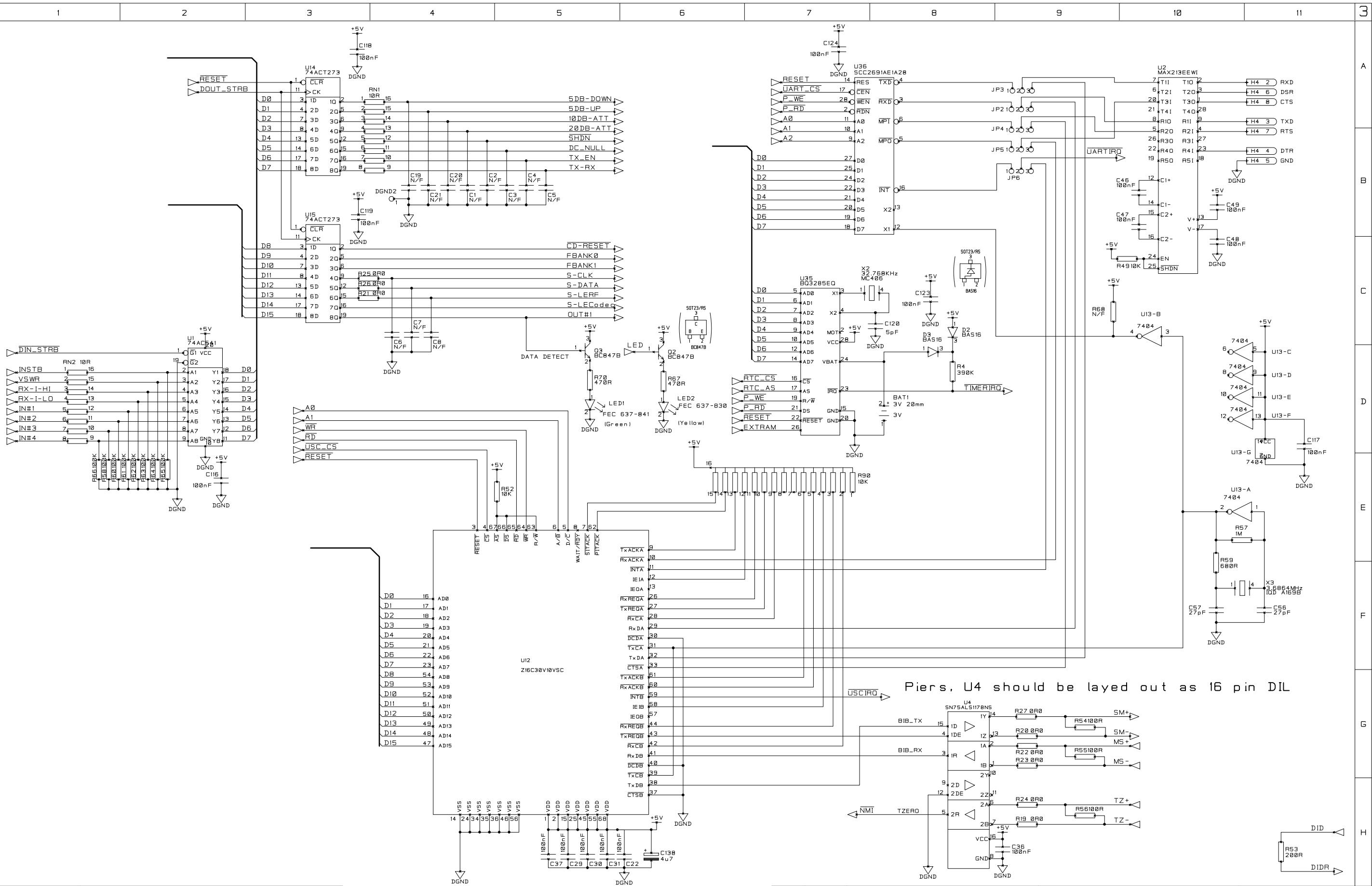


Command Unit Baseband Board (5 sheets)



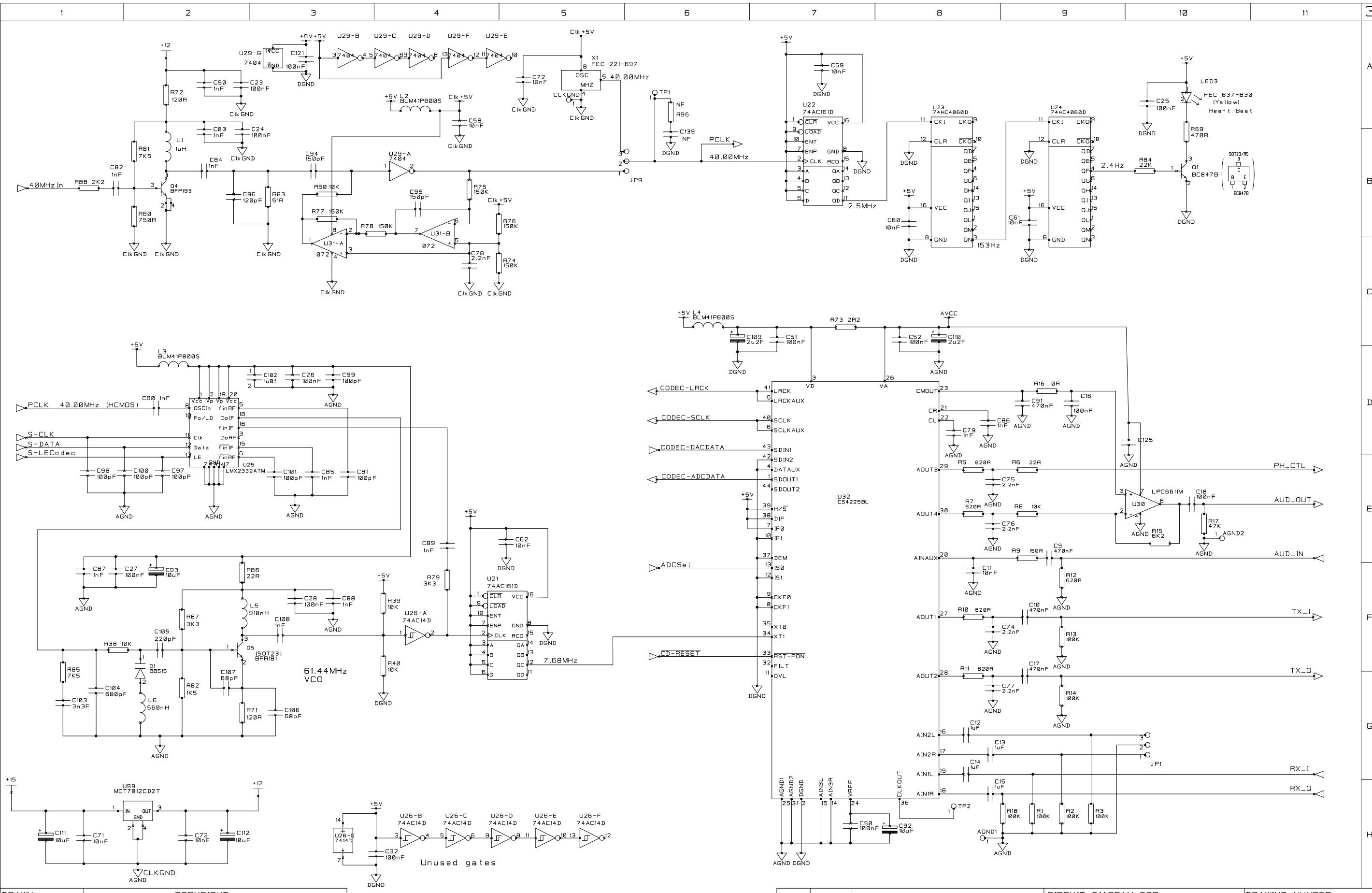
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DATE 15/11/96	NOTES:-	
CHECKED PRC	SHEET DESCRIPTION:	
DATE 23/11/98		

ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:- COMMAND UNIT BASEBAND BOARD PROCESSOR AND MEMORY	DRAWING NUMBER
0.1	12/12/96	First draft		1016/511/B
1.0	23/12/96	Formal Issue		
1.1	23/12/96	Formal Issue Mods for Layout		
B	18/03/97	Update from Rev A		
2.0	23/11/98	End of Phase 5		
WIRELESS SYSTEMS INT. LTD.				



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DATE 15/11/96		
NOTES:-		
SHEET DESCRIPTION:		

ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:- COMMAND UNIT DIGITAL I/O, BIB, RS-232 AND TIMER	DRAWING NUMBER
0.1	12/12/96	First draft		1016/511/B
1.0	23/12/96	Formal Issue		
1.1	23/12/96	Formal Issue Mods for Layout		
B	18/03/97	Update from Rev A		
2.0	23/11/98	End of Phase 5		
SHT. 2 OF 5				
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CHECKED PRC
DATE 23/11/98

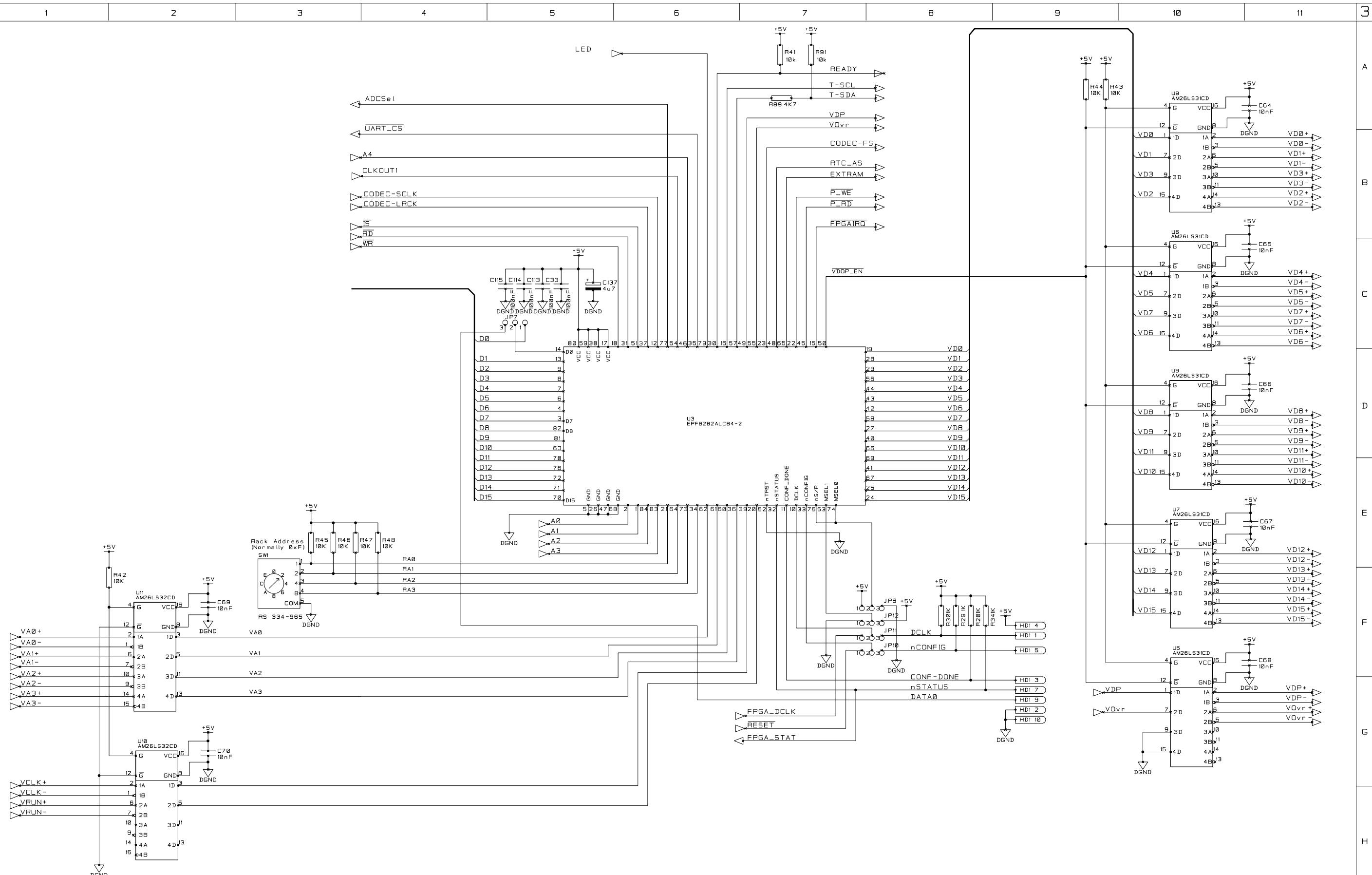
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SHEET DESCRIPTION:

ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:-	DRAWING NUMBER
0.1	12/12/96	First draft	COMMAND UNIT	1016/511/B
1.0	23/12/96	Formal Issue	CODEC AND CLOCK	
1.1	23/12/96	Formal Issue Mods for Layout	GENERATION	
B	18/03/97	Update from Rev A		SHT.3 OF 5
2.0	23/11/98	End of Phase 5		

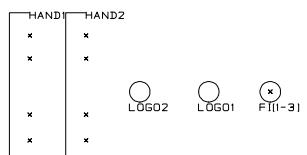
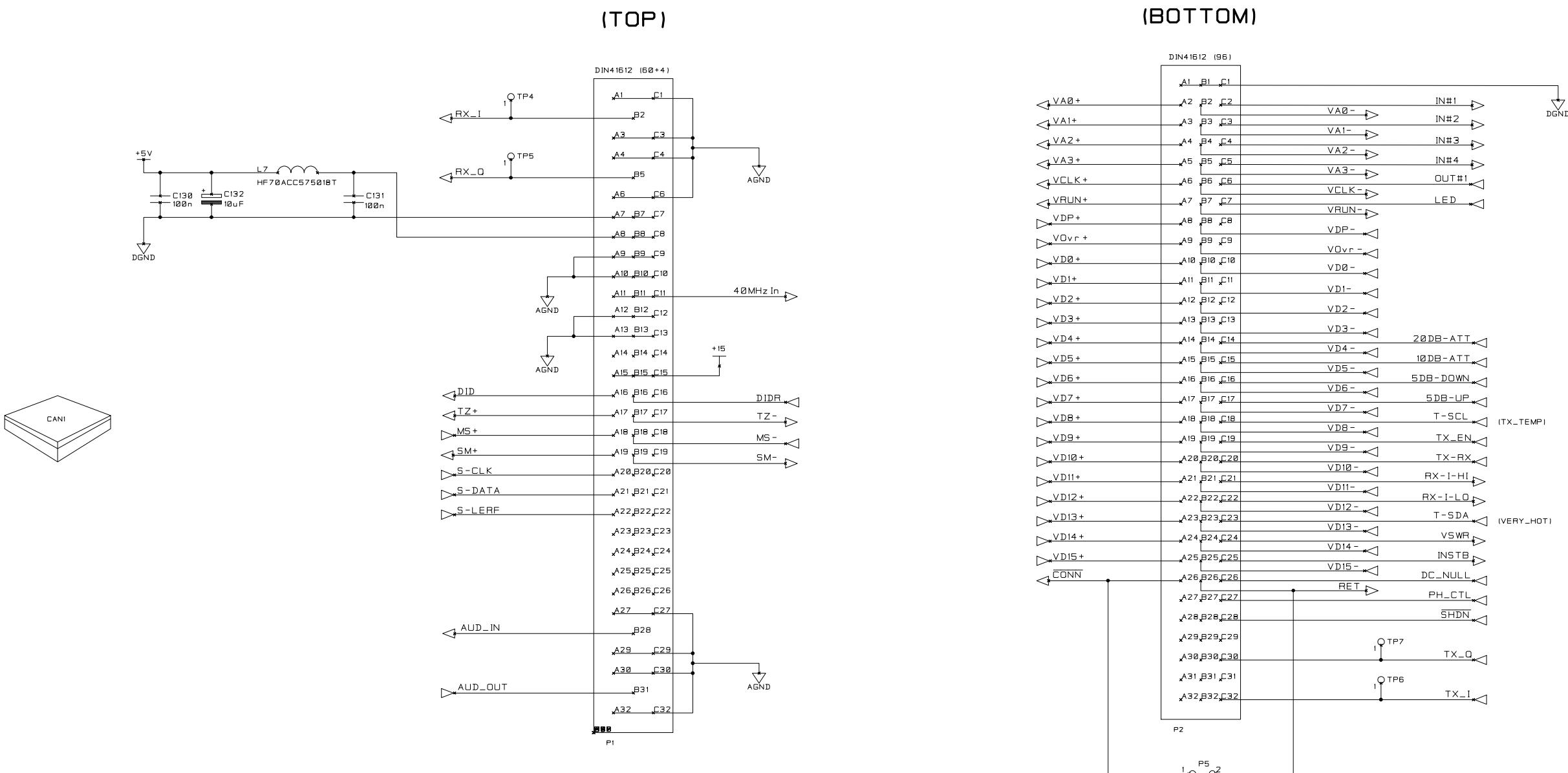
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DATE 15/11/96	NOTES:-
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DATE 23/11/98	

SHEET DESCRIPTION:

ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:- COMMAND UNIT BASEBAND BOARD FPGA & SEISMIC DATA BUS	DRAWING NUMBER 1016/511/B SHT. 4 OF 5
0.1	12/12/96	First draft		
1.0	23/12/96	Formal Issue		
1.1	23/12/96	Formal Issue Mods for Layout		
B	18/03/97	Update from Rev A		
2.0	23/11/98	End of Phase 5		
				WIRELESS SYSTEMS INT. LTD.

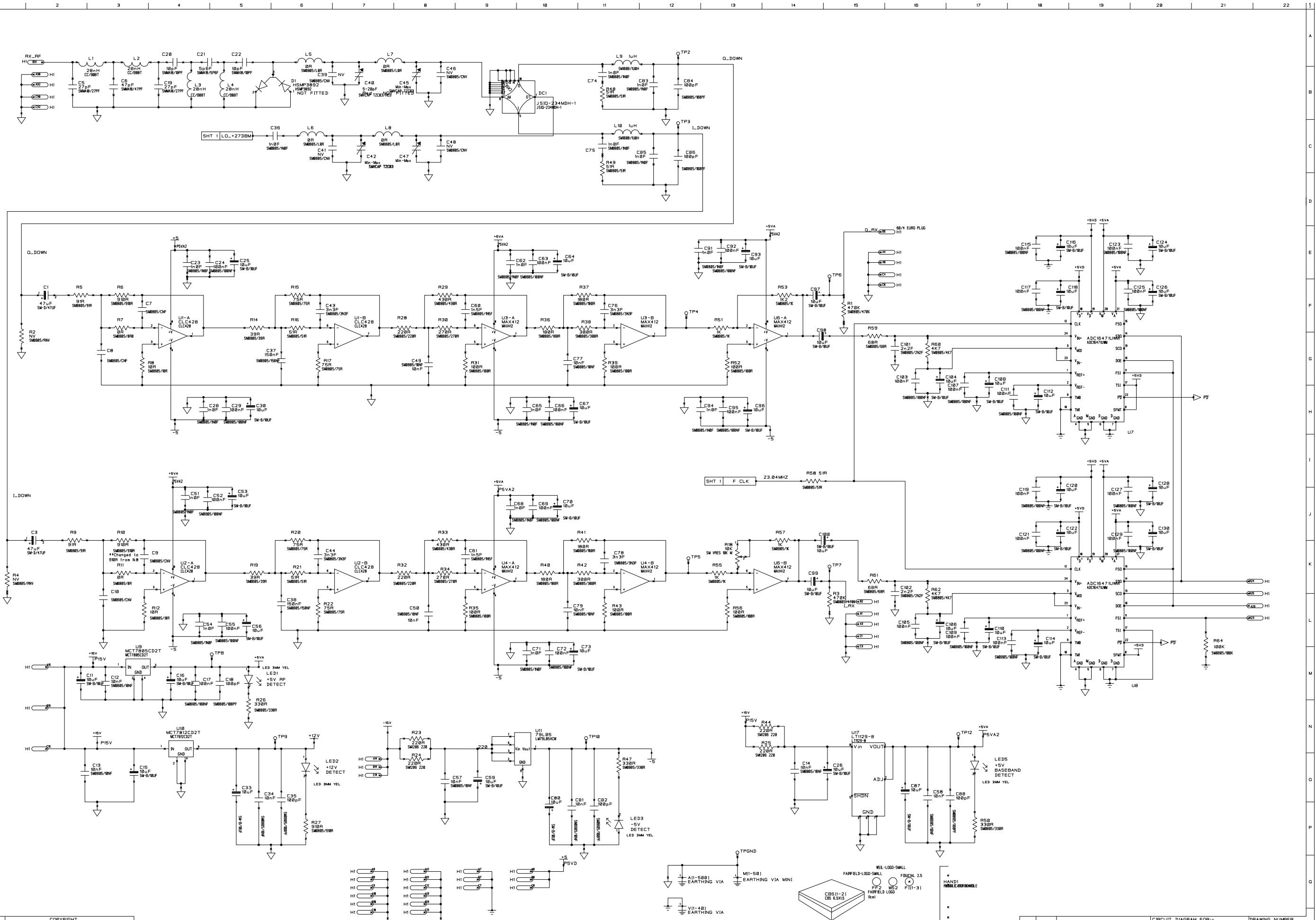


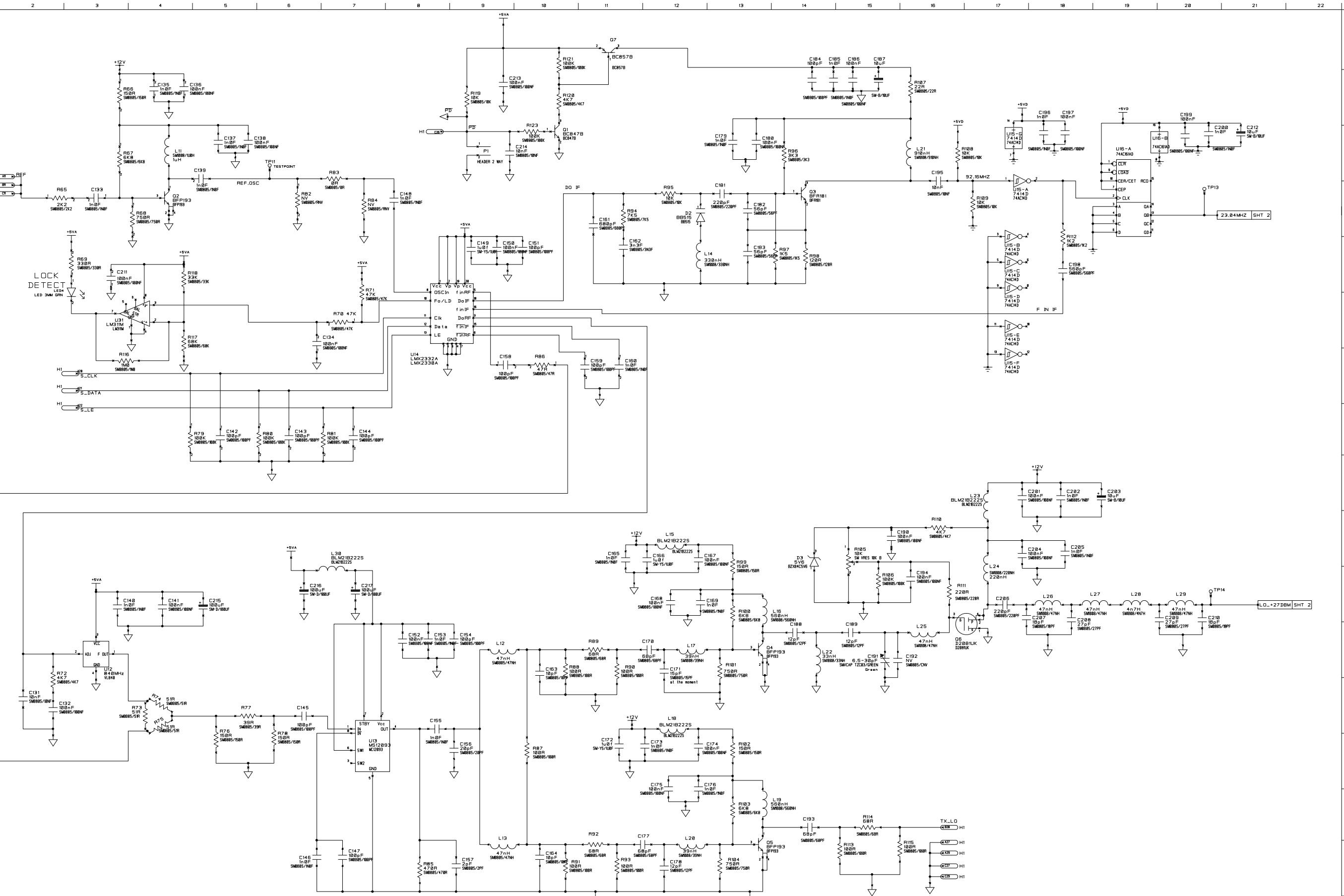
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DATE 15/11/98	NOTES:-
CHECKED PRC	
DATE 23/11/98	

SHEET DESCRIPTION:

ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:- COMMAND UNIT BASEBAND BOARD CONNECTORS	DRAWING NUMBER 1016/511/B SHT. 5 OF 5
0.1	12/12/96	First draft		
1.0	23/12/96	Formal Issue		
1.1	23/12/96	Formal Issue Mods for Layout		
B	18/03/97	Update from Rev A		
2.0	23/11/98	End of Phase 5		
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Data Receiver RF Board (2 sheets)





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DATE 24/11/98	

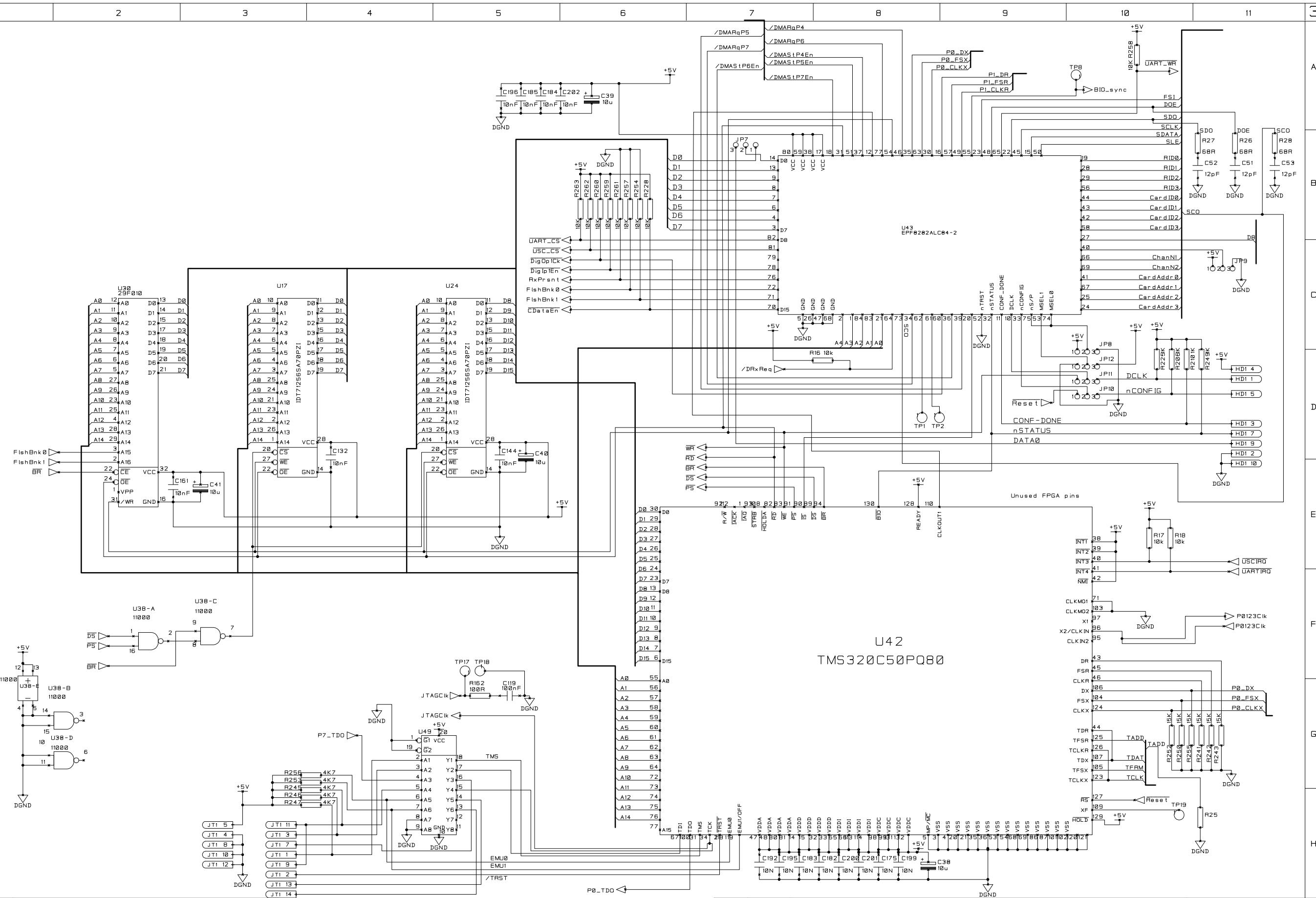
SHEET DESCRIPT

DATA

DATA RECEIVER RF CARD

ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:-	DRAWING NUMBER
C	26/01/98	Production build	DATA RECIEVER RF CARD	1016/516/C
2B	24/1/98	End of Phase 5		SHT. 2 OF 2

Data Rx Processor Board



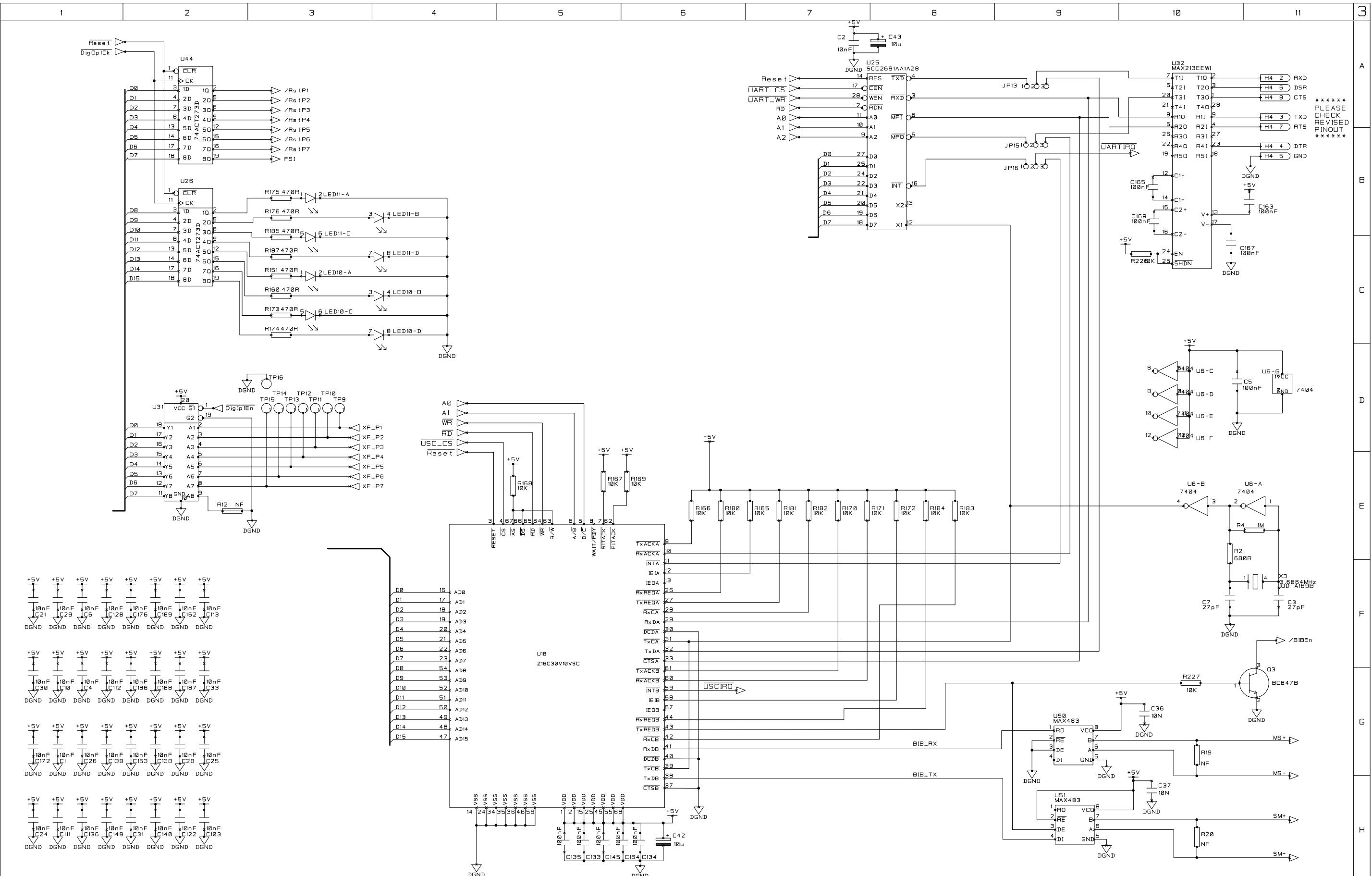
DRAWN PGT
DATE 20/12/
CHECKED RM
DATE 18/11/

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SHEET DESCRIBE

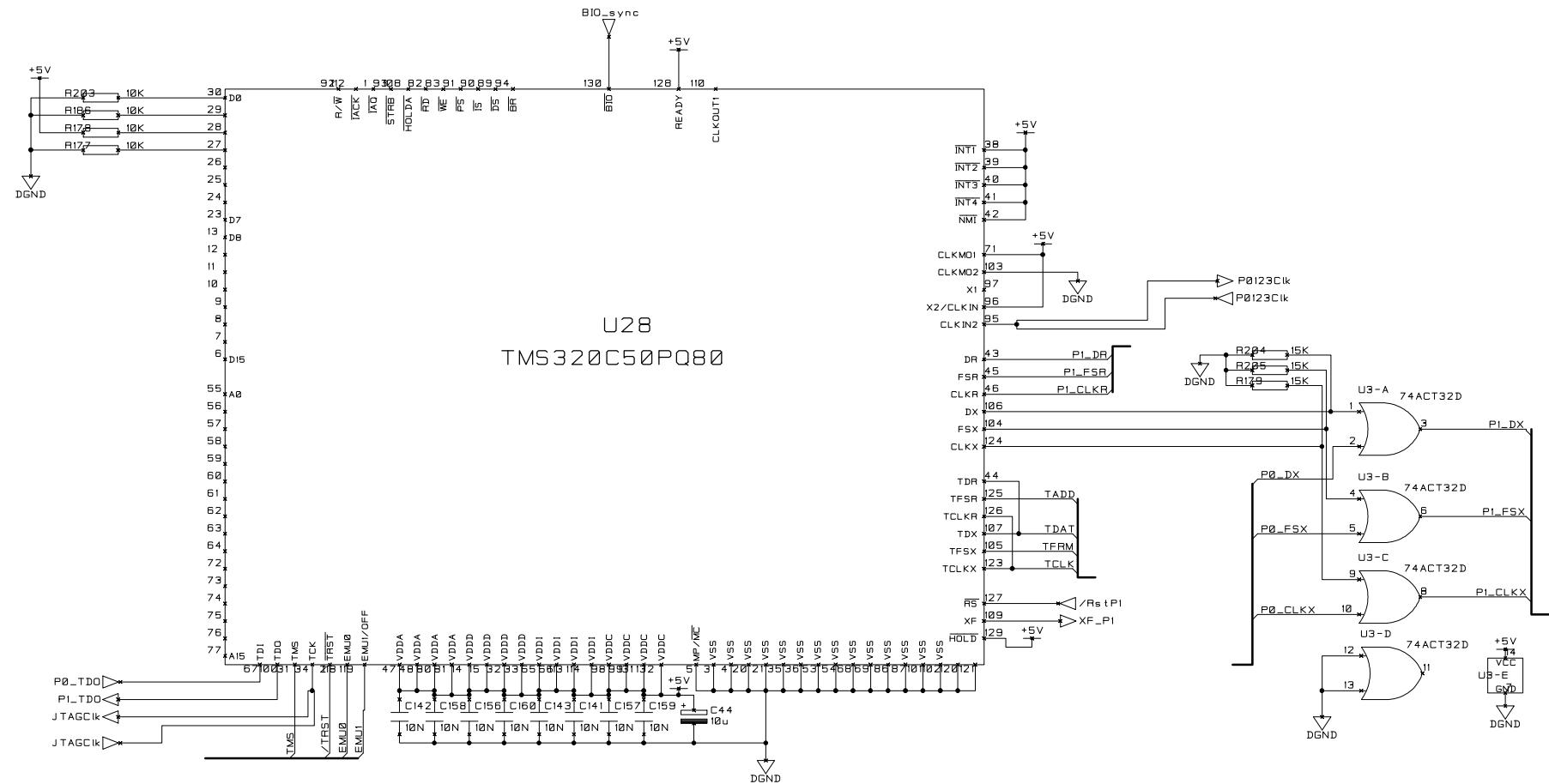
ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:-	DRAWING NUMBER
0.1	28/12/96	First draft	Data Rx Processor Card,	1016/513/C
2.0	5/6/97	A to B model changes	Control Processor P0	SHT.1 OF 7
C	27/01/98	Production Build		
2.1	18/11/98	END OF PHASE 5		

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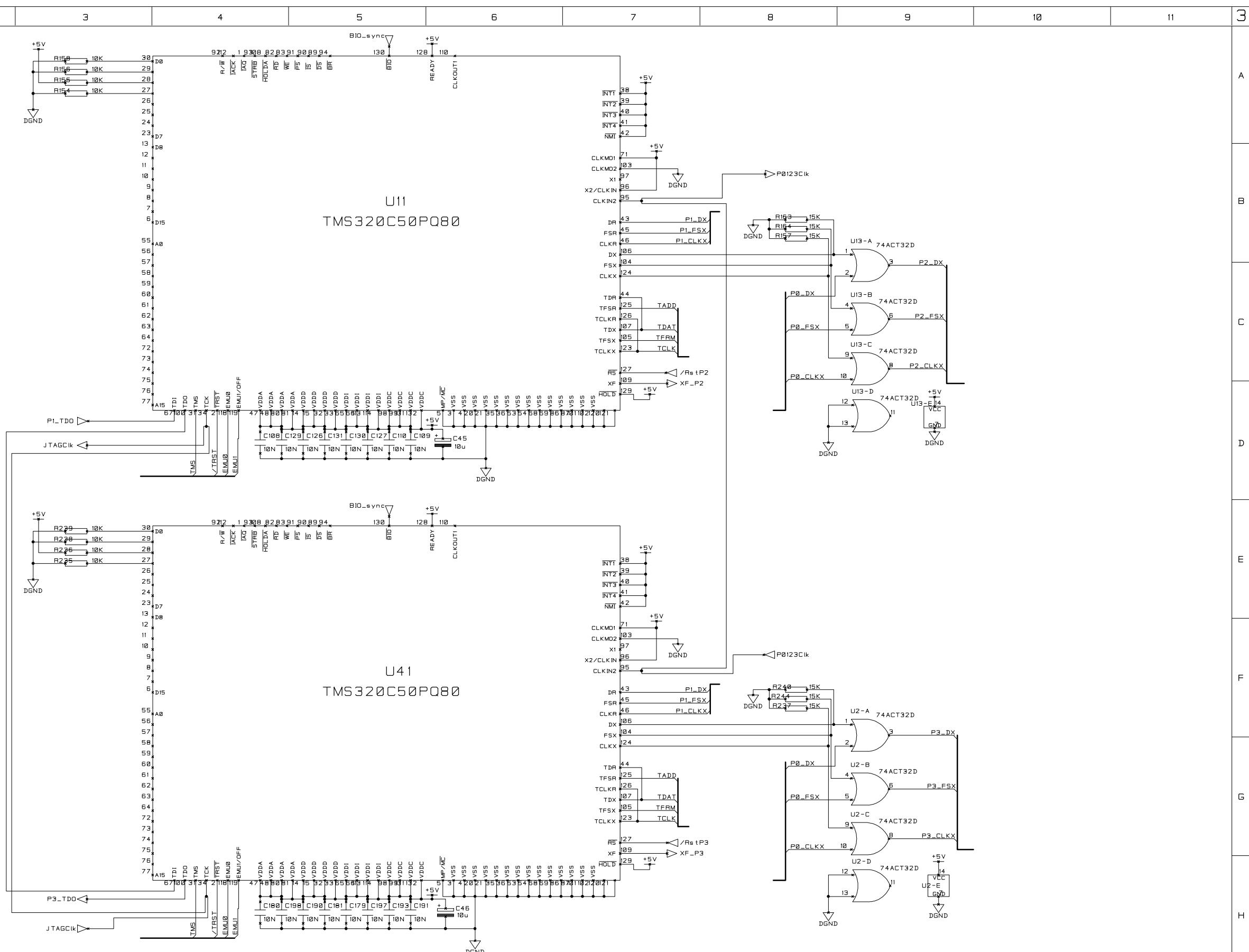
ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:- Data Rx Processor Card, Control Processor Peripherals	DRAWING NUMBER 1016/513/C
0.1	20/12/96	First draft		
2.0	5/6/97	A to B model changes		
C	27/01/98	Production build		SHT.2 OF 7
2.1	16/11/98	END OF PHASE 5		
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A
B
C
D
E
F
G
H

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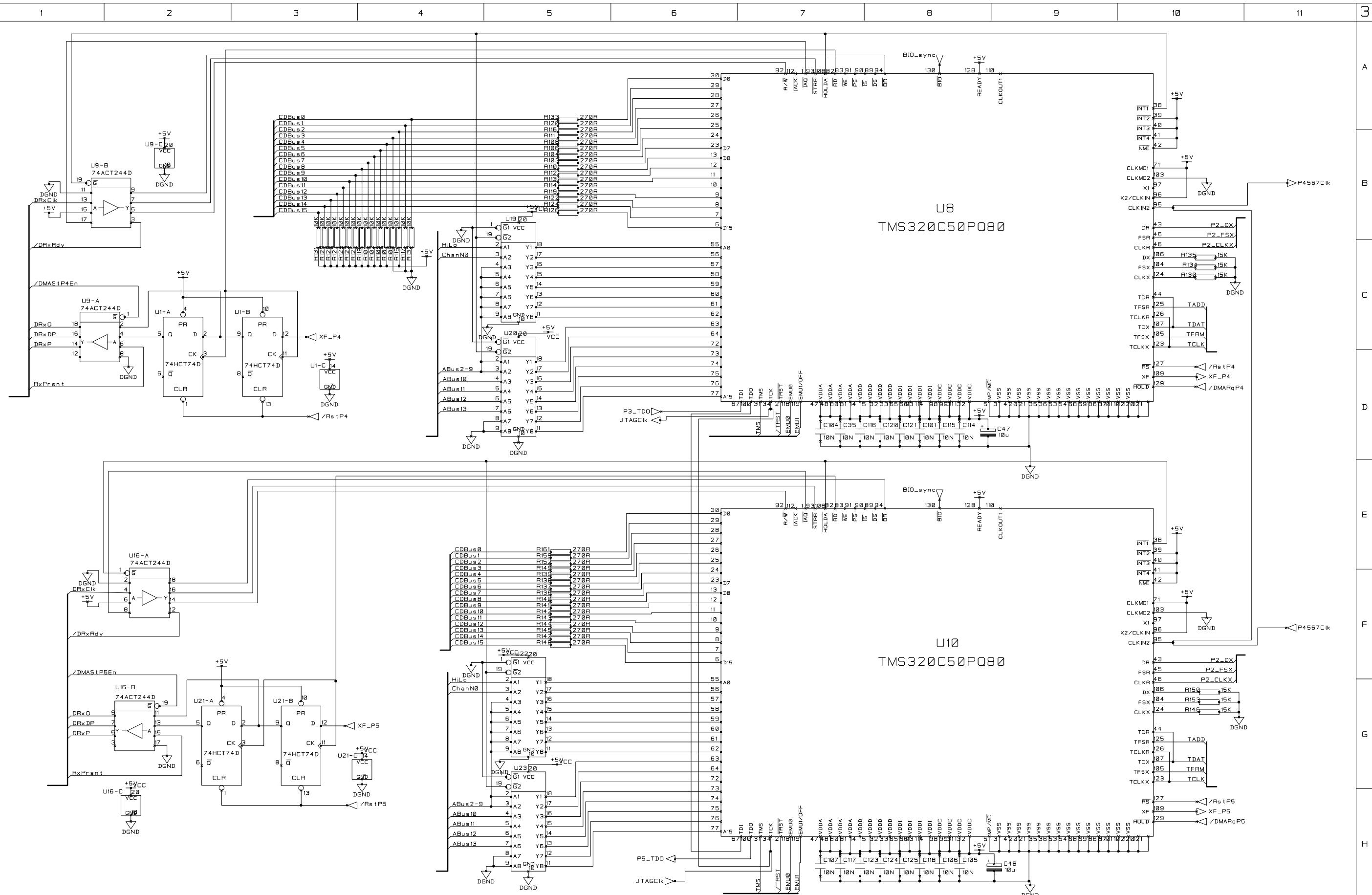
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SHEET DESCRIPTION

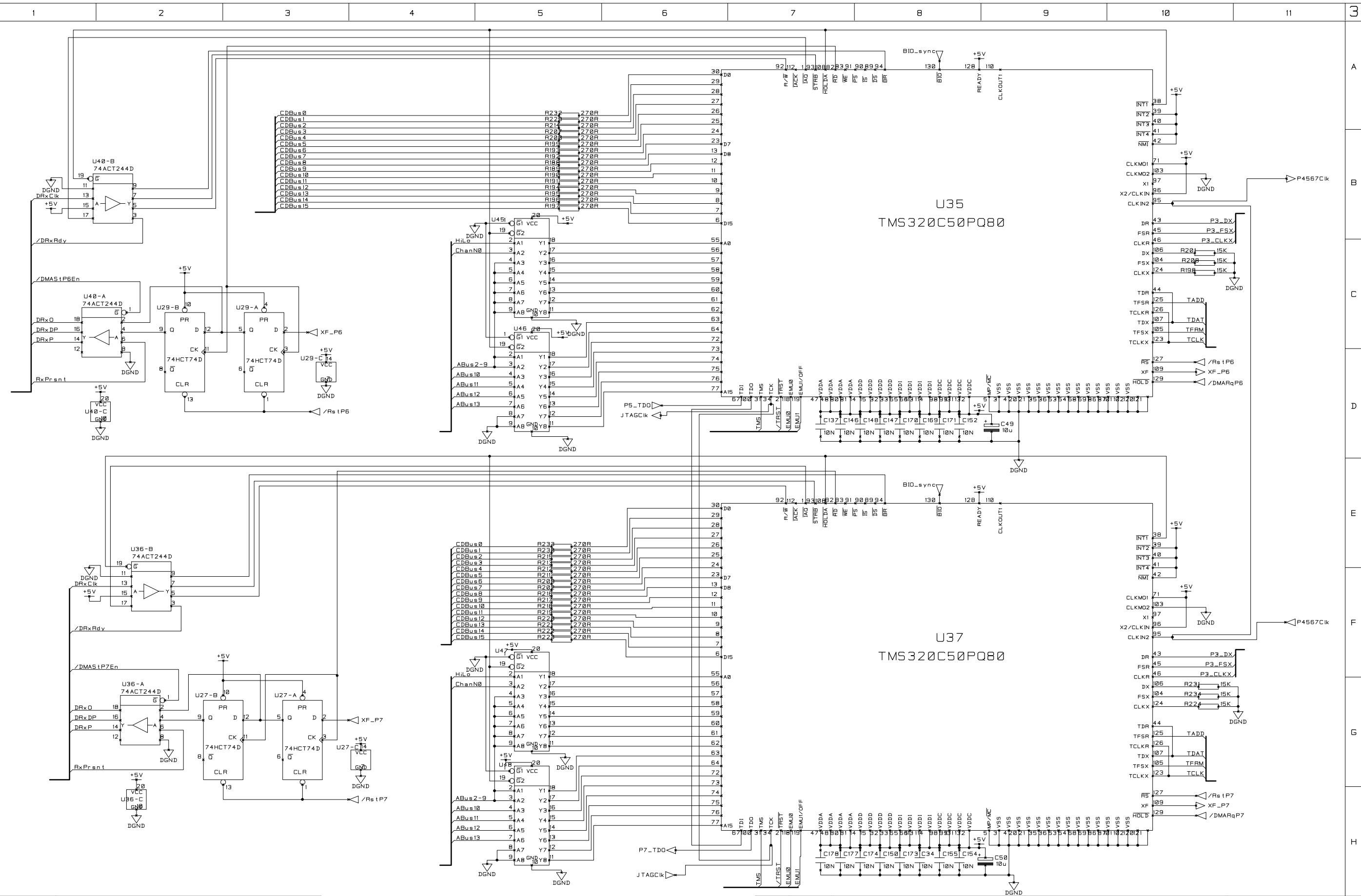
ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:- Data Rx Processor Card, Column B Processors P2 and P3	DRAWING NUMBER 1016/513/C SHT.4 OF 7
0.1	20/12/96	First draft		
2.0	5/6/97	A to B model changes		
C	27/01/98	Production Build		
2.1	18/11/98	END OF PHASE 5		



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DATE 20/12/96	NOTES:-	
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DATE 18/11/98		

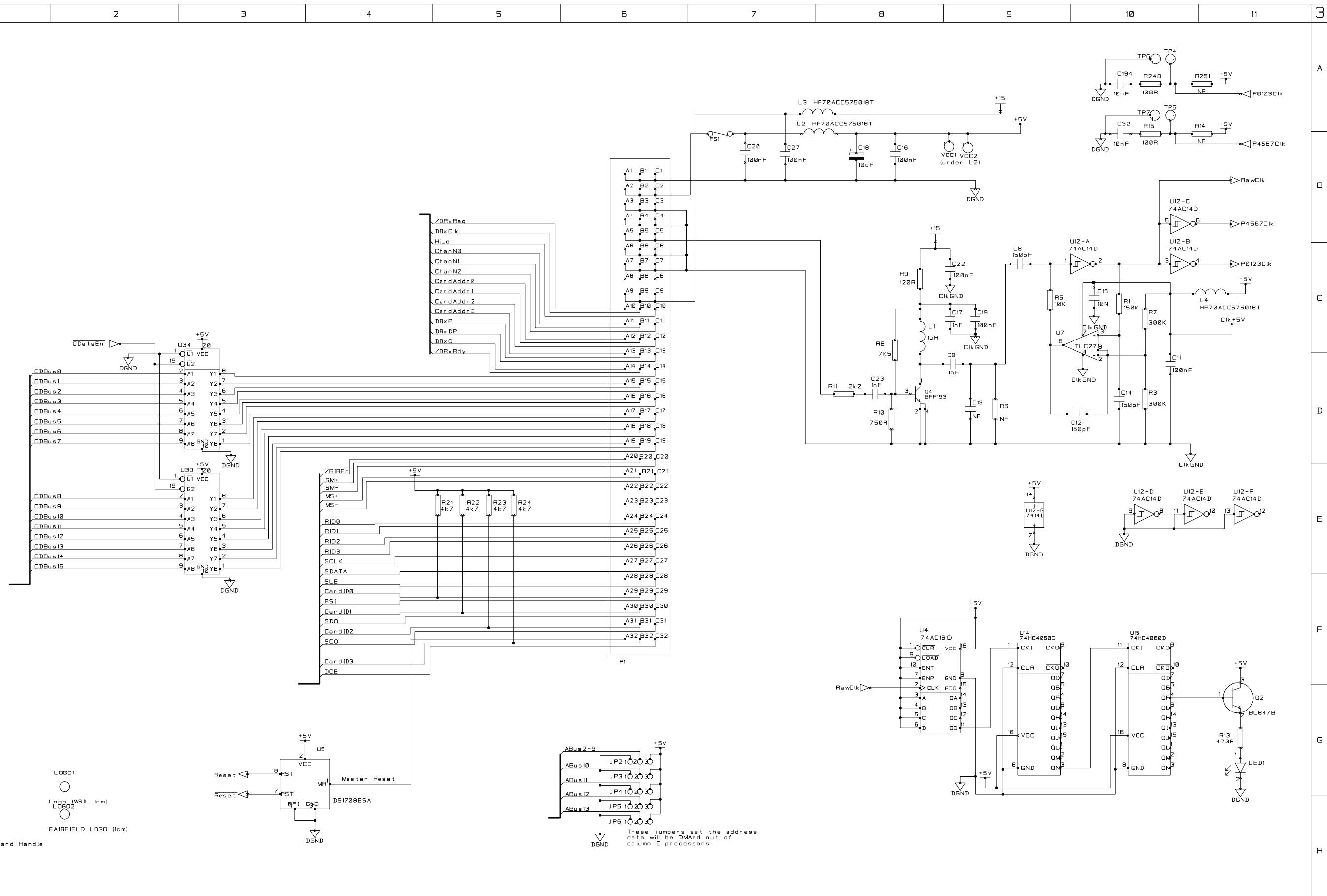
ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:- Data Rx Processor Card, Column C Processors P4 and P5	DRAWING NUMBER
0.1		First draft		1016/513/C
2.0	5/6/97	A to B model changes		
C	27/01/98	Production Build		SHT.5 OF 7
2.1	18/11/98	END OF PHASE 5		

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DATE 18/11/98		

ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:- Data Rx Processor Card, Column C Processors P6 and P7	DRAWING NUMBER
0.1		First draft		1016/513/C
2.0	5/6/97	A to B model changes		
C	27/01/98	Production Build		
2.1	18/11/98	END OF PHASE 5		
				SHT.6 OF 7
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DATE 20/12/96	NOTES:-
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DATE 18/11/98	

ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:- Data Rx Processor Card, Main Connector	DRAWING NUMBER 1016/513/C SHT.7 OF 7
0.1	20/12/96	First draft		
2.0	5/6/97	A to B model changes		
C	27/01/98	Production Build		
2.1	18/11/98	END OF PHASE 5		