

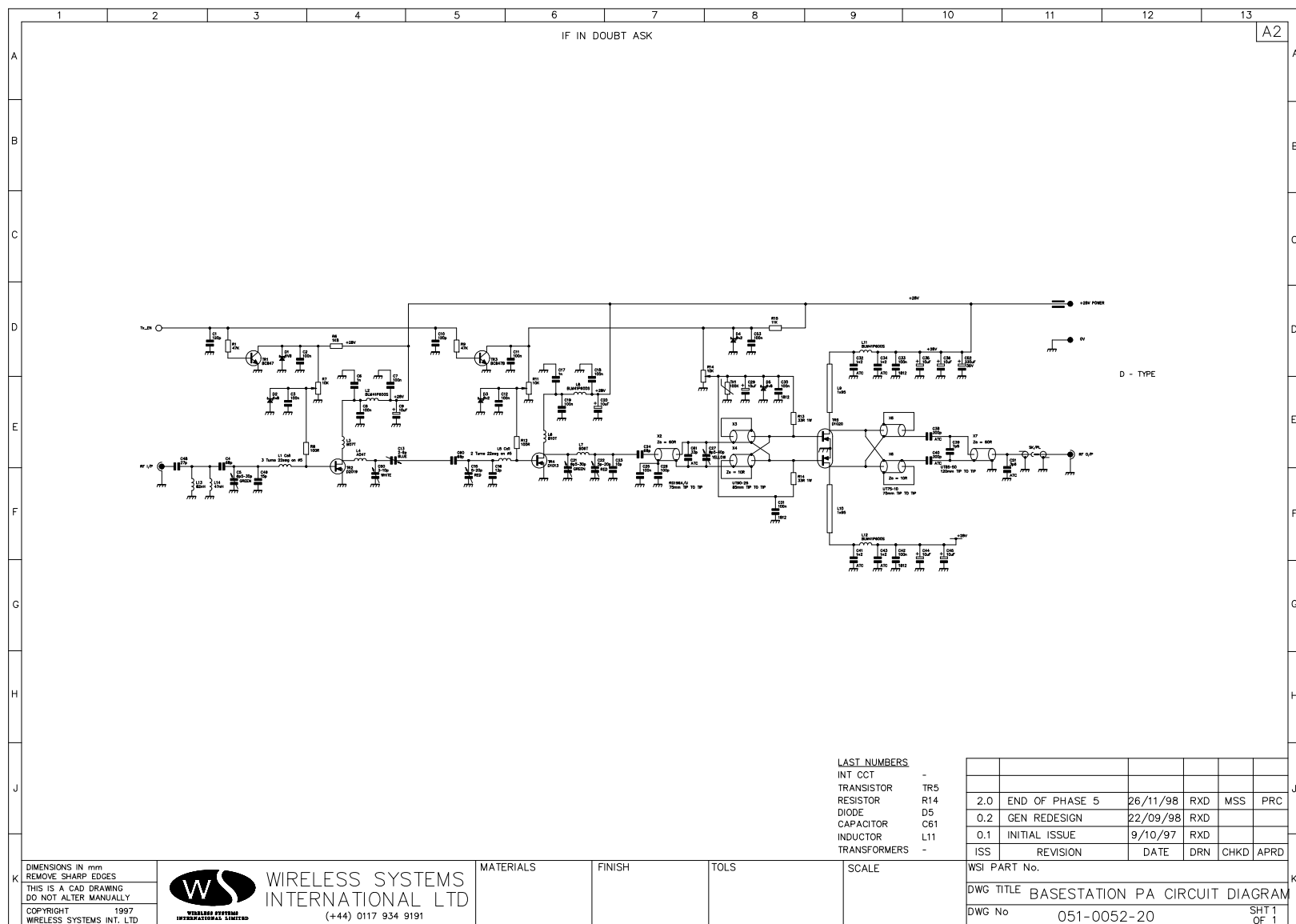
APPENDIX 3

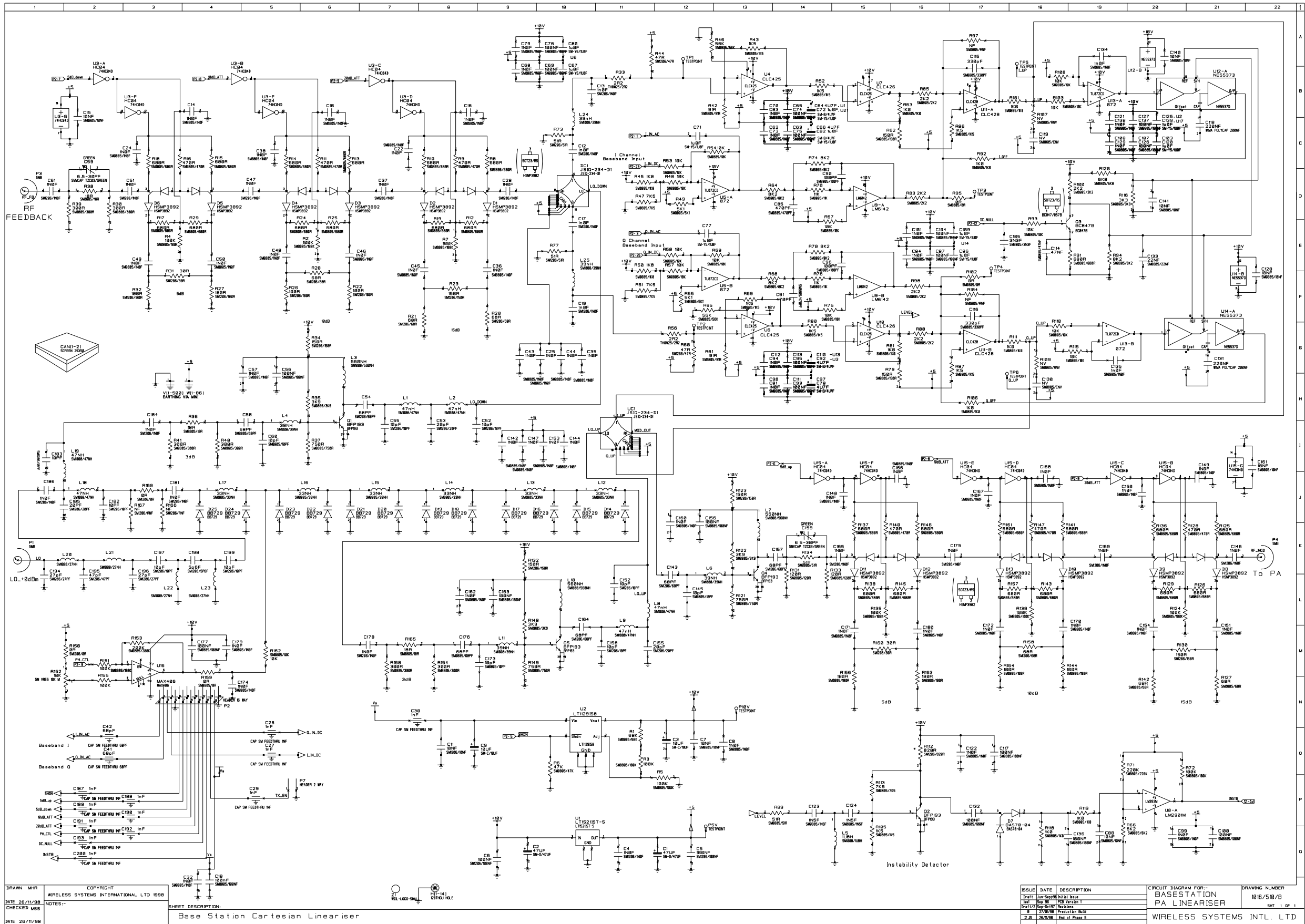
Circuit Diagrams

Drawing Number	Number of sheets	Description
101.6509	1	Power Amplifier
101.6510	1	Linearizer
101.6508	1	Power Amplifier Control
1015/511/B	5	Command Unit Baseband Board
106/516/C	2	Rx RF Board
106/513/C	7	Data Rx Baseband Board

RU Transmitter (3 sheets)

**Power Amplifier
Linearizer
Power Amplifier Control**



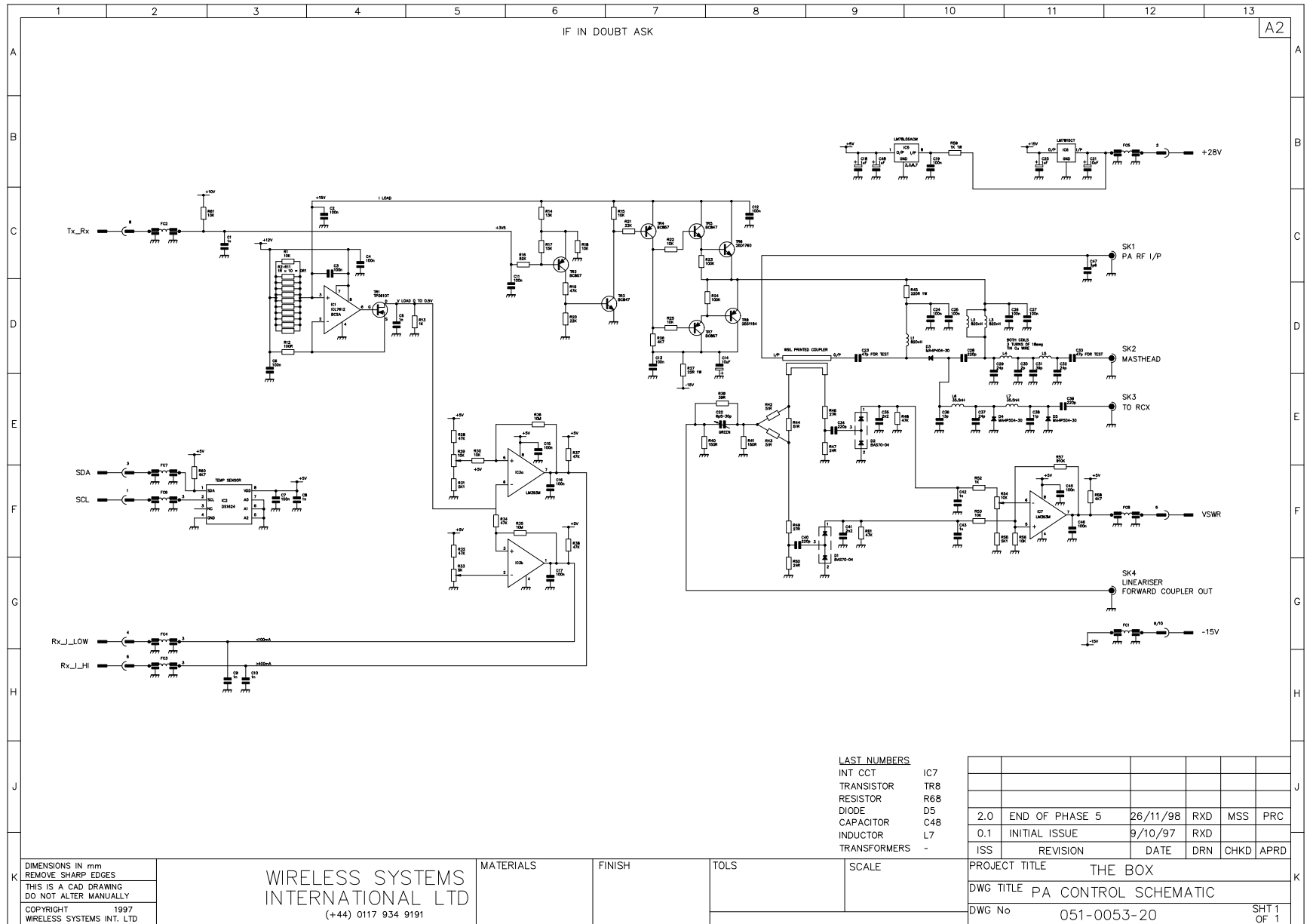


DRAWN: MHR
 DATE: 26/11/98
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 DATE: 26/11/98

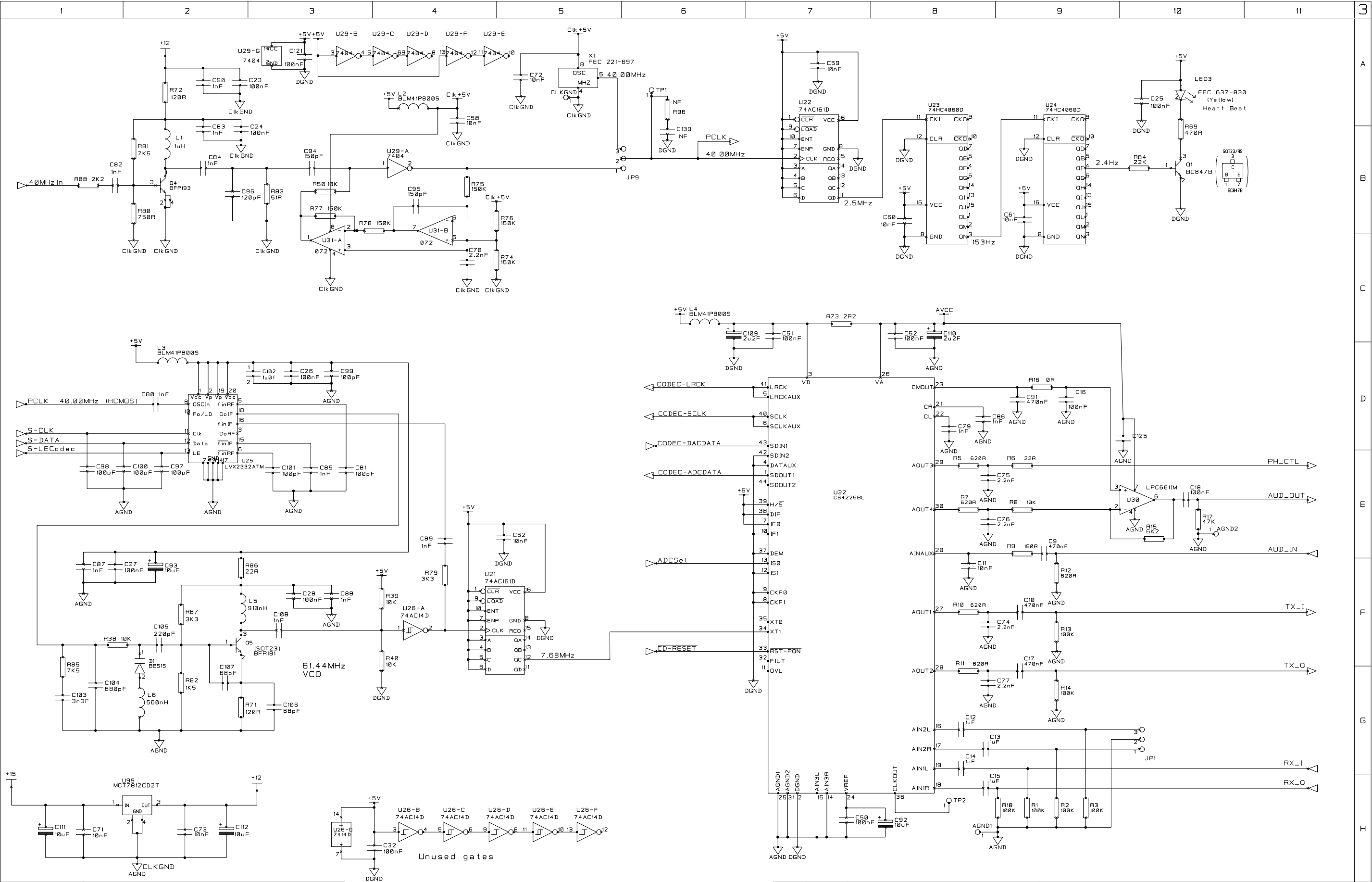
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 SHEET DESCRIPTION:
 Base Station Cartesian Lineariser

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ISSUE	DATE	DESCRIPTION	CIRCUIT DIAGRAM FOR:-	DRAWING NUMBER
1	26/11/98	Initial Design	BASESTATION	1016/SB/B
2	26/11/98	Revised Design	PA LINEARISER	1016/SB/B
3	26/11/98	Final Design	WIRELESS SYSTEMS INT'L LTD	1016/SB/B



Command Unit Baseband Board (5 sheets)



DRAWN WTS

DATE 15/11/96

CHECKED PRC

DATE 23/11/98

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SHEET DESCRIPTION:

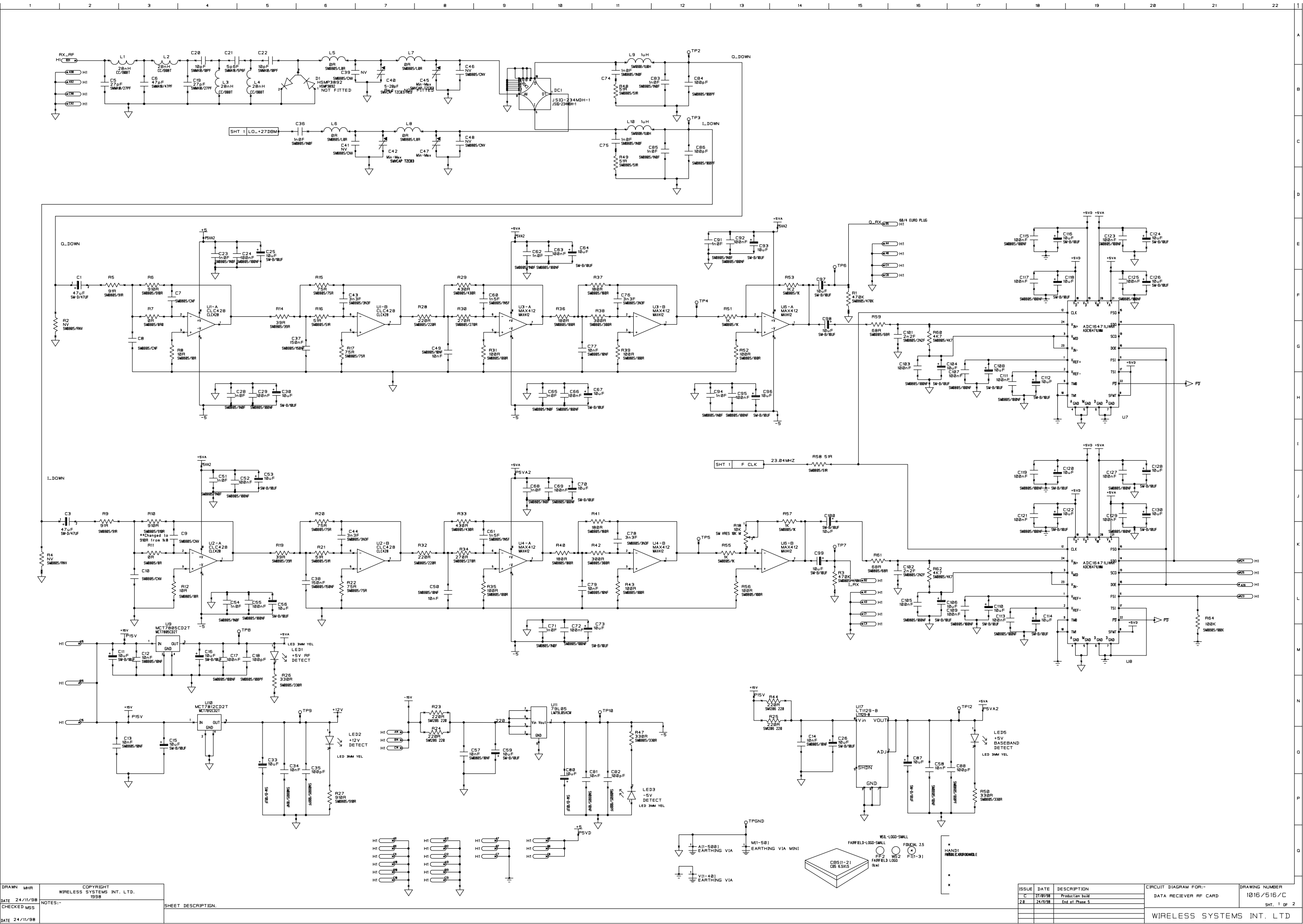
ISSUE	DATE	DESCRIPTION
0.1	12/12/96	First draft
1.0	23/12/96	Formal issue
1.1	23/12/96	Formal issue Made for Layout
B	18/03/97	Update from Rev A
2.0	23/11/98	End of Phase 5

CIRCUIT DIAGRAM FOR:-
COMMAND UNIT
CODEC AND CLOCK
GENERATION

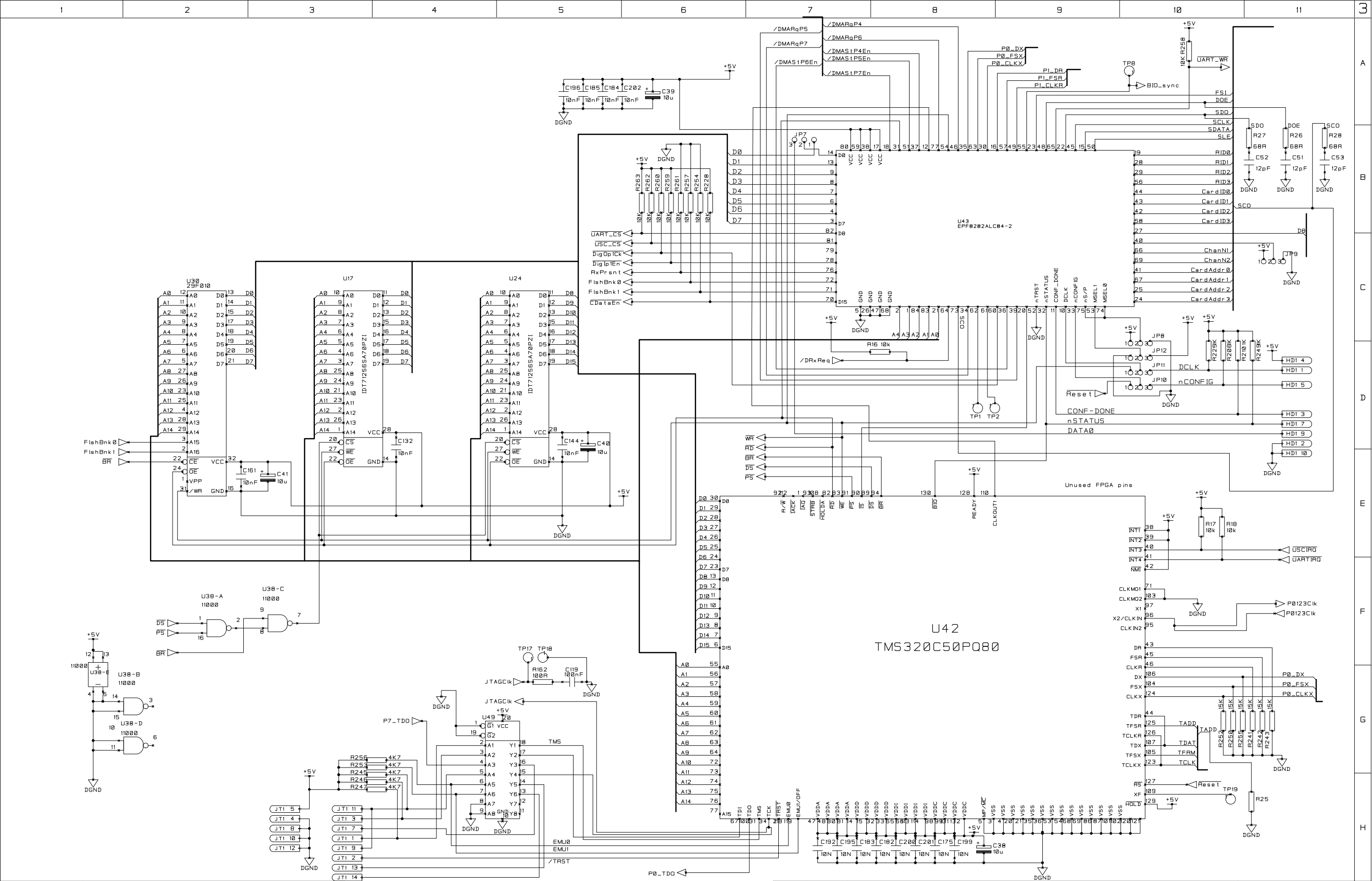
DRAWING NUMBER
1016/511/B
SHT.3 OF 5

WIRELESS SYSTEMS INT. LTD.

Data Receiver RF Board (2 sheets)



Data Rx Processor Board



DRAWN PGT
DATE 20/12/96
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DATE 18/11/98

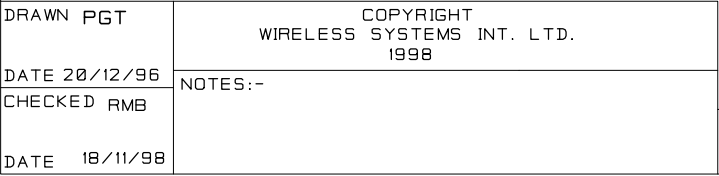
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1998

NOTES:-

SHEET DESCRIPTION:

ISSUE	DATE	DESCRIPTION
0.1	20/12/96	First draft
2.0	5/6/97	A to B model changes
C	27/01/98	Production Build
2.1	18/11/98	END OF PHASE 5

CIRCUIT DIAGRAM FOR:- Data Rx Processor Card, Control Processor P0	DRAWING NUMBER 1016/513/C SHT.1 OF 7
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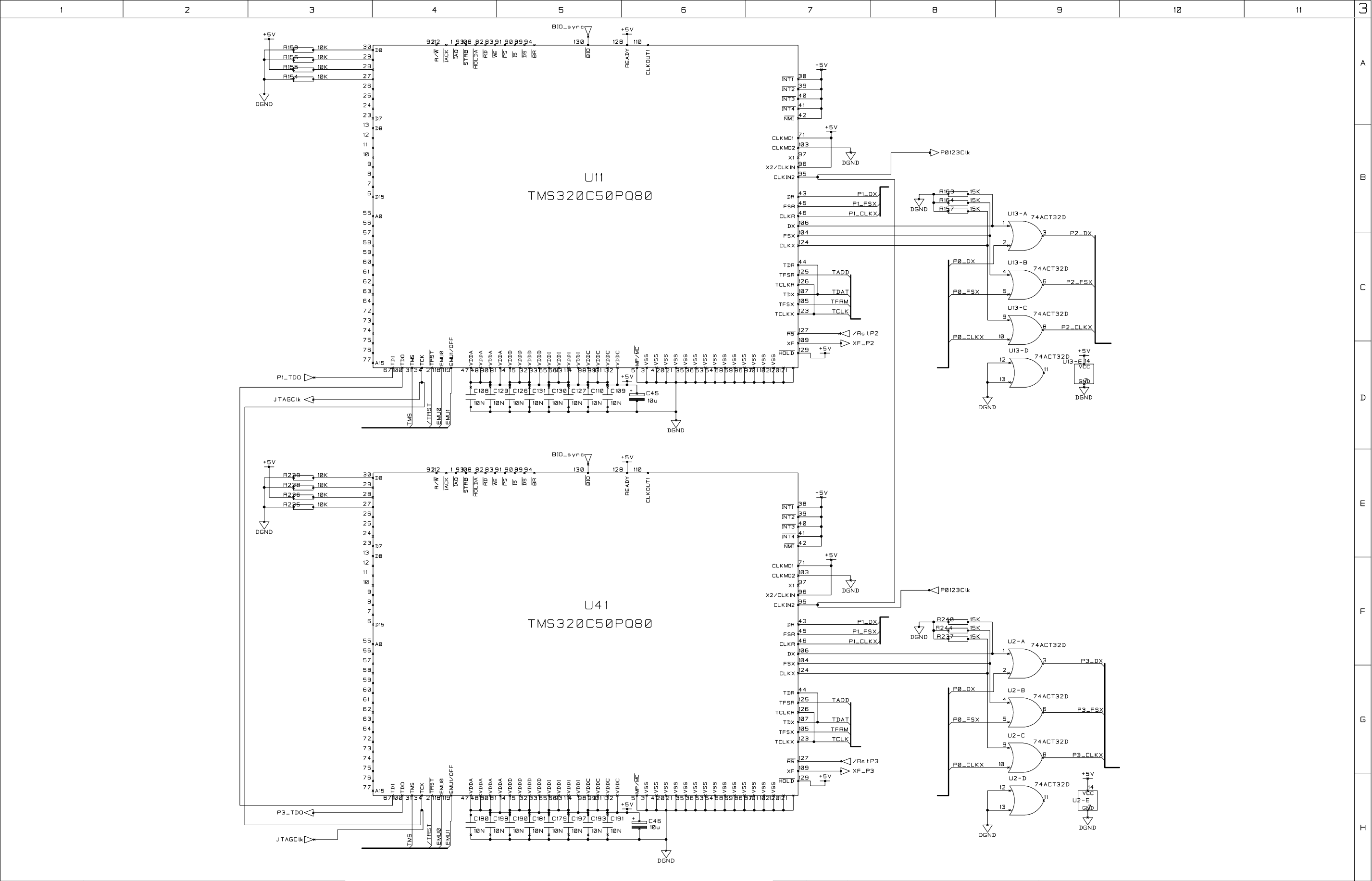


ISSUE	DATE	DESCRIPTION
0.1	20/12/96	First draft
2.0	5/6/97	A to B model changes
C	27/01/98	Production build
2.1	18/11/98	END OF PHASE 5

CIRCUIT DIAGRAM FOR:-
Data Rx Processor Card,
Control Processor
Peripherals

DRAWING NUMBER
1016/513/C
SHT.2 OF 7

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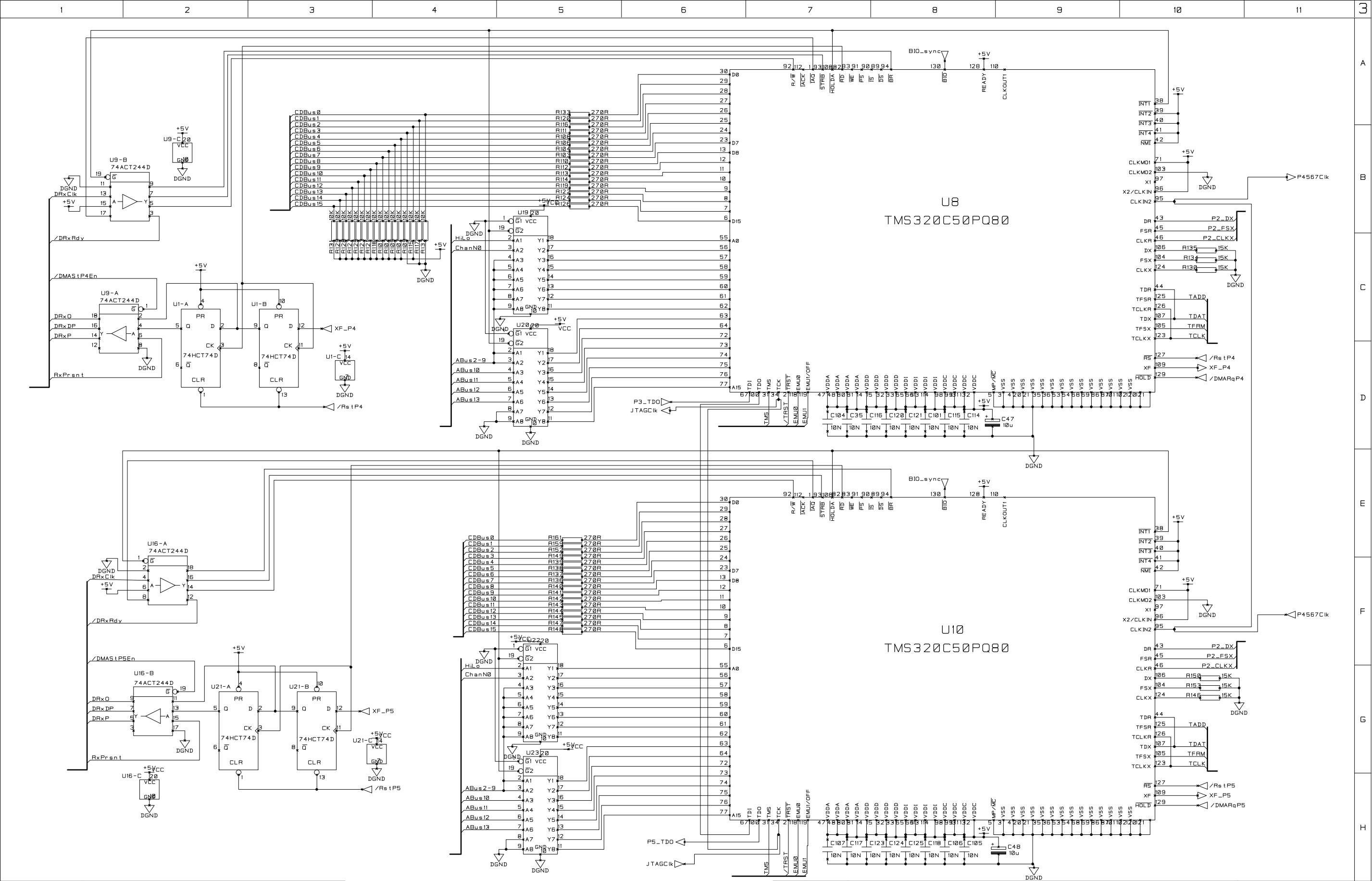


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DATE 20/12/96	NOTES:-
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DATE 18/11/98	

SHEET DESCRIPTION:

ISSUE	DATE	DESCRIPTION
0.1	20/12/96	First draft
2.0	5/6/97	A to B model changes
C	27/01/98	Production Build
2.1	18/11/98	END OF PHASE 5

CIRCUIT DIAGRAM FOR:- Data Rx Processor Card, Column B Processors P2 and P3	DRAWING NUMBER 1016/513/C SHT.4 OF 7
WIRELESS SYSTEMS INT. LTD.	

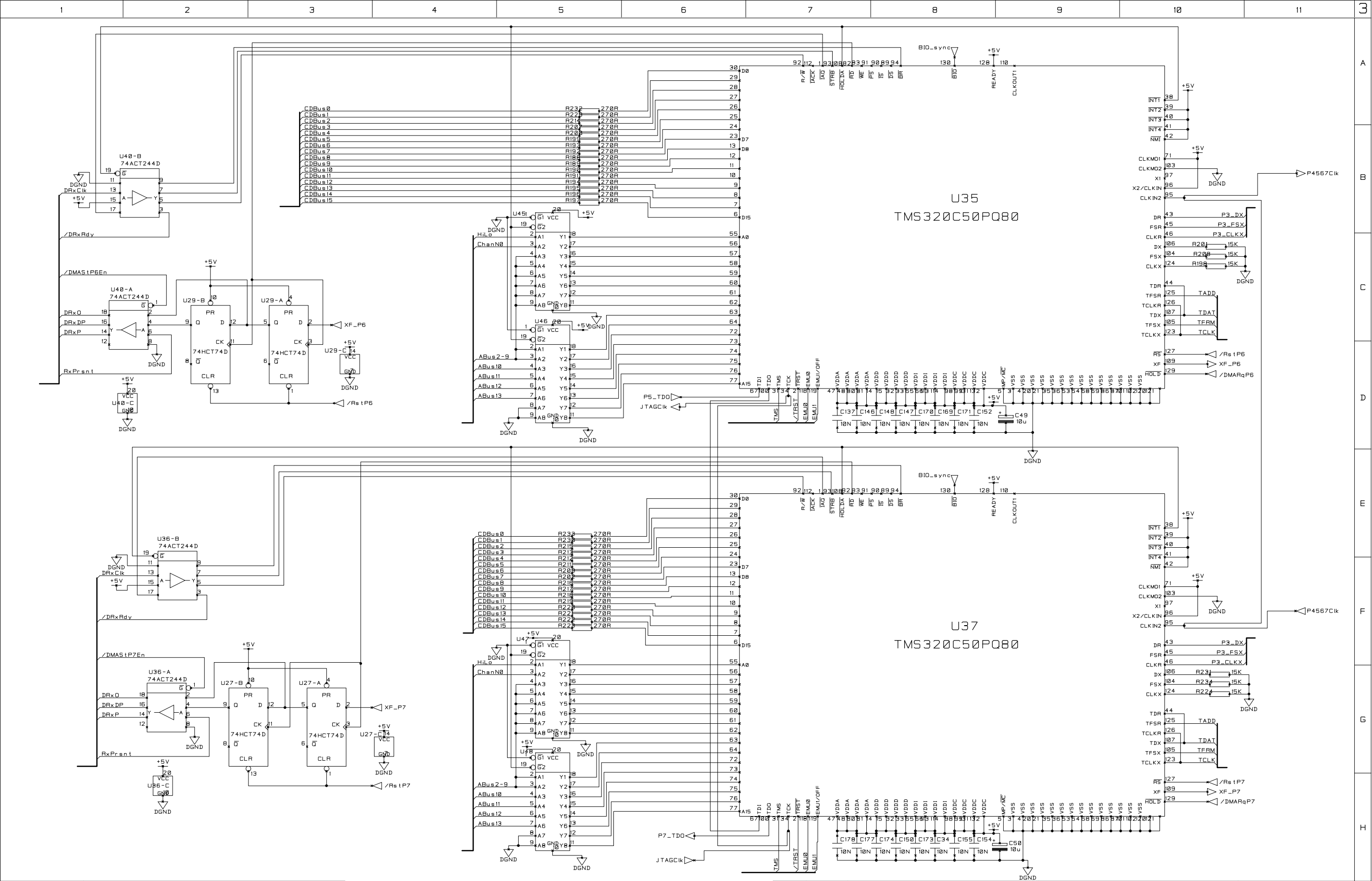


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DATE 18/11/98	

SHEET DESCRIPTION:

ISSUE	DATE	DESCRIPTION
0.1		First draft
2.0	5/6/97	A to B model changes
C	27/01/98	Production Build
2.1	18/11/98	END OF PHASE 5

CIRCUIT DIAGRAM FOR:- Data Rx Processor Card, Column C Processors P4 and P5	DRAWING NUMBER 1016/513/C SHT.5 OF 7
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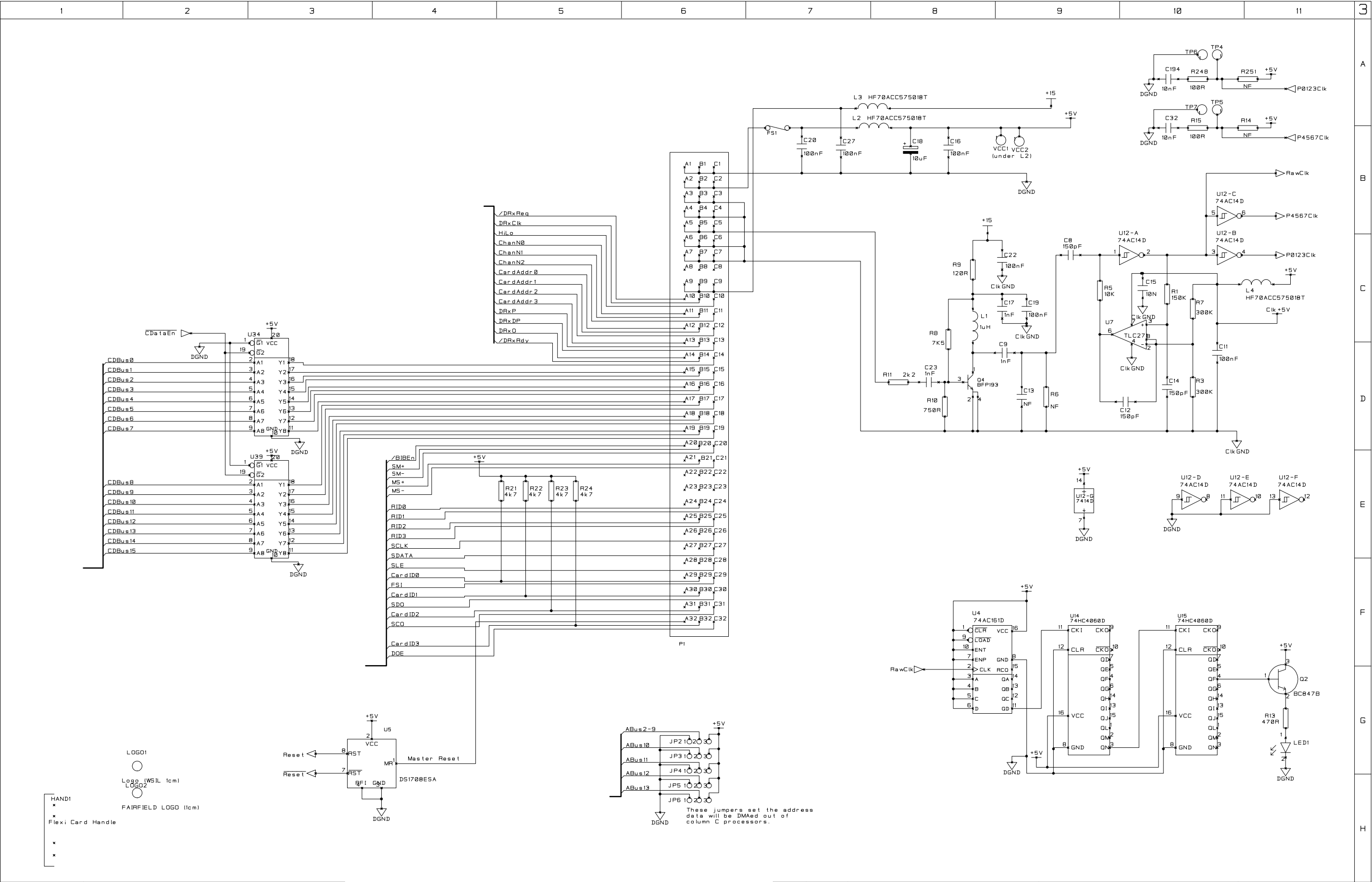
SHEET DESCRIPTION:

ISSUE	DATE	DESCRIPTION
0.1		First draft
2.0	5/6/97	A to B model changes
C	27/01/98	Production Build
2.1	18/11/98	END OF PHASE 5

CIRCUIT DIAGRAM FOR:-
Data Rx Processor Card,
Column C
Processors P6 and P7

DRAWING NUMBER
1016/513/C
SHT.6 OF 7

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SHEET DESCRIPTION:

ISSUE	DATE	DESCRIPTION
0.1	20/12/96	First draft
2.0	5/6/97	A to B model changes
C	27/01/98	Production Build
2.1	18/11/98	END OF PHASE 5

CIRCUIT DIAGRAM FOR:- Data Rx Processor Card, Main Connector	DRAWING NUMBER 1016/513/C SHT.7 OF 7
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