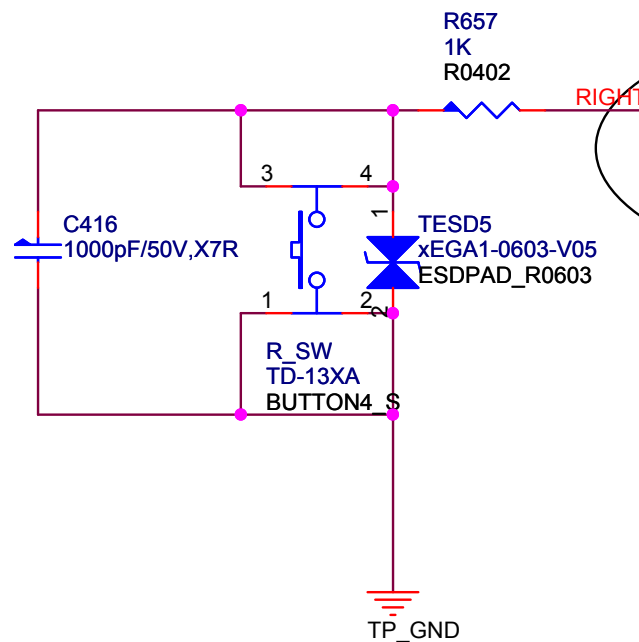
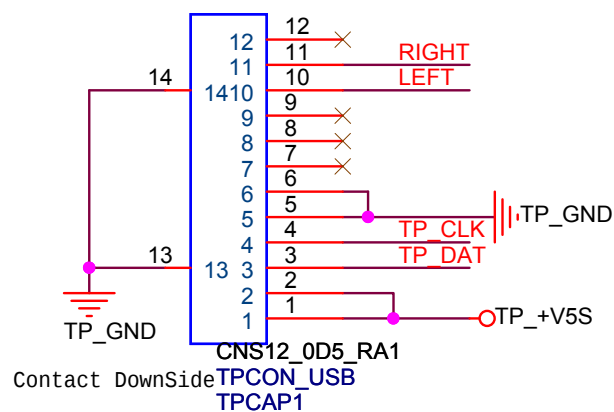
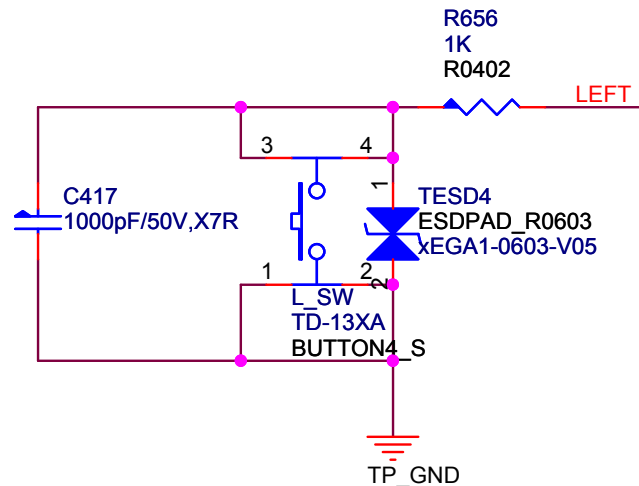
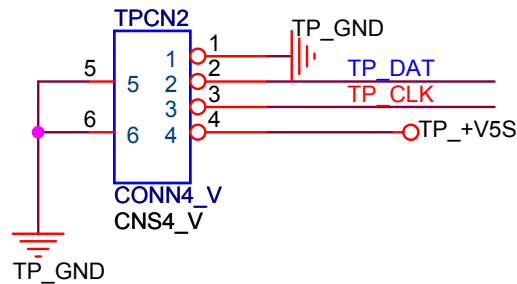
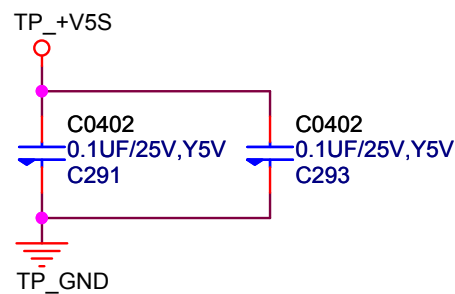


使用立式

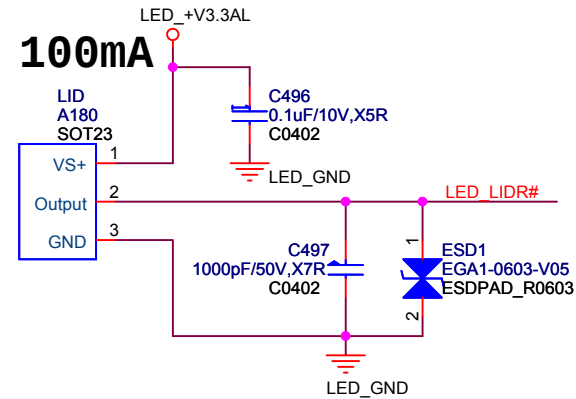
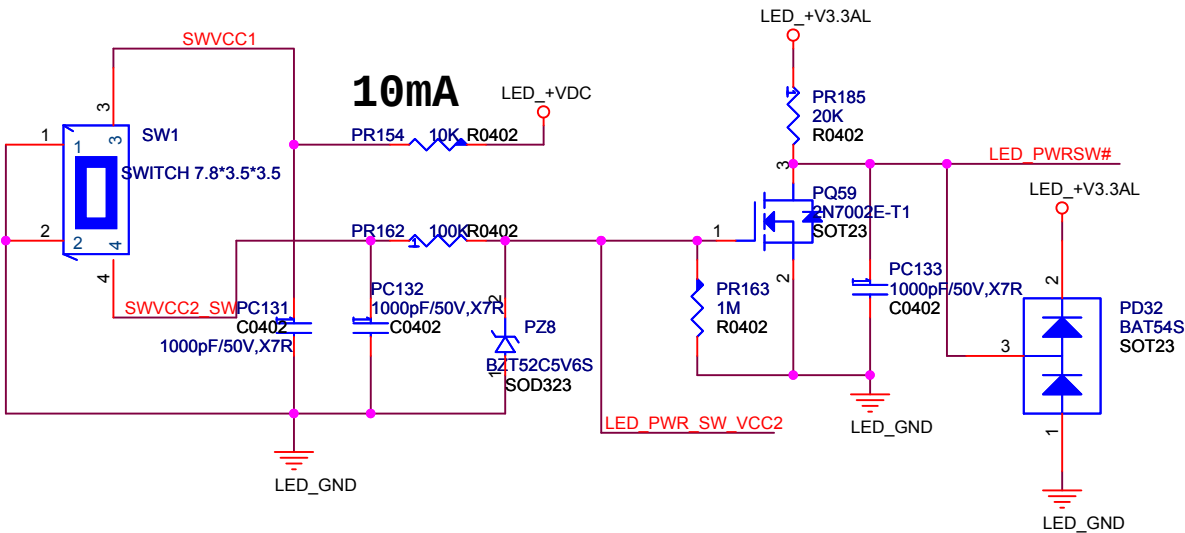
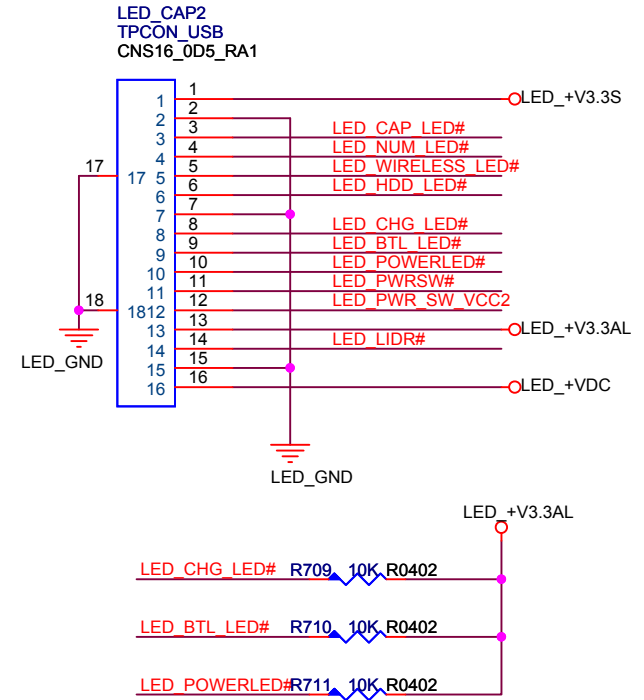
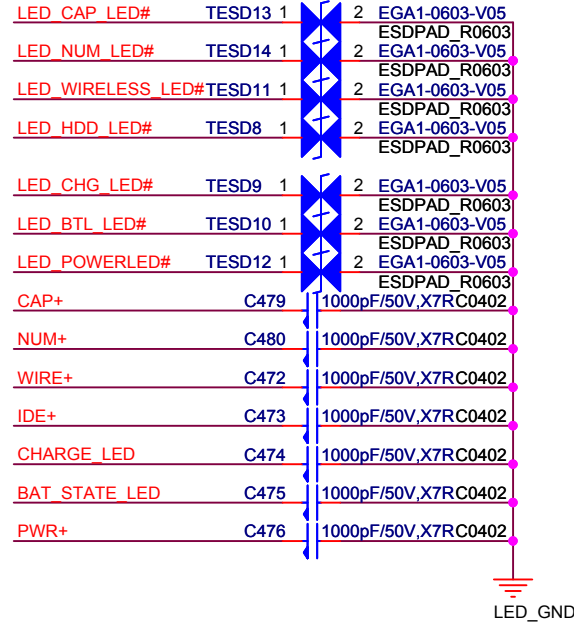
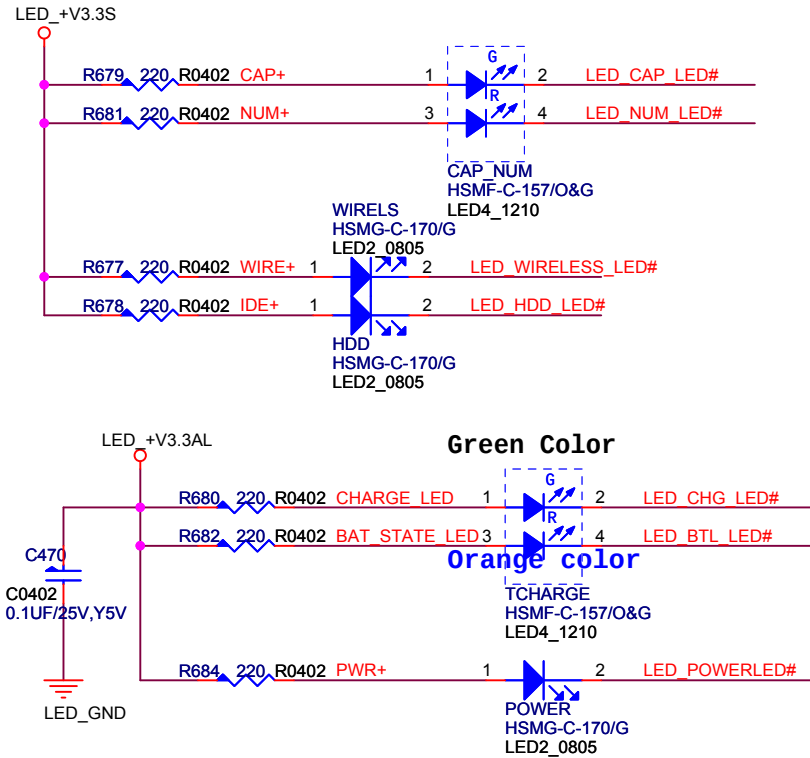


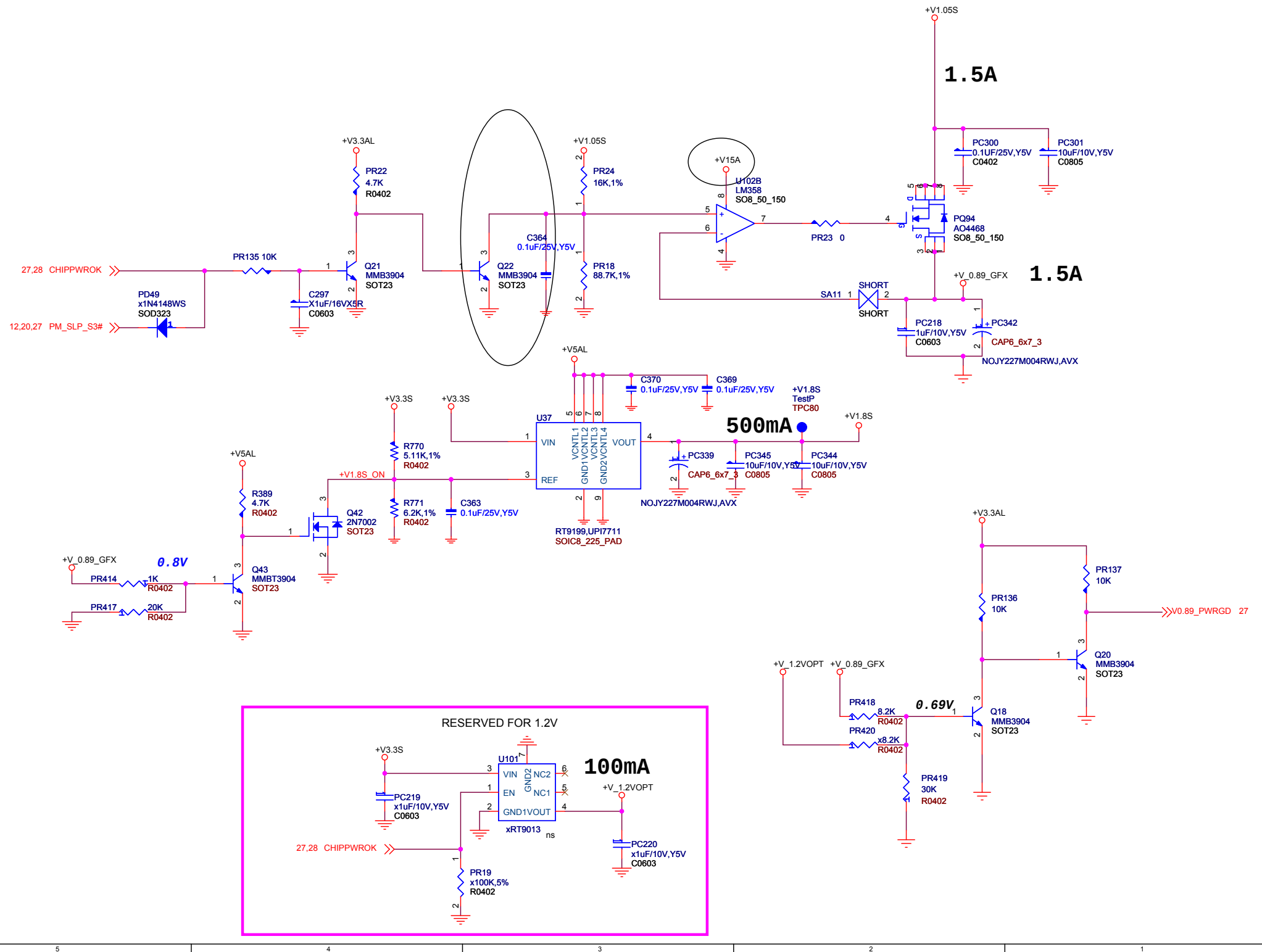
VerB: Exchange signal.

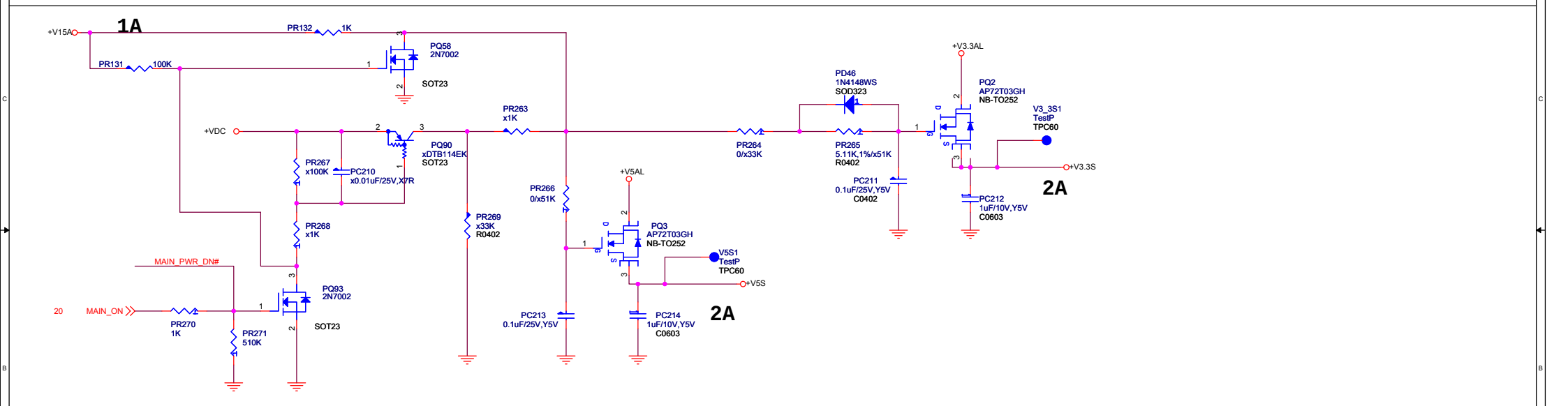
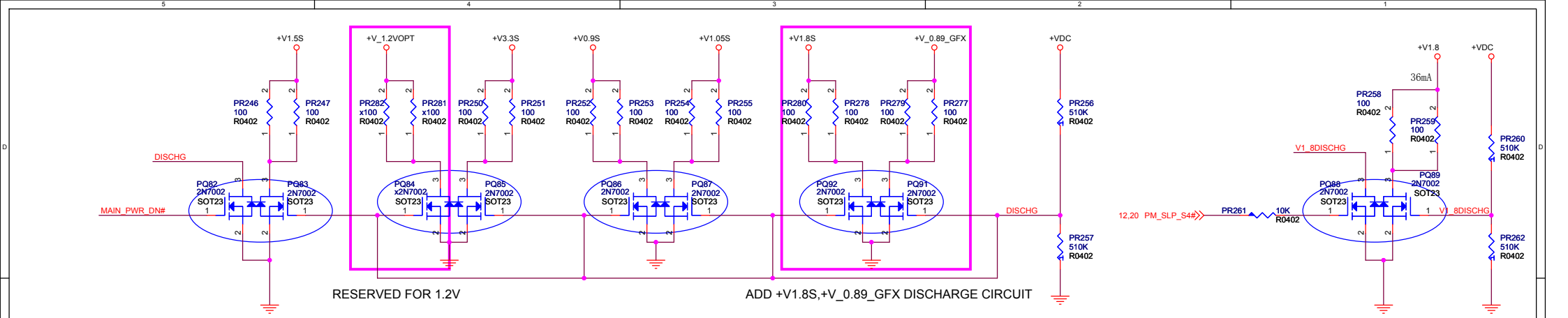
Touchpad

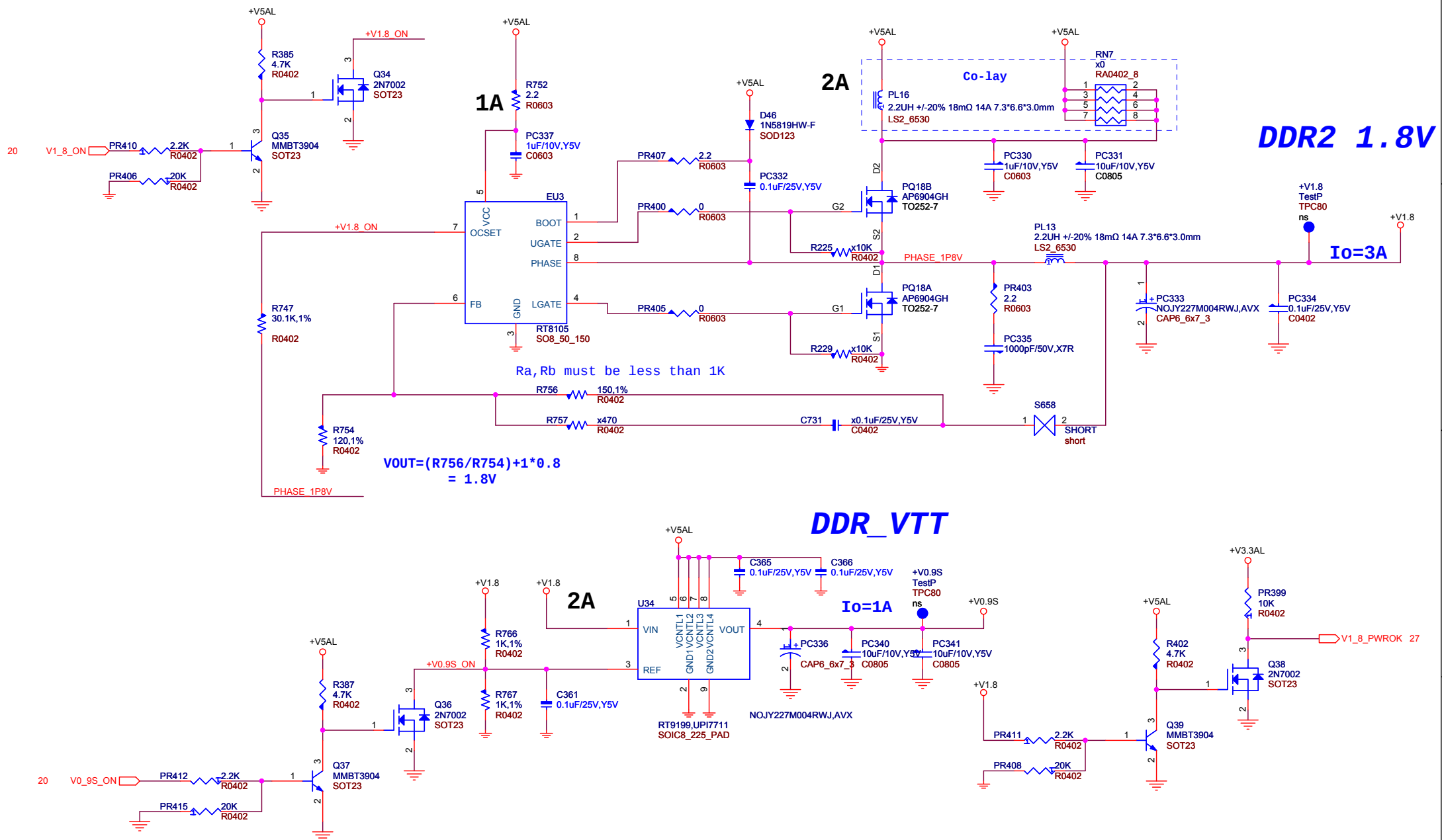


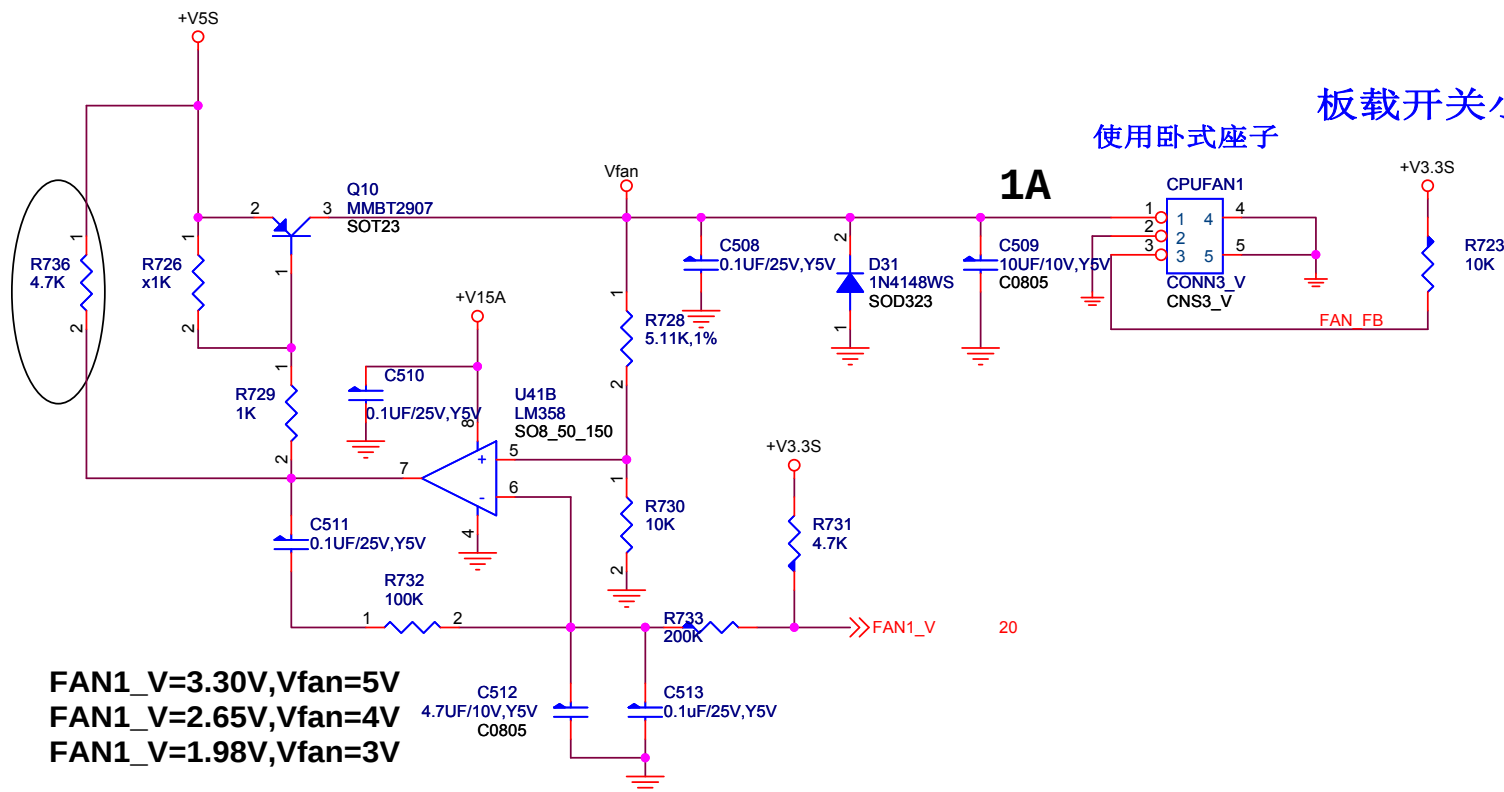
LED





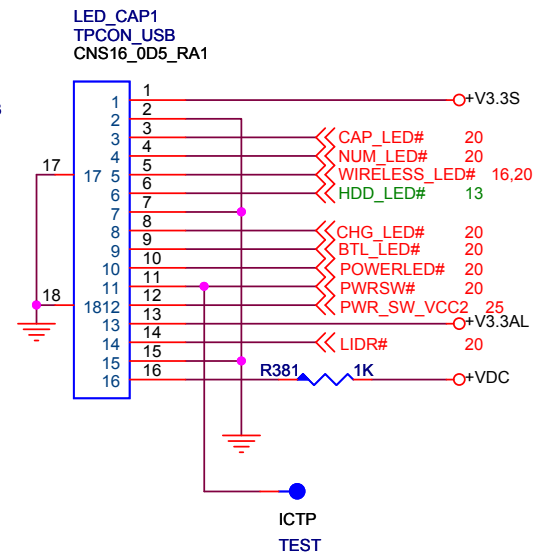




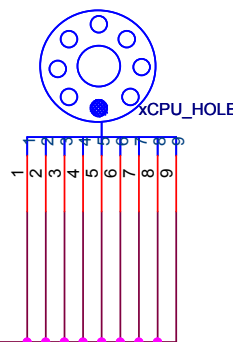
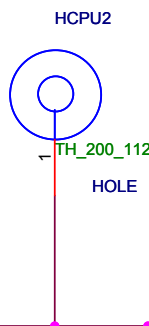
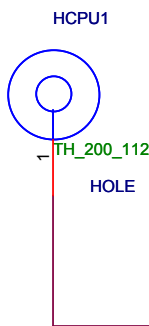
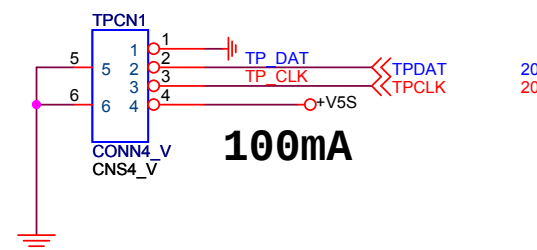


使用卧式座子

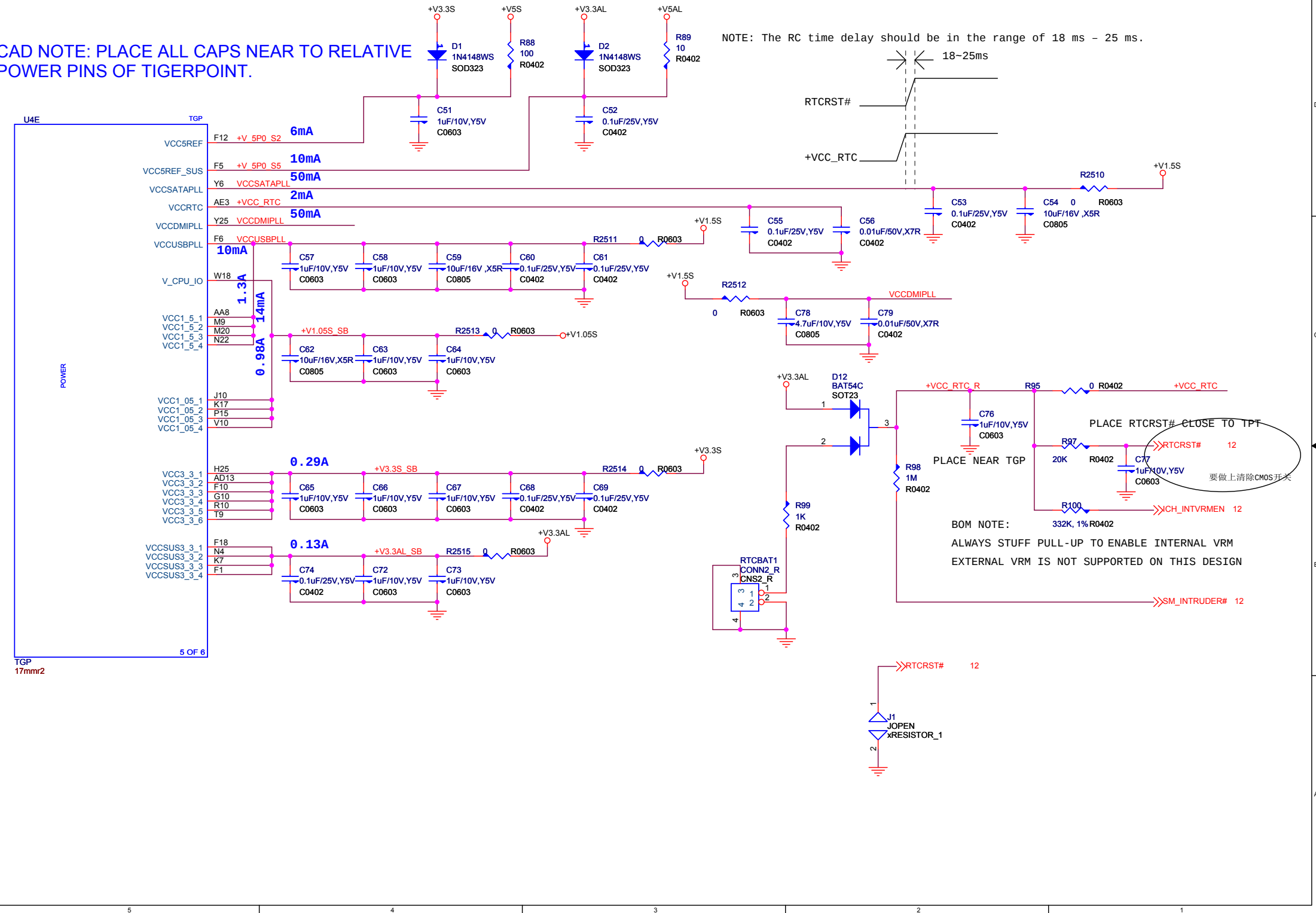
板载开关小板使用异面线, 上接触座子.



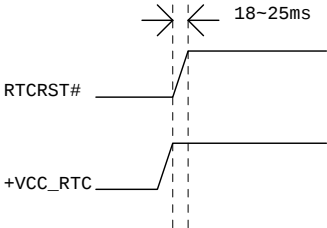
使用立式接口



CAD NOTE: PLACE ALL CAPS NEAR TO RELATIVE POWER PINS OF TIGERPOINT.



NOTE: The RC time delay should be in the range of 18 ms - 25 ms.

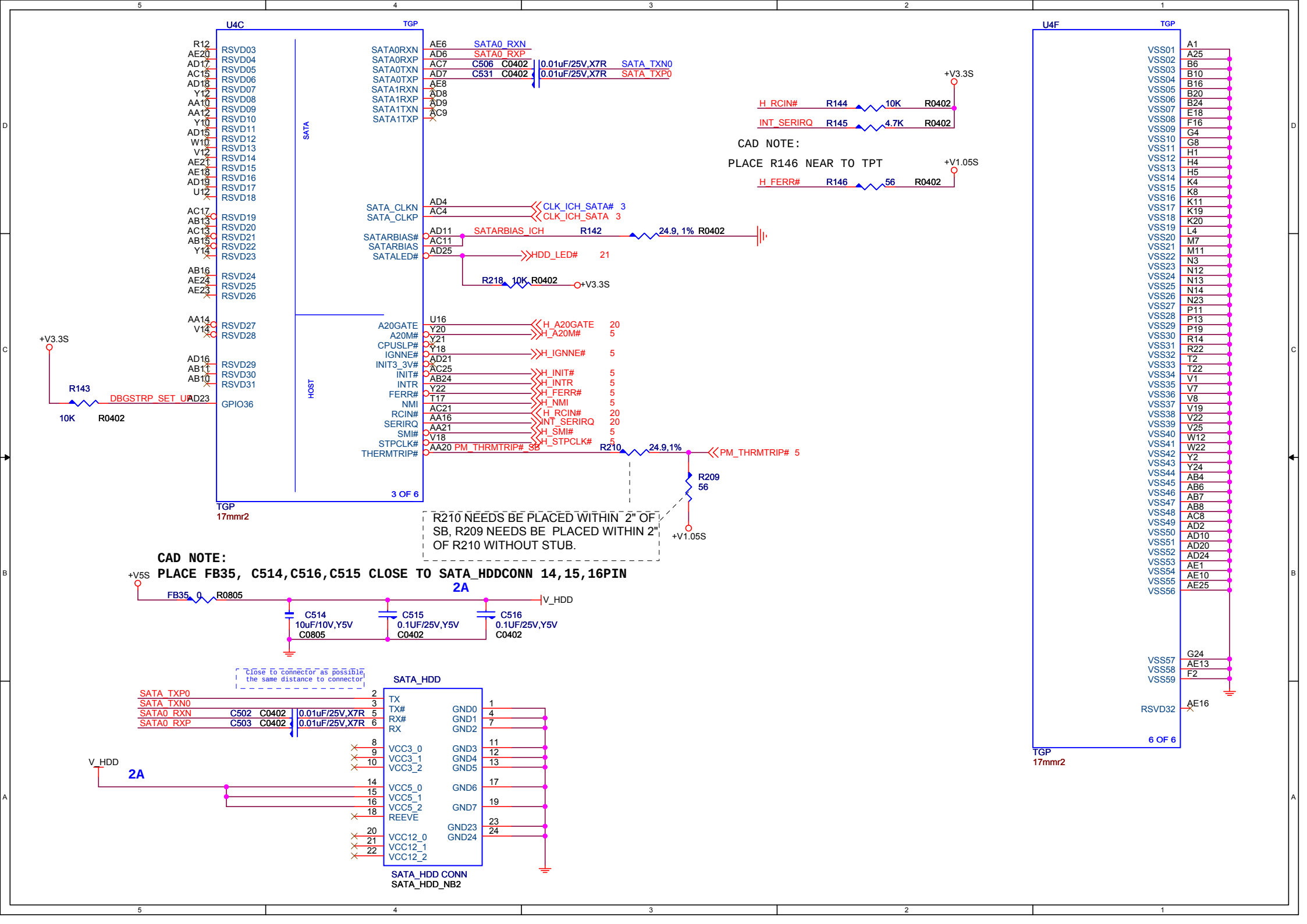


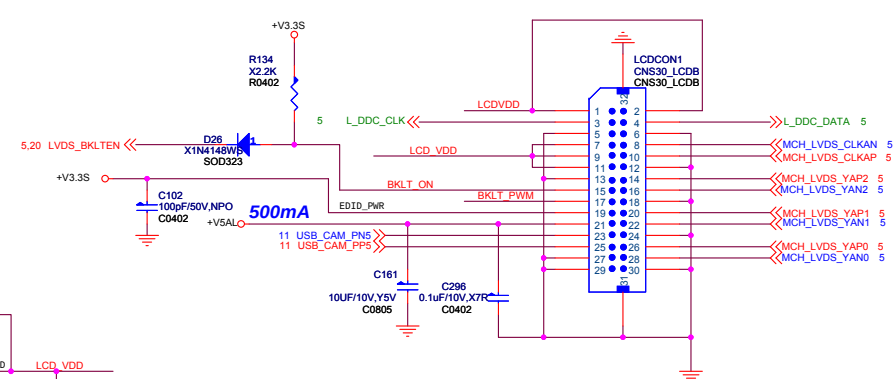
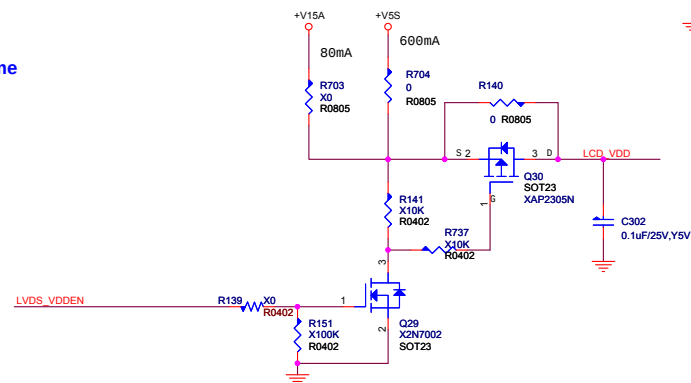
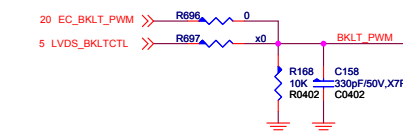
PLACE RTCRST# CLOSE TO TPT

PLACE NEAR TGP

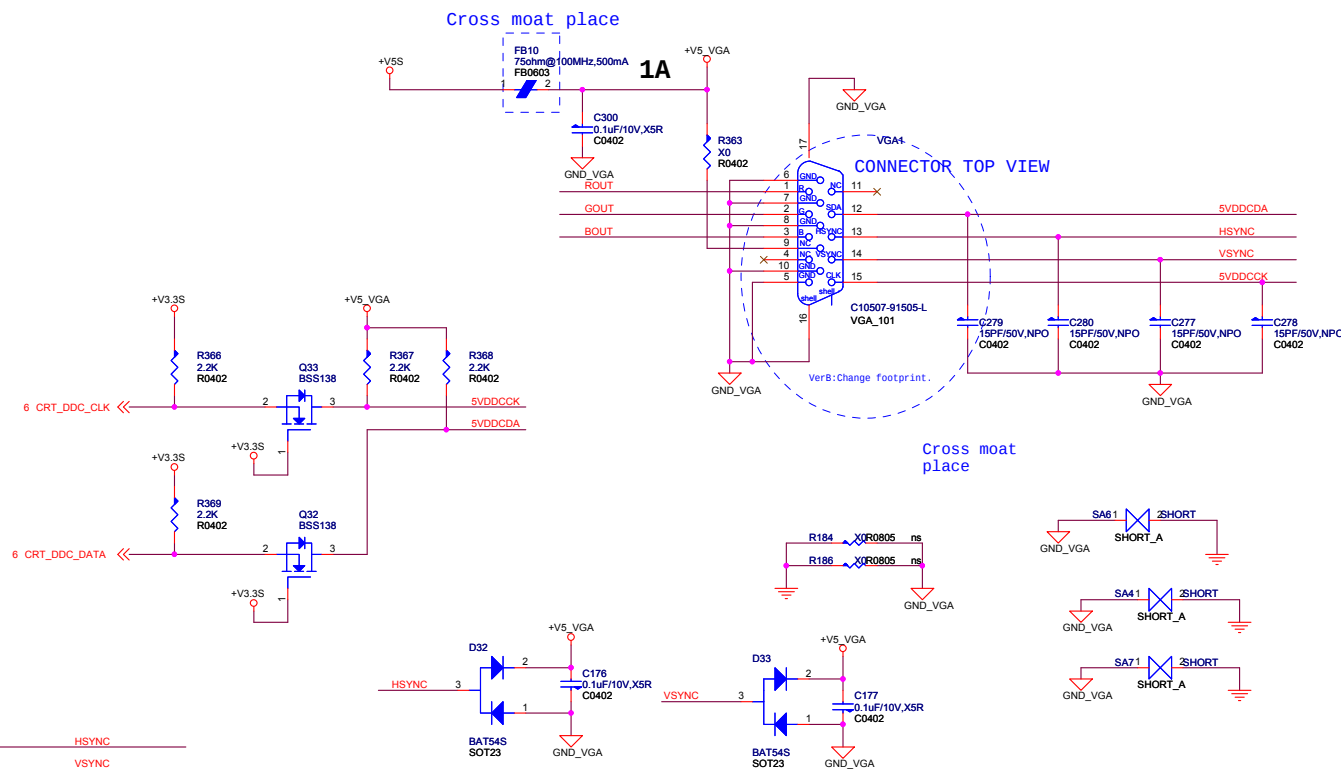
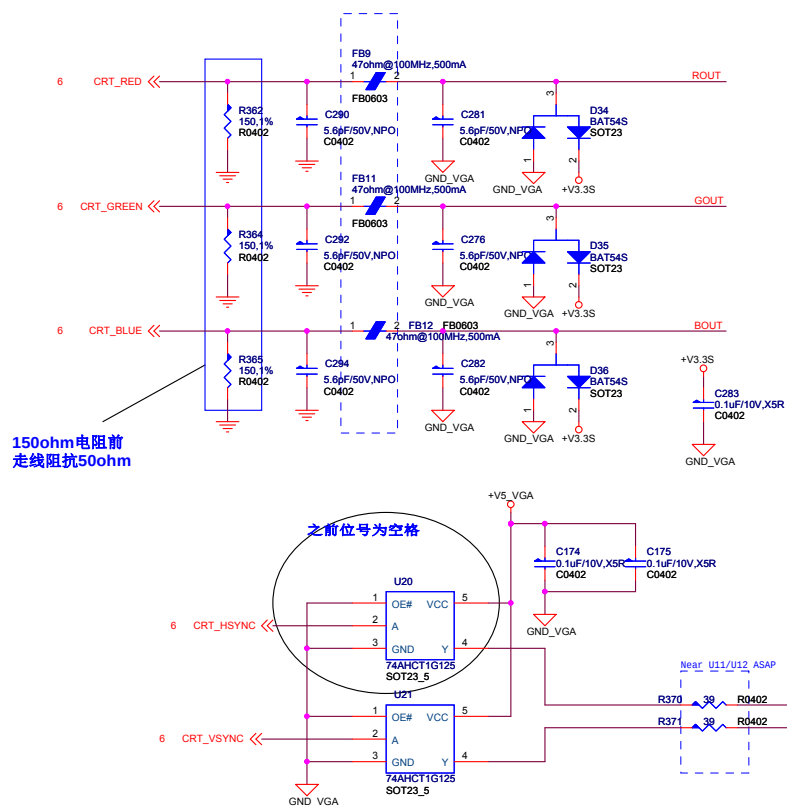
BOM NOTE:
ALWAYS STUFF PULL-UP TO ENABLE INTERNAL VRM
EXTERNAL VRM IS NOT SUPPORTED ON THIS DESIGN

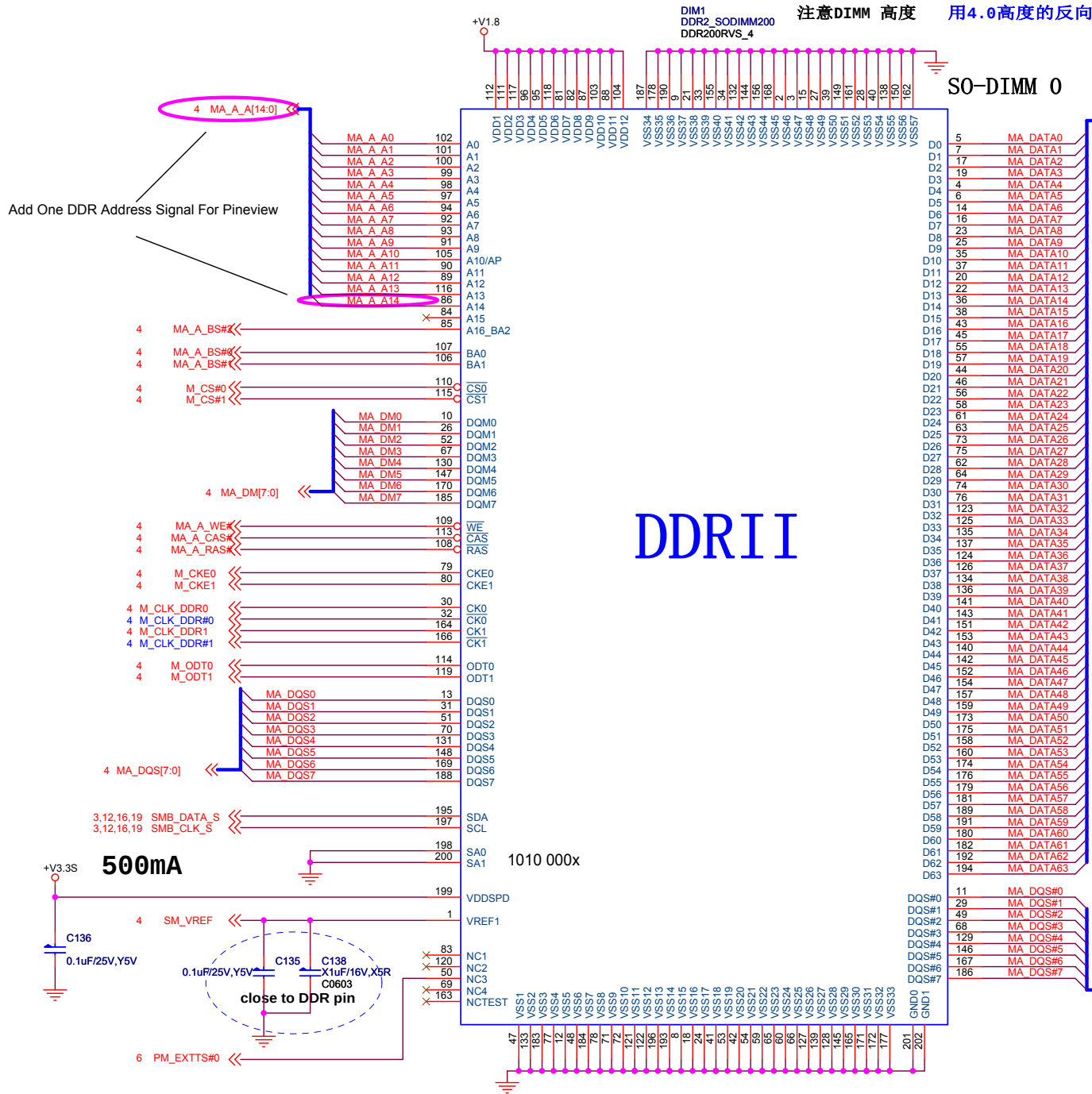
要做上清除CMOS开关



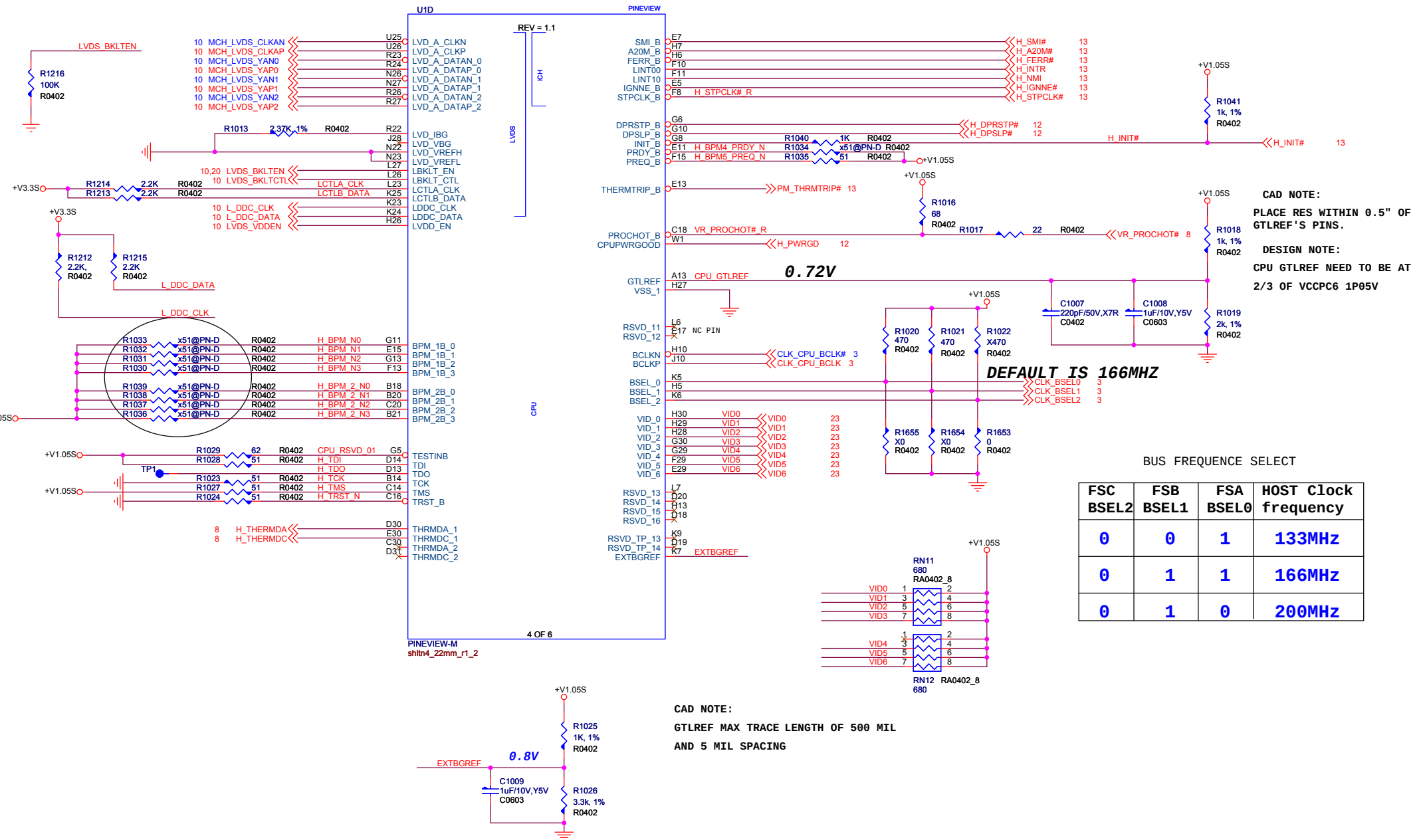


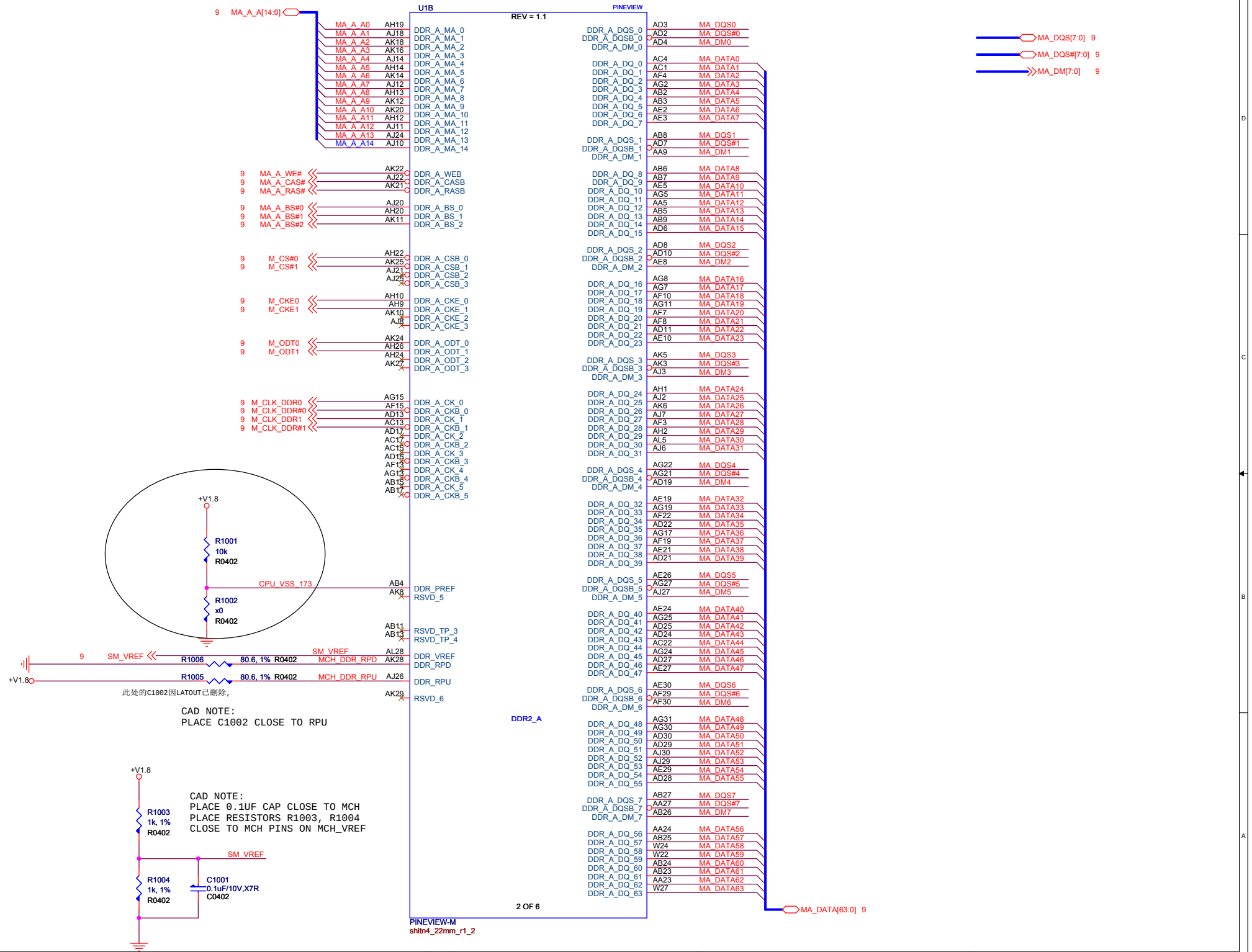
同P10BR110的IOCN1是相同的



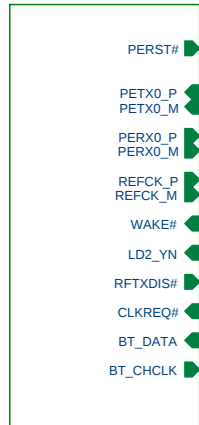


```
LCTLA_CLK I2C to control LCD backlight I/O COD
LCTLB_DATA I2C to control LCD backlight I/O COD
LDDC_CLK I2C to control LCD panel I/O COD
LDDC_DATA I2C to control LCD panel I/O COD
```



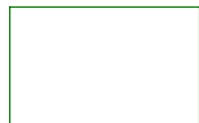


PCle Mini Card-Half I/F



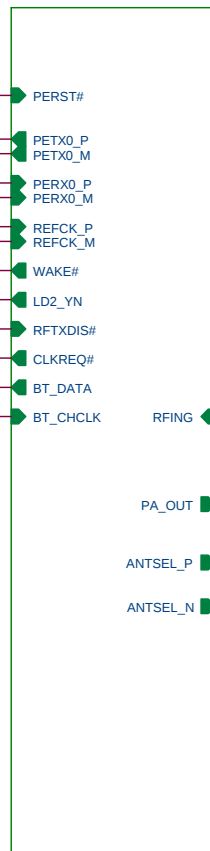
3-MiniCard-Half.SCH

POWER



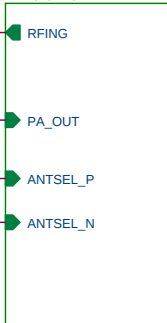
4-POWER

MAC/Transceiver RT5390



2-RT5390-WLAN

Front-End

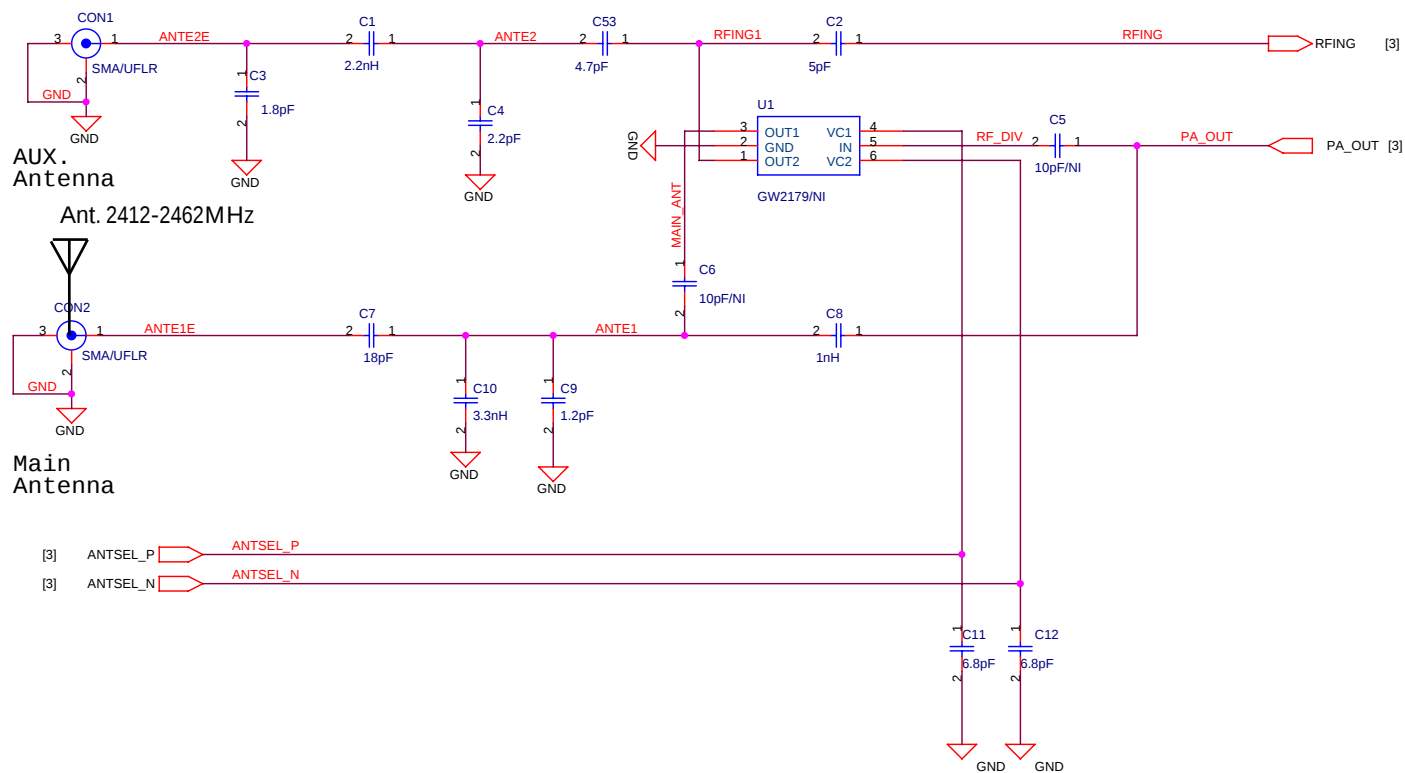


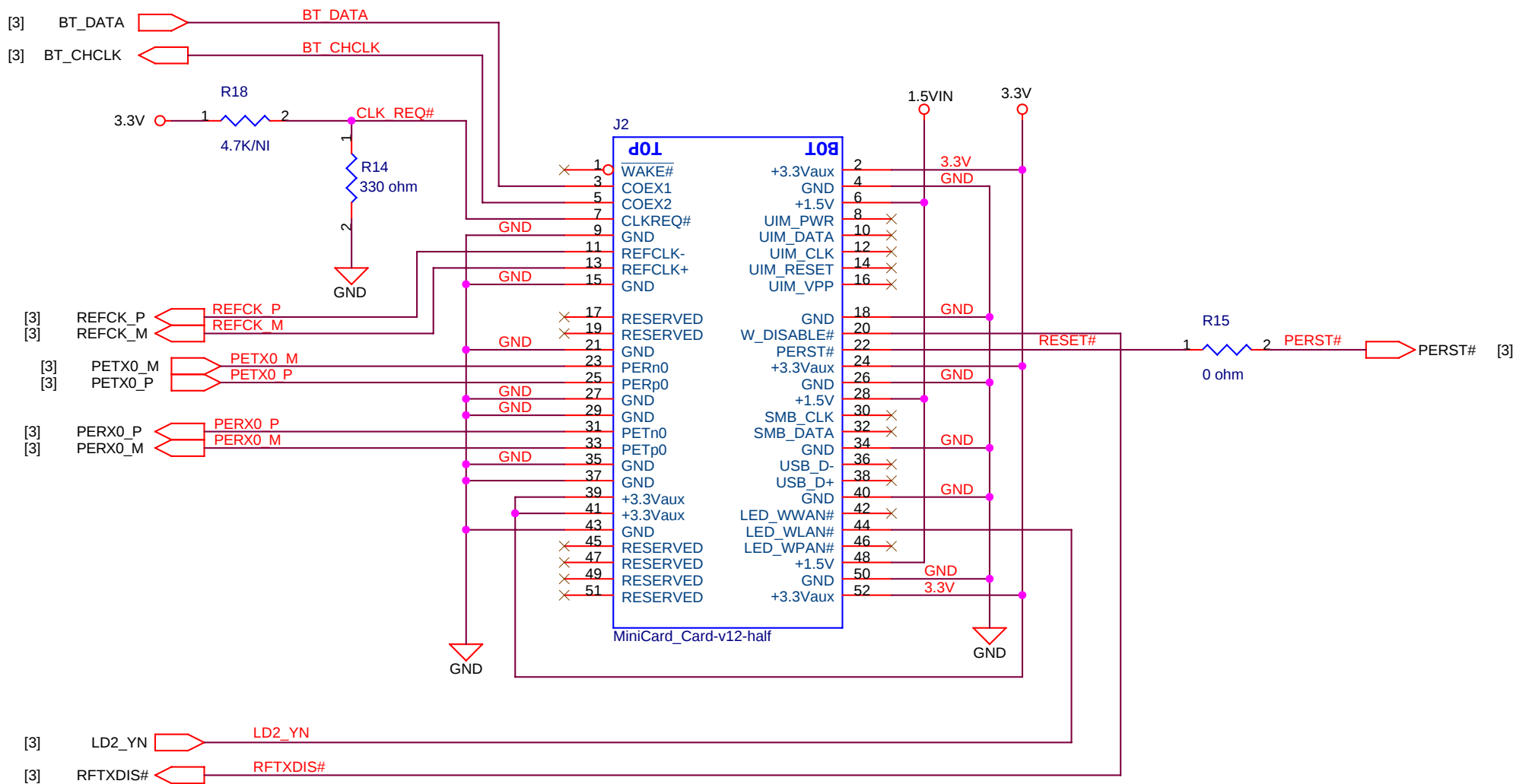
1-FrontEnd

Filename:
HMC-RT5390-V12-QFN56-DIV-3.3V-100817.DSN

		RALINK TECHNOLOGY, CORP.	
		Title 2.4GHz PCIe Half-Mini Card - RT5390	
Size B	Document Number HMC-RT5390-V12-DIV-3.3V.SCH		Rev 1.2
Date: Tuesday, August 17, 2010		Sheet	1 of 6

Wi-Fi Module





RALINK TECHNOLOGY, CORP.

Title

PCI Express Mini Card Interface

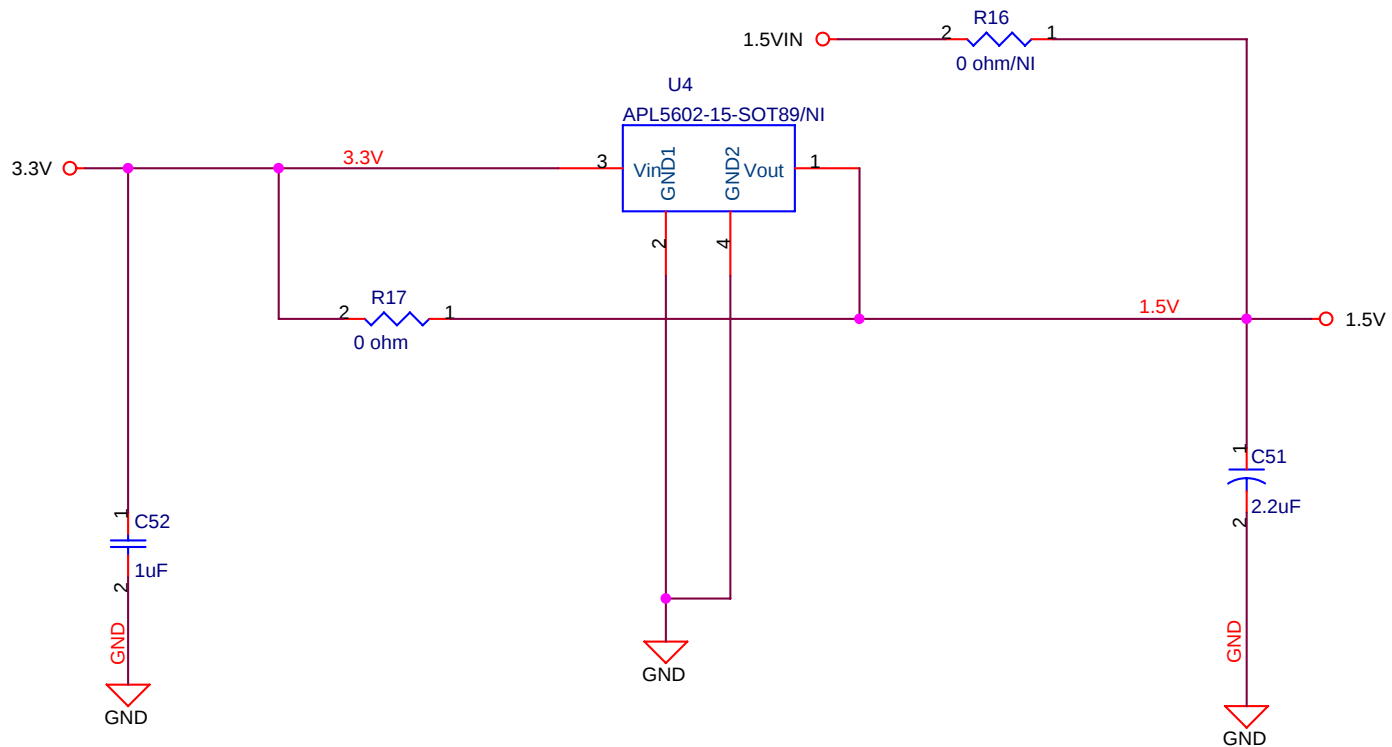
Size
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Document Number
4-MiniCard_Half.SCH

Rev
1.2

Date: Thursday, June 24, 2010

Sheet 4 of 6



U5
APL5602 - 15DI
RT9166A - 15GX

		RALINK TECHNOLOGY, CORP.	
Title		Power Supply	
Size A	Document Number 5-POWER.SCH		Rev 1.2
Date:	Thursday, June 24, 2010	Sheet	5 of 6