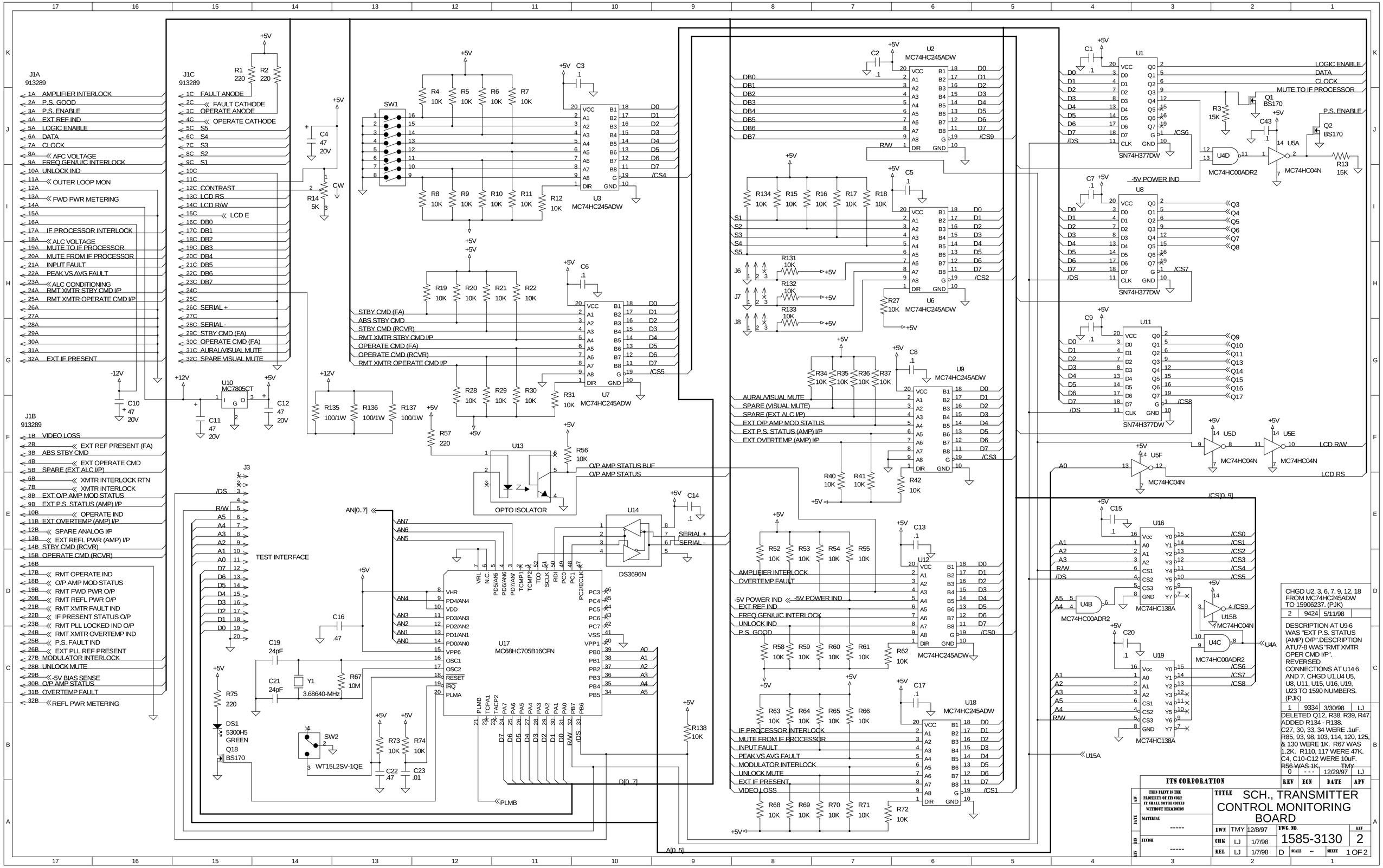
J1A 913289
1A AMPLIFIER INTERLOCK
2A P.S. GOOD
3A P.S. ENABLE
4A EXT REF IND
5A LOGIC ENABLE
6A DATA
7A CLOCK
8A « AFC VOLTAGE
9A FREQ GEN/U/C INTERLOCK
10A UNLOCK IND
11A « OUTER LOOP MON
12A
13A « FWD PWR METERING
14A
15A
16A
17A IF PROCESSOR INTERLOCK
18A « ALC VOLTAGE
19A MUTE TO IF PROCESSOR
20A MUTE FROM IF PROCESSOR
21A INPUT FAULT
22A PEAK VS AVG FAULT
23A « ALC CONDITIONING
24A RMT XMTR STBY CMD I/P
25A RMT XMTR OPERATE CMD I/P
26A
27A
28A
29A
30A
31A
32A EXT IF PRESENT
J1B 913289
1B VIDEO LOSS
2B « EXT REF PRESENT (FA)
3B ABS STBY CMD
4B « EXT OPERATE CMD
5B SPARE (EXT ALC I/P)
6B « XMTR INTERLOCK RTN
7B « XMTR INTERLOCK
8B EXT O/P AMP MOD STATUS
9B EXT P.S. STATUS (AMP) I/P
10B « OPERATE IND
11B EXT OVERTEMP (AMP) I/P
12B « SPARE ANALOG I/P
13B « EXT REFL PWR (AMP) I/P
14B STBY CMD (RCVR)
15B OPERATE CMD (RCVR)
16B
17B « RMT OPERATE IND
18B « O/P AMP MOD STATUS
19B « RMT FWD PWR O/P
20B « RMT REFL PWR O/P
21B « RMT XMTR FAULT IND
22B « IF PRESENT STATUS O/P
23B « RMT PLL LOCKED IND O/P
24B « RMT XMTR OVERTEMP IND
25B « P.S. FAULT IND
26B « EXT PLL REF PRESENT
27B MODULATOR INTERLOCK
28B UNLOCK MUTE
29B « -5V BIAS SENSE
30B O/P AMP STATUS
31B OVERTEMP FAULT
32B « REFL PWR METERING
J1C 913289
1C FAULT ANODE
2C « FAULT CATHODE
3C OPERATE ANODE
4C « OPERATE CATHODE
5C S5
6C S4
7C S3
8C S2
9C S1
10C
11C
12C CONTRAST
13C LCD RS
14C LCD R/W
15C « LCD E
16C DB0
17C DB1
18C DB2
19C DB3
20C DB4
21C DB5
22C DB6
23C DB7
24C
25C
26C SERIAL +
27C
28C SERIAL -
29C STBY CMD (FA)
30C OPERATE CMD (FA)
31C AURAL/VISUAL MUTE
32C SPARE VISUAL MUTE
J3
TEST INTERFACE
U17 MC68HC705B16CFN
U2 MC74HC245ADW
U3 MC74HC245ADW
U6 MC74HC245ADW
U7 MC74HC245ADW
U9 MC74HC245ADW
U12 MC74HC245ADW
U18 MC74HC245ADW
U1 SN74H377DW
U8 SN74H377DW
U11 SN74H377DW
U16 MC74HC138A
U19 MC74HC138A
U10 MC7805CT
U13 OPTO ISOLATOR
U14 DS3696N
SW1
SW2 WT15L2SV-1QE
Y1 3.68640-MHz
DS1 5300H5 GREEN
Q18 BS170
Q1 BS170
Q2 BS170
U4A U4B U4C U4D U5A U5D U5E U5F U15A U15B
MC74HC00ADR2
MC74HC04N
LOGIC ENABLE
DATA
CLOCK
MUTE TO IF PROCESSOR
P.S. ENABLE
-5V POWER IND
LCD R/W
LCD RS
/CS[0..9]
AN[0..7]
D[0..7]
A[0..5]
O/P AMP STATUS BUF
O/P AMP STATUS
SERIAL +
SERIAL -
STBY CMD (FA)
ABS STBY CMD
STBY CMD (RCVR)
RMT XMTR STBY CMD I/P
OPERATE CMD (FA)
OPERATE CMD (RCVR)
RMT XMTR OPERATE CMD I/P
AURAL/VISUAL MUTE
SPARE (VISUAL MUTE)
SPARE (EXT ALC I/P)
EXT O/P AMP MOD STATUS
EXT P.S. STATUS (AMP) I/P
EXT OVERTEMP (AMP) I/P
AMPLIFIER INTERLOCK
OVERTEMP FAULT
EXT REF IND
FREQ GEN/U/C INTERLOCK
UNLOCK IND
P.S. GOOD
IF PROCESSOR INTERLOCK
MUTE FROM IF PROCESSOR
INPUT FAULT
PEAK VS AVG FAULT
MODULATOR INTERLOCK
UNLOCK MUTE
EXT IF PRESENT
VIDEO LOSS
CHGD U2, 3, 6, 7, 9, 12, 18 FROM MC74HC245ADW TO 15906237. (PJK)
2 9424 5/11/98
DESCRIPTION AT U9-6 WAS "EXT P.S. STATUS (AMP) O/P".DESCRIPTION ATU7-8 WAS "RMT XMTR OPER CMD I/P". REVERSED CONNECTIONS AT U14 6 AND 7. CHGD U1,U4 U5, U8, U11, U15, U16, U19, U23 TO 1590 NUMBERS. (PJK)
1 9334 3/30/98 LJ
DELETED Q12, R38, R39, R47. ADDED R134 - R138. C27, 30, 33, 34 WERE .1uF. R85, 93, 98, 103, 114, 120, 125, & 130 WERE 1K. R67 WAS 1.2K. R110, 117 WERE 47K. C4, C10-C12 WERE 10uF. R56 WAS 1K.
0 - - - 12/29/97 LJ
REV ECN DATE APV
ITS CORPORATION
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MATERIAL -----
FINISH -----
TITLE SCH., TRANSMITTER CONTROL MONITORING BOARD
DWN TMY 12/8/97
CHK LJ 1/7/98
REL LJ 1/7/98
DWG. NO. 1585-3130
REV 2
D
SCALE ---
SHEET 1 OF 2