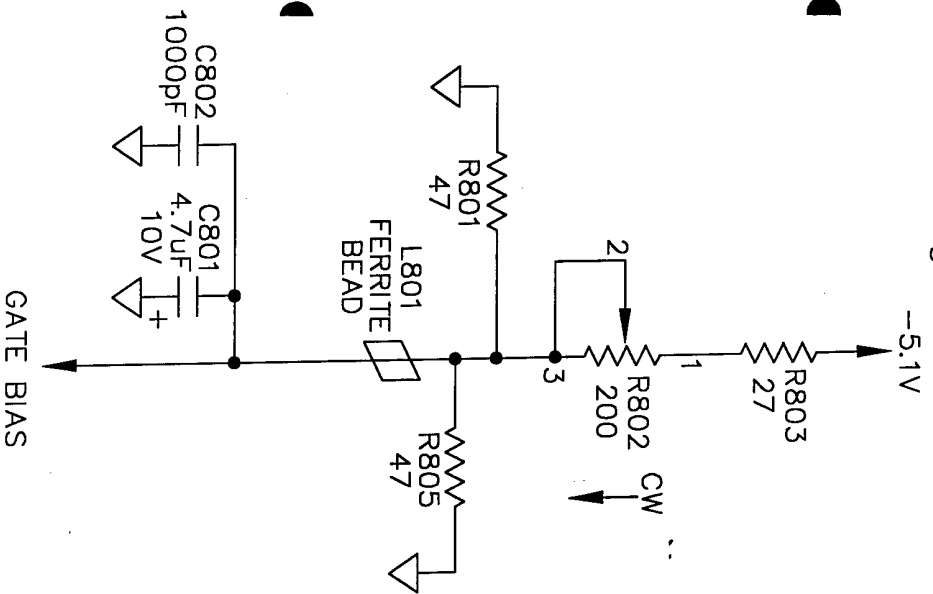
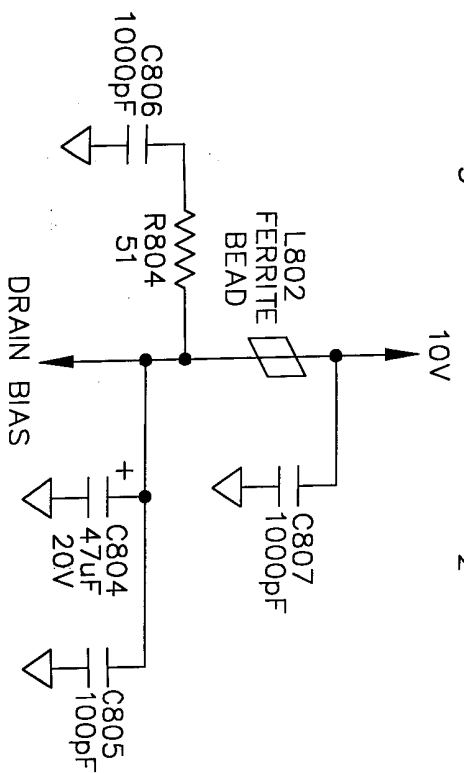




NOTES:

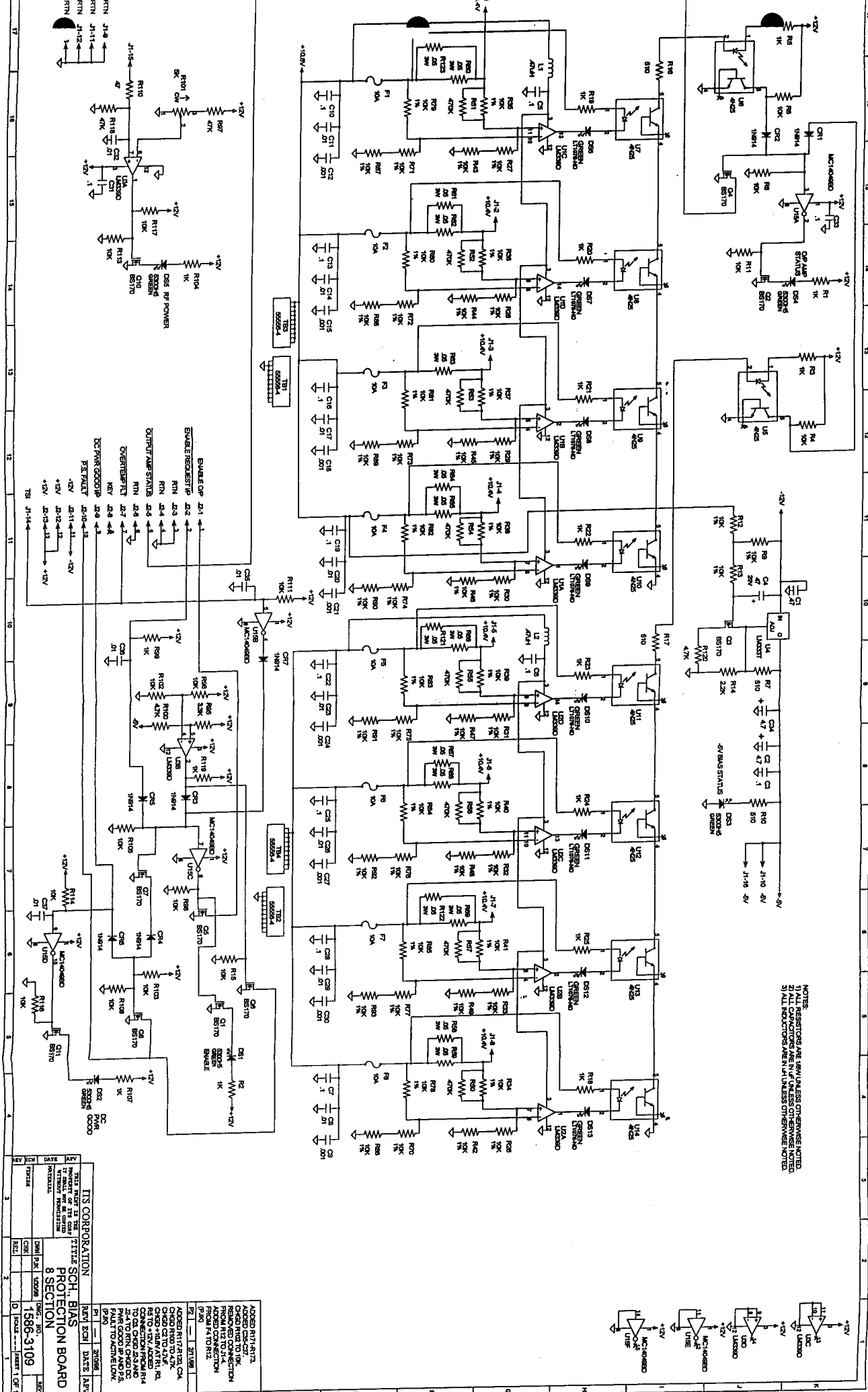
- 1) ALL RESISTORS ARE 1/8W, 5% & IN VALUES OF OHMS UNLESS OTHERWISE NOTED.
- 2) IF 200 OHM POTS ARE NOT AVAILABLE, 100 OHM POTS MAY BE USED. HOWEVER, RESISTOR VALUES MAY HAVE TO CHANGE (SEE NOTE 2).
- 3) TO ACCOUNT FOR VARIATIONS IN THE FLL300 & FLL200, R803 MAY BE RAISED TO INCREASE DRAIN CURRENT (TYP 150 OHM) OR LOWERED TO DECREASE DRAIN CURRENT (TYP 27 OHM).



ITS CORPORATION				TITLE			
THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.				SCH, AMP DAUGHTER BOARD, LEFT SIDE			
APV	DATE	ECN	REV	DWN	GK	DWG. NO.	REV
				FINISH		1512-3110	1
MATERIAL				REL	BKW	4/7/97	A
				SCALE	SHEET 1 OF 1		

ADDED NOTES & pF TO C802, C805-C807. (GK)

1 8940 4/9/97

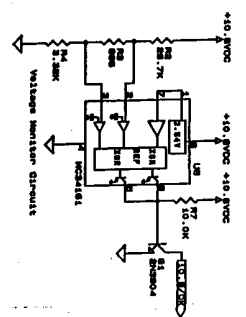
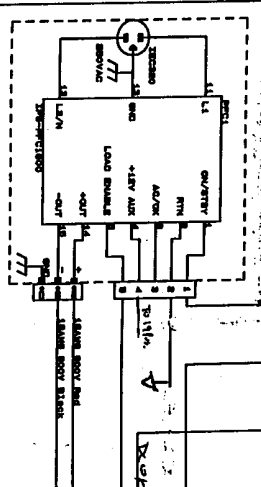
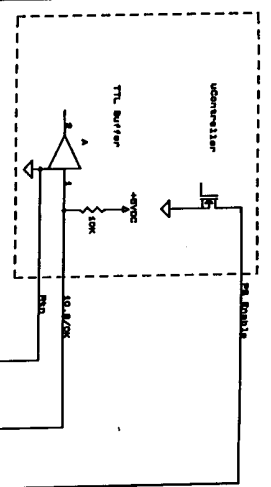


NOTES:
 1) ALL RESISTORS ARE 100Ω UNLESS OTHERWISE NOTED.
 2) ALL CAPACITORS ARE IN μF UNLESS OTHERWISE NOTED.
 3) ALL INDUCTORS ARE IN mH UNLESS OTHERWISE NOTED.

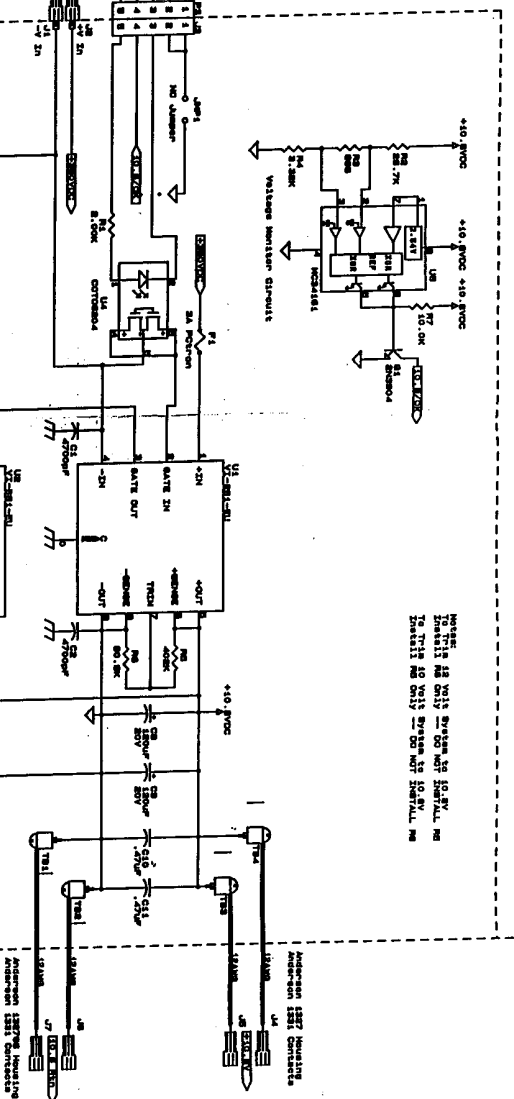
REV	DATE	BY	CHK	APP
1	15/08/08	15/08/08	15/08/08	15/08/08

REV	DATE	BY	CHK	APP
1	15/08/08	15/08/08	15/08/08	15/08/08

REV	DATE	BY	CHK	APP
1	15/08/08	15/08/08	15/08/08	15/08/08



NOTE: 10VDC is only used to 10.8V. 10.8V is only used to 10.8V. 10.8V is only used to 10.8V. 10.8V is only used to 10.8V.



WARNING
LETHAL VOLTAGES PRESENT

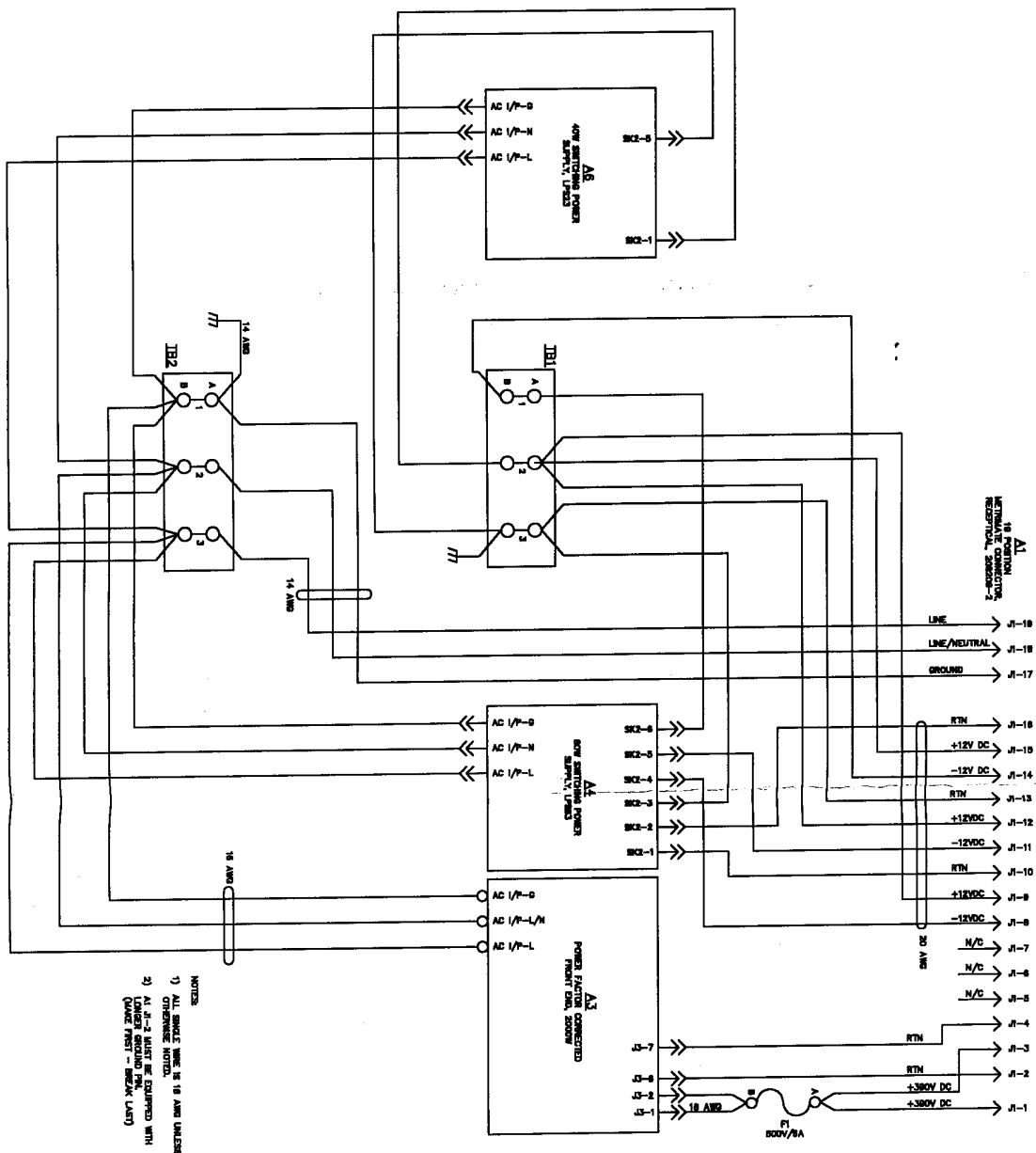


WARNING
LETHAL VOLTAGES PRESENT

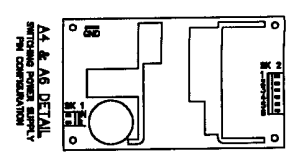
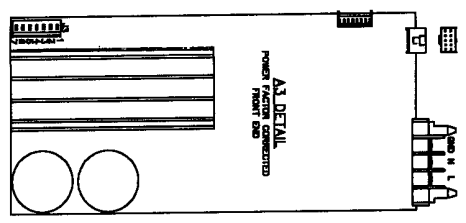
PROTOTYPE

ITS-DC380-14

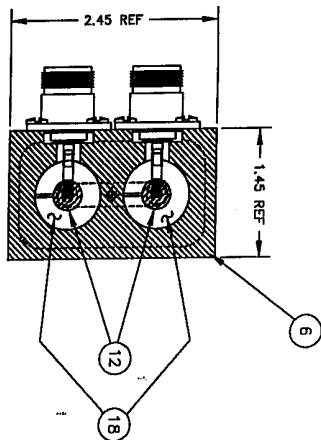
Integrated Power Solutions Inc.
8800 Commerce Park
Bethesda, MD 20814
ITS Power Supply Division
ITS-DC380-14
Rev. 1.0



NOTES
 1) ALL SIGNALS ARE IN 16 AMP UNLESS OTHERWISE NOTED.
 2) A1-2-3 MUST BE EQUIPPED WITH (UNDER TEST - BREAK LAST)



JTS CORPORATION		REV	DATE	BY
TITLE		I/C		
PFC FRONT END		MODULE		
REV	DATE	BY	DATE	BY
1	12/1/78	1586-813610		
2	12/1/78	1586-813610		



SEE NOTE 2

RED

(TYP 2 PLCS)

(TYP 2 PLCS)

(TYP 4 PLCS)

(TYP 28 PLCS)

(TYP 4 PLCS)

2

4

10

12

14

16

18

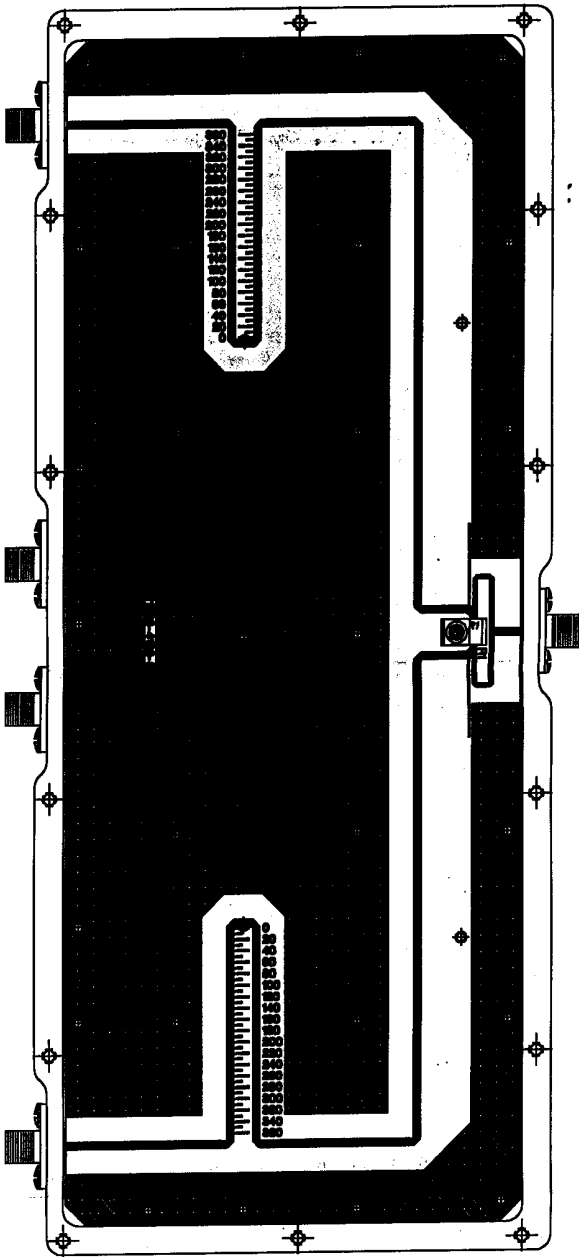
20

22

1. SEPARATE PIN FROM 82-97 CONNECTOR, BEFORE ASSEMBLY. ATTACH PINS TO CENTER CONDUCTORS USING PLIERS. PINS MUST BE FULLY SEATED AND TIGHT. REPLACE 82-97 CONNECTORS OVER PINS, PUSH WITH A CLOCKWISE MOTION. A GAP OF .150" TO .170" MUST BE MAINTAINED FROM END OF PIN TO END OF CONNECTOR.
2. HOUSING (ITEM 6) MUST BE ORIENTED AS SHOWN FOR PROPER ASSEMBLY.

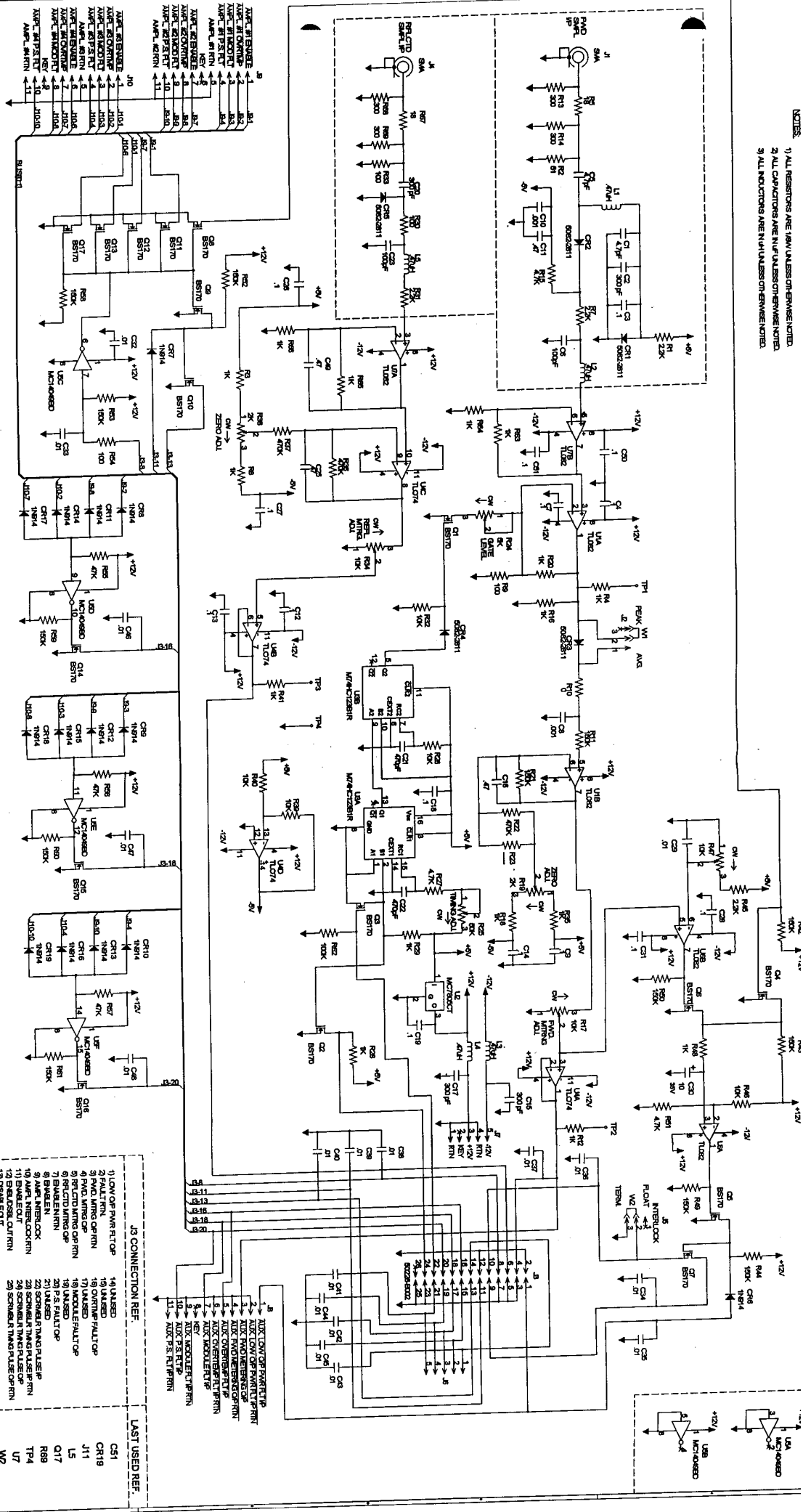
SECTION Y-Y

REV	ECN	DATE	APV	THIS POINT IS THE POINT OF NO RETURN IF SHALL NOT BE COMED WITHOUT PERMISSION MATERIAL
TITS CORPORATION				
TITLE				
2 WAY COMBINER/SPLITER A/D, 2.5-2.7 GHz				
FINISH	DOWN	SAC/127/AS	DWG. NO.	REV
	CHK		211-5008	1
	REL			2
C/SKALE 1=1			SHEET 1	OF 1

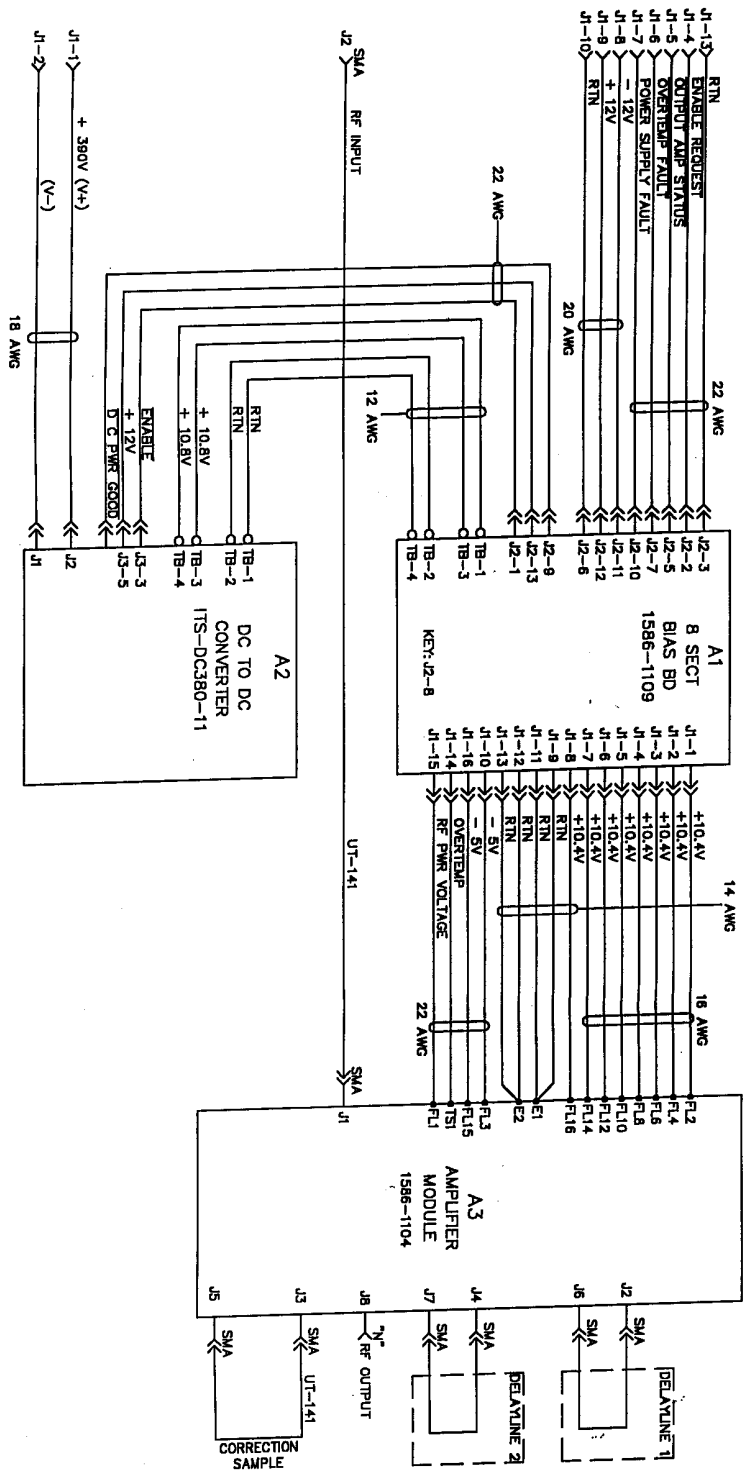


TTS CORPORATION		REV	EDN	DATE	APV
TITLE		A/D, 2 WAY			
SPLITTER		(1586-1134)			
MATERIAL		DIN. NO.			
1586-5134		PO			
DIN. NO.		1586-5134			
REV		1			
DIN. NO.		1586-5134			
REV		1			

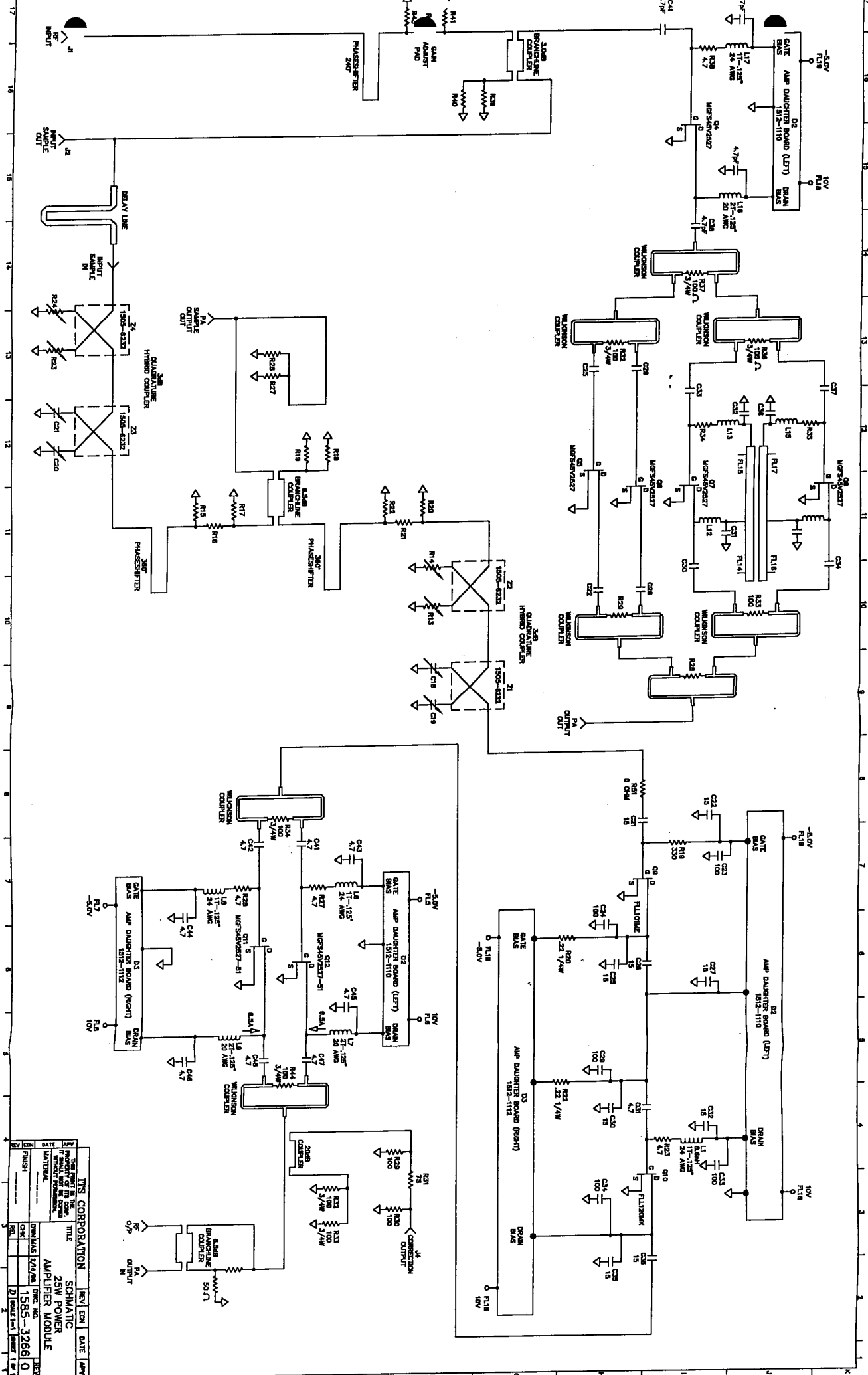
3) ALL INDUCTORS ARE IN μ H UNLESS OTHERWISE NOTED.



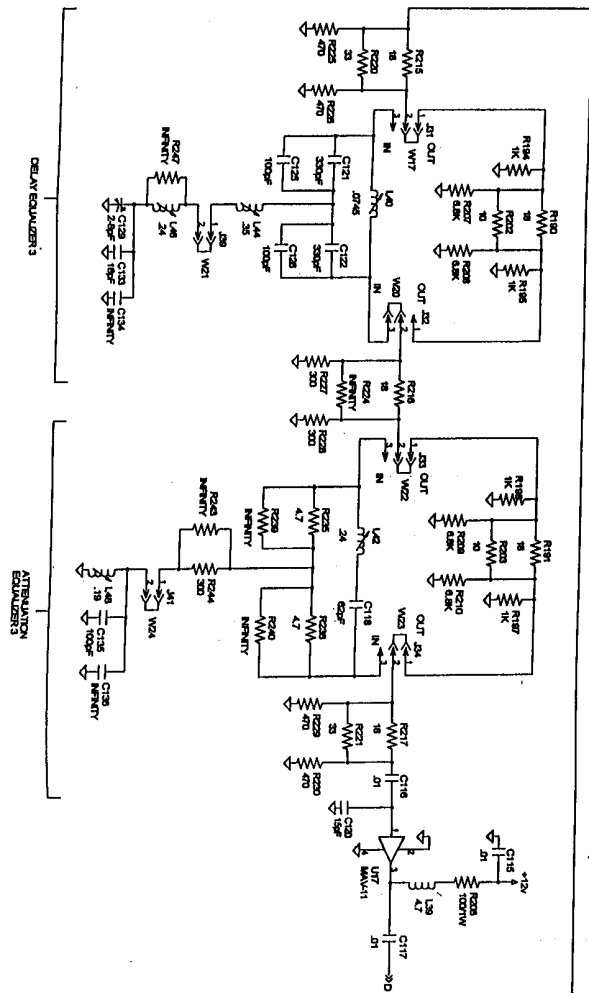
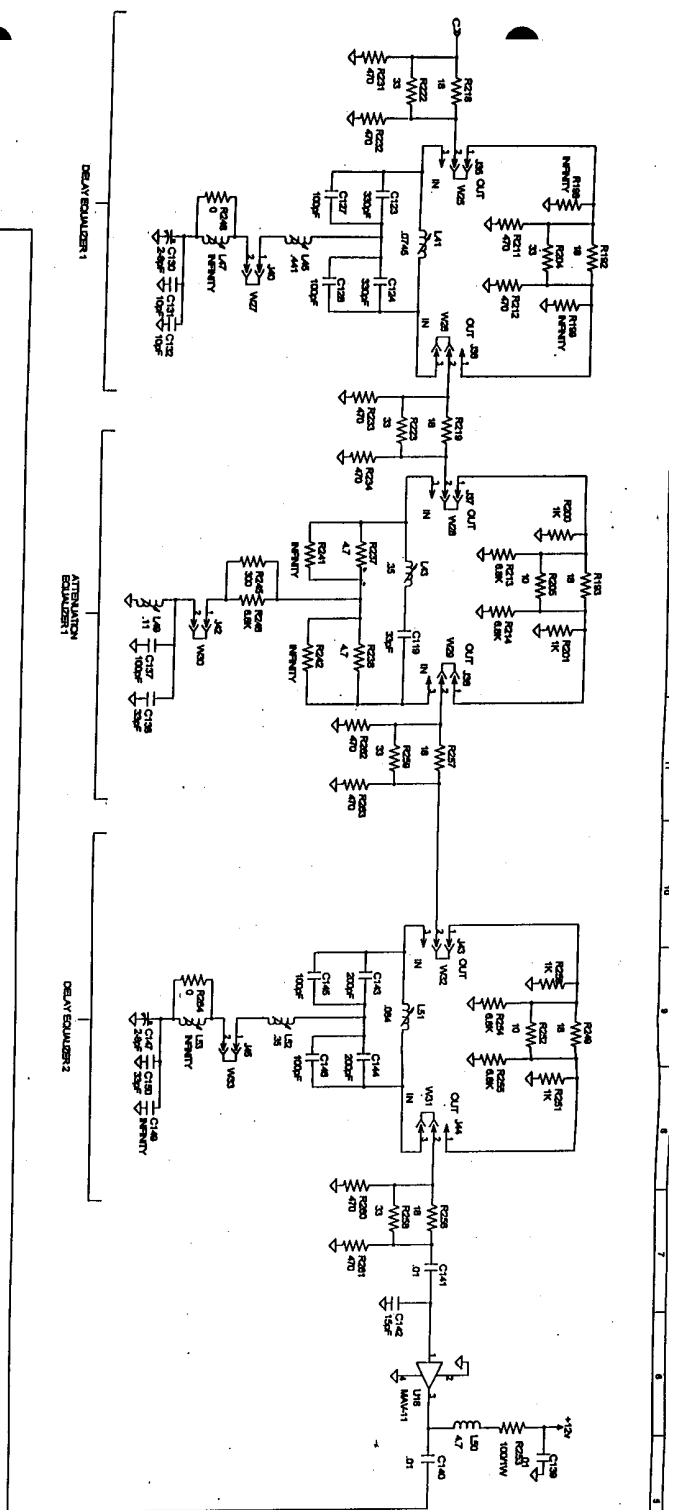
REV	ECON	DATE	AW
ICS CORPORATION TITLE SCH., POWER DETECTOR / CONTROL BD. (1585-1118)			
APPROV	DATE	THIS DRAWING IS THE PROPERTY OF ITS OWNER IT SHALL NOT BE COPIED WITHOUT PERMISSION	
DRAWN BY	CHECKED BY	DATE	NOTED BY
DNB JCF/126988	JMB	01/26/88	JMC
CAD	CHK	DATE	APP
NEL			
SHEET 1 OF 1			



IT'S CORPORATION				REV	ECN	DATE	APV
TITLE				I/C, AMPL ASSY,			
2.50-2.70 GHz W/FF				(1586-1117)			
MATERIAL				DWM			
FINISH				CHK			
REL				REL			
C				SCALE			
SHEET 1 OF 1				REV			
P0				REV			



TNS CORPORATION			
REV	ECN	DATE	APPV
1			
TITLE			
25W POWER			
AMPLIFIER MODULE			
DRAWN BY: JRM/ML			
CHKD BY: JRM/ML			
DATE: 1585-3266			
REV: 1 OF 1			



CHECK PRINT
10/28/97

ITS CORPORATION		REV ECH	DATE	JAV
TITLE SCHEMATIC IF PROCESSING CARD				
DESIGNER	DATE	CHKD BY	DATE	REV
10/28/97				
1685-3198	P0			

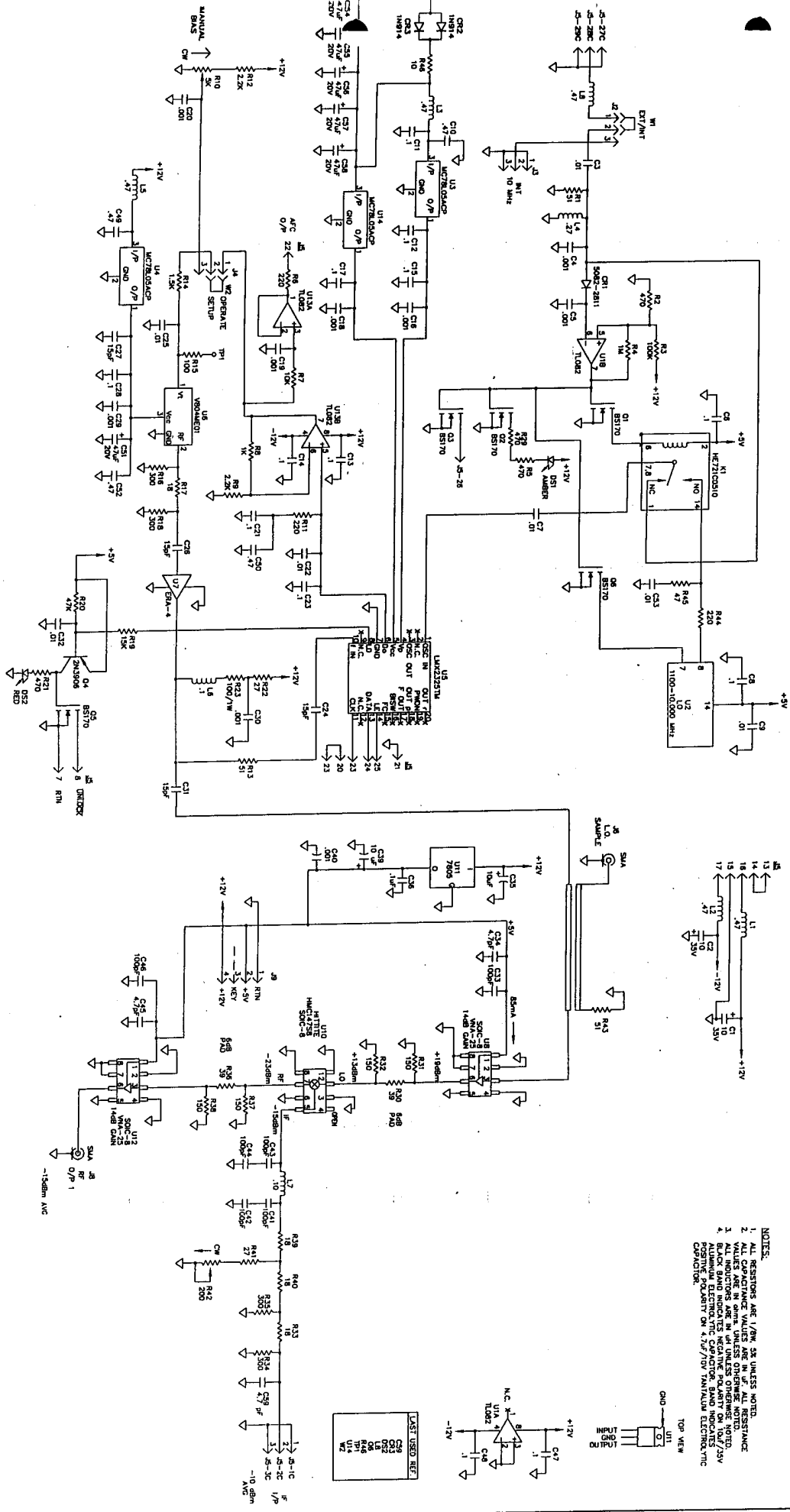


2) ALL CAPACITORS ARE 100V UNLESS OTHERWISE NOTED.

2) ALL CAPACITORS ARE 100V UNLESS OTHERWISE NOTED.

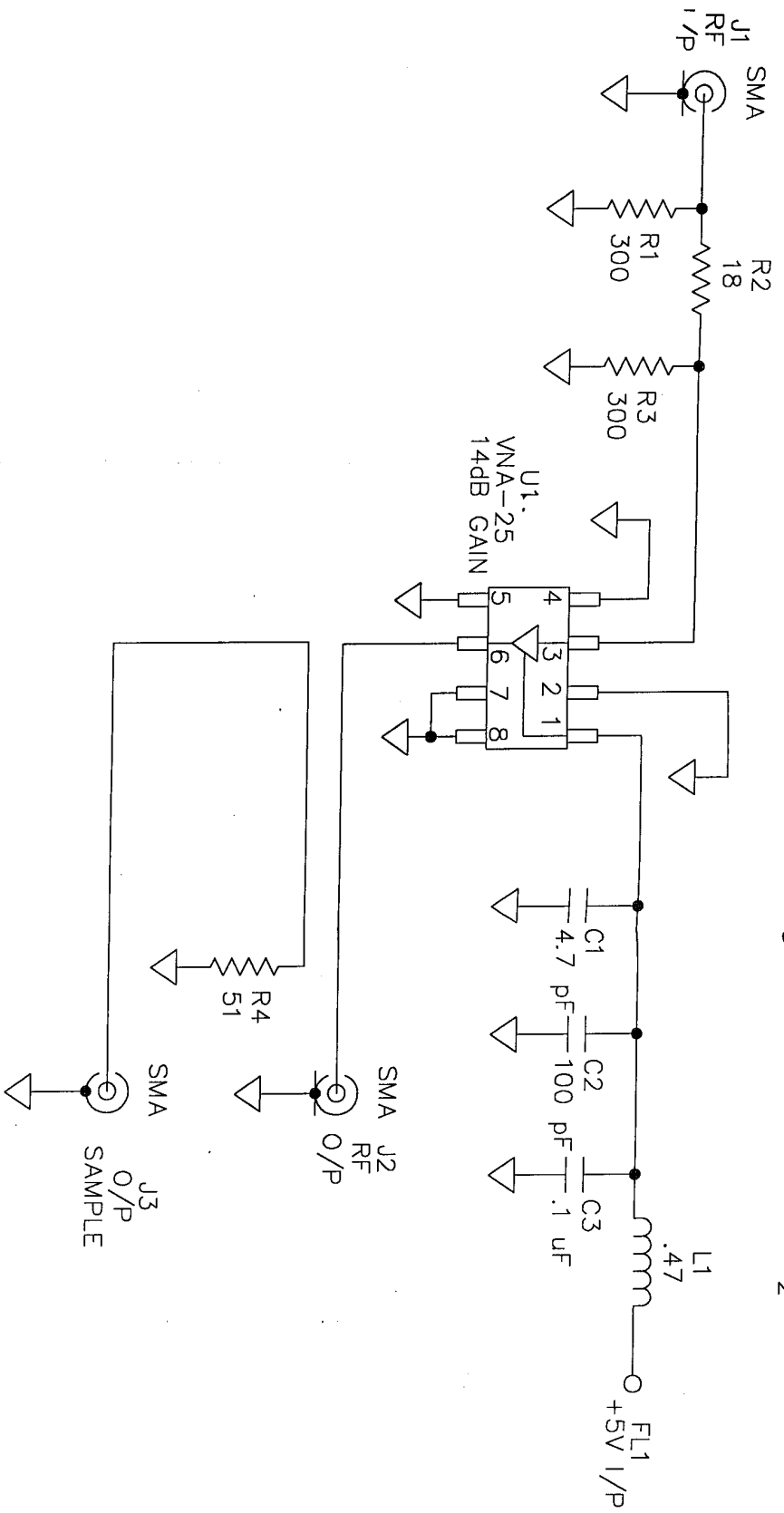
REV	ECN	DATE	REV	ECN	DATE	REV
HIS COMPANY TITLE SCH., BIAS PROTECTION BOARD, 3 SECTION						
THIS PRINT IS THE PROPERTY OF ITS OWNER IT SHALL NOT BE LOANED OR REPRODUCED WITHOUT PERMISSION						
REV	ECN	DATE	REV	ECN	DATE	REV
1	1	10-1-61	2	2	10-1-61	3
DEN P.K. 11/25/61 1585-3250 REL.						

- NOTES:
1. ALL RESISTORS ARE 1/8W 5% UNLESS NOTED OTHERWISE.
 2. CAPACITORS ARE IN PFD UNLESS OTHERWISE NOTED.
 3. ALL INDUCTORS ARE IN μ H UNLESS OTHERWISE NOTED.
 4. ALL CAPACITORS ARE IN PFD UNLESS OTHERWISE NOTED.
 5. POSITIVE POLARITY ON 4.7 μ F/50V TANTALUM ELECTROLYTIC CAPACITOR.

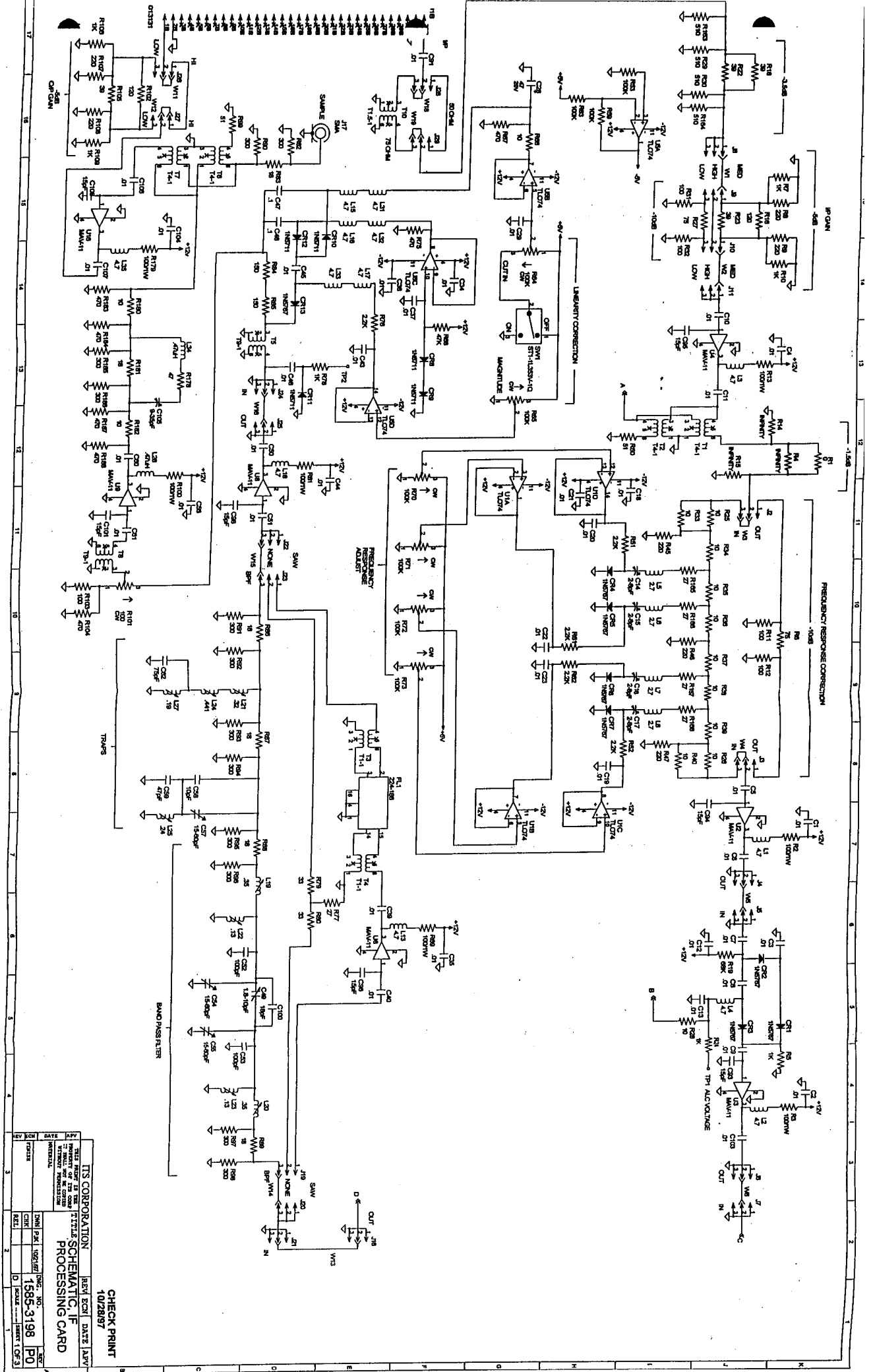


REV	DATE	BY	CHKD	APPV
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2	9/2/87	SC	SC	SC
3	9/2/87	SC	SC	SC
4	9/2/87	SC	SC	SC
5	9/2/87	SC	SC	SC
6	9/2/87	SC	SC	SC
7	9/2/87	SC	SC	SC
8	9/2/87	SC	SC	SC
9	9/2/87	SC	SC	SC
10	9/2/87	SC	SC	SC
11	9/2/87	SC	SC	SC
12	9/2/87	SC	SC	SC
13	9/2/87	SC	SC	SC
14	9/2/87	SC	SC	SC
15	9/2/87	SC	SC	SC
16	9/2/87	SC	SC	SC
17	9/2/87	SC	SC	SC
18	9/2/87	SC	SC	SC
19	9/2/87	SC	SC	SC
20	9/2/87	SC	SC	SC

ITS CORPORATION
SCH. L.O.
UP CONVERTER BD.
(1583-1117)
REV. 10/1



ITS CORPORATION				REV	ECN	DATE	APV
THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.				TITLE SCH., SINGLE STAGE AMPLIFIER			
MATERIAL -----				DWN	TM Y	7/28/97	DWG. NO.
FINISH -----				CHK			1585-3101
REV	ECN	DATE	APV	REL			A SCALE----
				SHEET	1 OF 1		

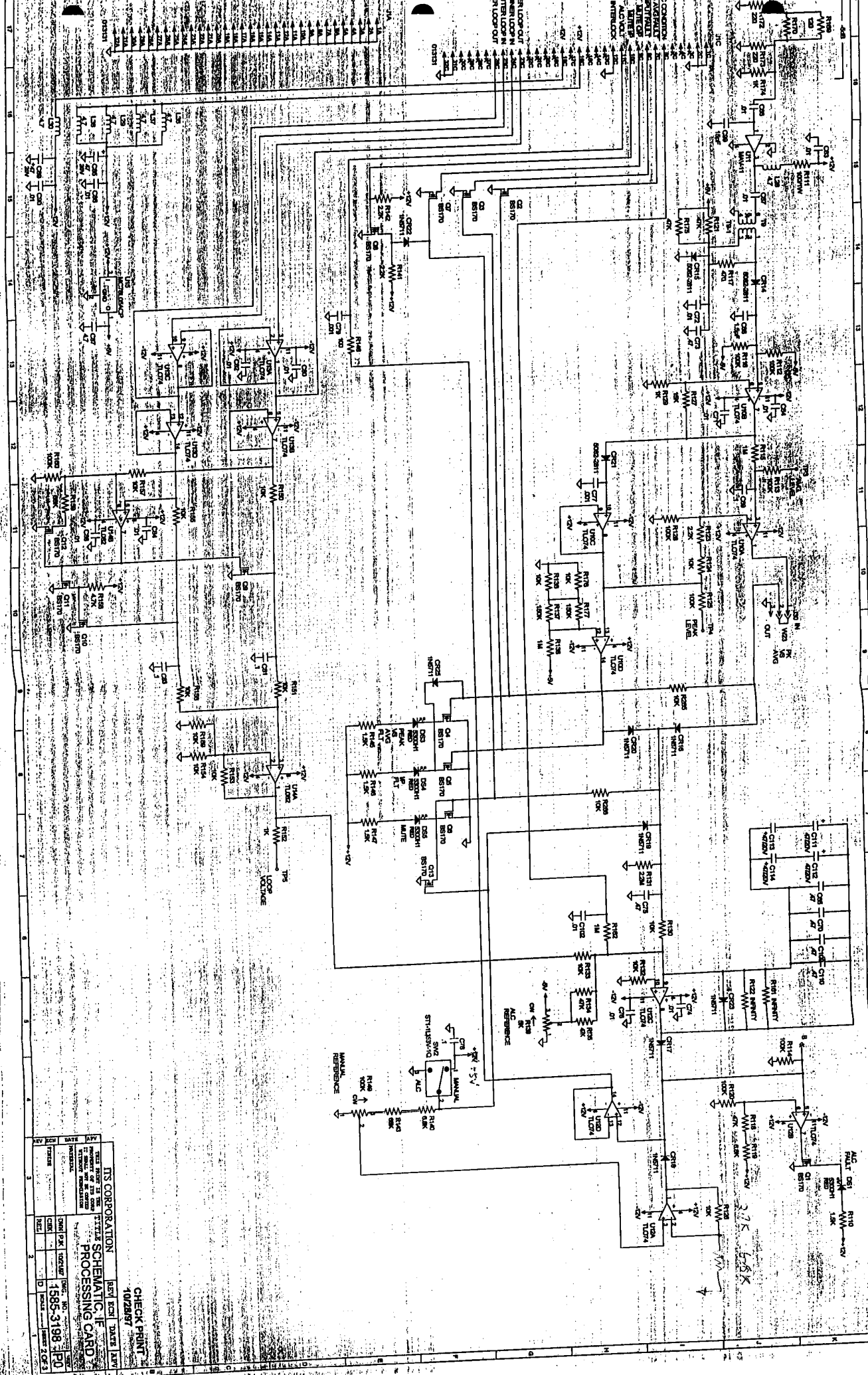


CHECK PRINT
10/28/87

ITS CORPORATION
REV ECH DATE LAV

TITLE SCHEMATIC, IF PROCESSING CARD

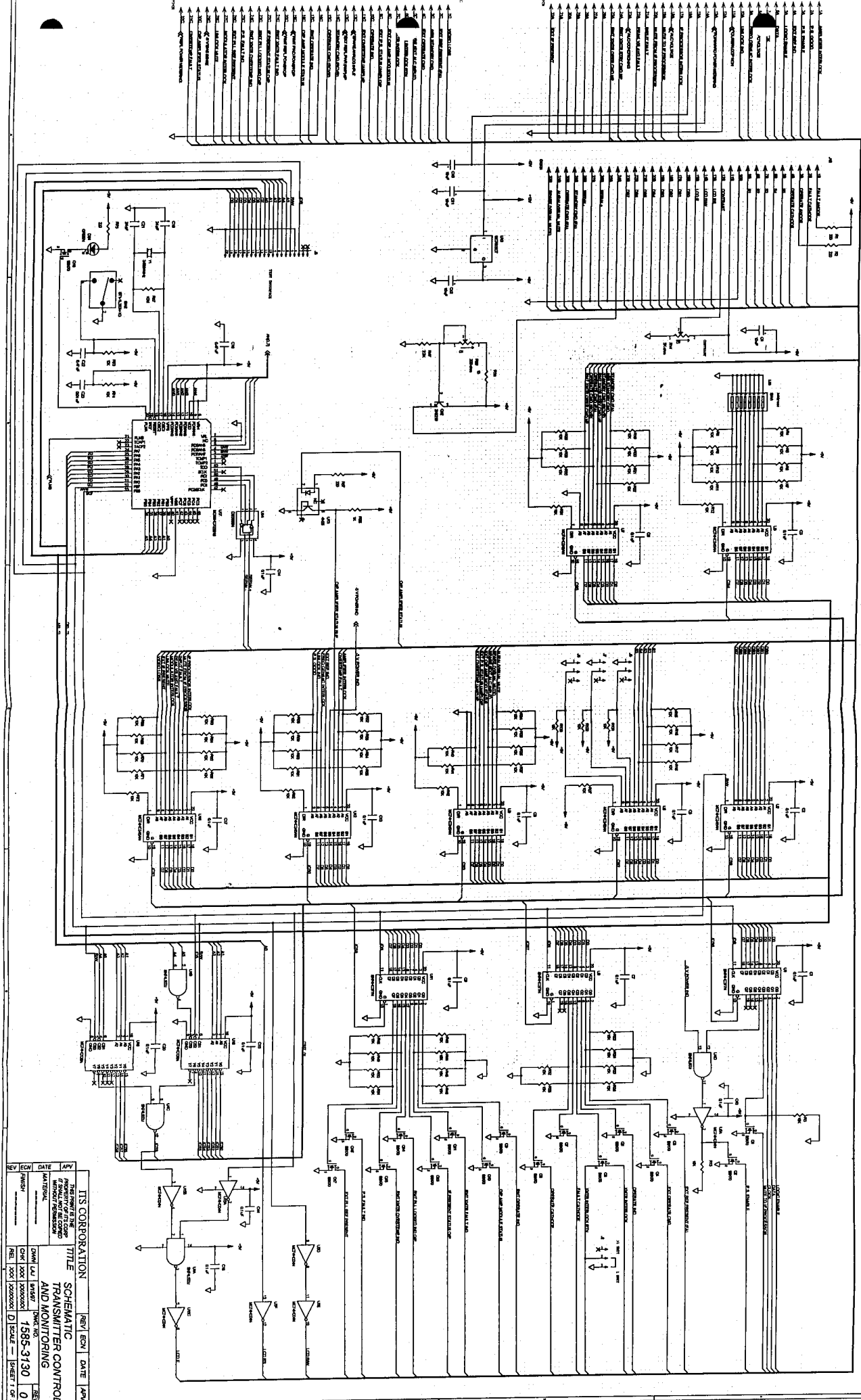
REV	DATE	BY	CHK	APP
1	10/28/87	1585-3198	PO	
2				
3				



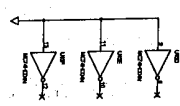
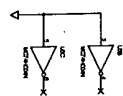
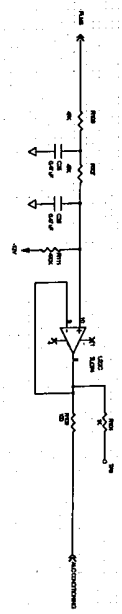
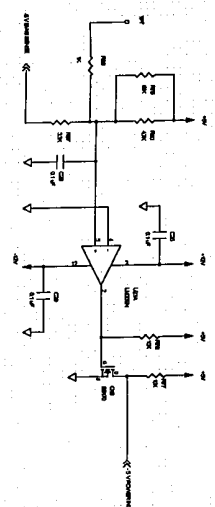
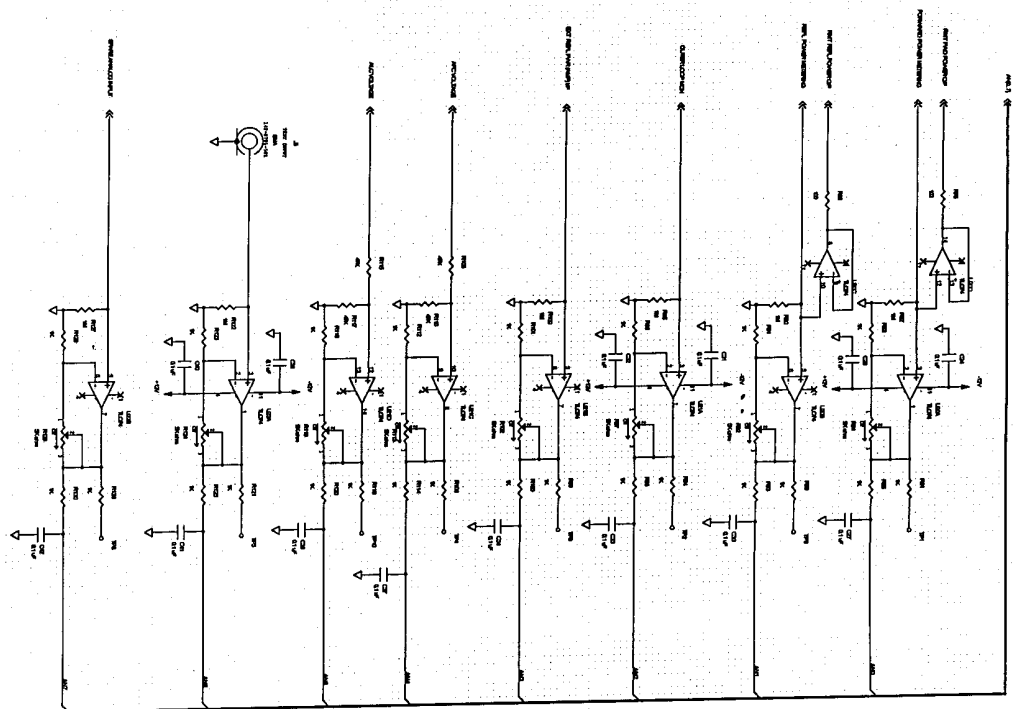
TITLE SCHEMATIC, IF
 PROCESSING CARD
 1585-3198-1P0
 1072897

REV	DATE	BY	CHK	APP
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2	7/78	WJ	WJ	WJ
3	7/78	WJ	WJ	WJ

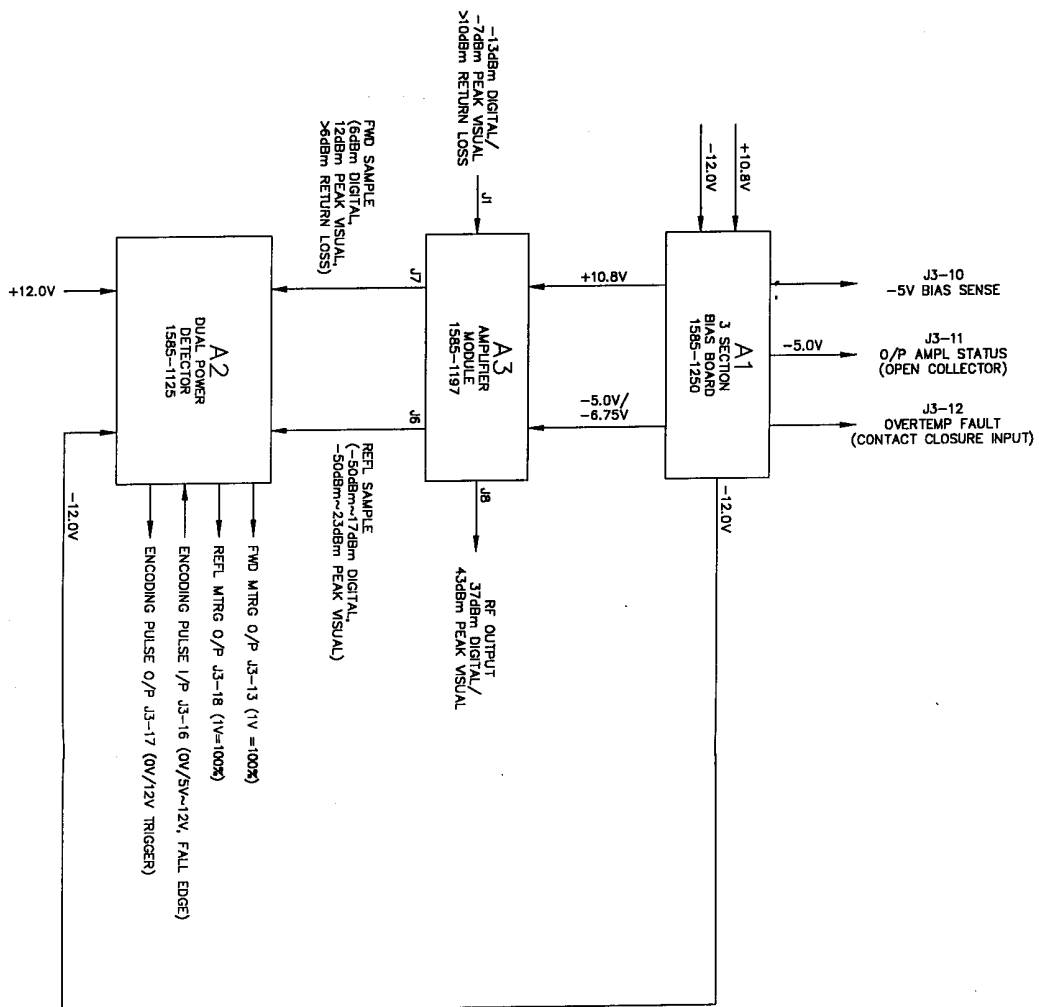
CHECK PRINT
 1072897



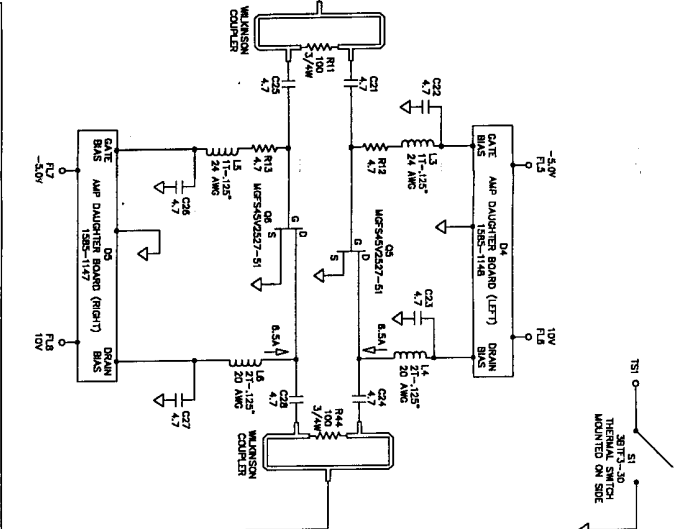
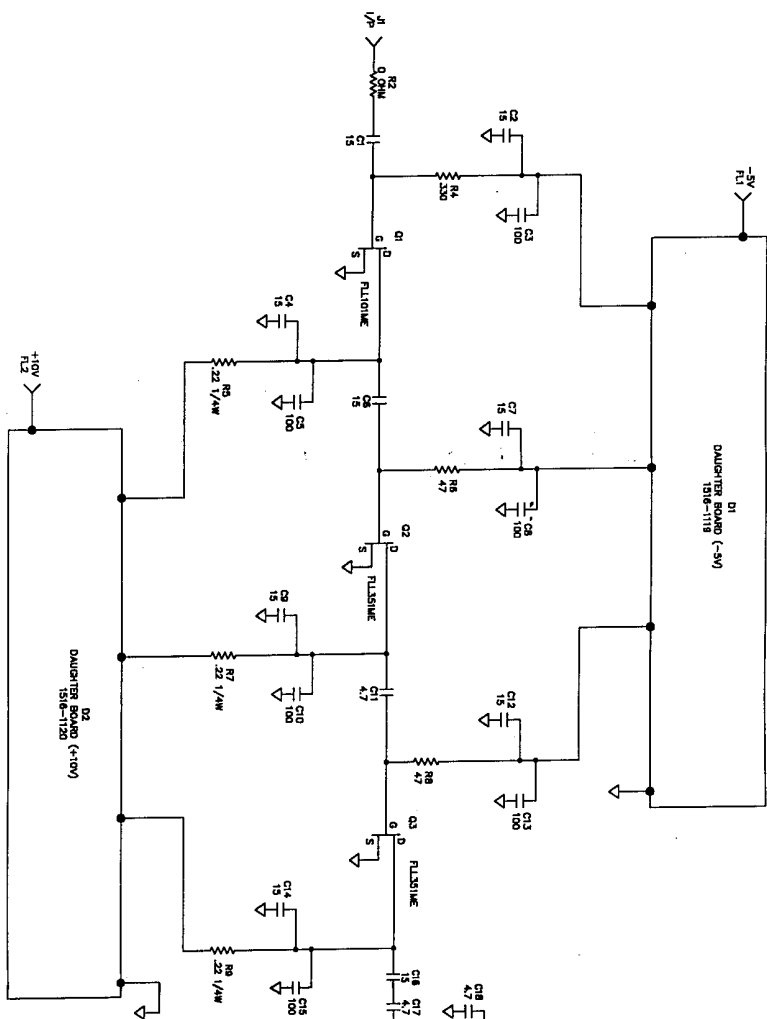
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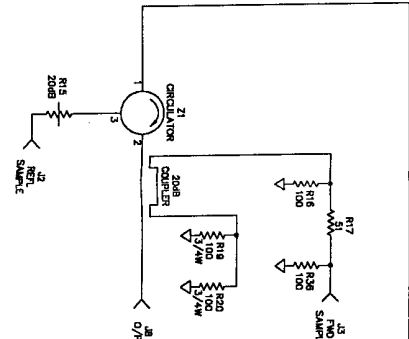
TTS CORPORATION		REV	ECN	DATE	APP
TITLE		SCHEMATIC			
TRANSMITTER CONTROL AND MONITORING		1585-3130			
DRAWN BY		J. L. BROWN			
CHECKED BY		J. L. BROWN			
DATE		1585-3130			
SCALE		1:1			
SHEET		1 OF 1			



THIS PRINT IS THE PROPERTY OF ITS CORPORATION. IT SHALL NOT BE COPIED OR REPRODUCED WITHOUT PERMISSION.			
REV	ECN	DATE	APV
FINISH			
REL			
TITLE			
B/D, AMPL ASSY, 40W PEP, 2.50-2.70 GHz (1585-1239)			
DWN	RTB	12/09/87	DWG. NO.
CHK			1585-3239
REL			0
C SCALE 1:1 SHEET 1 OF 1			

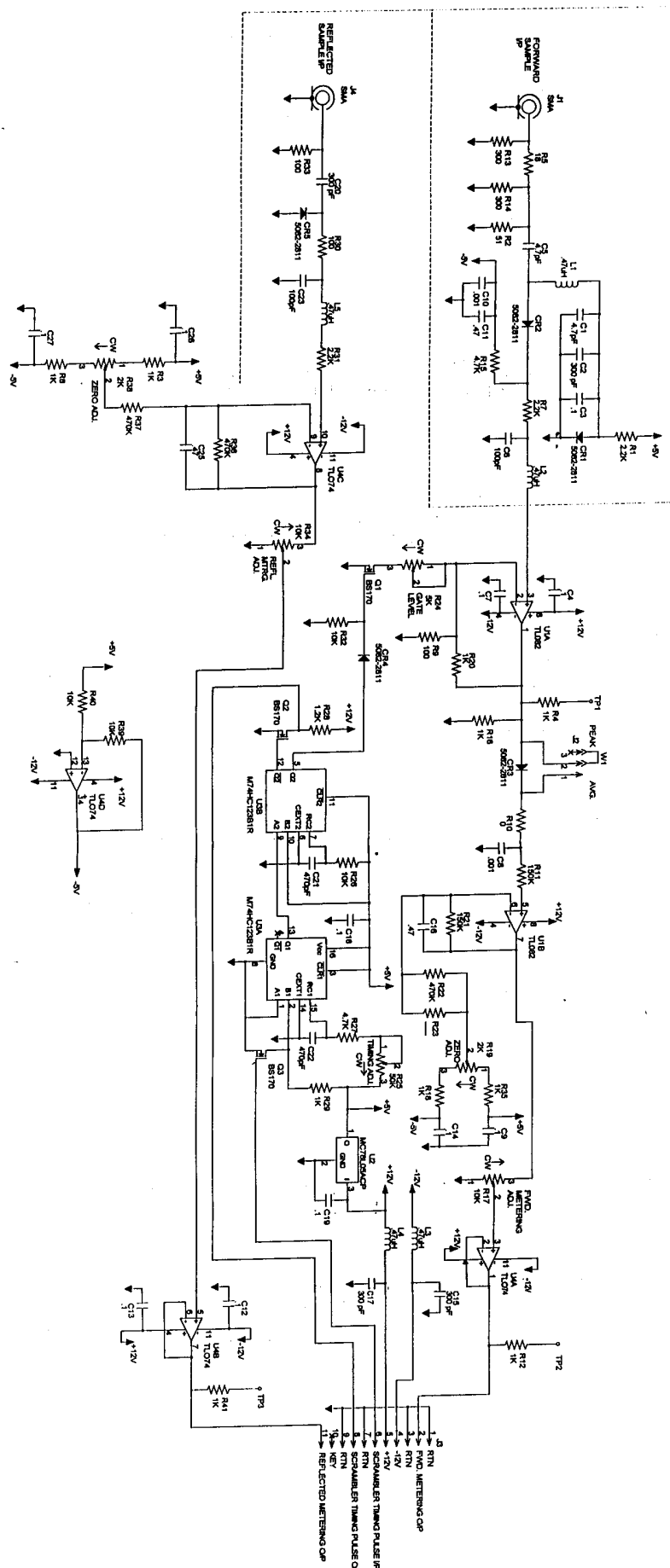


NOTES:
 1) ALL RESISTORS ARE 1/8W, 5% UNLESS OTHERWISE NOTED.
 2) ALL CAPACITORS ARE IN P.F. UNLESS OTHERWISE NOTED. ARE NOT
 3) DIMENSIONS ARE IN INCHES AND ARE SHOWN FOR REFERENCE ONLY.



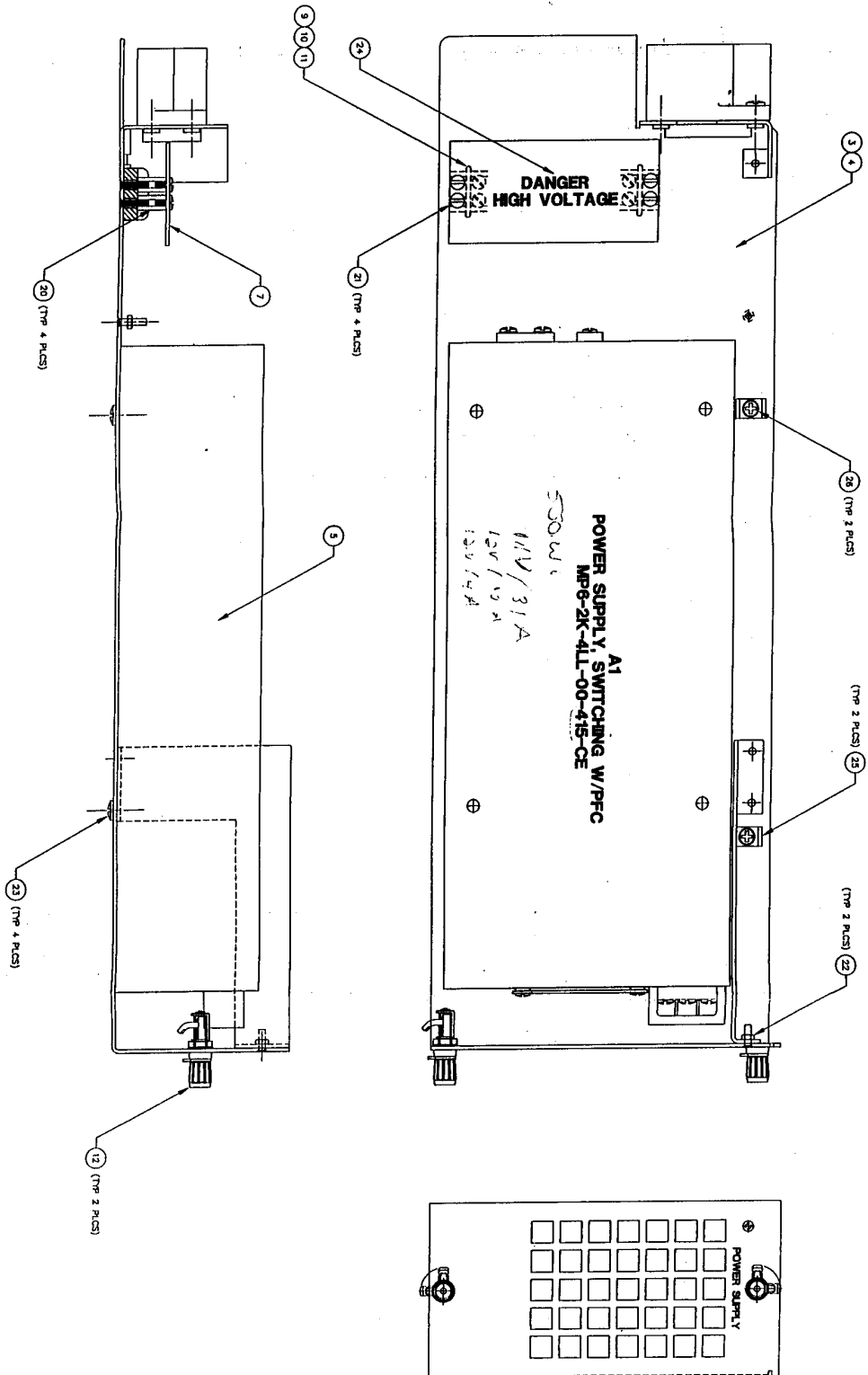
REV	DATE	BY	CHK	APP	REV	DATE	BY	CHK	APP
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4	10/1/67	DM	SK	1/1/67	DM	SK	1/1/67	DM	SK
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12	10/1/67	DM	SK	1/1/67	DM	SK	1/1/67	DM	SK
13	10/1/67	DM	SK	1/1/67	DM	SK	1/1/67	DM	SK
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17	10/1/67	DM	SK	1/1/67	DM	SK	1/1/67	DM	SK
18	10/1/67	DM	SK	1/1/67	DM	SK	1/1/67	DM	SK
19	10/1/67	DM	SK	1/1/67	DM	SK	1/1/67	DM	SK
20	10/1/67	DM	SK	1/1/67	DM	SK	1/1/67	DM	SK

TTS CORPORATION
 TITLE: SCH. 40W PEP
 AMPLIFIER BD.
 (2.5-2.7 GHz)
 1585-319610
 REV. 1 OF 1

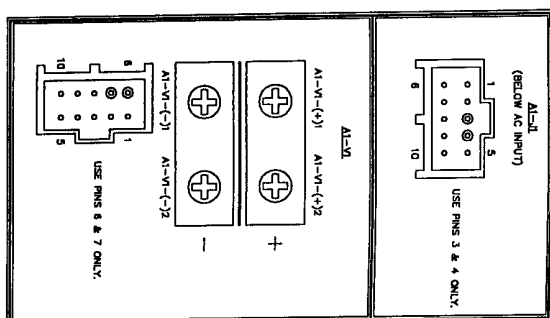
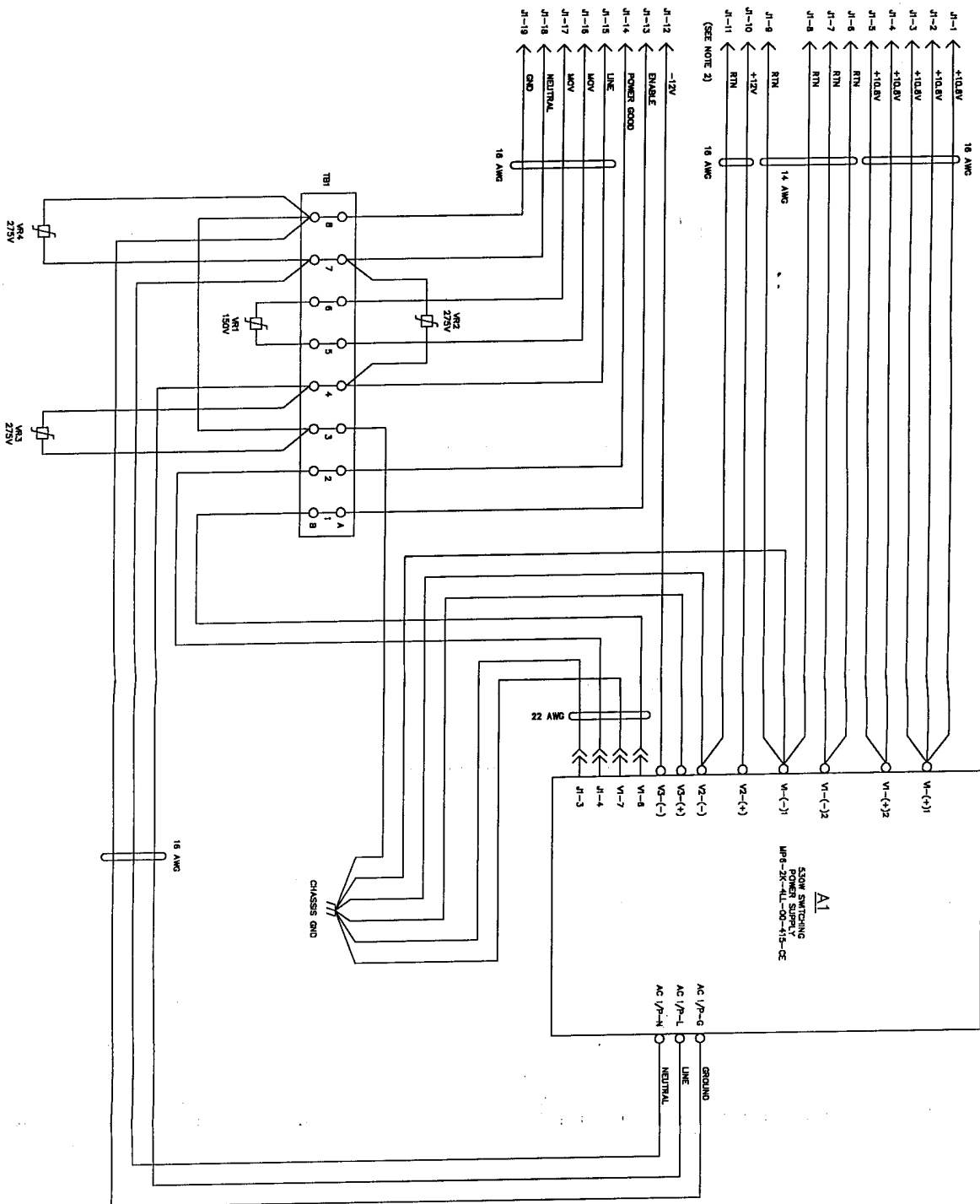


NOTES:
 1) ALL RESISTORS ARE 1/4W UNLESS OTHERWISE NOTED.
 2) ALL CAPACITORS ARE 50V UNLESS OTHERWISE NOTED.
 3) ALL INDICATORS ARE IN UNLESS OTHERWISE NOTED.

ITS CORPORATION		REV	ECN	DATE	REV
TITLE		SCH. DUAL			
POWER DETECTOR BD.		(1585-1125)			
DESIGNED BY	9/1/67	DRG. NO.	1585-3125		
CHECKED BY	9/1/67	REV.	1		
REL. BY	9/1/67	REV.	1		

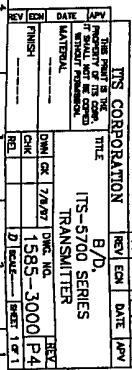
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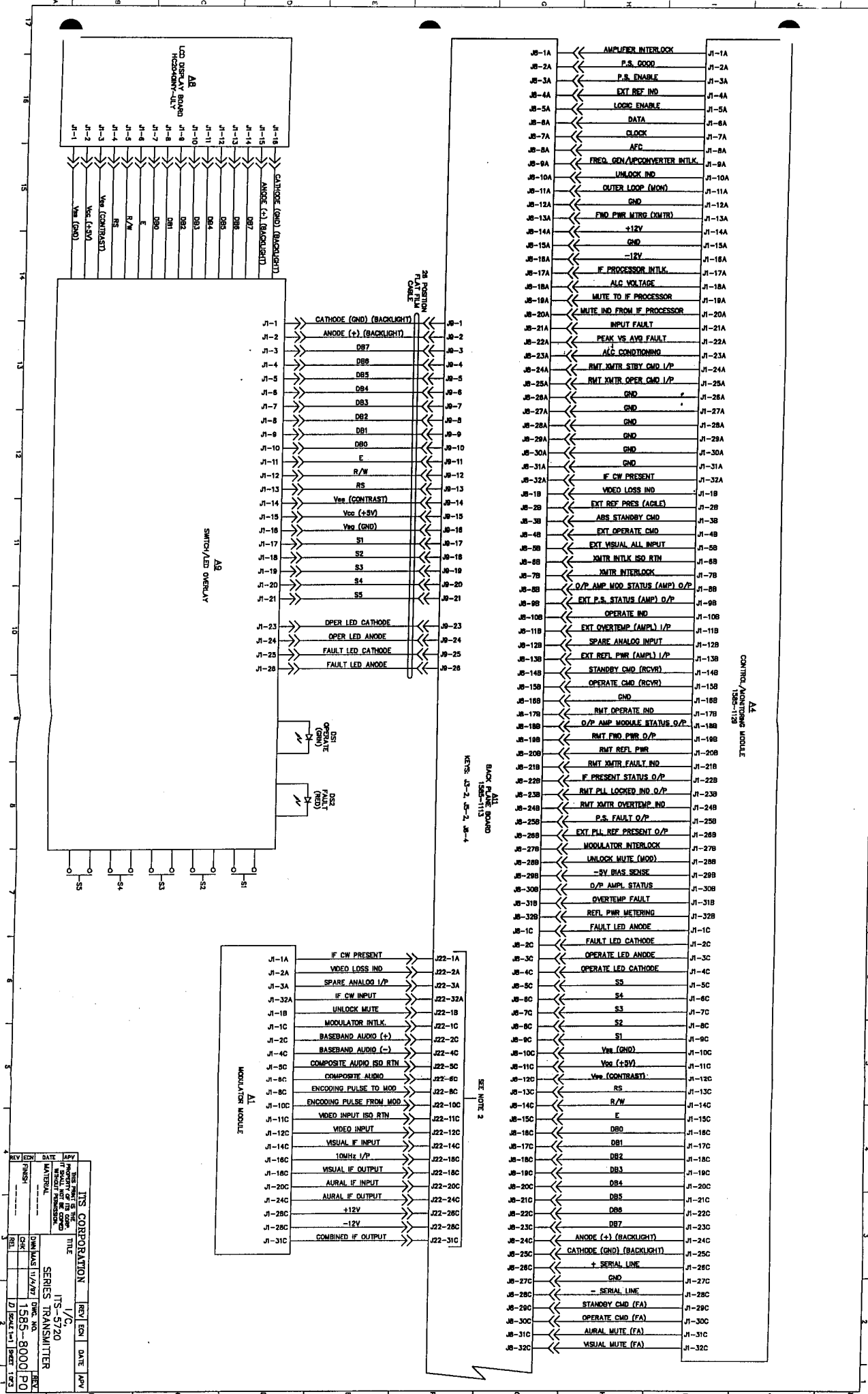
A2
18 POSITION TERMINATE
CONNECTIONS (SEE FIGURE 2)

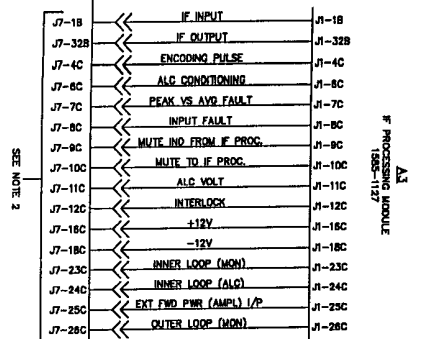


NOTE:
1) ALL SOLDER WIRE IS 18AWG UNLESS OTHERWISE NOTED.
2) A2-V-11 MUST BE EQUIPPED WITH (WAVE FIRST-BREAK LAST)

JTS CORPORATION			
REV	EDN	DATE	BY
1	0175		
TITLE			
POWER SUPPLY			
I/C			
MODULE			
Dwg No. 1585-8110.1			
REL. SEC. 1/29/77			
REV. 1			





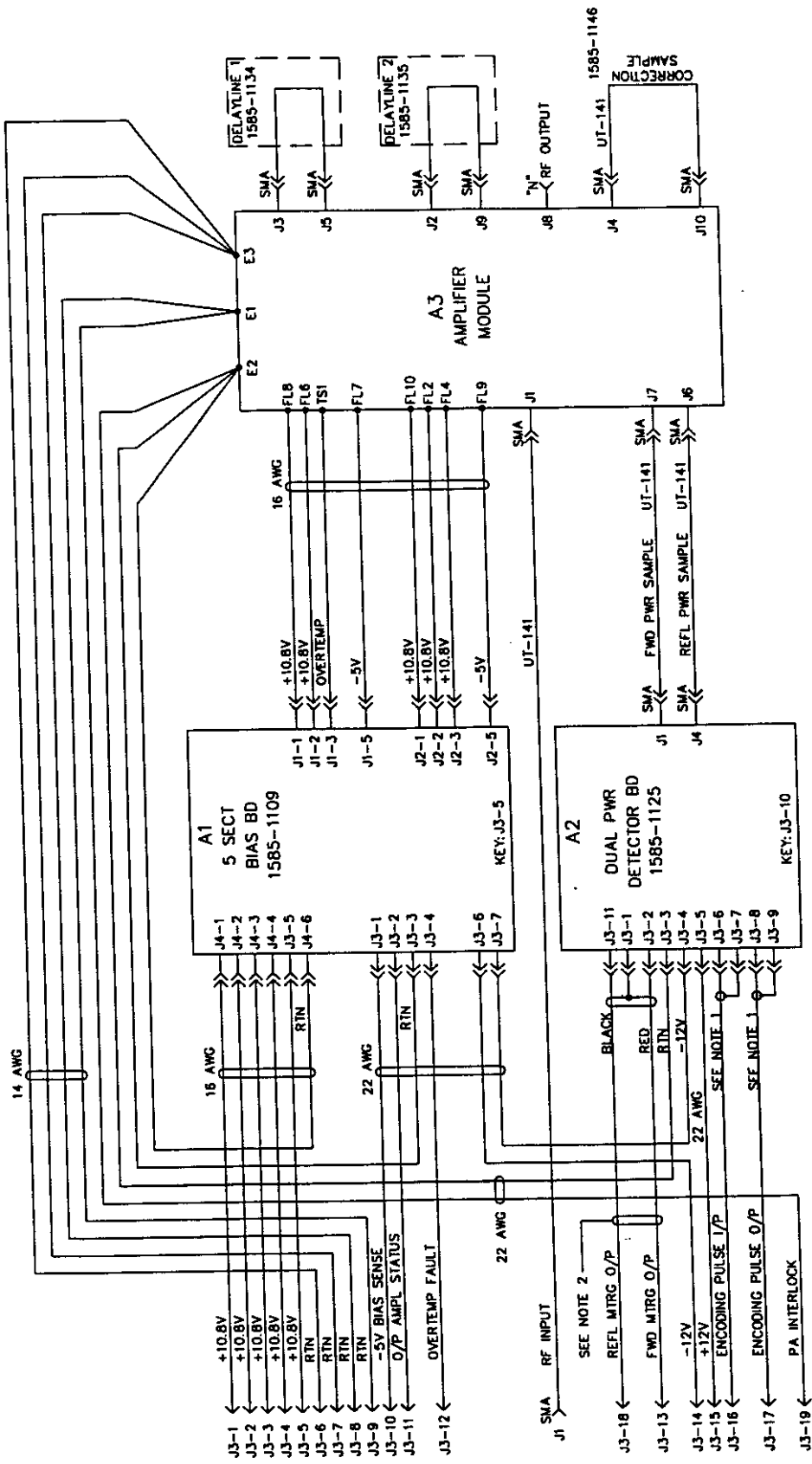


IF PROCESSING MODULE
1585-117

J12-1	STBY CMD (FA)
J12-2	GND
J12-3	DPK CMD (FA)
J12-4	GND
J12-5	AURAL/VISUAL MUTE (FA)
J12-6	VISUAL MUTE (FA)
J12-7	NOT USED
J12-8	GND
J12-9	EXT REF PRESENT (FA)
J12-10	GND
J12-11	ARS STANDBY CMD
J12-12	GND
J12-13	EXT OPERATE CMD
J12-14	GND
J12-15	EXT VISUAL ALC INPUT
J12-16	GND
J12-17	ENCODING PULSE I/P
J12-18	XMTR INTLK ISOLATED RTN
J12-19	XMTR INTERLOCK
J12-20	GND
J12-21	SPARE ANALOG INPUT
J12-22	GND
J12-23	NOT USED
J12-24	O/P AMP MOD STATUS (AMPL) O/P
J12-25	GND
J12-26	EXT P.S. STATUS (AMPL) O/P
J12-27	STANDBY CMD (RCVR)
J12-28	GND
J12-29	OPERATE CMD (RCVR)
J12-30	GND
J12-31	OPERATE IND
J12-32	GND
J12-33	EXT OVERTEMP (AMPL) I/P
J12-34	GND
J12-35	EXT FWD PWR (AMPL) I/P
J12-36	GND
J12-37	EXT REFL PWR (AMPL) I/P
J10-1	NO CONNECTION
J10-2	GND
J10-3	GND
J10-4	+ SERIAL LINE
J10-5	- SERIAL LINE
J10-6	GND
J10-7	GND
J10-8	NO CONNECTION
J11-1	NO CONNECTION
J11-2	GND
J11-3	GND
J11-4	+ SERIAL LINE
J11-5	- SERIAL LINE
J11-6	GND
J11-7	GND
J11-8	NO CONNECTION
J13-1	RMT XMTR OPER CMD I/P
J13-2	GND
J13-3	RMT XMTR STBY CMD I/P
J13-4	RMT OPER IND
J13-5	GND
J13-6	GND
J13-7	O/P AMP MODULE STATUS O/P
J13-8	RMT FWD PWR O/P
J13-9	GND
J13-10	NOT USED
J13-11	GND
J13-12	RMT REFL PWR
J13-13	GND
J13-14	RMT XMTR FAULT IND
J13-15	IF PRESENT STATUS O/P
J13-16	RMT PLL LOCKED IND O/P
J13-17	NOT USED
J13-18	RMT XMTR OVERTEMP IND
J13-19	P.S. FAULT O/P
J13-20	EXT OVERTEMP (AMPL) I/P
J13-21	EXT FWD PWR (AMPL) I/P
J13-22	EXT REFL PWR (AMPL) I/P
J13-23	O/P AMP MOD STATUS (AMPL) O/P
J13-24	EXT P.S. STATUS (AMPL) O/P
J13-25	EXT PLL REF PRESENT O/P

ALL
BACK PLANE BOARD
1585-113
REV: J1-2, J5-2, J6-4

REV	DATE	BY	DESCRIPTION
1	1585-113	1585-113	1585-113
2	1585-113	1585-113	1585-113
3	1585-113	1585-113	1585-113
4	1585-113	1585-113	1585-113
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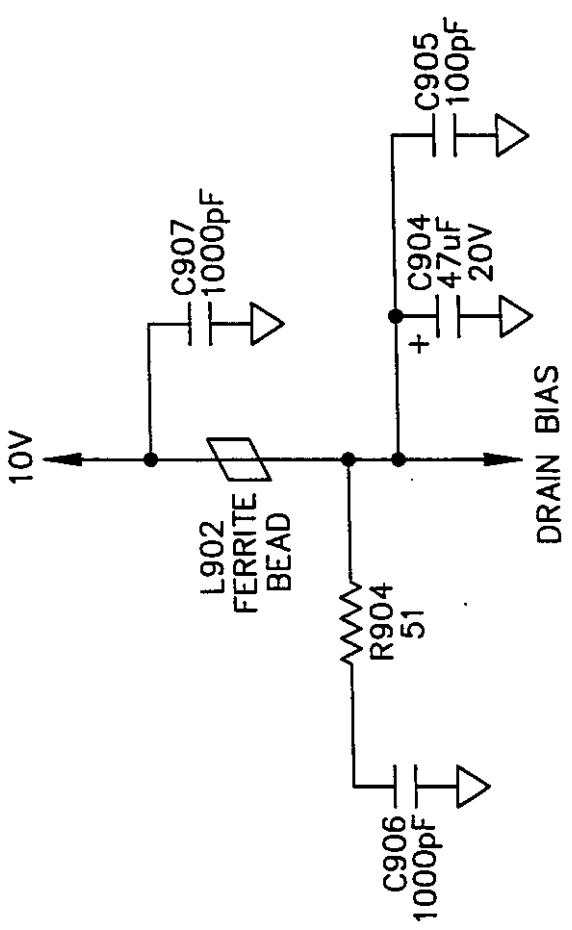
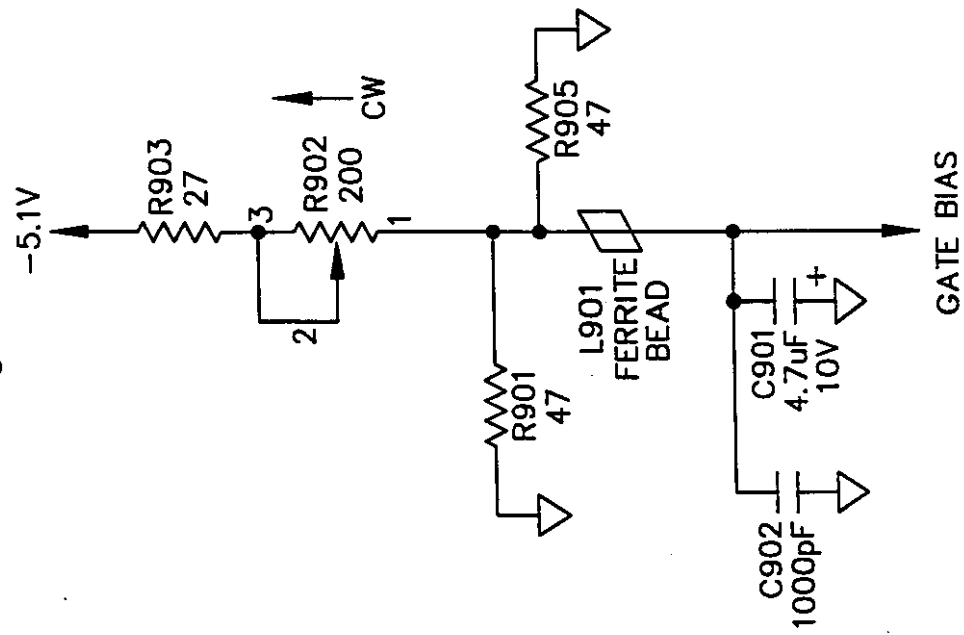


NOTE:
1) WIRES ARE BELDEN YR40143. THE SHIELDS AT THE J3 END OF THE WIRES ARE CUT BACK (NO CONNECTION) AND COVERED WITH HEAT SHRINKABLE TUBING.
2) WIRE IS 941. THE SHIELD AT THE J3 END OF THE WIRE IS CUT BACK (NO CONNECTION) AND COVERED WITH HEAT SHRINKABLE TUBING.

REV	ECN	DATE	APV
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2	2	1585-1136	0
3	3	1585-1136	0
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8	8	1585-1136	0
9	9	1585-1136	0
10	10	1585-1136	0
11	11	1585-1136	0

REV	ECN	DATE	APV
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2	2	1585-1136	0
3	3	1585-1136	0
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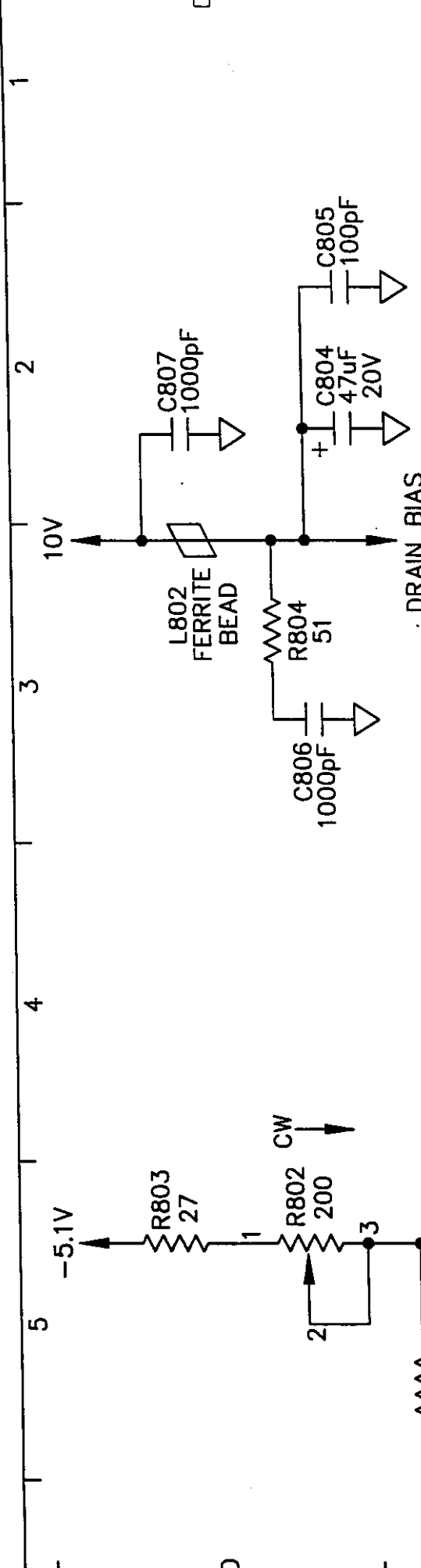
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6	6	1585-1136	0
7	7	1585-1136	0
8	8	1585-1136	0
9	9	1585-1136	0
10	10	1585-1136	0
11	11	1585-1136	0



NOTES:

- 1) ALL RESISTORS ARE 1/8W, 5% & IN VALUES OF OHMS UNLESS OTHERWISE NOTED.
- 2) IF 200 OHM POTS ARE NOT AVAILABLE, 100 OHM POTS MAY BE USED. HOWEVER, RESISTOR VALUES MAY HAVE TO CHANGE (SEE NOTE 2).
- 3) TO ACCOUNT FOR VARIATIONS IN THE FLL300 & FLL200, R903 MAY BE RAISED TO INCREASE DRAIN CURRENT (TYP 150 OHM) OR LOWERED TO DECREASE DRAIN CURRENT (TYP 27 OHM).

ITS CORPORATION				REV	ECN	DATE	APV	
THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.		TITLE SCH, AMP. DAUGHTER BOARD, RIGHT SIDE (1585-1147)						
		DWN	JKF	10/1/97	DWG. NO.	REV		
		CHK			1585-3147	0		
MATERIAL				REL		A	SCALE----	SHEET 1 OF 1
FINISH								
APV	DATE	ECN	REV					



NOTES:

- 1) ALL RESISTORS ARE 1/8W, 5% & IN VALUES OF OHMS UNLESS OTHERWISE NOTED.
- 2) IF 200 OHM POTS ARE NOT AVAILABLE, 100 OHM POTS MAY BE USED. HOWEVER, RESISTOR VALUES MAY HAVE TO CHANGE (SEE NOTE 2).
- 3) TO ACCOUNT FOR VARIATIONS IN THE FLL300 & FLL200, R803 MAY BE RAISED TO INCREASE DRAIN CURRENT (TYP 150 OHM) OR LOWERED TO DECREASE DRAIN CURRENT (TYP 27 OHM).

IT'S CORPORATION		REV	ECN	DATE	APV
THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.		TITLE SCH, AMP. DAUGHTER BOARD, LEFT SIDE		DWG. NO. 1585-3148	
		(1585-1148)		REV '0	
REV	ECN	DWN	JKF	10/1/97	
		CHK			
		REL			
MATERIAL		SCALE		SHEET 1 OF 1	
FINISH		A			

